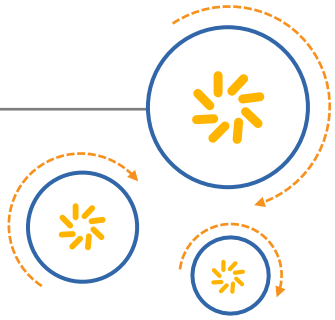
Qualcomm Technologies, Inc.



MSM8909/MSM8905 Linux Android Software User Manual

Software Product Document

SP80-NR964-4 G

August 24, 2017

Qualcomm Technologies, Inc.
5775 Morehouse Drive
San Diego, CA 92121
U.S.A.

Revision history

Revision	Date	Description
A	October 2014	Initial release
B	November 2014	Added Sections 4.5 and 5.1 Updated Sections 2.4, 3.3.8, and 4.2 Updated Table 2-1, Table 3-2, and Table 4-1
C	December 2014	Updated Sections 3.2, 3.3.2, 3.3.3, 3.3.4, and 3.3.6 Updated Table 2-1 and Table 3-2
D	July 2017	Added Sections 3.4, 3.3.9, and 3.4.6 Updated Sections 3.3.2, 3.3.6, and 3.3.7
E	July 2017	Updated Section 3.3.7
F	August 2017	Updated Section 3.3.2 and 3.3.7
G	August 2017	Updated Tables in Section 3.3.2 and 3.3.7

Contents

Figures

Tables

1 Introduction

1.1 Purpose

This document describes how to obtain, build, and program software applicable to the MSM8909 Linux Android Software Product (SP) "as-is" into a reference platform including:

- Set up a development environment and installing the software
- Build the software and flashing it onto a reference platform
- Configure and bring up call, GPS, multimedia, and so on.

1.2 Conventions

Function declarations, function names, type declarations, attributes, and code samples appear in a different font, for example, `#include`.

Code variables appear in angle brackets, for example, `<number>`.

Commands to be entered appear in a different font, for example, **`copy a:*.* b:`**.

Button and key names appear in bold font, for example, click **Save** or press **Enter**.

Shading indicates content that has been added or changed in this revision of the document.

1.3 Technical assistance

For assistance or clarification on information in this document, submit a case to Qualcomm Technologies, Inc. (QTI) at https://createpoint.qti.qualcomm.com/.

If you do not have access to the CDMATech Support website, register for access or send email to support.cdmatech@qti.qualcomm.com.

2 Installation and setup

2.1 Required equipment and software

Table 2-1 identifies the equipment and software to install and run the software.

NOTE: Due to a possibility of version change for the tools listed in the following table with software releases, refer to MSM8909 software release note for the exact version of the tools.

Table 2-1 Required equipment and software

	Item description	Version	Source/vendor	Purpose
1	Linux development workstation that exceeds minimum requirements for running Ubuntu 64-bit OS	—	—	Android build machine
2	Windows 7 or Windows XP workstation	Windows 7 or Windows XP	Microsoft	Alternate Non-HLOS build machine and Windows-based programming tools
3	Ubuntu 12.0.4 LTS Linux distribution for 64-bit architecture	12.0.4 LTS	Ubuntu Community/ Canonical, Ltd.	Android build host OS
4	Java SE JDK for Linux x64	6 7 (for Android L)	Oracle	Building Android
5	Repo	—	Android open source project	Android source management tool
6	ARM toolchain	ARM Compiler Tools 5.01 update 3 (build 94)	ARM Ltd.	Building boot images, RPM, and TrustZone (TZ)
7	Qualcomm® Hexagon™ DSP toolchain	6.4.04	QTI/ GNU	Building Modem Processor Subsystem (MPSS)
8	Python	2.7.6	Python.org	Building boot, subsystem, and so on. ▪ Boot – Ver 2.6.6 ▪ RPM – Ver 2.6.6 ▪ TZ – Ver 2.6.6 ▪ Meta – Ver 2.7.5 ▪ MPSS – Ver 2.7.6
9	SCons Ver 2.0.0 or higher		scons.org	Building all non-HLOS source code releases

2.2 Install ubuntu

Log on as root or use sudo to have root permissions during the installation.

1. Create an installation CD and install it onto the computer by following the instructions at http://releases.ubuntu.com.
2. After installation, perform a software update using one of the following options:
   - Using the GUI, select System→Administration→Update Manager

     or
   - Using the shell command line
     i. Edit the source config file directly, as follows:

     ```
     sudo vi /etc/apt/sources.list
     ```

     ii. Edit the file to enable the universe and multiverse sources, and disable the Ubuntu installation CD source.
     iii. From the command line, perform the package list update and package upgrades:

     ```
     sudo apt-get update

     sudo apt-get upgrade
     ```

3. Use apt-get to install the additional required packages.

   ```
   $ sudo apt-get install git-core gnupg flex bison gperf build-essential
   zip curl zlib1g-dev libc6-dev lib32ncurses5-dev ia32-libs x11proto-core-
   dev libx11-dev lib32readline5-dev lib32z-dev libgl1-mesa-dev g++-
   multilib mingw32 tofrodos python-markdown libxml2-utils xsltproc
   ```

4. IMPORTANT! Make bash the default shell (Android build scripts contain bash shell dependencies that require the system default shell /bin/sh to invoke bash) using one of the following options:
   - Reconfigure the package:
     i. Use the command:

     ```
     sudo dpkg-reconfigure dash
     ```

     ii. Answer *no*.
   - Manually change the symlink /bin/sh→dash to /bin/sh→bash using the following commands:

```
sudo rm /bin/sh

sudo ln -s /bin/bash /bin/sh
```

NOTE: See the Ubuntu Wiki page at https://wiki.ubuntu.com/DashAsBinSh for more information.

2.3 Configure samba for Windows sharing (optional)

1. Use the following command to install the Samba server and configuration manager for Windows sharing:

   ```
   sudo apt-get install samba system-config-samba
   ```

2. Configure the Samba server using:

   ```
   System->Administration->Samba
     preferences->server settings:
     vmgroup, security=user authentication
     encrypt pw=yes, guest accnt=no guest accnt
   add share directory=/, share name=root, description=root directory
   ```

2.4 Install JDK

The Sun JDK is no longer in the main package repository of Ubuntu. To download it, add the appropriate repository and indicate to the system about the use of JDK.

```
sudo add-apt-repository “deb http://archive.canonical.com/ lucid partner”
sudo apt-get update
sudo apt-get install sun-java6-jdk
```

If Android L version is used, install the JDK 7:

```
# get package openjdk-7-jdk
sudo apt-get install openjdk-7-jdk
```

2.5 Install repo

The repo tool is a source code configuration management tool used by the Android project (see Installing Repo at http://source.android.com/source/downloading.html). It is a front end to git written in Python that uses a manifest file to aid downloading the code organized as a set of projects stored in different git repositories.

To install repo:

1. Create a ~/bin directory in home directory, or, if you have root or sudo access, install for all system users under a common location, such as /usr/local/bin or somewhere under /opt.

2. Download the repo script.
   ```
   $ curl https://dl-ssl.google.com/dl/googlesource/git-repo/repo
   >~/bin/repo
   ```
3. Set the repo script attributes to executable.
   ```
   $ chmod a+x ~/bin/repo
   ```
4. Include the installed directory location for repo in the PATH.
   ```
   $ export PATH=~/bin:$PATH
   ```
5. Run **repo --help** to verify installation; a message similar to the following should appear:
   ```
   $ repo --help
   usage: repo COMMAND [ARGS]
   repo is not yet installed.  Use “repo init” to install it here.
   The most commonly used repo commands are:
     init      Install repo in the current working directory
     help      Display detailed help on a command
   ```

NOTE: For access to the full online help, install repo (repo init).

2.6 Install the ARM compiler tools

Building the non-HLOS images requires the specific version of the ARM Compiler Tools indicated in Section 2.1. Linux is the recommended build environment for building all software images; however, either Windows or Linux-hosted versions work for building the non-HLOS images. For more information about the ARM developer suite and toolchains, go to the ARM support website at http://infocenter.arm.com/help/index.jsp?topic=/com.arm.doc.subset.swdev.coretools/index.html.

Installing on a Linux host

1. Obtain the required ARM toolchain from the ARM vendor.
2. Follow the vendor instructions to install the toolchain and flex license manager onto the Linux build system.

Installing on a Windows host

1. Obtain the required ARM toolchain from the ARM vendor.
2. Follow the vendor instructions to install the toolchain and flex license manager onto the Windows build system.
3. Access the software from https://silver.arm.com/download/download.tm?pv=1245960.
4. The default install location is C:\Program Files (x86)\ARM_Compiler5\.
5. If necessary, change the directory where the files are extracted to match the location where the tools are installed. For example, the installing directory for QTI is C:\Program Files (x86)\ARM_Compiler5\bin.
6. Confirm that the updated tools are installed by opening a DOS command prompt window and checking the versions for the compilers, linker, assembler, and fromelf.
7. To check the versions, run **armcc –vsn**. It must return the following:

```
ARM/Thumb C/C++ Compiler, 5.01 [Build 94]
For support contact support-sw@arm.com
Software supplied by: ARM Limited

armar –-vsn
armlink –-vsn
armasm –-vsn
fromelf --vsn
```

The returned version must be `Build 94` for all.

2.7 Install the Hexagon toolchain

Linux is the recommended build environment for building all software images; however, either Windows or Linux-hosted versions work for building the non-HLOS images.

See Hexagon Tools Installation Guide 80-VB419-25 for detailed procedures to download and install the Hexagon toolchain software. See Hexagon Development Tools Overview 80-VB419-74 for more documentation on using the Hexagon tools.

See *Hexagon LLVM C/C++ Compiler User Guide* (80-VB419-89) and *LLVM Compiler for Hexagon Processor Deployment Plan* (80-VB419-87) for a detailed explanation of the LLVM compiler in the Hexagon toolchain. LLVM compilers work with Hexagon software development tools and utilities to provide a complete programming system for developing high-performance software.

3 Download and build the software

Table 3-1 describes the software for this product line. It is divided into the release packages (download separately) combined to have a complete product line software set.

Table 3-1 Release packages

From https://chipcode.qti.qualcomm.com/	From https://www.codeaurora.org/
Proprietary non-HLOS software Contains proprietary source and firmware images for all non-Apps processors. This software is an umbrella package built from a combined set of individual component releases that are already integrated.	**Open source HLOS software** Contains open source for Apps processor HLOS
Proprietary HLOS software Contains proprietary source and firmware images for the apps processor HLOS.	–

The proprietary and open source HLOS packages are obtained from separate sources and then combined according to the downloading instructions given in Section 3.3.8. Each package is identified by a unique build identification (build ID) code, which follows this naming convention:

<PL_Image>-<Version>-<Chipset>

Where:

- PL_Image – LA.BR64.Branch for Linux Android
- Version – Variable number of digits used to represent the build ID version.
- Chipset – 8909 for MSM8909

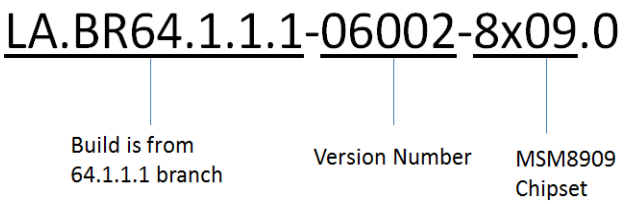


Figure 3-1 Build ID naming convention

Figure 3-2 shows the combined software release packages.

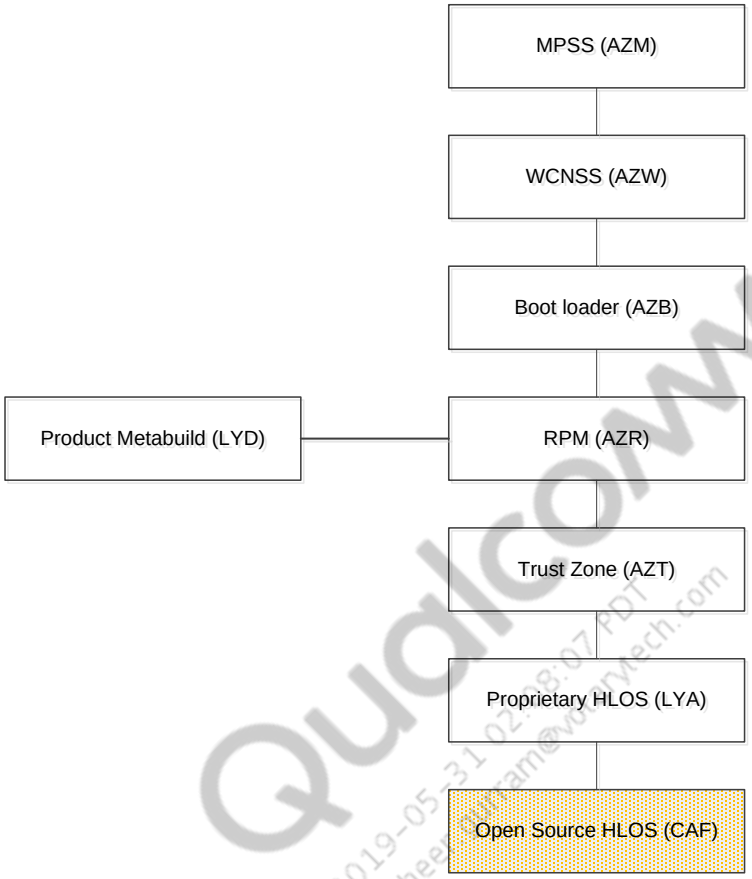


Figure 3-2 Combined software release packages

Table 3-2 gives the component release build properties. The compiler, Python, Perl, and Cygwin version information for each of the non-HLOS build modules is also provided. Ensure that the build PC has the correct versions for each tool.

NOTE: Due to a possibility of version change for the tools listed in the following table with software releases, refer to MSM8909 software release note for the exact version of the tools.

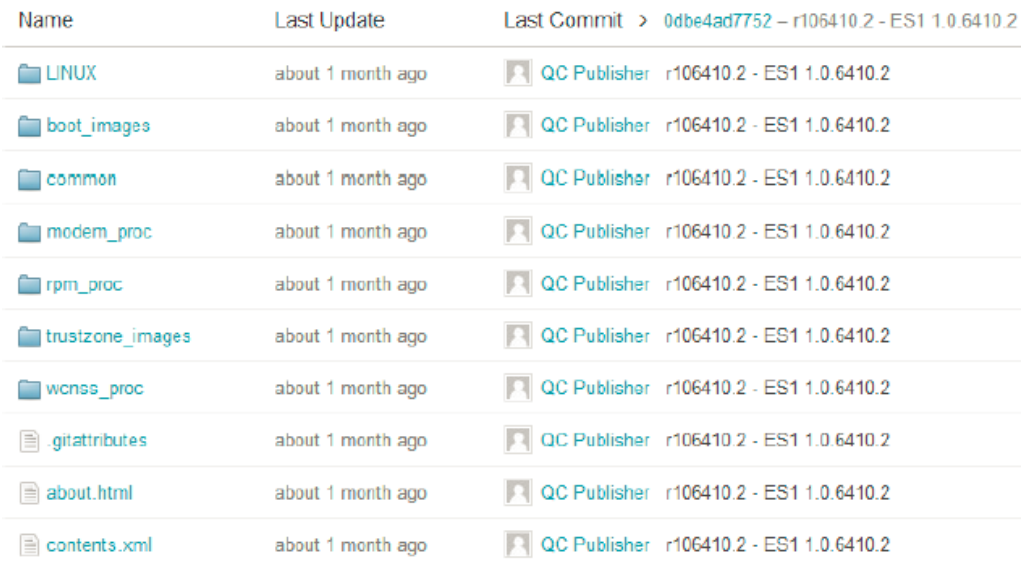
Table 3-2 Component release build properties

Component build release	Source or binary only	Toolchain required for building source	Python version	Perl version	Cygwin	Supported build hosts
Android HLOS (LYA)	Source	Android gnu toolchain	—	—	—	Linux only
MPSS (AZM)	Source	Hexagon 6.4.04	Python 2.7.6 (64-bit)	Perl 5.14.2	Windows builds only; Needs tee.exe	Linux, Windows XP, and Windows 7

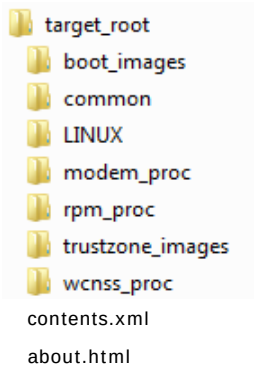
Component build release	Source or binary only	Toolchain required for building source	Python version	Perl version	Cygwin	Supported build hosts
Boot loaders (AZB)	Source	ARM Compiler Tools 5.01 update 3 (build 94)	Python 2.6.6	Perl 5.8.x Linux builds only	Windows builds only; Needs tee.exe	Linux, Windows XP, and Windows 7
RPM (AZR)	Source	ARM Compiler Tools 5.01 update 3 (build 94) RPM - (ARM compiler Tools Q4 Full version)	Python 2.6.6	Perl 5.6.1	Windows builds only; Needs tee.exe	Linux, Windows XP, and Windows 7 only
TZ (AZT)	Source	ARM Compiler Tools 5.01 update 3 (build 94)	Python 2.6.6	Perl 5.10.1	Windows builds only; Needs tee.exe	Linux, Windows XP, and Windows 7 only
WCNSS (AZW)	Binary	—	—	—	—	—

3.1 Downloading QTI software from Qualcomm ChipCode™ portal

1. The software distribution package (distro) is composed of multiple subsystem image files. Download the distro components to the build PC.



2. Create a top-level directory on the build PC and unzip each of the subsystem images to generate the following directory structure. In this example, <target_root> is the top-level directory.



3. Ensure to download the `about.html` and `contents.xml` files. These files contain the build ID that is used to download the open source software from CAF.

3.2 Download open source Android HLOS software from CAF

1. See the release notes for the link to access the open source software from CAF.

   Alternatively, go to https://www.codeaurora.org/xwiki/bin/QAEP/release and find the release branch containing the matching build ID in the branch releases table.

2. In an empty directory, use the repo init command with the correct branch and manifest as indicated in the branch releases table.

   ```
   $ repo init -u git://codeaurora.org/platform/manifest.git -b release -m <build_id>.xml --repo-url=git://codeaurora.org/tools/repo.git --repo-branch=caf-stable
   ```

3. Type the repo sync command.

   ```
   $ repo sync
   ```

4. Copy the vendor/qcom/proprietary directory tree into the open source HLOS source tree contained in the workspace.

   ```
   $cp -r <LYA_build_location>/HY11-<build_id>/LINUX/android/* .
   ```

Find the Linux build ID for other releases

To obtain the Linux build ID for another release for which there is no release notes, the build ID are obtained in the following ways:

- From ChipCode, on the distro page, the `about.html` section displays the build ID.

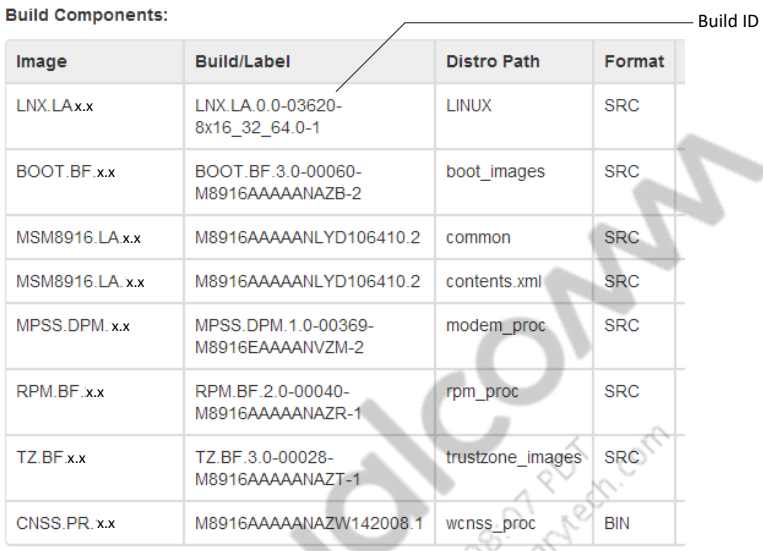


Figure 3-3 Build ID information from about.html

- From the `about.html` file that was downloaded to the build PC.
- Search for `<name>apps</name>`, then locate the `build_id`, from the contents.xml file that was downloaded to the build PC.

```
<build>
<name>apps</name>
<role>apps</role>
<chipset>msm8909</chipset>
<build_id>LA.64.1.1.1-06002-8x09.2-1</build_id>
```

3.3 Compile the Non-HLOS software

3.3.1 Set build Windows environment

Before issuing the non-HLOS build commands, certain command environment settings are set to ensure the correct executable path and toolchain configuration. The specific environment settings vary based on the host software installation, but it is similar to the example "myenviron_amss.cmd" script shown below (for Windows), which sets the path to point to the ARM toolchain lib, include, bin, and license file configuration.

```
#
# myenviron_amss_8916
#
SET ARMLMD_LICENSE_FILE=<mylicense_file>@<mylicense_server>
```

```
set ARM_COMPILER_PATH=C:\apps\ARMCT5.01\94\bin64
set PYTHON_PATH=C:\Python26
set PYTHONPATH=C:\Python26
set MAKE_PATH=C:\apps\ARMCT5.01\94\bin64
set GNUPATH=C:\cygwin\bin
set CRMPERL=C:\Perl64\bin
set PERLPATH=C:\Perl64\bin

set ARMHOME=C:\Apps\ARMCT5.01\94
set ARMINC=C:\Apps\ARMCT5.01\94\include
set ARMLIB=C:\Apps\ARMCT5.01\94\lib
set ARMBIN=C:\Apps\ARMCT5.01\94\bin
set ARMPATH=C:\Apps\ARMCT5.01\94\bin
set ARMINCLUDE=C:\Apps\ARMCT5.01\94\include
set ARMTOOLS=ARMCT5.01
set
PATH=.;C:\Python26;C:\Apps\ARMCT5.01\94\bin;C:\apps\ARMCT5.01\94\bin64;C:\c
ygwin\bin;%PATH%
set HEXAGON_ROOT=C:\Qualcomm\HEXAGON_Tools
set HEXAGON_RTOS_RELEASE=5.0.07
set HEXAGON_Q6VERSION=v4
set HEXAGON_IMAGE_ENTRY=0x08400000
```

3.3.2 Build MPSS

To build MPSS (<target_root> is the top-level directory that is created in Section 3.1):

1. For Linux, verify that the following paths are set. Refer to setenv.sh in the boot build.

```
<target_root>\modem_proc\build\ms\setenv.sh
export ARMLMD_LICENSE_FILE=<LICENSE FILE INFO>
ARM_COMPILER_PATH=/pkg/qct/software/arm/RVDS/2.2BLD593/RVCT/Programs/2.2
/593/linux-pentium
PYTHON_PATH=/pkg/qct/software/python/2.7.5/bin
MAKE_PATH=/pkg/gnu/make/3.81/bin
export ARMTOOLS=RVCT221
export ARMROOT=/pkg/qct/software/arm/RVDS/2.2BLD593
export ARMLIB=$ARMROOT/RVCT/Data/2.2/349/lib
export ARMINCLUDE=$ARMROOT/RVCT/Data/2.2/349/include/unix
export ARMINC=$ARMINCLUDE
export ARMCONF=$ARMROOT/RVCT/Programs/2.2/593/linux-pentium
export ARMDLL=$ARMROOT/RVCT/Programs/2.2/593/linux-pentium
export ARMBIN=$ARMROOT/RVCT/Programs/2.2/593/linux-pentium
export PATH=$MAKE_PATH:$PYTHON_PATH:$ARM_COMPILER_PATH:$PATH
export ARMHOME=$ARMROOT
export HEXAGON_ROOT=/pkg/qct/software/hexagon/releases/tools
```

2. Navigate to the following directory:
   `cd <target_root>/modem_proc/build/ms`
3. Depending on the build environment, use one of the following commands:

Build variant	Build environment	Build commands	Comments
LWG Segment Loading Enabled	Linux	■ Build images – `./build.sh 8909.lwg.prod -k` ■ Clean the Build – `./build.sh 8909.lwg.prod -c`	Modem flavor including L+ W/T+G RATs
	Windows	■ Build images – `build.cmd 8909.lwg.prod -k` ■ Clean the Build – `build.cmd 8909.lwg.prod -c`	
LWG Segment Loading Enabled with F3 logs enabled	Linux	■ Build images – `./build.sh 8909.lwgf3.prod -k` ■ Clean the Build – `./build.sh 8909.lwgf3.prod -c`	Modem flavor including L+ W/T+G RATs with F3 logs enabled
	Windows	■ Build images – `build.cmd 8909.lwgf3.prod -k` ■ Clean the Build – `build.cmd 8909.lwgf3.prod -c`	
Kitchen sink (ALL RATs) Segment Loading Disabled.	Linux	■ Build images – `./build.sh 8909.genns.prod -k` ■ Clean the Build – `./build.sh 8909.genns.prod -c`	Modem flavor including all RATs (W+T+G+C+L).

Build variant	Build environment	Build commands	Comments

	Windows	▪ Build images –
		`build.cmd 8909.genns.prod -k`
		▪ Clean the Build –
		`build.cmd 8909.genns.prod -c`	

Kitchen sink (ALL RATs) Segment Loading Enabled.	Linux	▪ Build images –
		`./build.sh 8909.gen.prod -k`
		▪ Clean the Build –
		`./build.sh 8909.gen.prod -c`
			Modem flavor including all RATs (W/T+G+C+L).

	Windows	▪ Build images –
		`build.cmd 8909.gen.prod -k`
		▪ Clean the Build –
		`build.cmd 8909.gen.prod -c`	

APQ Flavor	Linux	▪ Build images –
		`./build.sh 8909.gps.prod -k`
		▪ Clean the Build –
		`./build.sh 8909.gps.prod -c`
			Modem Flavor compiles GPS code only.

	Windows	▪ Build images –
		`build.cmd 8909.gps.prod -k`
		▪ Clean the Build –
		`build.cmd 8909.gps.prod -c`	

CT flavor with QChat™ platform enabled	Linux	▪ Build images –
		`./build.sh 8909.lcgq.prod -k`
		▪ Clean the Build –
		`./build.sh 8909.lcgq.prod -c`
			Modem Flavor including C+G+L RATs

	Windows	▪ Build images –
		`build.cmd 8909.lcgq.prod -k`
		▪ Clean the Build –
		`build.cmd 8909.lcgq.prod -c`	

LWG Segment Loading Enabled with QChat support	Linux	▪ Build images – `./build.sh 8909.lwgq.prod -k` ▪ Clean the Build –	Modem flavor including L+

Build variant	Build environment	Build commands	Comments
		`./build.sh 8909.lwgq.prod -c`	W/T+G RATs with QChat support
	Windows	▪ Build images – `./build.cmd 8909.lwgq.prod -k` ▪ Clean the Build – `./build.cmd 8909.lwgq.prod -c`	

3.3.3 Build boot loaders

To build the boot loaders:

1. For Linux, verify that the following paths are set. Refer to setenv.sh in the boot build:

```
<target_root>\boot_images\build\ms\setenv.sh.
export ARMLMD_LICENSE_FILE=<LICENSE FILE INFO>
export ARM_COMPILER_PATH=/<Path to compiler>/arm/RVDS/5.01bld94/bin64
export PYTHON_PATH=/<Path to python>/python/2.6.6/bin
export MAKE_PATH=/<Path to make>/gnu/make/3.81/bin
export ARMTOOLS=ARMCT5.01
export ARMROOT=/<Path to compiler>/arm/RVDS/5.01bld94
export ARMLIB=$ARMROOT/lib
export ARMINCLUDE=$ARMROOT/include
export ARMINC=$ARMINCLUDE
export ARMBIN=$ARMROOT/bin64
export PATH=$MAKE_PATH:$PYTHON_PATH:$ARM_COMPILER_PATH:$PATH
export ARMHOME=$ARMROOT
export_armlmd_license
```

2. Navigate to the following directory:

```
cd <target_root>/boot_images/build/ms
```

   Where <target_root> is the top-level directory that is created in Section 3.1.

3. Depending on the build environment/release, choose one of the following build command options:

Build environment	MSM8909 1.0 PL
Linux	Build boot images $ ./build.sh TARGET_FAMILY=8909 --prod Cleaning the build $ ./build.sh TARGET_FAMILY=8909 –prod -c Depending on the configuration, edit the script boot_images/build/ms/build_8909.sh to change if [-e “setenv.sh”]; then - source setenv.sh + source ./setenv.sh fi
Windows	Building boot images build.cmd TARGET_FAMILY=8909 --prod Cleaning the build build.cmd TARGET_FAMILY=8909 –prod -c

NOTE: Currently NAND compilation is not enabled.

3.3.4 Build TrustZone images

To build the MSM8909 TrustZone images:

1. Navigate to the following directory:
   `cd <target_root>/trustzone_images/build/ms`
2. Run the following command to build all images:

Build environment	MSM8909 1.0 PL
Linux	**Build images** – ./build.sh CHIPSET=msm8909 tz sampleapp tzbsp_no_xpu playready widevine isdbtmm aostlm securitytest keymaster commonlib **Clean the build** – ./build.sh CHIPSET=msm8909 tz sampleapp tzbsp_no_xpu playready widevine isdbtmm aostlm securitytest keymaster commonlib -c
Windows	**Build images** – build.cmd CHIPSET=msm8909 tz sampleapp tzbsp_no_xpu playready widevine isdbtmm aostlm securitytest keymaster commonlib **Clean the build** – build.cmd CHIPSET=msm8909 tz sampleapp tzbsp_no_xpu playready widevine isdbtmm aostlm securitytest keymaster commonlib -c

3.3.5 Build RPM

Use the following commands to build RPM (<target_root> is the top-level directory). Ensure that the tools used are of versions specified in Table 3-2.

NOTE: Building on Linux may not work for early ES releases.

1. Open a command prompt and change to the following directory:

   ```
   cd <target_root>\rpm_proc\build
   ```

2. Depending on the build environment, use one of the following commands:

Build environment	MSM8909 1.0 PL
Linux	**Build images** – ./build_8909.sh **Clean the build** – ./build_8909.sh -c
Windows	**Build images** – build_8909.bat **Clean the build** – build_8909.bat -c

NOTE: rpm.mbn are found at rpm_proc\build\ms\bin\8909

3.3.6 Build WCNSS

NOTE: WCNSS image is released as a binary and no build compilation is needed.

The contents.xml files (apart from the default contents.xml) are now placed in the common/build folder. Copy the required contents file to the root folder, and rename it as contents.xml.

If MPSS/WCNSS/aDSP images are recompiled, use the following commands to update the NON-HLOS.bin file with the new images (<target_root> is the top-level directory).

1. Navigate to the following directory:

   ```
   cd <target_root>/common/build
   ```

2. Enter the command:

   ```
   python build.py
   ```

In Linux, you are advised to copy Linux/android code downloaded from codeaurora.org to the LINUX/android directory downloaded from Qualcomm ChipCode portal for compilation.

3.3.7 Update NON-HLOS.bin

If any MPSS, aDSP, or WCNSS is recompiled, use the following commands to update the NON-HLOS.bin file with the new images (<target_root> is the top-level directory that is created in Section 3.1):

1. Navigate to the following directory:

   ```
   cd <target_root>/common/build
   ```

2. Enter the command:

   ```
   python update_common_info.py
   ```

NOTE: If you are creating the whole sparse_images (which is required when contents.xml is burned to be a whole image using Qualcomm Product Support Tool (QPST)), the image compiled from Linux/Android must be copied into the LINUX/android/out/target/product/MSM8909/ directory. For details about the required files, refer to apps-related configuration information in the

contents.xml file. In Linux, you are advised to copy Linux/android code downloaded from codeaurora.org to the LINUX/android directory downloaded from Qualcomm ChipCode for compilation.

Componentized META

- Files such as partition.xml and pil-splitter.py have been moved to the common/config folder.
- All the TRACE32 files, build loading files, and debug scripts have been moved to the common/core folder.
- All sectools related files have been moved to the common/sectools folder.
- META Scripts reside under common/build folder.

The following table describes the supported build flavors.

The contents.xml files (apart from the default contents.xml) are now placed in the common/build folder. Copy the required contents file to the root folder, and rename it as contents.xml.

Meta flavor description for MSM8909

Meta Contents. xml	**MPSS build ID**	**RATs supported**	**MPSS Linux compilation command (for Windows use *.cmd)**	**META compilation procedure**	**Comments**
Contents. xml	8909.gen. prod (Kitchen sink)	W/T+C+ G+L	./build.sh 8909.gen. prod -k	1. CD <target_root >\common\b uild 2. Run python update_com mon_info.py	This build includes all RATs. Segment loading for W/T enabled For PM8916 support, replace the rpm.mbn path rpm_proc/build/ms/bi n/8909/pm8909 with rpm_proc/build/ms/bi n/8909/pm8916 in contents.xml and then compile the meta **Note:** MPSS image size is set to 85 MB. For additional instructions to modify MPSS image, refer to section 3.4.1.

Meta Contents. xml	MPSS build ID	RATs supported	MPSS Linux compilation command (for Windows use *.cmd)	META compilation procedure	Comments
Contents_ genns. xml (rename Contents_genns. xml with Contents. xml)	8909.genns. prod (Kitchen sink segment loading disabled)	W/T+C+ G+L	./build.sh 8909.genns. prod -k	1. CD <target_root >\common\b uild 2. Run python update_com mon_info.py	This build includes all RATs. Segment loading for W/T is disabled. For PM8916 support, replace the rpm.mbn path rpm_proc/build/ms/bi n/8909/pm8909 with rpm_proc/build/ms/bi n/8909/pm8916 in contents.xml and then compile the meta. **Note:** MPSS image size is set to 85 MB by APSS. For additional instructions to modify MPSS image allocation, refer to section 3.4.1
Contents_ gps. xml (rename Contents_ gps. xml with Contents. xml)	8909.gps. prod	–	./build.sh 8909.gps. prod -k	1. CD <target_root >\common\b uild 2. Run python update_com mon_info.py	This is the APQ/GPS flavor. **Note:** MPSS image size is set to 85 MB. For additional instructions to modify MPSS image, refer to section 3.4.1
Contents_ lcgq. xml (rename Contents_ lcgq. xml with Contents. xml)	8909.lcgq.pr od (CT with QChat enabled)	C+G+L	./build.sh 8909.lcgq. prod -k	1. CD <target_root >\common\b uild 2. Run python update_com mon_info.py	This build has W and T compiled out with QChat enabled. **Note:** MPSS image size is set to 85 MB. For additional instructions to modify the MPSS image, refer to section 3.4.1.

Meta flavor description for MSM8905

NOTE: The following table has been updated.

Meta Contents. xml	MPSS build ID	RATs supported	MPSS Linux compilation command (for Windows use *.cmd)	META compilation procedure	Comments
Contents. xml	8909.lwg. prod (LWG)	L+W/T+G	./build.sh 8909.lwg. prod -k	1. CD <target_root>\common\build 2. Run python update_common_info.py	This build includes L+ W/T+G RATs. Segment loading for W/T enabled
Contents_lwgf3. xml (rename Contents_lwgf3. xml with Contents. xml)	8909.lwgf3. prod (LWGF3)	L+W/T+G	./build.sh 8909.lwgf3. prod -k	1. CD <target_root>\common\build 2. Run python update_common_info.py	This build includes L+ W/T+G RATs with F3 logs enabled. Segment loading for W/T enabled
contents_lwgq. xml (rename contents_lwgq. xml with contents. xml)	8909.lwgq. prod (LWGQ)	L+W/T+G	./build.sh 8909.lwgq. prod -k	1. CD <target_root>\common\build 2. Run python update_common_info.py	This build includes L+ W/T+G RATs with QChat support. Segment loading for W/T enabled

3.3.8 Build the Apps processor Android HLOS

1. In a BASH shell, navigate to the Android source tree base directory.
   `cd <build id>/LINUX/android`
2. Enter the following command to configure the build environment shell settings:
   `source build/envsetup.sh`

NOTE: Use the source command so that the environment settings are defined in the current shell.

3. Enter the lunch command to select the build configuration, or enter with no parameters to see an interactive menu for making selections.
   a. For MSM8909 1.0 PL:
      `lunch msm8909-userdebug`
   b. For 512 MB DDR memory:
      `lunch msm8909_512-userdebug`

4. Run `make` to start the build. To run parallel builds for faster build times on a multicore build machine, run the following command:
   ```
   make –j4
   ```

3.3.9 Build HLOS for MSM8905

The information is for MSM8905.LF.1.2 (Linux Kai OS) Release 01227 for the MSM8905 Devices (MSM8905.LF.1.2-01227), a source code of release of software.

To make a build, get access to **kaiostech** repositories to Gecko/Gaia sources and other dependent projects. All the projects need to sync with Gecko and Gaia versions.

NOTE: Ensure that the aforementioned patches are applied on top of the build to get a successful build

The following is a complete list of projects that needs to be synced from git.kaiostech.com and need to put together with the CAF manifest mentioned earlier.

Source – <remote fetch="ssh://git@git.kaiostech.com/quoin/" name="quoin"/>

KaiOS specific projects need be added in CAF manifest:

```
¨   <project name="gaia" path="gaia" remote="quoin"
revision="refs/tags/kaios_2_0_quoin_20170630_18.r1"/>
¨  <project name="gecko_scripts" path="gecko" remote="quoin"
revision="refs/tags/kaios_2_0_quoin_20170630_18.r1"/>
¨  <project name="gonk-misc" path="gonk-misc" remote="quoin"
revision="refs/tags/kaios_2_0_quoin_20170630_18.r1"/>
¨  <project name="platform_system_bluetoothd" path="system/bluetoothd"
remote="quoin" revision="refs/tags/kaios_2_0_quoin_20170630_18.r1"/>
¨  <project name="fake-libdvm" path="dalvik" remote="quoin"
revision="refs/tags/kaios_2_0_quoin_20170630_18.r1"/>
¨  <project name="librecovery" path="librecovery" remote="quoin"
revision="refs/tags/kaios_2_0_quoin_20170630_18.r1"/>
¨  <project name="moztt" path="external/moztt" remote="quoin"
revision="refs/tags/kaios_2_0_quoin_20170630_18.r1"/>
¨  <project name="rilproxy" path="rilproxy" remote="quoin"
revision="refs/tags/kaios_2_0_quoin_20170630_18.r1"/>
¨  <project name="platform_hardware_libhardware_moz"
path="hardware/libhardware_moz" remote="quoin"
revision="refs/tags/kaios_2_0_quoin_20170630_18.r1"/>
¨  <project name="platform_system_libfdio" path="system/libfdio"
remote="quoin" revision="refs/tags/kaios_2_0_quoin_20170630_18.r1"/>
¨  <project name="platform_system_libpdu" path="system/libpdu"
remote="quoin" revision="refs/tags/kaios_2_0_quoin_20170630_18.r1"/>
¨  <project name="platform_system_mmitest" path="system/mmitest"
remote="quoin" revision=" refs/tags/kaios_2_0_quoin_20170630_18.r1"/>
¨  <project name=" platform_system_nfcd" path="system/nfcd" remote="quoin"
revision="refs/tags/kaios_2_0_quoin_20170630_18.r1"/>
¨  <project name="platform_external_librt9" path="external/librt9"
remote="quoin" revision="refs/tags/kaios_2_0_quoin_20170630_18.r1"/>
```

The following normal build procedure steps can be followed:

1. source build/envsetup.sh
2. lunch msm8909_512-userdebug
3. make -j4

3.4 Build components and commands for MSM8909W.LW

Build commands, required tools, and supported build environments for each component are listed in the following table.

Build component	Supported build environments	Required tool version	Build commands
MPSS	▪ Linux ▪ Windows	▪ Qualcomm® Hexagon™ tool set 6.4.06 ▪ Python Version 2.7.6 ▪ Perl Version 5.14.2	**Build images** – `./build.sh 8909.gpss.prod -k` **Clean the build** – `./build.sh 8909.gpss.prod -c` To compile the GENS flavor (connected configuration, see Table 3-3): **Build images** – `./build.sh 8909.gens.prod -k` **Clean the build** – `./build.sh 8909.gens.prod -c` For Windows, use `build.cmd`

BOOT	▪ Linux
	▪ Windows
		ARM Compiler tools
		5.01 update 3 (build 94)
			Windows
			Build images
			`boot_images/build/ms/build.cmd TARGET_FAMILY=8909w --prod`
			Clean the build
			`boot_images/build/ms/build.cmd -c TARGET_FAMILY=8909w –prod`
			Linux
			Build images
			`boot_images/build/ms/build.sh TARGET_FAMILY=8909w --prod`
			Clean the build
			`boot_images/build/ms/build.sh -c TARGET_FAMILY=8909w --prod`
			Depending on the configuration, edit the script boot_images/build/ms/build_8909w.sh to change
			`if [ -e “setenv.sh” ]; then`
			`- source setenv.sh`
			`+ source ./setenv.sh`
			`fi`

Build component	Supported build environments	Required tool version	Build commands

RPM	▪ Linux
	▪ Windows
		ARM Compiler tools
		5.01 update 3 (build 94)
			Windows
			Build images
			`rpm_proc/build/build_8909w.bat`
			Clean the build
			`rpm_proc/build/build_8909w.bat -c`
			Linux build
			Build images
			`rpm_proc/build/build_8909w.sh`
			Clean the build
			`rpm_proc/build/build_8909w.sh -c`

CNSS	Binary only	–	–

TZ	▪ Linux
	▪ Windows
		ARM Compiler Tools 5.01 update 3 (build 94)	**Linux**
			Build images
			`./build.sh CHIPSET=msm8909w tz sampleapp tzbsp_no_xpu playready widevine keymaster commonlib`
			Clean the build
			`./build.sh CHIPSET=msm8909w tz sampleapp tzbsp_no_xpu playready widevine keymaster commonlib –c`
			Windows
			Build images
			`build.cmd CHIPSET=msm8909w tz sampleapp tzbsp_no_xpu playready widevine keymaster commonlib`
			Clean the build
			`build.cmd CHIPSET=msm8909w tz sampleapp tzbsp_no_xpu playready widevine keymaster commonlib –c`

APSS	Linux only	▪ Ubuntu 14.04
		▪ arm-linux-androideabi-gcc (GCC) 4.7
			Linux
			Build images
			1. In a BASH shell, navigate to the Android source tree base directory.
			`cd <build id>/LINUX/android`
			2. Enter the following command to configure the build environment shell settings.
			`source build/envsetup.sh`
			Use the source command so that the environment settings are defined in the current shell.
			3. Enter the lunch command to select the build configuration, or enter with no parameters to see an interactive menu for making selections.
			`lunch msm8909w-userdebug`
			4. Run **make** to start the build (shown with –j 20 option to run parallel builds for faster build times on a multicore build machine).
			`make –j20`

This release supports various META configurations. Tethered configuration is the default META configuration. Tethered Meta configuration supports APQ/GPS.

Connected META configurations are based on various modem build flavors. Each modem flavor is derived from various RATs that are supported. Table 3-3 captures the RAT support details.

Refer to Table 3-3 to change the default META configuration from Tethered, to identify the actual size of the MPSS image, and the start address of the relocatables section for the build variant that is used.

Table 3-3 Meta flavor description

Meta Contents. xml	MPSS build ID	RATs supported	MPSS image size	MPSS Linux compilation command (for Windows use *.cmd)	META compilation procedure	Comments
Contents. xml **Tethered**	8909.gpss. prod	–	38 MB	`./build.sh 8909.gpss. prod -k`	1. `CD <target _root>\ common\ build` 2. Run `python update_ common_ info.py`	Reduce MPSS image size to 38 MB
contents_1X GWLT.xml *(rename contents_1XG WLT.xml to contents.xml)* **Connected**	8909.gens. prod (Kitchen sink)	W/T+C+G+ L	80 MB	`./build.sh 8909.gens. prod -k`	1. `CD <target _root>\ common\ build` 2. Run `python update_ common_ info.py`	No change required for MPSS, as the image size is set to 80 MB by default.
contents_1X GWT.xml *(rename contents_1XG WT.xml to contents.xml)* **Connected**	8909.wcgs. prod (3G)	W/T+C+G	63 MB	`./build.sh 8909.wcgs. prod -k`	1. `CD <target _root>\ common\ build` 2. Run `python update_ common_ info.py`	Reduce MPSS image size to 63 MB

Meta Contents. xml	MPSS build ID	RATs supported	MPSS image size	MPSS Linux compilation command (for Windows use *.cmd)	META compilation procedure	Comments
contents_GWLT.xml *(rename contents_GWLT.xml to contents.xml)* **Connected**	8909.lwgs.prod (CMCC)	W/T+G+L	74 MB	`./build.sh 8909.lwgs.prod -k`	1. `CD <target_root>\common\build` 2. Run `python update_common_info.py`	Reduce MPSS image size to 74 MB.
contents_1XGL.xml *(rename contents_1XGL.xml to contents.xml)* **Connected**	8909.lcgs.prod (CT)	C+G+L	71 MB	`./build.sh 8909.lcgs.prod -k`	1. `CD <target_root>\common\build` 2. Run `python update_common_info.py`	Reduce MPSS image size to 71 MB.

3.4.1 Modify memory map for tethered configuration

3.4.1.1 MPSS updates

NOTE: The MPSS changes are already part of the release build. The following is for reference only:

- To reach the optimized size the following change can be done in MPSS (Tethered configuration MPSS Builds Flavor 8909.gpss.prod):
  - cust_config.xml file: under tag <physical_pool name="DEFAULT_PHYSPOOL">

    `Line 68: <region base="0x88000000" size="0x2600000" />`

3.4.1.2 APSS updates

- To reduce the modem size and reallocate CNSS in APSS side, changes are required in:
  - arch/arm/boot/dts/qcom/apq8009w-memory.dtsi
- The following are the changes for Tethered configuration flavor (38 MB) only:

```
&external_image_mem {
        reg = <0x0 0x87a00000 0x0 0x0600000>;
};
&modem_adsp_mem {
        reg = <0x0 0x88000000 0x0 0x2600000>;
};
```

```
&peripheral_mem {
        reg = <0x0 0x8A600000 0x0 0x0500000>;
};
```

Reduce modem size

- Breakdown of modem_adsp_mem region:
- 0x0 0x88000000 – Starting address for modem
- 0x2600000 – Modem size

Update start address of peripheral_region

- Breakdown of peripheral_mem region:
  0x8A600000 corresponds to WCNSS start address and it is calculated as → Modem start address + modem size (For 38 MB modem, it must be updated accordingly: 0x88000000 + 0x2600000 = 0x8A600000)

3.4.2 Modify memory map for connected configuration

3.4.2.1 MPSS updates

NOTE: The MPSS changes are already part of the release build. The following is for reference only:

- To reach the optimized size following change is performed in MPSS (Connected configuration MPSS Builds Flavor 8909.gens.prod):
  - cust_config.xml file: under tag <physical_pool name="DEFAULT_PHYSPOOL">
    `Line 68: <region base="0x88000000" size="0x5000000" />`

3.4.2.2 APSS updates

NOTE: To reduce the modem size and reallocate CNSS in APSS side, changes are required in:

- arch/arm/boot/dts/qcom/msm8909w-memory.dtsi

- The following are the changes for Connected configuration flavor (80 MB) only:

```
&external_image_mem {
        reg = <0x0 0x87a00000 0x0 0x0600000>;
};
&modem_adsp_mem {
        reg = <0x0 0x88000000 0x0 0x0500000>;
};
&peripheral_mem {
        reg = <0x0 0x8D000000 0x0 0x0500000>;
};
```

Reduce modem size

- Breakdown of modem_adsp_mem region:
- 0x0 0x88000000 – Starting address for modem
- 0x5000000 – Modem size

Update start address of peripheral_region

- Breakdown of peripheral_mem region:
  0x8D000000 corresponds to WCNSS start address and it is calculated as → Modem start address + modem size (For 80 MB modem, it must be updated accordingly: 0x88000000 + 0x500000 = 0x8D000000)
- For 80 MB modem, the updated lines (95 and 103) should appear as follows:

```
reg = <0x0 0x88000000 0x0 0x5000000>;
reg = <0x0 0x8D000000 0x0500000>;
```

3.4.3 Modify memory map for connected 3G configuration

3.4.3.1 MPSS updates

NOTE: The MPSS changes are already part of the release build. The following is for reference only:

- To reach the optimized size following change is performed in MPSS (Connected 3G configuration MPSS Builds Flavor 8909.wcgs.prod):
  - cust_config.xml file: under tag <physical_pool name="DEFAULT_PHYSPOOL">

    ```
    Line 68: <region base="0x88000000" size="0x3F00000" />
    ```

3.4.3.2 APSS updates

NOTE: To reduce the modem size and reallocate CNSS in APSS side, changes are required in:

- arch/arm/boot/dts/qcom/msm8909w-memory.dtsi

- The following are the changes for Connected 3G configuration flavor (63 MB) only:

```
&external_image_mem {
        reg = <0x0 0x87a00000 0x0 0x0600000>;
};
&modem_adsp_mem {
        reg = <0x0 0x88000000 0x0 0x3F00000>;
};
&peripheral_mem {
        reg = <0x0 0x8BF00000 0x0 0x0500000>;
};
```

Reduce modem size

- Breakdown of modem_adsp_mem region:
- 0x0 0x88000000 – Starting address for modem
- 0x3F00000 – Modem size

Update start address of peripheral_region

- Breakdown of peripheral_mem region:
  0x8AB00000 corresponds to WCNSS start address and it is calculated as → Modem start address + modem size (For 63 MB modems, it is updated accordingly: 0x88000000 + 0x3F00000= 0x8BF00000)

3.4.4 Modify memory map for connected CMCC configuration

3.4.4.1 MPSS updates

NOTE: The MPSS changes are already part of the release build. The following is for reference only:

- To reach the optimized size following change is performed in MPSS (Connected CMCC configuration MPSS Builds Flavor 8909.lwgs.prod):
  - cust_config.xml file: under tag <physical_pool name="DEFAULT_PHYSPOOL">

    ```
    Line 68: <region base="0x88000000" size="0x4A00000" />
    ```

3.4.4.2 APSS updates

NOTE: To reduce the modem size and reallocate CNSS in APSS side, changes are required in:

- arch/arm/boot/dts/qcom/msm8909w-memory.dtsi

- The following are the changes for Connected CMCC configuration flavor (74 MB) only:

```
&external_image_mem {
        reg = <0x0 0x87a00000 0x0 0x0600000>;
};
&modem_adsp_mem {
        reg = <0x0 0x88000000 0x0 0x4A00000>;
};
&peripheral_mem {
        reg = <0x0 0x8CA000000x0 0x0 0x0500000>;
};
```

Reduce modem size

- Breakdown of modem_adsp_mem region:
- 0x0 0x88000000 – Starting address for modem
- 0x4A00000 – Modem size

Update start address of peripheral_region

- Breakdown of peripheral_mem region:
  0x8CA00000 corresponds to WCNSS start address and it is calculated as → Modem start address + modem size (For 74 MB modem, it is updated accordingly: 0x88000000 + 0x4A00000 = 0x8CA00000)

3.4.5 Modify memory map for connected CT configuration

3.4.5.1 MPSS updates

NOTE: The MPSS changes are already part of the release build. The following is for reference only:

- To reach the optimized size following change is performed in MPSS (Connected CT configuration MPSS Builds Flavor 8909.lcgs.prod):
  - cust_config.xml file: under tag <physical_pool name="DEFAULT_PHYSPOOL">

    ```
    Line 68: <region base="0x88000000" size="0x4700000" />
    ```

3.4.5.2 APSS updates

NOTE: To reduce the modem size and reallocate CNSS in APSS side, changes are required in:

- arch/arm/boot/dts/qcom/msm8909w-memory.dtsi

- The following are the changes for Connected CMCC configuration flavor (71 MB) only:

```
&external_image_mem {
        reg = <0x0 0x87a00000 0x0 0x0600000>;
};
&modem_adsp_mem {
        reg = <0x0 0x88000000 0x0 0x4700000>;
};
&peripheral_mem {
        reg = <0x0 0x8C700000 0x0 0x0500000>;
};
```

Reduce modem size

- Breakdown of modem_adsp_mem region:
- 0x0 0x88000000 – Starting address for modem
- 0x4700000 – Modem size

Update start address of peripheral_region

Breakdown of peripheral_mem region:
0x8C700000 corresponds to WCNSS start address and it is calculated as → Modem start address + modem size (For 71 MB modem, it is updated accordingly: 0x88000000 + 0x4700000 = 0x8C700000)

3.4.6 Memory configuration and usage for MSM8905

APSS side changes

- To reduce the modem size and re-allocate CNSS in APSS side, changes are required in arch/arm/boot/dts/qcom/msm8905-pm8909-512mb-mtp.dts
- The following example is for LWG segment loading enabled flavor only (68 MB); similar changes must be performed for other flavors based on the image size:

```
&modem_adsp_mem {
      reg = <0x0 0x88000000 0x0 0x04400000>;
};
&peripheral_mem {
          reg = <0x0 0x8C400000 0x0 0x0500000>;
};
```

The following figure shows the details on MSM8905 KaiOS illustrative memory map diagram.

Memory Map	MSM8905 KaiOS 512MB
Non-HLOS	77
Kernel	18.5
Apps + Framework	187.5
Free + Cached	229

This is a preliminary memory map that will change in future. It should not be used for memory optimization.

Optimizations are made for the following performance parameters:

- Avoidance known RF desense frequencies
- Maximize throughput
- Minimize power

NOTE: Modifications to the released DDR frequency plan can adversely impact system performance.

4 Program firmware

4.1 Required software

Table 4-1 lists the software required for programming firmware images into a target device.

Table 4-1 Required software

	Item description	Version	Source/vendor	Purpose
1	QPST	2.7.421 or later	Qualcomm Technologies, Inc.	Programming firmware images using QPST
2	QXDM Professional™ tool	3.14.827 or later	Qualcomm Technologies, Inc.	Programming NV Item values, reading diagnostic, and so on.
3	Lauterbach TRACE32 ARMv7 license extension	LA-7843X or LA-7843	Lauterbach GmbH	Programming firmware images using JTAG and applications processor debugging
4	Lauterbach TRACE32 Hexagon license extension	LA-3741A	Lauterbach GmbH	Modem software processor, firmware processor, aDSP debugging using JTAG
5	Lauterbach TRACE32 Cortex-M3 license extension	LA-7844X or LA-7844	Lauterbach GmbH	Programming firmware images using JTAG and RPM debugging using JTAG
6	Lauterbach TRACE32 ARM9 license extension	LA-7742X or LA-7742	Lauterbach GmbH	Venus and WCNSS debugging using JTAG
7	Lauterbach TRACE32 Windows	Feb 2014 R.2014.02.000053364 Build: 51144--53364	Lauterbach GmbH	Programming firmware images and debugging using JTAG

	Item description	Version	Source/vendor	Purpose
8	Android SDK tools (Host USB drivers, adb, fastboot)	r10 or higher ADB 1.0.29 or later	Android Open Source Project	Windows host USB driver for adb and fastboot; adb and fastboot tools for Windows
9	Qualcomm USB network driver combo	1.00.29 or later	Qualcomm Technologies, Inc.	Windows host USB drivers for Qualcomm composite devices

4.2 Install TRACE32

QPST is used for the firmware download; however, TRACE32 is also used when the QPST download does not work. The February 2014 Build 51144--53364 version of TRACE32 is the mandatory minimum revision that is needed for binary download and debugging. The TRACE32 links under common\t32\t32_dap\ are used for binary download and debugging.

By default, these files assume that the TRACE32 installing directory is C:\T32. If TRACE32 is installed in a different directory, and the .lnk shortcut files are modified.

To modify the .lnk shortcut files:

1. Locate the .lnk shortcut files.
2. Right-click the mouse and select **Properties**.
3. In the Target field, change the path to the proper path of t32marm.exe. The default path is C:\t32\t32marm.exe.

4.3 Install Android ADB, Fastboot, and USB driver for Windows

Android CDP support requires the following USB device support:

- Android USB driver (android_winusb.inf)
  - Android ADB interface
  - Android boot loader interface (fastboot)
- Qualcomm Composite USB Modem/Serial Driver (qcmdm.inf, qcser.inf)
  - Qualcomm HS-USB Android DIAG
  - Qualcomm HS-USB Android modem
  - Qualcomm HS-USB Android GPS (NMEA)

- Qualcomm Composite USB network combo driver (qcnet.inf)
  - Qualcomm wireless HS-USB Ethernet adapter

Install Android ADB, Fastboot, and USB driver for Windows

1. Before installing the drivers, edit the qcmdm.inf and qcser.inf files. This ensures that they contain support for the Android SURF VID/PID with appropriate entries in each section as indicated in Step 4.
2. Go to http://developer.android.com/sdk/win-usb.html, and follow the instructions to install the SDK and USB driver.
3. Right-click **My Computer,** and select **Properties → Advanced → Environment Variables**, and set the path to include the c:\android-sdk-windows\tools directory.
4. The Android USB driver for ADB and Fastboot must add the Qualcomm SURF VID/PID, which supports the connection to the URF. Edit the file android-sdk-windows\usb_driver\android_winusb.inf to add the Qualcomm VID/PID lines to each section.

   ```
   android_winusb.inf
   [Google.NTx86]
   ;Qualcomm SURF/FFA
   %SingleAdbInterface%        = USB_Install, USB\VID_05C6&PID_9025
   %CompositeAdbInterface%     = USB_Install, USB\VID_05C6&PID_9025&MI_01
   %SingleBootLoaderInterface% = USB_Install, USB\VID_18D1&PID_D00D
   [Google.NTamd64]
   ;Qualcomm SURF/FFA
   %SingleAdbInterface%        = USB_Install, USB\VID_05C6&PID_9025
   %CompositeAdbInterface%     = USB_Install, USB\VID_05C6&PID_9025&MI_01
   %SingleBootLoaderInterface% = USB_Install, USB\VID_18D1&PID_D00D
   ```

   In addition, ensure that there are matching entries under the [Strings] section.

   ```
   [Strings]
   SingleAdbInterface          = “Android ADB Interface”
   CompositeAdbInterface       = “Android Composite ADB Interface”
   SingleBootLoaderInterface   = “Android Bootloader Interface”
   ```

5. The ADB client (adb.exe) supports a built-in list of recognized USB VID/PID devices. To add the SURF or another device to the list of recognized devices, which is not included in the built-in support list, create a %USERPROFILE%\.android directory if it does not exist.
6. Navigate to the %USERPROFILE%\.android directory.

7. In the %USERPROFILE%\.android directory, create /edit the adb_usb.ini file. If the file exists, it contains a DO NOT EDIT message. Disregard this message and edit the file anyway. Add a line containing 0x05C6 to the end of the file.

NOTE: Do not run the Android update ADB as it resets the contents of this file and overwrites the line just added.

After editing, the adb_usb.ini file must look like the following:

```
# ANDROID 3RD PARTY USB VENDOR ID LIST—DO NOT EDIT.
# USE ‘android update adb’ TO GENERATE.
# 1 USB VENDOR ID PER LINE.
0x05C6
```

8. Obtain the latest version of the Qualcomm Composite USB driver from Documents and Downloads. (To include network interface support, use the Qualcomm Composite USB Network Combo driver.)

   Android debugging is enabled/disabled in user space with composition 9025/9026 respectively.

```
qcmdm.inf
[Models]
%QUALCOMM90252% = Modem2, USB\VID_05C6&PID_9025&MI_02
%QUALCOMM90261% = Modem2, USB\VID_05C6&PID_9026&MI_01
[Models.NTamd64]
%QUALCOMM90252% = Modem2, USB\VID_05C6&PID_9025&MI_02
%QUALCOMM90261% = Modem2, USB\VID_05C6&PID_9026&MI_01
[Models.NTia64]
%QUALCOMM90252% = Modem2, USB\VID_05C6&PID_9025&MI_02
%QUALCOMM90261% = Modem2, USB\VID_05C6&PID_9026&MI_01
[Strings]
QUALCOMM90252 = “Qualcomm Android Modem 9025”
QUALCOMM90261 = “Qualcomm HS-USB Android Modem 9026”
qcser.inf
[QcomSerialPort]
%QcomDevice90250%  = QportInstall00, USB\VID_05C6&PID_9025&MI_00
%QcomDevice90253%  = QportInstall00, USB\VID_05C6&PID_9025&MI_03
%QcomDevice90260%  = QportInstall00, USB\VID_05C6&PID_9026&MI_00
%QcomDevice90262%  = QportInstall00, USB\VID_05C6&PID_9026&MI_02
[QcomSerialPort.NTia64]
%QcomDevice90250%  = QportInstall00, USB\VID_05C6&PID_9025&MI_00
%QcomDevice90253%  = QportInstall00, USB\VID_05C6&PID_9025&MI_03
%QcomDevice90260%  = QportInstall00, USB\VID_05C6&PID_9026&MI_00
%QcomDevice90262%  = QportInstall00, USB\VID_05C6&PID_9026&MI_02
[QcomSerialPort.NTamd64]
%QcomDevice90250%  = QportInstall00, USB\VID_05C6&PID_9025&MI_00
%QcomDevice90253%  = QportInstall00, USB\VID_05C6&PID_9025&MI_03
%QcomDevice90260%  = QportInstall00, USB\VID_05C6&PID_9026&MI_00
%QcomDevice90262%  = QportInstall00, USB\VID_05C6&PID_9026&MI_02
[Strings]
QcomDevice90250 = “Qualcomm HS-USB Android DIAG 9025”
```

```
QcomDevice90253 = “Qualcomm HS-USB Android GPS (NMEA)9025”
QcomDevice90260 = “Qualcomm HS-USB Android DIAG 9026”
QcomDevice90262 = “Qualcomm HS-USB Android GPS (NMEA)9026”
qcnet.inf
[QCOM]
qcwwan.DeviceDesc90254 = qcwwan.ndi, USB\VID_05C6&PID_9025&MI_04
qcwwan.DeviceDesc90263 = qcwwan.ndi, USB\VID_05C6&PID_9026&MI_03
[QCOM.NTia64]
qcwwan.DeviceDesc90254 = qcwwan.ndi, USB\VID_05C6&PID_9025&MI_04
qcwwan.DeviceDesc90263 = qcwwan.ndi, USB\VID_05C6&PID_9026&MI_03
[QCOM.NTamd64]
qcwwan.DeviceDesc90254 = qcwwan.ndi, USB\VID_05C6&PID_9025&MI_04
qcwwan.DeviceDesc90263 = qcwwan.ndi, USB\VID_05C6&PID_9026&MI_03
[Strings]
qcwwan.DeviceDesc90254      = “Qualcomm Wireless HS-USB Ethernet Adapter
9025”
qcwwan.DeviceDesc90263      = “Qualcomm Wireless HS-USB Ethernet Adapter
9026”
```

4.4 Install ADM and Fastboot in Linux

1. Before installing adb, modify the USB driver by navigating to the following directory:
   `cd /etc/udev/rules.d/`
2. Enter the command:
   `sudo vi 50-android.rules`

   The result must be similar to the following:

   ```
   #Sooner low-level bootloader
   SUBSYSTEM==”usb”, SYSFS{idVendor}==”18d1”, SYSFS{idProduct}==”d00d”,
   MODE=”0664”, GROUP=”plugdev”
   # adb composite interface device 9025
   SUBSYSTEM==”usb”, SYSFS{idVendor}==”05C6”, SYSFS{idProduct}==”9025”,
   MODE=”0664”, GROUP=”plugdev”
   ```

3. After editing the file, to see the list of target devices connected to the Linux box, type:
   `Lsusb`
4. The adb and fastboot executables for Linux are located in the android\out\host\linux-x86\bin directory in the Android software release after a build is complete. If the android\out\host\linux-x86\bin directory is not in the executable search path, use the following steps to add it. If it is already in the executable search path, skip to Step 5:

   a. Type the command:
      `source build/envsetup.sh`

b. Type the command:
```
choosecombo 1 msm8909 userdebug
```

5. Verify that the fastboot has properly flashed the Android images to the target, type the command:
```
sudo fastboot devices
```

6. Verify that device is displayed by fastboot in response to typing in the fastboot devices command.

NOTE: To run adb or fastboot, sudo or root access on the Linux machine may be required.

4.5 Program procedures

This section describes the procedures to be used for programming and reprogramming each firmware image and device.

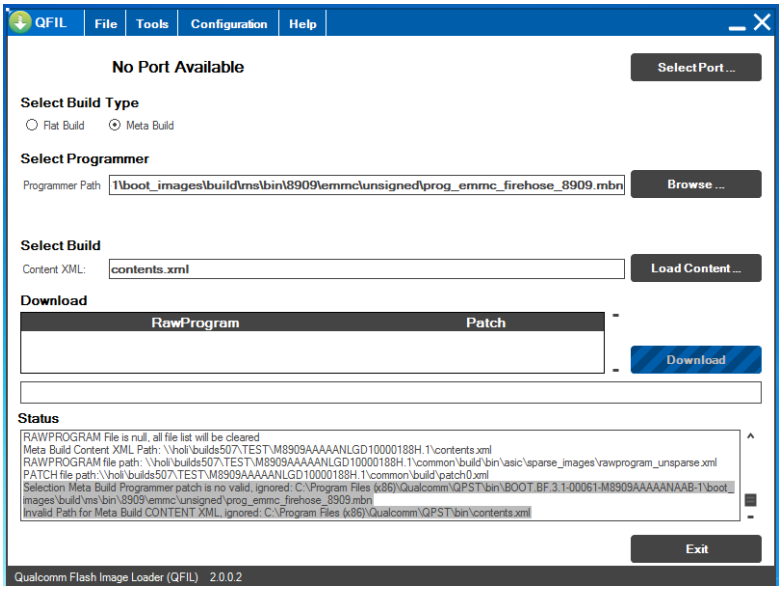
4.5.1 Program eMMC with QPST

If no image is flashed to eMMC (this is the situation for the first-time binary download in the factory line), the PBL enumerates USB, as the Qualcomm Sahara download interface waits for the download command from the host PC. In this case, the minimum initial binary is needed to be flashed so that the rest of the Android images can be downloaded.

NOTE: On the MSM8909 CDP, you can force the device into the binary download mode by erasing the device entirely or using the dip switch S7 pin 3.

NOTE: The QPST version must meet the minimum requirement specified in Section 4.1.

1. Launch QFIL from QPST. If USB 9091 port is displayed in the Windows Device Manager, it is automatically detected.



2. Select the contents.xml from the root directory of the build, it fills the Programmer Path field is populated automatically.
3. Click Download. The image is downloaded to the device and the eMMC is programmed.

4.5.2 Program CDT using QPST/QFIL

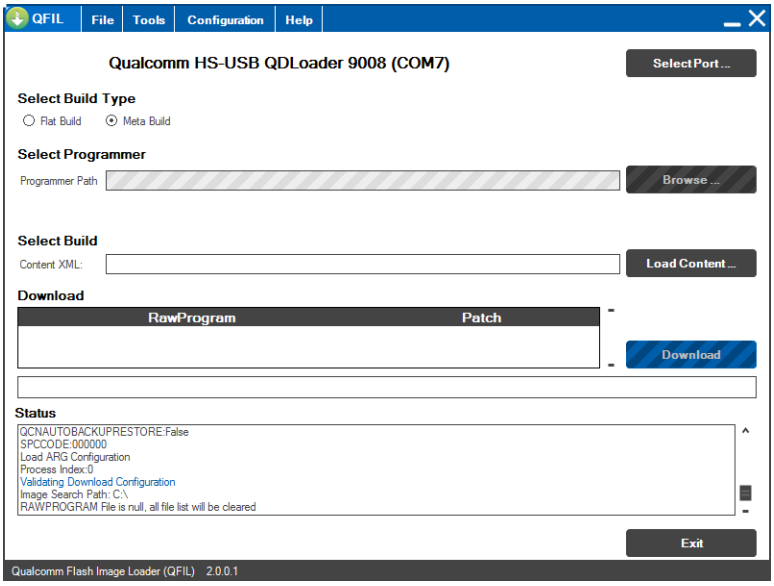
To program CDT using the QFIL tool, ensure that you have the following:

- Device programmer – `prog_emmc_firehose_8909.mbn`
- CDT xmls
  - Example: 8909\emmc_cdt_program_script\cdt_prog_xml\rawprogram2_cdp.xml
  - 8909\emmc_cdt_program_script\cdt_prog_xml\patch2.xml
- CDT binary corresponding to the CDT that is programmed (present in the rawprogram2_cdp.xml)
  - Example: 8909\emmc_cdt_program_script\cdt_bin\cdp_1.0_platform_jedec_lpddr2_single_channel.bin

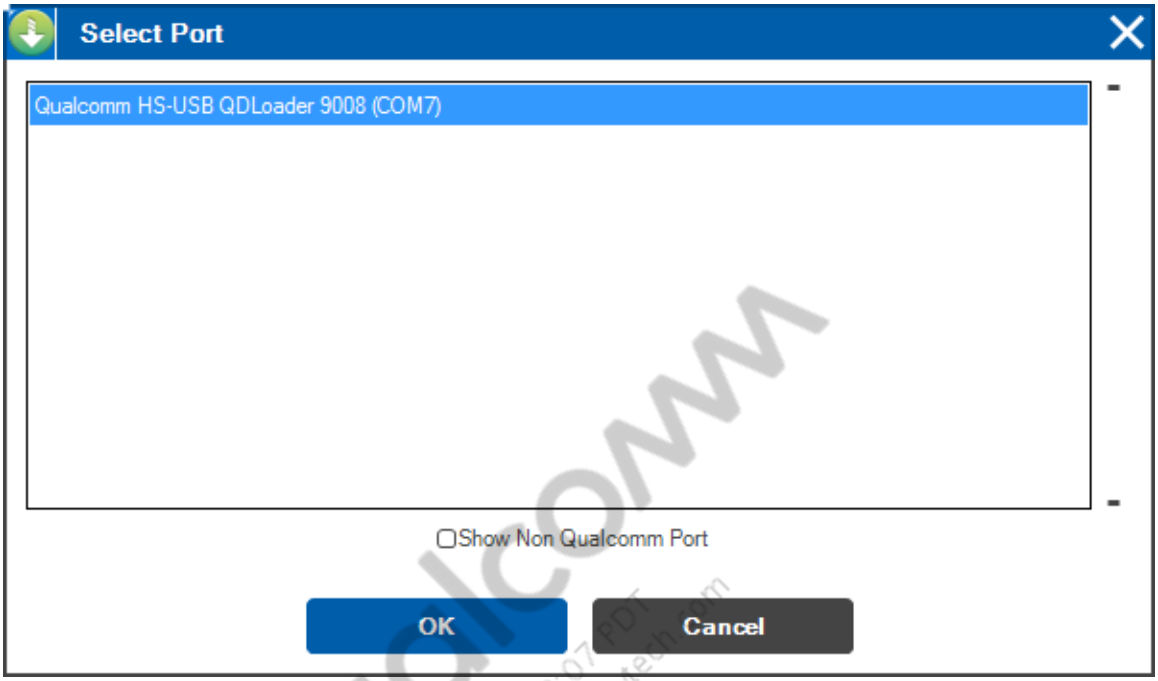
1. Copy the above into one folder
2. Launch the QFIL application.
3. Select the programmer from the Boot build location similar to 4.5.1.
4. Select the CDT xmls (rawprogram2_cdp.xml and the patch2.xml) from the folder in which it has been copied.
5. Select download to program the CDT.

Flash CDT file

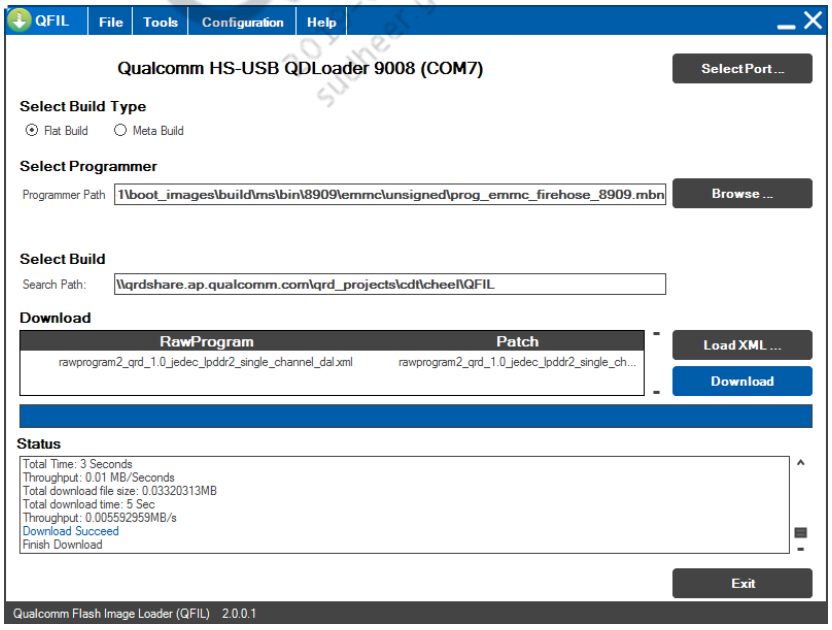
1. Launch QFIL from QPST. If USB 9008 port is displayed in the Windows Device Manager, it is automatically detected.



2. Click OK. The 9008 port window appears.



3. Select Build Type as Flat Build.
4. Select the programmer path from the Programmer Path field.
5. Click Load XML.

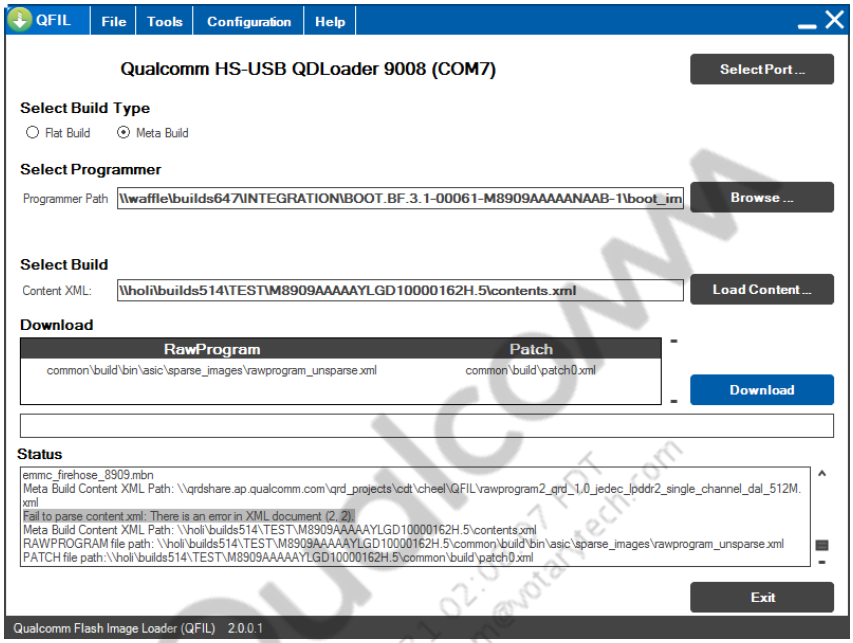


6. Click Download to start the CDT file download.

Download image by QFIL

1. Select Build Type as Meta Build.
2. Load contents.xml file.

   The Programmer Path field is populated automatically.

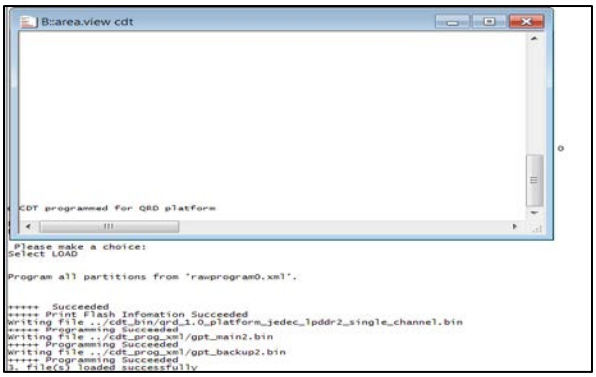


3. Click Download.

4.5.3 Program CDT using JTAG

1. Set up T32 debug environment first (see section 4.5.4)
2. Run the CDT script in CortexA53Core0 session. The following information is shown if the script succeeds:

```
cd.do C:\emmc_cdt_program_script\
mtp_1.0_lpddr2_single_channel_emmc_cdt_program.cmm
```



4.5.4 Program system images using Fastboot

Before programming the system images, using Fastboot, the Android boot loaders must already be flashed on the target:

1. Plug the USB cable into the target.
2. Depending on your build environment, choose one of the following options:
   - From the Windows command shell, run:
     `fastboot devices`
   - From Linux:
     i Navigate to the following directory:
     `cd <AndroidRoot>/LINUX/device/out/host/linux-x86/bin`
     ii Run:
     `sudo fastboot devices`

   A list of registered devices is shown on the console.
3. Once the device is detected, Flash the binaries to the target. The following commands run all the Fastboot steps at once:
   ```
   cd <target_root>/common/build
   fastboot_all.py
   ```
   Each binary can also be flashed selectively through the following fastboot command options:
   ```
   fastboot flash modem  <path to NON-HLOS.bin> or <path to APQ.bin>
   fastboot flash sbl1 <path to sbl1.mbn>
   fastboot flash rpm <path to rpm.mbn>
   fastboot flash QSEE <path to tz.mbn>
   fastboot flash aboot <path to emmc_appsboot.mbn >
   fastboot flash boot <path to boot.img>
   fastboot flash system <path to system.img>
   fastboot flash userdata <path to userdata.img>
   fastboot flash persist <path to persist.img>
   fastboot flash recovery <path to recovery.img>
   ```
   To derive a list of all fastboot partitions supported by fastboot programming, refer to the source code in LINUX/android/bootable/bootloader/lk/platform/msm_shared/mmc.c.

4.5.5 Flash applications to Android using ADB

1. Plug the USB cable into the target.
2. Navigate to the following directory:
   `cd <root>/LINUX/device/out/host/linux-x86/bin`
3. Enter the following command to register a device:
   `sudo adb devices`
4. Navigate to the following directory:
   `cd <root>/LINUX/device/out/target/product/surf/obj/APPS/AppName_intermediates/`

5. Copy the following files:
   `cp package.apk AppName.apk`
6. Push the files as follows:
   `adb push AppName.apk /system/app/.`

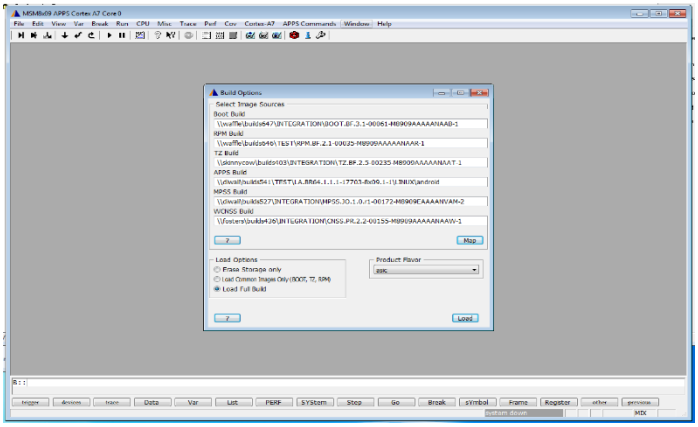
NOTE: In general, the syntax is: adb push <file_name> <location_on_the_target>

4.5.6 Program eMMC boot loaders with T32

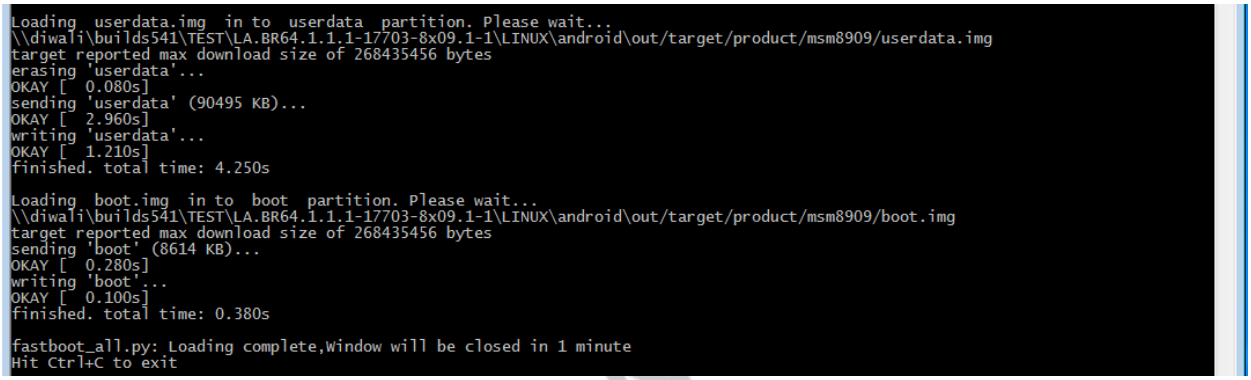
1. Initiate T32 by using the t32start.cmd in the <meta build>\common\t32 folder.
2. Navigate to Configuration Tree → JTAG DAP Mode → Podbus Device Chain → Power Trace ethernet

3. Select CortexA7 Core0 and click **Start**.
4. In CortexA7 Core0 T32 window, click **APPS COMMANDS → Build Options**, then select **ASIC** flavor in "Product Flavor" field and click **MAP**.

   The Build Options window displays.
5. Click **Load** in the Build Options window.



6. Keep the USB connected to PC, as it uses fastboot to flash **NON-HLOS.bin** and apps binaries. Fastboot download process starts automatically by script after T32 finishes programming boot loaders.



7. After fastboot completes flashing the binaries, power cycle the device.

5 Operational guide

5.1 CDP and MTP SIM slots, primary antenna, and USB ports

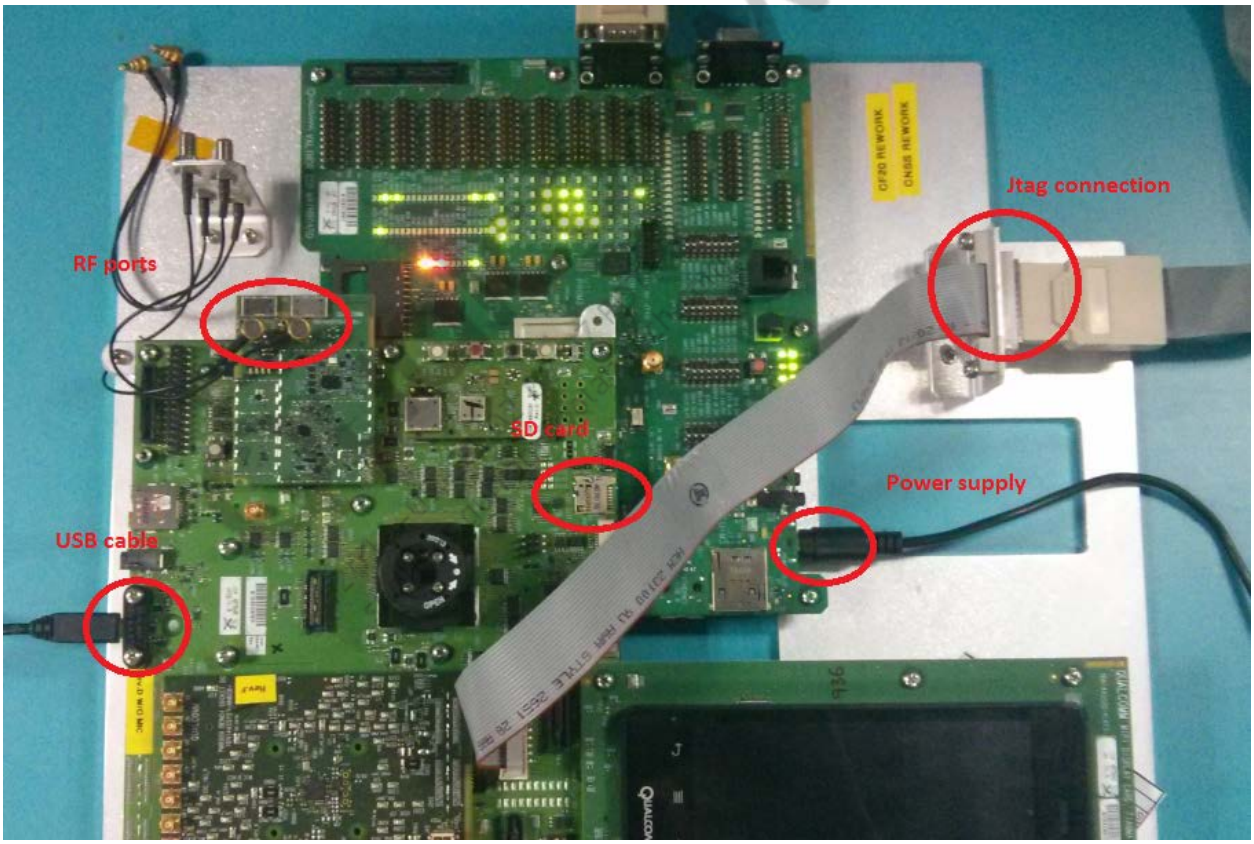


Figure 5-1 CDP USB/JTAG/RF/SD card/power configuration

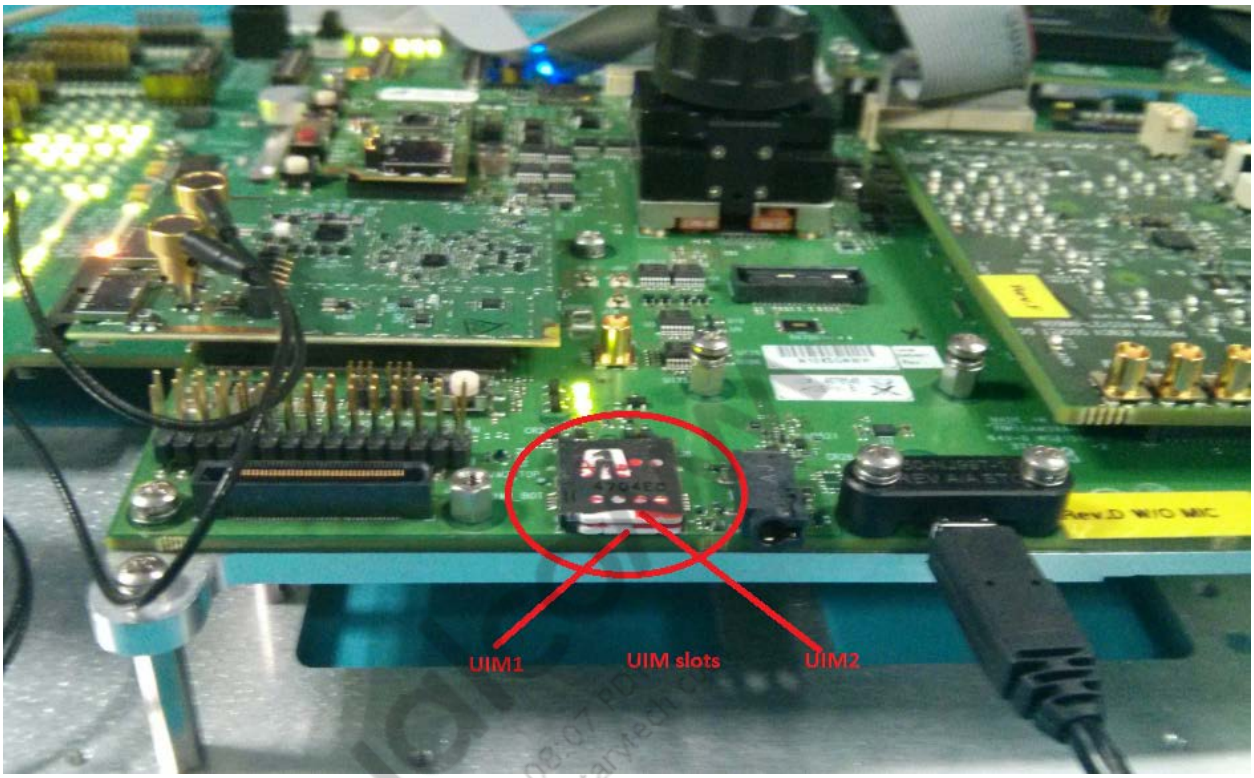


Figure 5-2 CDP UIM configuration

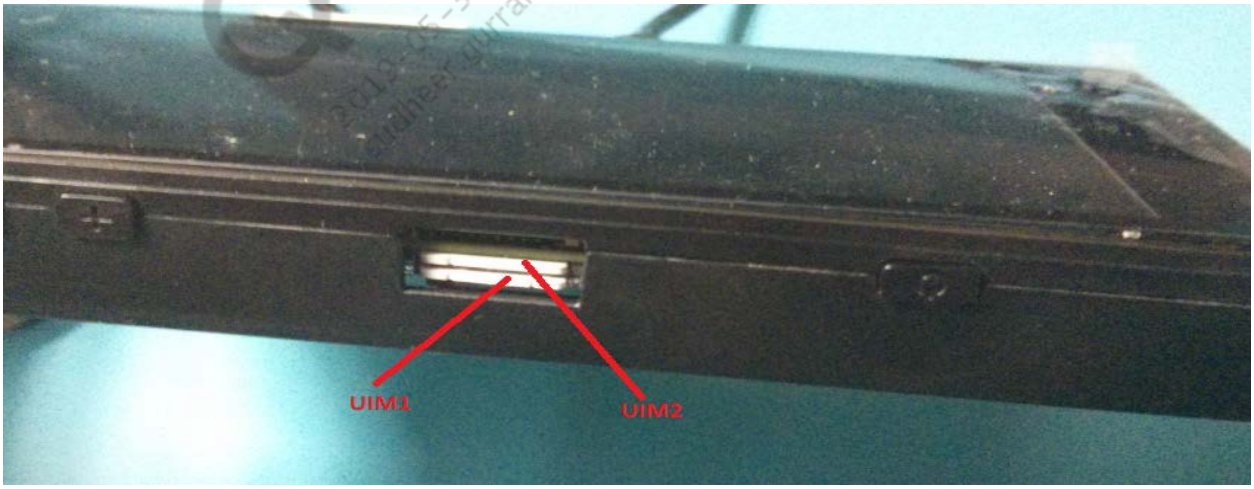


Figure 5-3 MTP UIM configuration

5.2 Common NV settings

5.2.1 RF NV settings

1. Static QCN is generated from the XML shipped within the MPSS build. Pick the appropriate xml file from the following path:

   <MPSS ROOT>\modem_proc\rftarget_dimepm\common \rftarget_dimepm\common\qcn\

2. Choose an RF card per the requirements.
3. Run QRCT to generate a static QCN, select the RF configuration to be used and then convert it to QCN.
4. Navigate to Tool → NV tools and pick the master xml file from the following build path:

   <MPSS ROOT>\modem_proc\rftarget_dimepm\common \rftarget_dimepm\common\qcn\

   For example, for RF card: RFC_WTR1605_CHILE_SVDSDA, the build path must be as follows:

   \modem_proc\rftarget_dimepm\common\qcn\wtr1605_chile_svdsda\etc
   MSM8909_WTR1605_CHILE_SVDSDA_MASTERFILE.

5. Click Execute.

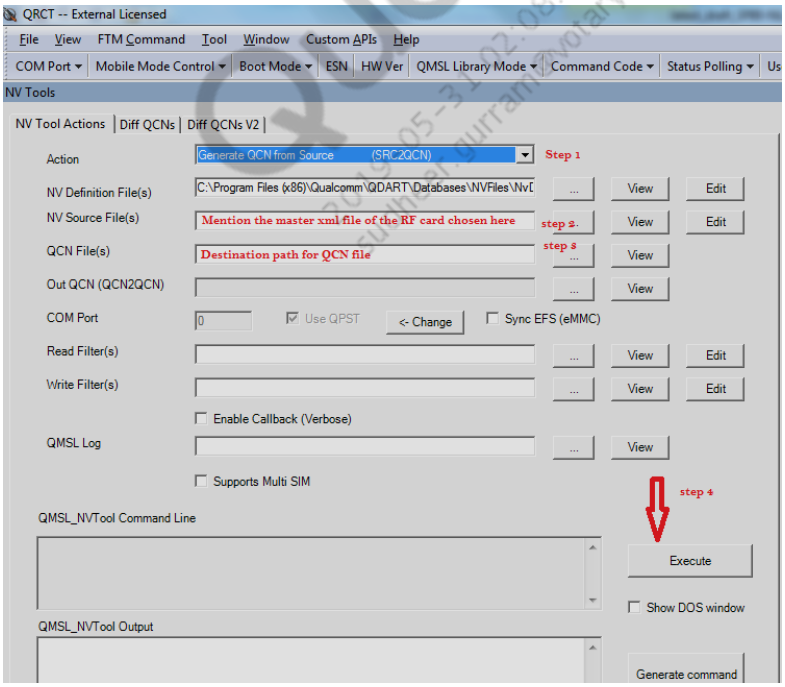


Figure 5-4 QRCT NV tool for generating QCN

NOTE: The MPSS build has a reference QCN as a deliverable. The QCN contains RF-only NV items and does not have XO TRIM NV item in it. Hence, for basic functional testing, this QCN is loaded followed by XO-only calibration. The reference QCNs are found at \modem_proc\rftarget_dimepm\common

5.2.2 NV configuration for WCDMA/GSM + GSM

Table 5-1 WCDMA/GSM + GSM DSDS NV settings

NV item	Subscription1 (WCDMA/GSM) value	Subscription2 (GSM) value
00010	17: Auto (WCDMA or GSM) 14: WCDMA only	13: GSM only
00441	0xFFFF (0x380 or 0x387 for specific bands)	Same as SUB1
00850	0x02 CS and PS	0x00 CS only
00855	0 RTRE configuration for WCDMA/GSM + GSM (Before setting this item send spc 000000 through QXDM command window)	Same as SUB1
00880	0x01 integrity enable	Same as SUB1
00881	0x01 ciphering enable	Same as SUB1
00882	0 fake security enable	Same as SUB1
00905	0x0000 fatal error option	Same as SUB1
00946	0x0040 IMT band (0CE0 US PCS band)	0x0040
03649 (RRC Version)	Inactive or 0→R99; 1→R5; 2→R6; 3→R7; 4→R8	Same as SUB1
03851	0 RxD control	Same as SUB1
04118 (HSDPA Cat)	Inactive or 24→DC	Same as SUB1
04210 (HSUPA Cat)	Inactive or 6→EUL 2 ms; 5→EUL 10 ms	Same as SUB1
04398	0→DSDS; 1→SS	Same as SUB1
04399	1 detect hardware reset	Same as SUB1
06876	00005→WCDMA/GSM to GSM Tune away 00006→DSDS	Same as SUB1
06907	1 Dual SIM hardware 0 Single SIM hardware	Same as SUB1

NOTE: For WCDMA/GSM + GSM DSDA NV configuration, the same as Table 5-1, except NV70266 (Dual standby preference) must be set to 2.

5.2.3 NV configuration for CDMA + GSM

Table 5-2 CDMA + GSM DSDS NV Settings

NV item	Subscription1 (CDMA+ HDR) value	Subscription2 (GSM) value
10 (Mode preference)	4 → Automatic mode 19: CDMA and HDR-only	13: GSM only
475 (HDR SCP session status)	0 → Inactive	Same as SUB1
850 (Service domain preference)	0x02 CS and PS	0x00 CS only
905 (Fatal error option)	0x0000 fatal error option	Same as SUB1

NV item	Subscription1 (CDMA+ HDR) value	Subscription2 (GSM) value
4204 (HDR SCP force)	0 → HDR SCP force release 0 session configuration	Same as SUB1
4964 (HDR SCP force at configuration)	0 → HDR rev 0, 1 → HDR rev A, 3 → HDR rev B.	Same as SUB1
03446 (TRM Configuration)	2, 0	Same as SUB1
4398 (UIM select default USIM application	0 → DSDS; 1 → SS	Same as SUB1
4399 (detect hardware reset)	1 detect hardware reset	Same as SUB1
6874 (ASID 1 Data)	255	Same as SUB1
6875 (ASID 2 Data)	255	Same as SUB1
562 (preference Hybrid mode)	1 → Hybrid operation allowed	0→ Hybrid operation not allowed
6876 (Dual standby configuration items)	00002 (Dual standby preference)	Same as SUB1
6907 (NV_UIM_HW_SIM_CONFIG)	1 → Dual SIM 0 → Single SIM	Same as SUB1
855 (RTRE configuration)	0 (Before setting this item send spc 000000 through QXDM Pro command window)	Same as SUB1

NOTE: For CDMA + GSM DSDA NV configuration, the same as Table 5-2, except NV70266 (Dual standby preference) must be set to 2.

5.2.4 LTE + GSM DSDS settings

The following changes are required to ensure that the device is correctly configured for LTE + GSM DSDS mode of operation.

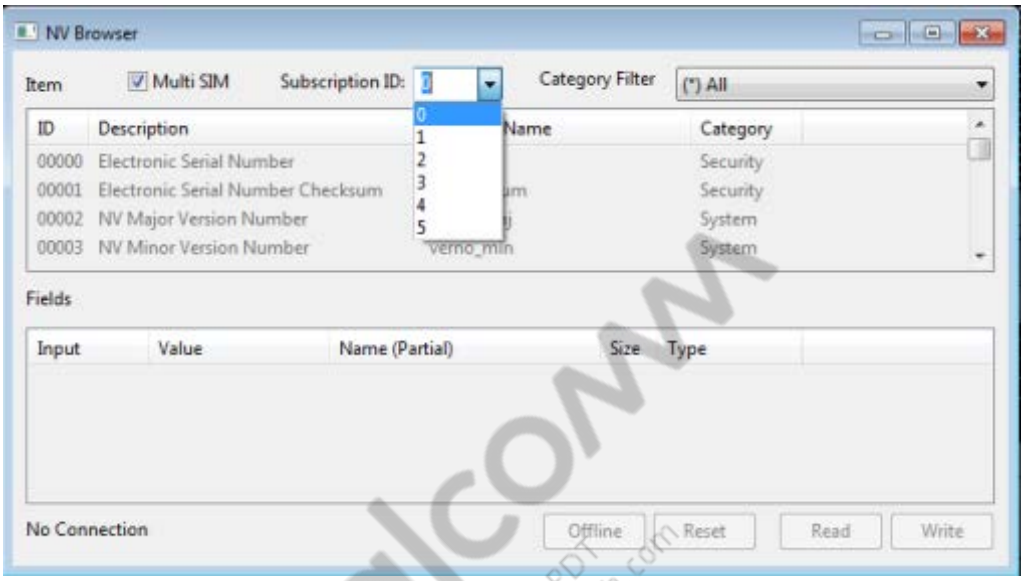
VoLTE/IMS/eMBMS and CSG features are not supported with LTE + GSM DSDS. This section explains all the required NV items for disabling VoLTE/IMS/eMBMS as well as other parameters for LTE + GSM mode of DSDS operation.

Table 5-3 LTE + GSM DSDS settings

Feature name	How to disable
VoLTE/IMS	Refer *SGLTE Device Configuration* (80-NJ017-11)
eMBMS	EFS file embms_feature_status (with value set as 0) must be copied under the path /nv/item_files/modem/lte/rrc
CSG	Create an EFS file viz. csg_control at the path "/nv/item_files/modem/lte/rrc/csg/". A 16-byte hexadecimal value is written into it 01 01 01 00 01 00 00 00 E0 93 04 00 00 00 00 00

Enable DSDS NV settings

1. Set the following NVs in QXDM Pro NV browser.



Subscription 0	
NV item number	**NV item value**
00010	31 (GWL)
00850	0x01
65777	1
70210	hw_config.UIM[1].DISABLE_UIM set to FALSE
06876	5
06907	1
04398	0

2. Perform “Spc 000000” in QXDM Pro and then set the following:

NV item number	NV item value
00855	0 (for both single SIM and dual SIM)
70266	1 (for dual SIM)

Subscription 1	
NV item number	**NV item value**
00010	13 (GSM-only)
00850	0x00
65777	0

3. Set the NV settings for IRAT (NV settings are required only on Subscription 0).

Subscription 0	
NV item number	**NV Item value**
00010	34 (G+L)
00850	0x02

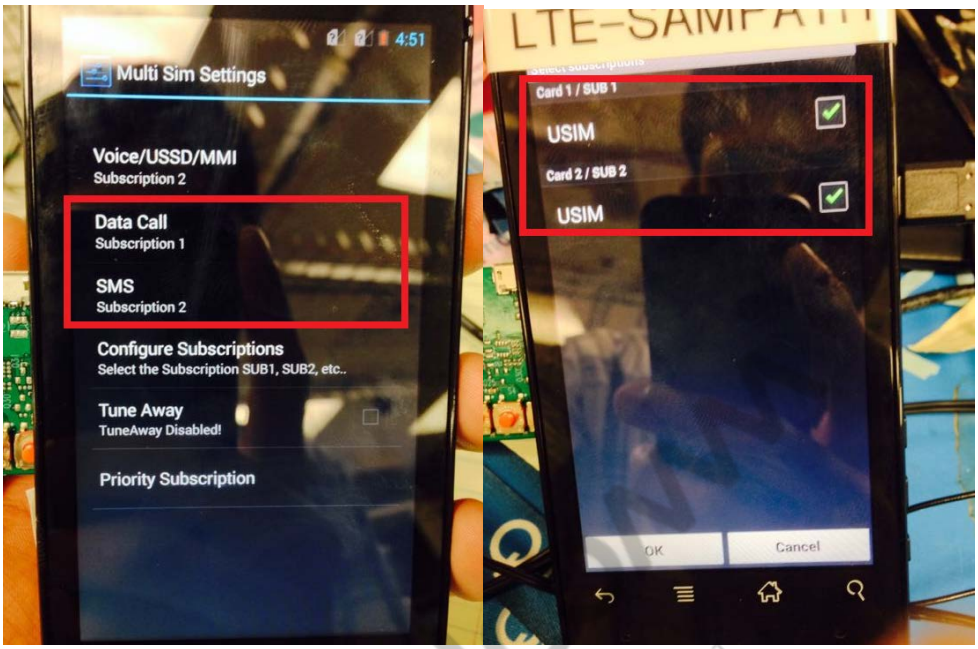
Subscription 0	
65777	0
00946	1F
02954	0

4. Restart the UE and run the dual SIM commands from ADB shell:

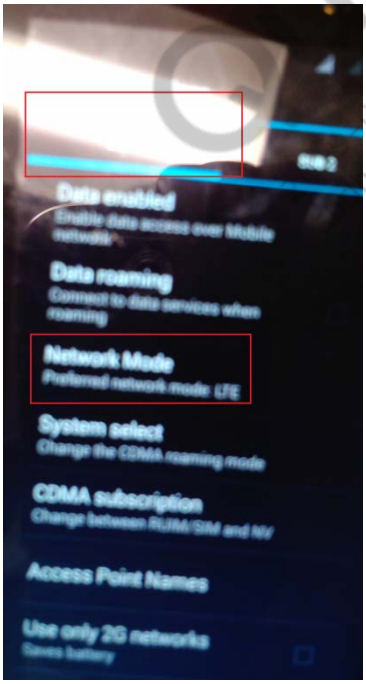
```
adb devices
adb root
adb shell
setprop persist.radio.multisim.config dsds
getprop persist.multisim.config (it must show up as DSDS)
```

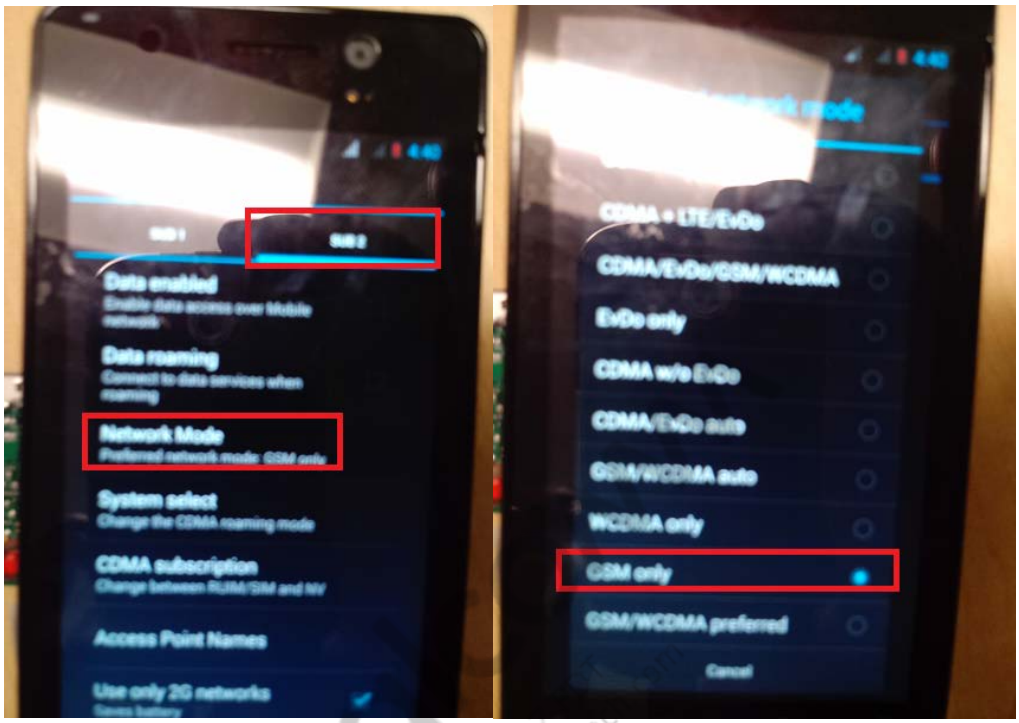
5. Restart the UE.
6. Go to Settings on the UI, and perform the below setting.

7. Select the Multi SIM settings on the UI.
8. Configure the UIM cards to respective Subs by selecting the Configure Subscriptions.



9. Go back to settings, select Mobile Networks, and select subscriptions. Set the respective subscriptions to LTE and GSM.





10. Run the below command after power-up, to check if both RILs are activated for DSDS.

```
C:\>adb shell ps rild
USER    PID  PPID  VSIZE  RSS     WCHAN    PC         NAME
radio   243   1     17968  2900  ffffffff 00000000 S  /system/bin/rild
radio   247   1     17952  2932  ffffffff 00000000 S  /system/bin/rild
```

11. Check whether the below command returns only one radio as output, and if it returns only one radio as output then the RIL still is in single SIM mode.

```
C:\>adb shell ps rild
USER    PID   PPID  VSIZE  RSS     WCHAN    PC         NAME
radio   166   1     16928  2828  ffffffff 00000000 S  /system/bin/rild
```

5.2.5 1xSRLTE settings

The following settings need to be done to configure the device in 1xSRLTE mode of operation.

Table 5-4 1xSRLTE settings

NV item	Value	Description
72539	1	ESR support/CSFB support for dual Rx UEs 1 – Supported 2 – Not supported
72550	500 (ms)	LTE NAS 1xSRLTE ESR delay timer

5.3 Segment loading configuration

As part of memory optimizations, there is a segment loading feature that assumes that there will not be any use case where the network supports both the modes (WCDMA and TD-SCDMA) at one place. The segment loading feature is controlled by FEATURE_SEGMENT_LOADING and when enabled in MPPS build and based on the UE supported modes, it supports two segments i.e., (LTE/GSM/WCDMA/CDMA) or (LTE/GSM/TD-SCDMA/CDMA). The segment loading works in two modes i.e. Automatic or Manual modes, and by default it is set to Manual mode.

Manual mode

For Manual mode:

- On a fresh load of the build, the UE is by default configured for Manual mode of segment loading operation.
  - NV72542 – 2 (default) → The UE works in WCDMA Segment mode.
  - NV72542 – 1 → The UE works in TD-SCDMA Segment mode.
- In Manual mode, there are no switches between WCDMA and TD-SCDMA segments, as the Manual mode means that the UE is set to operate as determined by the NV72542 setting.
  - Changing the NV72452 value between 1 and 2, the UE can be set to operate with the WCDMA segment or TD-SCDMA segment.

At power-up for the first time and based on the NV 72542 setting, the UE loads the said NV image initially and starts searching for the network as per the configured RAT priority order. If the UE fails to get the coverage on the network in Manual mode, change the NV72542 explicitly to search for other networks. To avoid the user intervention for NV switch and finally network search, set the segment loading to Automatic mode.

Automatic mode

1. To take the UE out of the Manual mode and to put it in Automatic mode, load the segment_loading.xml file as below:
2. Copy from <build_root>\modem_proc\mmcp\policyman\configurations\SegLoad\ segment_loading.xml to \..\policyman\ on the device.
3. Change NV 10 to “auto”.

- While in Automatic mode, the UE loads the image based on the NV 72542 setting.
  - Segment switching occurs as per the logic outlined in the segment_loading.xml file.
  - While in service on MCCs is listed in segment_loading.xml file, TDS segment is loaded.
  - While in service on MCCs not listed in segment_loading.xml file, WCDMA segment is loaded.
  - While in complete OOS, segment switching occurs as determined by the timers listed in segment_loading.xml.
  - Logic/comments in “<build_root>\modem_proc\mmcp\policyman\configurations\SegLoad\ segment_loading.xml” file offer detailed steps.
  - Segment switching occurs with modem subsystem reset.

- The segment_loading.xml file works independent of other policy manager configuration files.

NOTE: Do not set NV 72542 to 0. It causes device runtime to crash due to a heap exhaustion

5.4 Call configuration

5.4.1 1X voice call

Prerequisites

- NV setting (set NV # 10 to 4 for Automatic mode)
- QCN (be sure to perform RF calibration on the device; do not use a golden QCN…, also note that 1X is on the SV chain by default)
- PRL (must match the band/channel being tested, and SID & NID must also match)

Callbox setup

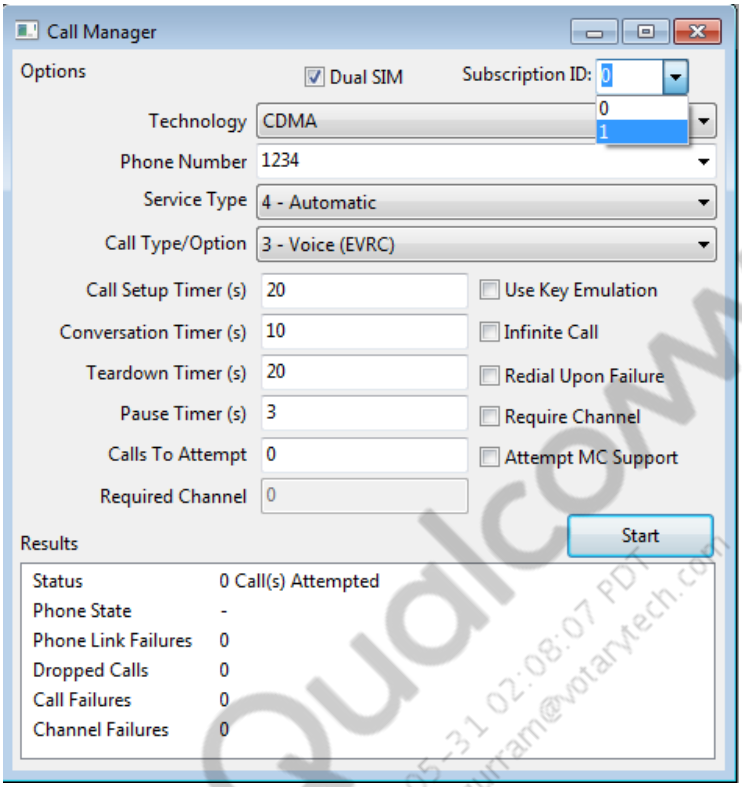
- Current testing has been done on Agilent 8960
- Callbox setup instructions
  - In System Config/Application Setup, select CDMA 2000 Lab App B (version should be B or above);
  - In Call Setup/Call Control screen
    - Set the Operating Mode to Active Cell;
    - Set the System Type to IS-2000;
    - Click "**More**" to move to page 2 of 5, select Cell Info/Cell Parameters. Configure the SID/NID(match with PRL, or set it as wildcard: SID=0, NID=65535);
  - In Call Setup/Call Params screen
    - Set Cell 1 Power to a proper value (between -45 ~ -65 dBm);
    - Set Cell Band and Channel, match the PRL setting;
    - Set Protocol Rev to 6(IS-2000-0);
    - Set Radio Config(RC) to (3,3) - (Fwd3, Rvs3);
    - Set FCH Service Option setup for (Fwd3, Rvs3) to SO3 (Voice);

Device setup

To make the call:

1. Use the Call Manager screen in QXDM Professional (QXDM Pro).
2. Set the phone number to something like 1234 and make sure that the service option matches the callbox setting.
3. Click **Call** to start a call. For MT, originate the call from the test box.

NOTE: For dual SIM device, the default subscription is 0. If subscription 1 is needed, check the **Dual SIM** and then select **Subscription ID**.



5.4.2 1X data call

Prerequisites

- NV setting (set NV #10 to 4 for Automatic mode)
- QCN (be sure to perform RF calibration on the device; do not use a “golden” QCN…; also note that 1X is on the SV chain by default)
- PRL (must match the band/channel being tested, and SID & NID must also match)

Callbox setup

- Current testing has been done on Agilent 8960 or Anritsu MT8820;
- In Call Setup/Call Params screen
  - Set Radio Config(RC) to (3, 3) - (Fwd3, Rvs3);
  - Set FCH Service Option Setup for (Fwd3, Rvs3) to SO32 (TDSO);
- Callbox setup instructions – Make sure band/channel and SID/NID match those specified in PRL; cell power must be set somewhere between -45 and -65 dB

Device setup

To make the call:

1. Use the Call Manager screen in QDXM Pro.
2. Set the phone number to something like 1234 and make sure that the service option matches the callbox setting.
3. Click **Call** to start a call; for MT, originate the call from the test box.

5.4.3 HDR call

NV settings

- NV setting (set NV # 10 to 4 for Automatic mode)
- For DO Rev A calls, NV #4964 must be set to Rev A mode and the callbox has to be put in Rev A mode. For example, Set NV #4964 = NV_HDRSCP_REVA_PROTOCOLS_WITH_MFPA.

RF calibration

- Ensure that the device has been RF-calibrated
- Roaming list – A preferred roaming list with HDR channels must be loaded and subnet ID in PRL must match the callbox setting
- Roaming list is loaded via QPST Service Programming; perform the following:
  a. Select the **Roam** tab
  b. Enter PRL path in the Preferred Roaming area
  c. Select **Write to Phone**

Callbox setup

- For DO Rev A calls, the callbox must be placed in Rev A mode

Device setup

To make a call:

1. Power-cycle the device
2. Enter **mode online** in the QXDM Pro command bar; the device must attempt to acquire HDR channel and negotiate a session

5.4.4 GSM voice call

Prerequisites

- NV settings
  a. Set NV #10 (Mode Preference) to 13 for GSM-only operation
  b. Set NV #441 (Band Class Preference) to 0x200 for GSM900 band
  c. For multimode build, the mode preference is changed via the UI. Otherwise, multimode uses the default setting in Android (defaults to 1X only) and it uses this to overwrite the NV item, so the modem does not go into GSM.
- QCN (be sure to perform RF calibration on the device; do not use a golden QCN)

Callbox setup

- Current testing is performed on Agilent 8960 or Anritsu MT8820
- Set band to GSM900; cell power must be set somewhere between -45 and -65 dBm
- Set Channel mode to TCH/F (full rate TCH)

Device setup

To make a call:

1. Use the Call Manager screen in QDXM Pro.
2. Set the phone number, e.g., to 1234, and make sure that the service option matches the callbox setting.
3. Click **Call** to start a call; for MT, originate the call from the test box.

5.4.5 GPRS data call

RF calibration

- Ensure the device is RF-calibrated

Callbox setup (Agilent 8960)

- Select call setup screen
- Callbox setup
  - BCCH parameters → cell power = -75 dBm
  - BCCH parameters → cell band = EGSM
  - BCCH parameters → Broadcast chan = 20
  - PDTCH parameters → Multislot config = 1 Down 1 Up
  - Operating mode = Active mode GPRS
  - Data Conn = Type ETSI Type A

Device setup

To make a call:

1. Insert a SIM (test SIM can also be used)
2. Power up the device; Enter **mode online** in QXDM Pro command bar if necessary
3. The UE camps on GPRS cell and ATTACH
4. Initiate Test mode A data call; hit Start Data Connection; bottom of screen must display TRANSFERRING

5.4.6 WCDMA voice call

Prerequisites

- QCN – Ensure that the device is calibrated

Callbox setup

- Agilent 8960
  - Call box setup
    - Call Control/Security Info/Security Parameters/Security Operations – None
    - Call Parms/Cell Power – -50.00 dBm
    - Call Parms/Channel Type – 12.2 KRMC
    - Call Parms/Paging Service – AMR Voice
  - Test results – MO and MT passed 5/5
- Anritsu 8480C
  - Callbox setup
    - Station Globals – Spec_Release – 3; HSDPA – FALSE; EUL – FALSE
- Anritsu 8820
  - Callbox setup
    - Call Processing – On
    - Test Loop Mode – Off
    - Signal/Channel Coding – Voice

Device setup

To make a call (MO):

1. After starting firmware and software, attach the USB and then do a “mode online” from QXDM Pro command bar
2. Allow the mobile to acquire and register to network
3. Start a call using the Call Manager dialog in QXDM Pro
4. Set Technology to WCDMA

5. Set phone number to 1234
6. Check the **infinite call** box
7. Start the call
8. Phone state shows “Originating call” and then “Conversation”

5.4.7 WCDMA data call

Prerequisites

- QCN – Ensure device is calibrated

Callbox

- Agilent 8960
  - Callbox setup
    - Call Parms/Channel Type – HSPA
  - Test results – DUN data calls passed with ~384 kbps throughput; QMI not tried
- Anritsu 8480C
  - Callbox setup
    - Station Globals – Spec_Release – 5; HSDPA – TRUE; EUL – FALSE
  - Test results – DUN data calls was up but crashed later; QMI not tried
- Anritsu 8820
  - Callbox setup
    - Call processing – On
    - Test Loop Mode – Mode 1
    - Signal/Chanel Coding – Fixed reference channel

Device setup

To make a call:

1. Allow UE register to network
2. Click “**Connect/Dial**” on USB (DUN) or QMICM to initiate data call
3. Run iPerf or FTP applications to test throughput (we tested with FTP)

5.4.8 TD-SCDMA call

Prerequisites

- QCN – Ensure device is calibrated
- NV_PREF_MODE_1(10) – Set to 53 TD-SCDMA only
- Set NV 00850 = 0x2 (CS PS)

- TDS RRC integrity protection enabled (66011) – Set to 0; set to 1 for a live network
- TDS RRC ciphering enabled (66012) – Set to 0; set to 1 for a live network
- TDS RRC fake security status (66013) – Set to 0; set to 1 for a live network
- TDS RRC special frequency enabled (66014) – Set to 0
- TDS RRC PDCP disabled (66016) – Set to 1
- TDS RRC version (66017) – Set to 3
- TDS RRC HSDPA category (66020) – Set to 15
- TDS RRC NV version (66023) – Set to 2
- TDS RRC optional feature bitmasks (66024) – Set to 0x3F
- TDS RRC Special Test Setting Enabled (69731) – Set to 0; set to 1 when entering MTNet and CMCC tests

Callbox

- Agilent 8960
  - Callbox setup
    - Cell Power – -45.00 dBm
    - Channel Type – 12.2K RMC SC
    - Paging Service – AMR Voice
    - Channel – Desired band/channel
    - Screen2 – Cell Info → Cell Parameters → PS Domain Information → Present
- Anritsu 8820
  - Callbox setup
    - Call processing – On
    - Test Loop Mode – Mode 1
    - Signal/Chanel Coding – Fixed reference channel

Device setup

To make a call:

1. Configure the test equipment using the settings mentioned in Section 5.4.8 and check that the signal is acquired.
   a. MO and MT Calls - Initiate the MO call from device and MT call from the test equipment.
2. Send/receive SMS from/to device and test equipment.
3. DUN call using a dial-up connection “PS on USB” setup a DUN data Call.
4. Browse data from web browser to data session.

5.4.9 LTE data call

Prerequisites

- QCN – Ensure device is calibrated
- NV Settings – Set NV 00010 = 30 (LTE-only) and NV 00850 = 0x1 (PS-only) and NV 65777 = 0x1

Callbox setup

- Aeroflex
  - Cal box setup
    - Under home screen, select Mode as Callbox Mode
    - Select Parameter Config Tab.Parameter Group → Select System → Band ID Ex: For TDD Testing:38 and For FDD Tsting:1
    - Under Parameter Group select Layer 3 → MCC 001 and MNC 01 based on the SIM configuration.
- Anritsu 8820
  - Callbox setup
    - Call Processing – On
    - Test Loop Mode – Off
    - Signal/Channel Coding – Required LTE band/channel
    - Start Call

Device setup

To make a call:

1. Connect primary and secondary RFs to the respective ports.
2. Run mode LPM in QXDM.
3. Click Registry software ICON.
4. Click Application software ICON.
5. In Apps software, open TC and run.
6. Click Analyzer → Run. A message appears, “Please power on the phone” in application software.
7. Run mode online in QXDM.

   The UE registers to the network with status in Conversation state.
8. Verify browsing by opening a web page.

5.5 GPS configuration

Prerequisites

GNSS SubSysGNSS DLL Ver 1.0.44 or higher is required to perform offline RF dev.

Operation procedures

Running offline RF Dev requires QPSR, see *IZat Gen 8 Engine Family RF Development Test Procedures* (80-VM522-2) for complete details

5.6 Multimedia configuration

This section describes the bring-up of multimedia features like audio, display, video, and camera.

5.6.1 Audio configuration

This section will be included in a future revision of this document.

5.6.2 Display

For display panel bring-up and driver porting-related information, see *DSI Programing Guide for B-Family Android Devices* (80-NA157-174). It describes application usage of the Display Serial Interface (DSI) panel bring-up for the Android OS. It also provides sample code and PLL calculation pertaining to the DSI Mobile Industry Processor Interface (MIPI) panel bring-up. For details on Linux Android display driver porting, see *Linux Android Display Driver Porting Guide* (80-NN766-1).

5.6.3 Camera

To verify the camera features, the Android default camera application is used. For details related to sensor driver porting and migration, refer to *Sensor Driver Development and Troubleshooting* (80-NL239-32).

For techniques on debugging different kinds of camera errors, refer to *Linux Camera Debugging Guide* (80-NL239-33).

5.6.4 Video

To verify the playback of various video formats, the Android default video player application is used.

5.7 WCNSS configuration

WCNSS functionality does not require any specific configuration as everything is built-in. The basic functionality is verified using either FTM tool or GUI (default Android settings application). For FTM tool usage, see *WCN36x0 WLAN/BT/FM FTM Guide with QRCT Test Example* (80-WL300-27).

5.8 Subsystem restart

Subsystem restart (SSR) is a feature designed to give a seamless end-user experience when restarting after a system malfunction. The SoC is considered to be divided into individual subsystems (for example, Modem, WCNSS, etc.), and a central root, the Applications Processor (AP). The clients of these individual subsystems that are running on the AP receive notification from the kernel about a particular subsystem shutting down. The clients must be able to handle this notification in a graceful manner. The clients can expect to receive another notification when the subsystems are in back up. Examples of such clients are EFS sync, remote storage, etc.

The use case for this feature is a catastrophic restart, i.e., a software malfunction on the modem, or any other subsystem that could cause the phone to be dysfunctional. In this instance, the AP is expected to restart the respective subsystems, to restore them to normal operation.

The core restart module, powers up/down the registered subsystems when they crash and sends appropriate notifications.

Compile options to enable SSR – CONFIG_MSM_SUBSYSTEM_RESTART

Following are some useful adb commands for SSR:

- To find a specific subsystem
  ```
  cat /sys/bus/msm_subsys/devices/subsysX/name (here X = 0,1,2 for
  different subsystems modem,wcnss etc.)
  ```
- To know if SSR is enabled on a specific subsystem
  ```
  cat /sys/bus/msm_subsys/devices/subsysX/restart_level
  ```
- To enable SSR for various subsystems
  ```
  echo related > /sys/bus/msm_subsys/devices/subsysX/restart_level
  ```
- To disable SSR for various subsystems
  ```
  echo system > /sys/bus/msm_subsys/devices/subsysX/restart_level
  ```

For further information on SSR, refer Q34, Q35, and Q36.

6 Factory tools

6.1 QDART-MFG and TPP

The QDART-MFG installer is a set of factory tools designed to manufacture, reduce the setup steps, and optimize installation size and installation time. It provides GoNoGo UI, QSPR test framework, test solution for all test stations, including, software download and upgrade, RF calibration and verify, Bluetooth and WLAN, MMI, radiated, and service programming. For more details, see *QDART-MFG Installer* (80-NH711-3) and *TPP User Guide* (*80-NF136-1*).

6.2 FactoryKit

The FactoryKit is an Android application used for MMI test. It functions similar to FastMMI, but with longer boot-up time. The FactoryKit is used on customer devices and all QRD SKU devices.

A Android device tree structure

The Android device tree structure, for example, the <Android device tree root>, is laid out as follows:

`build/` – Build environment setup and makefiles
`bionic/` – Android C library
`dalvik/` – Android JVM
`kernel/` – Linux kernel
`framework/` – Android platform layer (system libraries and Java components)
`system/` – Android system (utilities and libraries, fastboot, logcat, liblog)
`external/` – Non-Android-specific Open Source projects required for Android
`prebuilt/` – Precompiled binaries for building Android, e.g., cross-compilers
`packages/` – Standard Android Java applications and components
`development/` – Android reference applications and tools for developers
`hardware/` – HAL (audio, sensors) and Qualcomm specific hardware wrappers
`vendor/qcom/` – Qualcomm target definitions, e.g., msm7201a_surf
`vendor/qcom-proprietary/` – Qualcomm-proprietary components, for example, MM, QCRIL, etc.
`out/` – Built files created by user
- `out/host/` – Host executables created by the Android build
- `out/target/product/<product>` – Target files
- `appsboot*.mbn` – Applications boot loader
  - `boot.img` – Android boot image (Linux kernel + root FS)
  - `system.img` – Android components (/system)
  - `userdata.img` – Android development applications and database
  - `root/` – Root FS directory, which compiles into ramdisk.img and merged into boot.img
  - `system/` – System FS directory, which compiles into system.img
  - `obj/` – Intermediate object files
    - `include/` – Compiled include files from components
    - `lib/`
    - `STATIC_LIBRARIES/`
    - `SHARED_LIBRARIES/`
    - `EXECUTABLES/`
    - `APPS/`

`symbols/` – Symbols for all target binaries

A.1 Android target tree structure

The Android target tree structure is laid out as follows:

`/` – Root directory (ramdisk.img, read-only)

- `init.rc` – Initialization config files (device config, service startups) init.qcom.rc
- `dev/` – Device nodes
- `proc/` – Process information
- `sys/` – System/kernel configuration
- `sbin/` – System startup binaries (ADB daemon; read-only)
- `system/` – From system.img (read-write)
  - `bin/` – Android system binaries
  - `lib/` – Android system libraries
  - `xbin/` – Nonessential binaries
  - `framework/` – Android framework components (Java)
  - `app/` – Android applications (Java)
  - `etc/` – Android configuration files
- `sdcard/` – Mount point for SD card
- `data/` – From userdata.img (read-write)
  - `app/` – User installed Android applications
  - `tombstones/` – Android crash logs

A.2 Building Tiny Android

Tiny Android, or TINY_ANDROID, is a build variant that creates only a superminimal build configuration used for board bringup and low-level debugging. The TINY_ANDROID configuration consists of only the Android Linux kernel and a system root file system containing a minimal set of system utilities.

To build Tiny Android, use:

```
$ make BUILD_TINY_ANDROID=true -j4
```

A.3 Building the Linux kernel manually

1. Set up the Android build environment (envsetup.sh/choosecombo).
2. Change to the kernel directory (kernel/).
3. Set up the correct kernel config with the command:

   ```
   make ARCH=arm CROSS_COMPILE=arm-eabi- msm8974_defconfig
   ```

4. Build the kernel image with the command:

   ```
   make -j3 ARCH=arm CROSS_COMPILE=arm-eabi- zImage
   ```

5. If desired, build the optional kernel modules with the command:

   ```
   make -j3 ARCH=arm CROSS_COMPILE=arm-eabi- modules
   ```

   The resulting kernel image appears in kernel/arch/arm/boot/zImage.

NOTE: In theory, it is possible to use –jn as long as 'n' is smaller than the number of processors in the server where the build is being created.

6. To start with a clean tree, use the following commands:
   a. To remove object files:

      ```
      make clean
      ```

   b. To remove all generated files:

      ```
      make distclean
      ```

A.4 Building Android manually

1. Set up the Android build environment (envsetup.sh/choosecombo).
2. Change to the main Android directory.
3. Build with the command:

   ```
   make -j4
   ```

4. To build individual components, choose one of the following options:
   - To run make from the top of the tree, use the command:

     ```
     m <component name>  # E.g. m libril-qc-1
     ```

   - To build all of the modules in the current directory, change to the component directory and use the command:

     ```
     Mm
     ```

5. To delete individual component object files, choose one of the following options:
   - To delete a particular module, use the command:

     ```
     m clean-<module name>
     ```

   - To delete a module within a given path, use the commands:

     ```
     rm -rf out/target/product/*/obj/STATIC_LIBRARIES/
     <module name>_intermediates
     rm -rf out/target/product/*/obj/SHARED_LIBRARIES/
     <module name>_intermediates
     rm -rf out/target/product/*/obj/EXECUTABLES/
     <module name>_intermediates
     ```

A.5 Other important Android build commands

Table 6-1 lists other important Android build commands.

Table 6-1 Other important Android build commands

Android build command	Description
printconfig	Prints the current configuration as set by the choosecombo commands.
m	Runs make from the top of the tree. This is useful because you can run make from within subdirectories. If you have the TOP environment variable set, the commands use it. If you do not have the TOP variable set, the commands look up the tree from the current directory, trying to find the top of the tree
mm	Builds all of the modules in the current directory.
mmm	Builds all of the modules in the supplied directories.
croot	cd to the top of the tree
sgrep	grep for the regex you provide in all .c, .cpp, .h, .java, and .xml files below the current directory
clean-$(LOCAL_MODULE) clean-$(LOCAL_PACKAGE_NAME)	Allows you to selectively clean one target. For example, you can type make clean-libutils, and it deletes libutils.so and all of the intermediate files, or you can type make clean-Home and it cleans just the Home application.
make clean	Deletes all of the output and intermediate files for this configuration. This is the same as `rm -rf out/<configuration>/`

Android makefiles (Android.mk) have the following properties:

- Similar to regular GNU makefiles; some differences are:
  - Predefined variables to assign for source files, include paths, compiler flags, library includes, etc.
    - Variables
      - `LOCAL_SRC_FILES` – List of all source files to include
      - `LOCAL_MODULE` – Module name (used for “m”)
      - `LOCAL_CFLAGS` – C compiler flags override
      - `LOCAL_SHARED_LIBRARIES` – Shared libraries to include
- Predefined action for compiling executables, shared libraries, static libraries, Android packages, using precompiled binaries, etc.
    - Action
      - `include $(CLEAR_VARS)` – Clears LOCAL* variables for the following sections:

```
include $(BUILD_EXECUTABLE)
include $(BUILD_SHARED_LIBRARIES)
include $(BUILD_STATIC_LIBRARIES)
```

NOTE: Paths in Android.mk are always relative to the Android device tree root directory.

Adding a module

To add a new module to the Android source tree, perform the following steps:

1. Create a directory to contain the new module source files and Android.mk file.
2. In the Android.mk file, define the LOCAL_MODULE variable with the name of the new module name to be generated from the Android.mk.

   For Applications modules, use LOCAL_PACKAGE_NAME instead.
3. Set the local path in the new module by inserting the following as the first line in the Android.mk file:

```
LOCAL_PATH := $(call my-dir).
LOCAL_SRC_FILES
```

NOTE: The local path in the new module is LOCAL_PATH. This is the directory where the Android.mk file is available.

4. Prefix them with the directory name:

```
LOCAL_SRC_FILES := \
file1.cpp \
dir/file2.cpp
```

NOTE: The build system looks at LOCAL_SRC_FILES to find out which source files to compile, .cpp, .c, .y, .l, and/or .java. For .lex and .yacc files, the intermediate .h and .c/.cpp files are generated automatically. If the files are in a subdirectory of the one containing the Android.mk file, it is necessary to prefix them with the directory name

5. Configure the new module with the following:

   LOCAL_STATIC_LIBRARIES – These are the static libraries that are included in the module.

```
LOCAL_STATIC_LIBRARIES := \
libutils \
libtinyxml
```

   LOCAL_MODULE_PATH – Instructs the build system to put the module somewhere other than what is normal for its type. If you override this, ensure to set LOCAL_UNSTRIPPED_PATH if it is an executable or a shared library so the unstripped binary also has somewhere to go; otherwise, an error occurs.

B References

B.1 Related documents

Title	Number
Qualcomm Technologies, Inc.	
Hexagon Tools Installation Guide	80-VB419-25
Hexagon Development Tools Overview	80-VB419-74
USB Host Driver for Windows 2000/Windows XP User Guide	80-V4609-1
USB Host Driver Installation Instructions for Microsoft Windows	80-VP092-1
IZat Gen 8 Engine Family RF Development Test Procedures	80-VM522-2
Qualcomm Createpoint User Guide	80-NC193-2
Qualcomm Flash Image Loader (QFIL) User Guide	80-NN120-1
Hexagon LLVM C/C++ Compiler User Guide	80-VB419-89
Presentation: LLVM Compiler for Hexagon Processor Deployment Plan	80-VB419-87
Presentation: MSM8x09 Chipset Software Overview	80-NR964-2
MSM8916+WTR1605 RF Calibration Software/XTT/DLL Revision Guide	80-NK201-10
DSI Programing Guide for B-Family Android Devices	80-NA157-174
Linux Android Display Driver Porting Guide	80-NN766-1
WCN36x0 WLAN/BT/FM FTM Guide with QRCT Test Example	80-WL300-27
QDART-MFG Installer	80-NH711-3
TPP User Guide	80-NF136-1
Application Note: MSM8916 External MI2S Interface Overview	80-NL239-42
Application Note: Sensor Driver Development and Troubleshooting	80-NL239-32
Linux Camera Debugging Guide	80-NL239-33
Application Note: Widevine DRM	80-N9340-1
Subsystem Restart User Guide	80-N5609-2
Presentation: MSM8x10 Android Subsystem Restart Overview	80-NC839-21
Presentation: MSM8974 Android Subsystem Restart	80-NA157-31
Application Note: SGLTE Device Configuration	80-NJ017-11
Application Note: Segment Loading Feature	80-NL239-46
Resources	
Android Open Source Project Page	http://source.android.com/
Android Developer Resources	http://developer.android.com/index.html
Android Source Download and System Setup	http://source.android.com/source/index.html
Code Aurora Forum	https://www.codeaurora.org/

Title	Number
Installing Repo	http://source.android.com/source/downloading.html
Qualcomm ChipCode Website	https://chipcode.qti.qualcomm.com/