### MIMXRT1024-EVK

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1. Unless Otherwise Specified:

All resistors are in ohms, 1/16 Watt,0402
All capacitors are in uF,0402
All voltages are DC
All polarized capacitors are aluminum electrolytic

2. Interrupted lines coded with the same letter or letter combinations are electrically connected.

#### **Revision History**

Rev. Code	Date	Ву	Description
х	2020-03-01	Shawn Shi	X Version for PPL BOM
А	2020-03-18	Shawn Shi	A Version for pilot board production
B1	2020-07-01	Shawn Shi	Change USER_LED PINMUX from GPIO_AD_B0_05 to GPIO_AD_B1_08     Change R177 value from 0ohm to 22ohm to improve EMI.

- 3. Device type number is for reference only. The number varies with the manufacturer.
- 4. Special signal usage:
  - \_B Denotes Active-Low Signal <> or [] Denotes Vectored Signals
- 5. Interpret diagram in accordance with American National Standards Institute specifications, current revision, with the exception of logic block symbology.

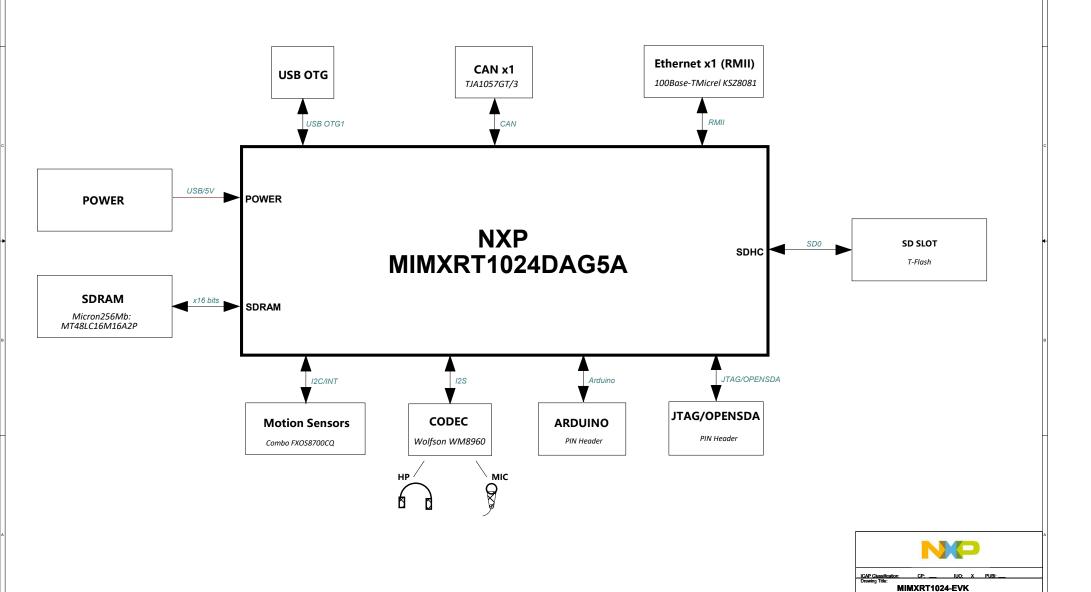
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##### Blcok Diagram Rev B1 #####

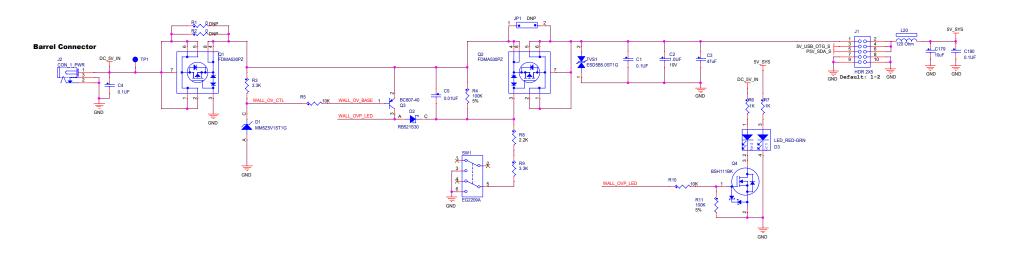
BLOCK DIAGRAM

SCH-47106, PDF: SPF-47106

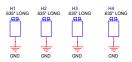
# MIMXRT1024-EVK



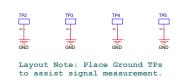
## Main Power



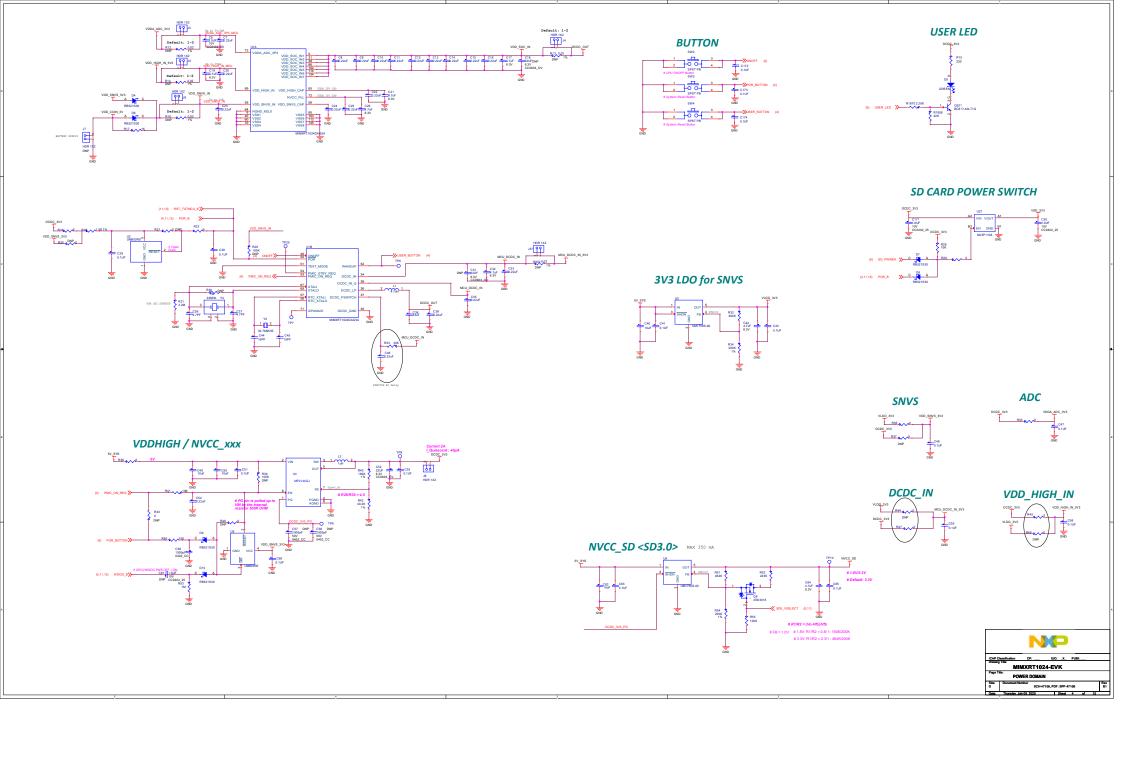
# **Board Mounting Holes**



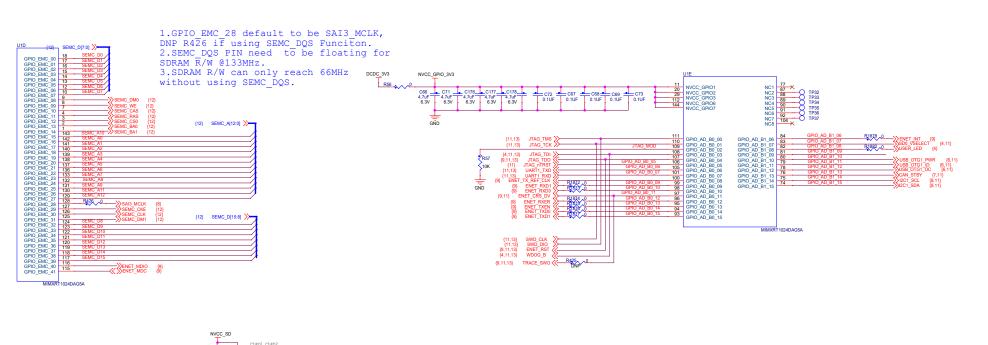
### Ground TPs

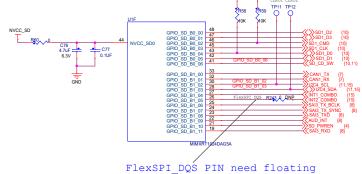






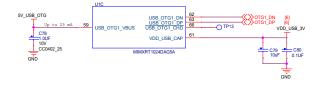
#### **MCU PINOUT**





for QSPI Flash RW @133MHz

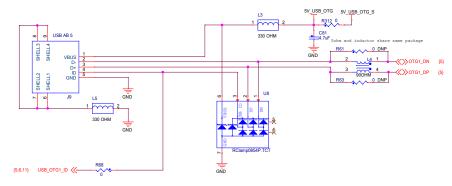




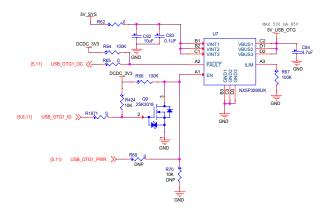




#### **USB OTG**

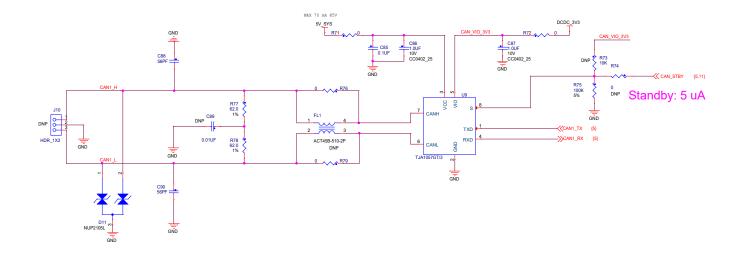


#### **USB POWER**

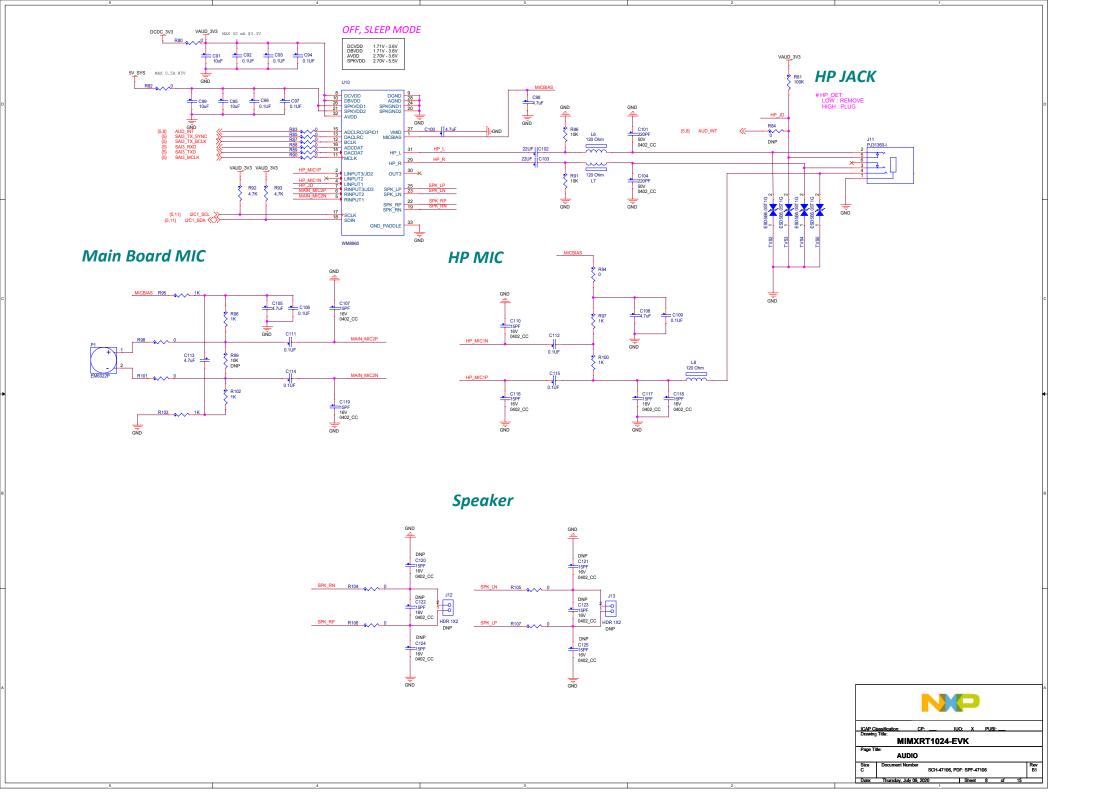


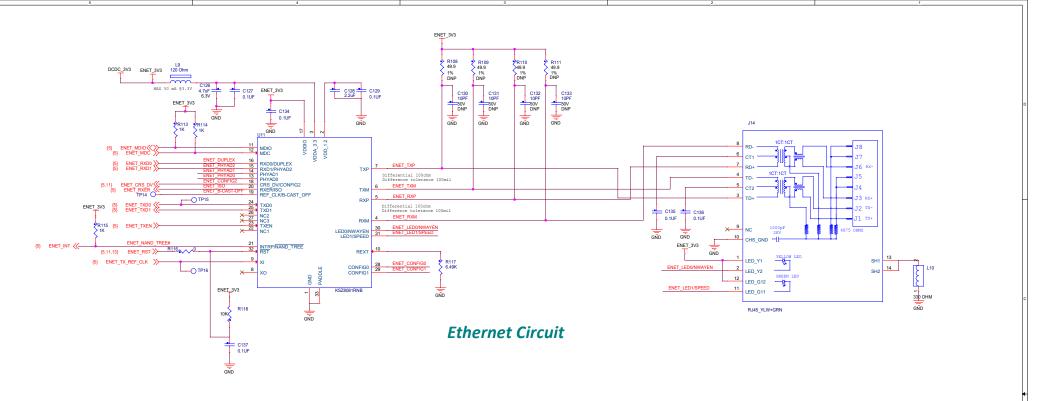


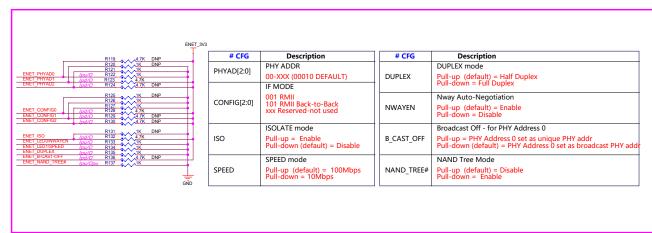
### CAN BUS



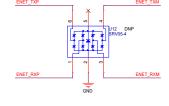




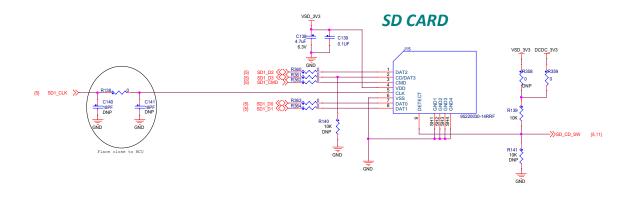






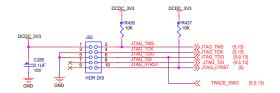






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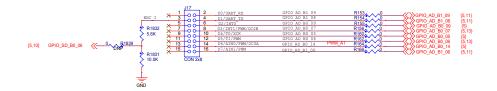
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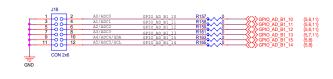


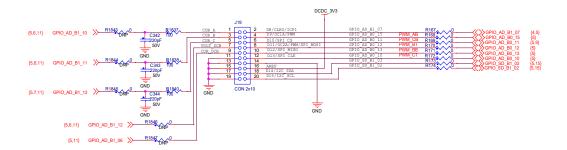
Note:to enable SWO trace function: 1. DNP R116

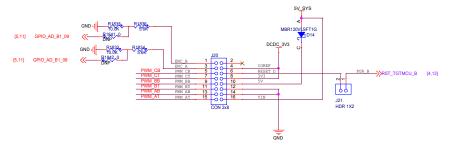
1. DNP R116 2. Populate R425

## Arduino Interface



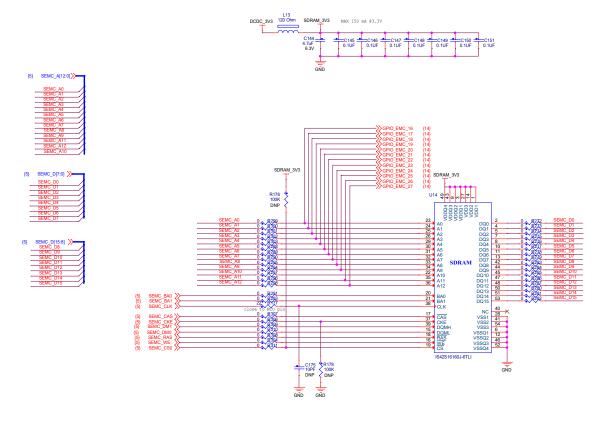






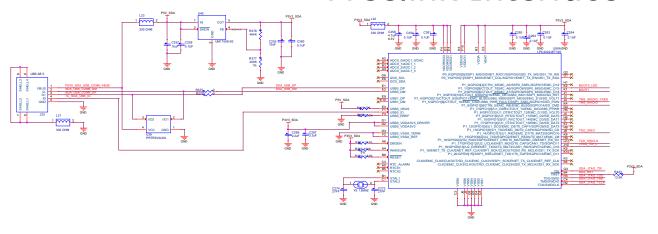
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#### **SDRAM**

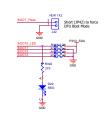


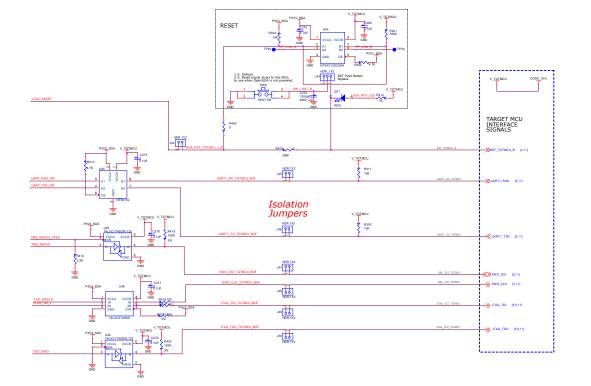


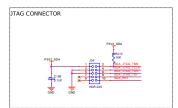
### Freelink Interface

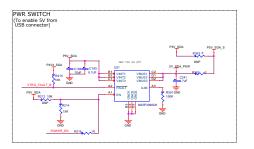








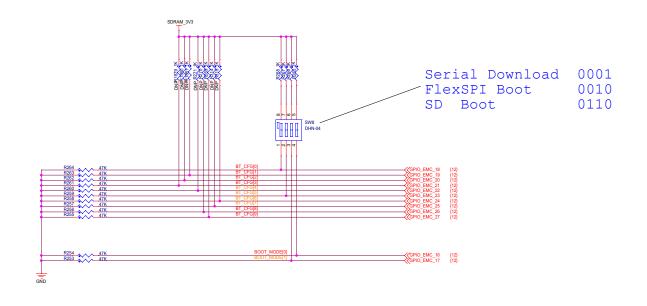






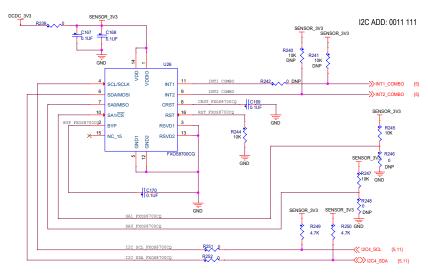
**FUSE MAP** 

FUSE IVIAP	0/1	0/1	0/1	0/1	0/1	0/1	0/1	0/1	0/1	0/1
TYPE	BOOT_CFG[9]	BOOT_CFG[8]	BOOT_CFG[7]	BOOT_CFG[6]	BOOT_CFG[5]	BOOT_CFG[4]	BOOT_CFG[3]	BOOT_CFG[2]	BOOT_CFG[1]	BOOT_CFG[0]
FlexSPI1 - Serial NOR	HOLD 00 - 5 01 - 1 10 - 3 11 - 1	00us ms ms	0	0	0	0	FLASH_TYPE:  000-Device supports 3B read by default  001-Device supports 4B read by default  010-HyperFlash 1V8  011-HyperFlash 3V3  100-MXIC Octal DDR  101 - Micron Octal DDR  111 - QSPI device supports 3B read by default  (on secondary pinmux option)			EncryptedXIP 0 - Disabled 1- Enabled
SD		250	0	0	1	Bus Width: 0 - 1-bit 1 - 4-bit	SD Power Cycle Enable: '0' - No power cycle '1' - Enabled via USDHC RST pad	SD Loopback Clock Source Sel: (for SDR50 and SDR104 only) '0' - through SD pad '1' - direct	Port Select: 0 - eSDHC1 1 - eSDHC2	Fast Boot: 0 - Regular 1 - Fast Boot



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#### **COMBO SENSOR**



FXOS8700CQ COMBO SENSOR

