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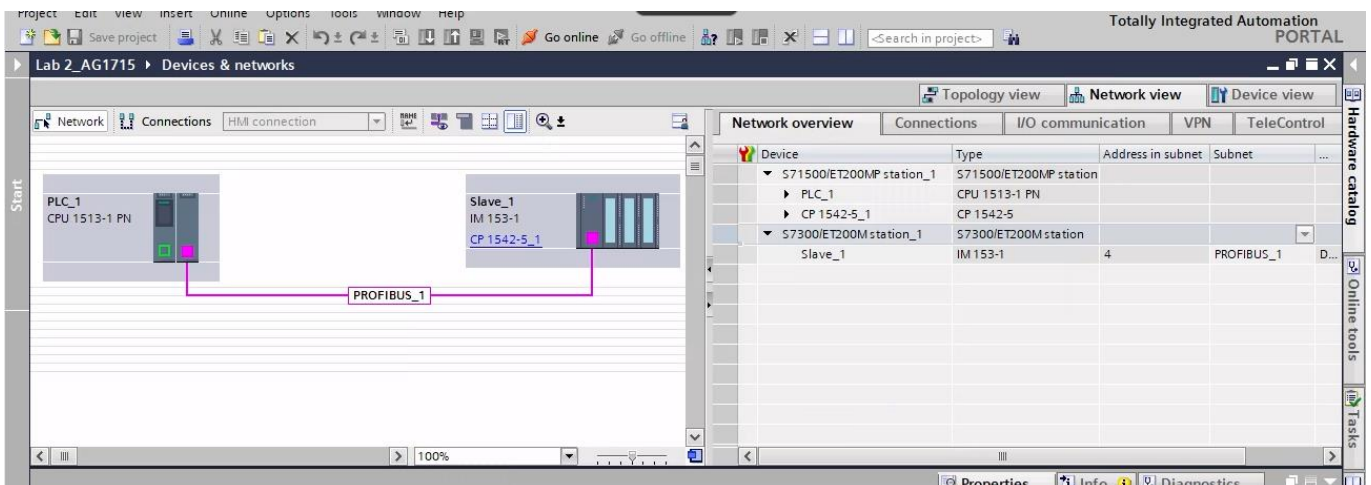
Group : TAR24S1

Change the screenshots below to match your own implementation, so that the things hidden in the boxes are readable.

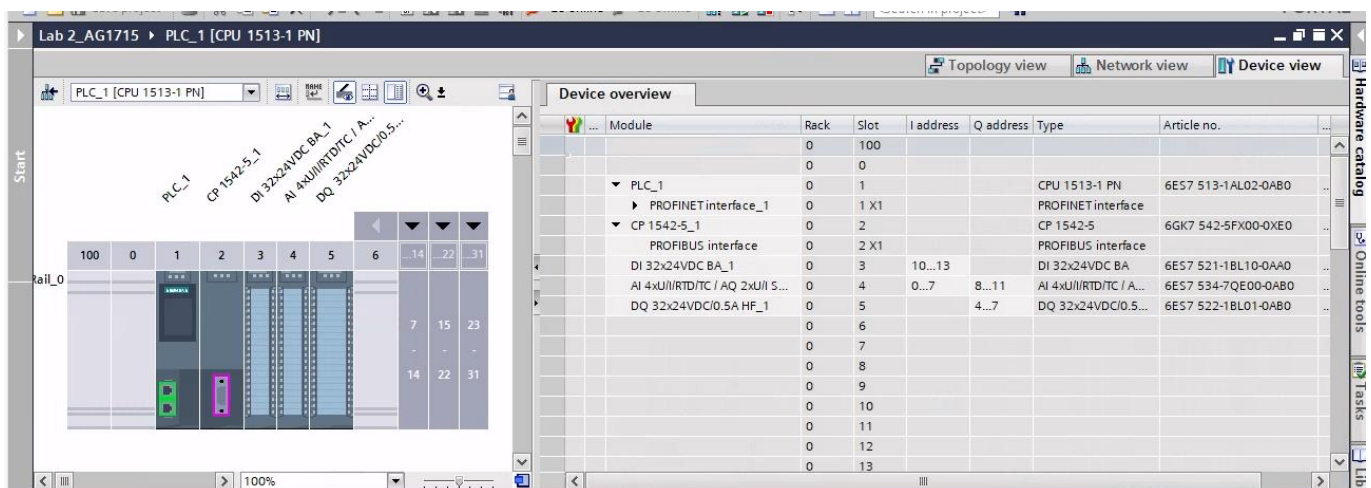
Please make sure that:

- Your Student ID is visible in screenshots (in a project name)
- Screenshot quality is good

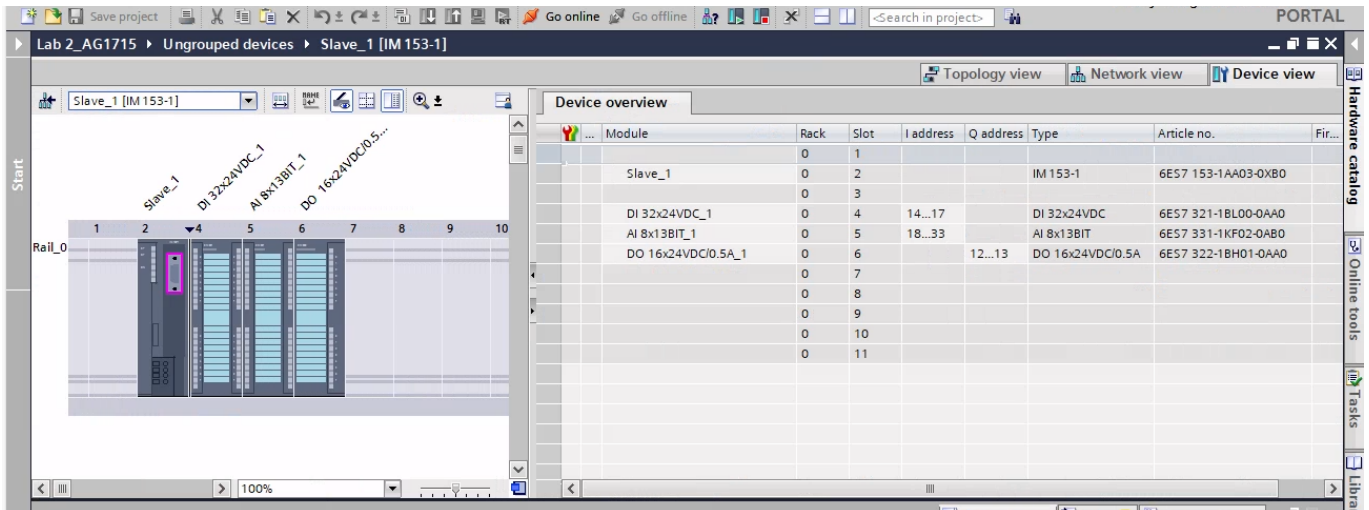
1 Devices & networks view, to display CPU and ET200 Distributed IO :



2 Device configuration of CPU to show installed cards, including "Device overview" tab with addresses :



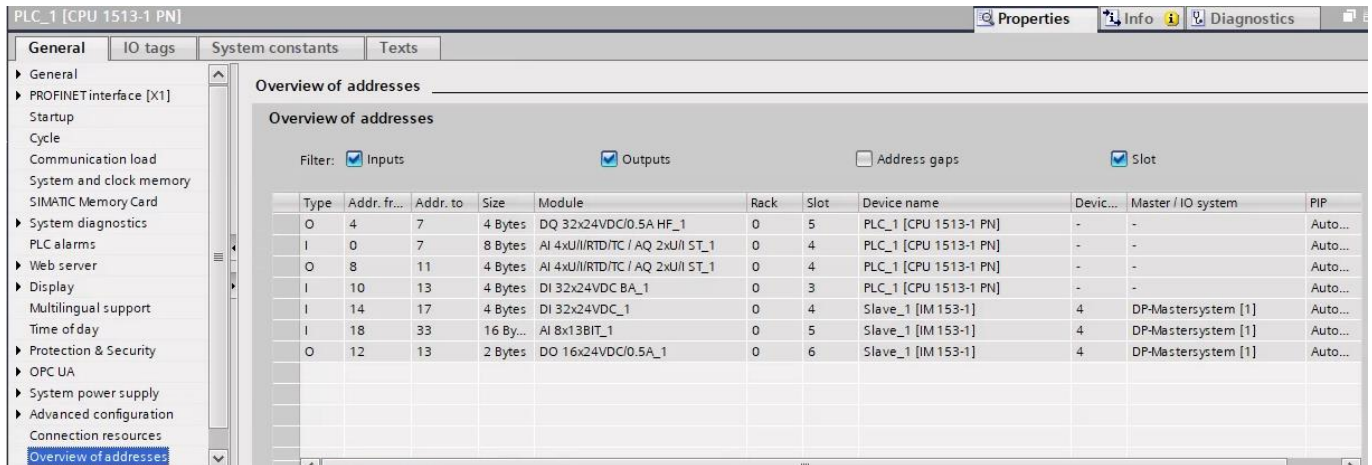
- Device configuration of **ET200 Slave_1** to show installed cards, including "Device overview" tab with addresses :



The screenshot displays the Siemens SIMATIC Manager interface. The left pane shows a rack diagram for 'Slave_1 [IM 153-1]' with modules in slots 1 through 6. The right pane shows the 'Device overview' table, which lists the modules and their addresses.

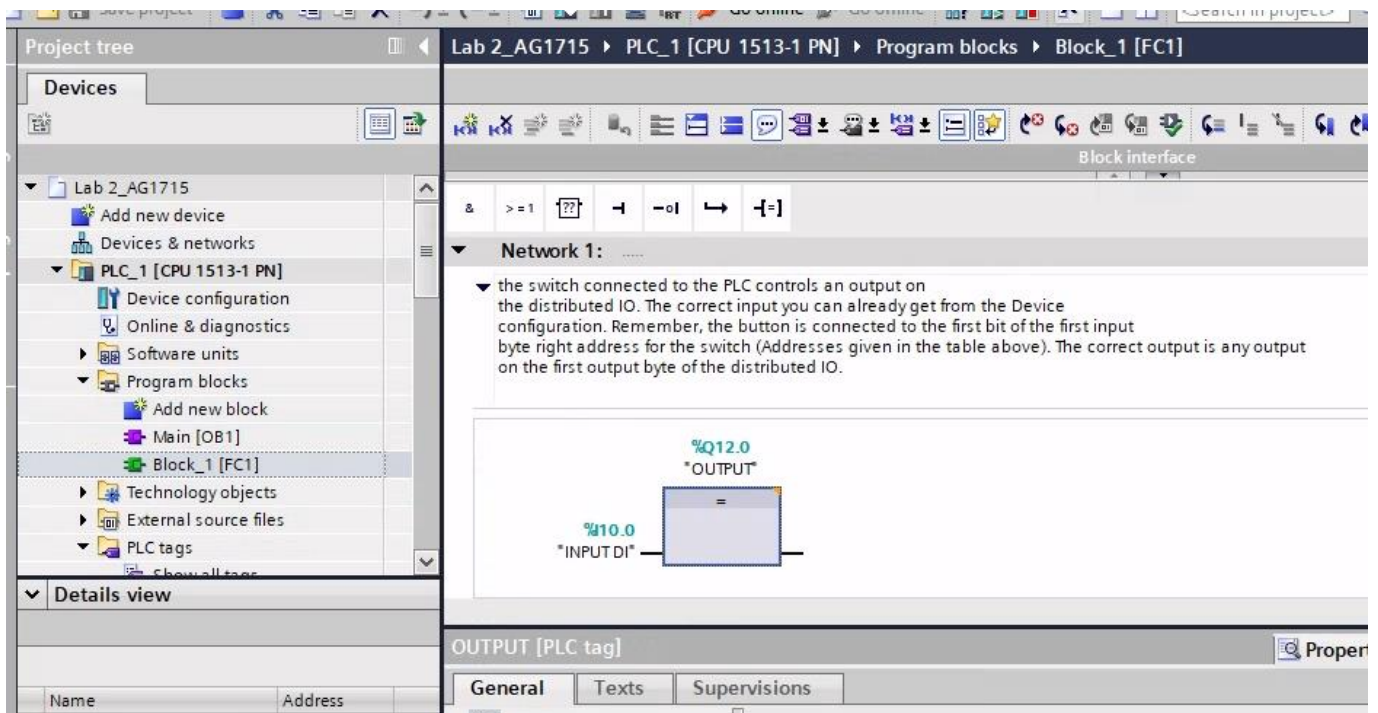
Module	Rack	Slot	I address	Q address	Type	Article no.	Fir...
Slave_1	0	1			IM 153-1	6ES7 153-1AA03-0XB0	
DI 32x24VDC_1	0	2			DI 32x24VDC	6ES7 321-1BL00-0AA0	
AI 8x13BIT_1	0	3			AI 8x13BIT	6ES7 331-7KF02-0AB0	
DO 16x24VDC/0.5A_1	0	4	14...17		DO 16x24VDC/0.5A	6ES7 322-1BH01-0AA0	
	0	5	18...33				
	0	6		12...13			
	0	7					
	0	8					
	0	9					
	0	10					
	0	11					

4 CPU Properties -> Overview of addresses :



Type	Addr. fr...	Addr. to	Size	Module	Rack	Slot	Device name	Devic...	Master / IO system	PIP
O	4	7	4 Bytes	DQ 32x24VDC/0.5A HF_1	0	5	PLC_1 [CPU 1513-1 PN]	-	-	Auto...
I	0	7	8 Bytes	AI 4xUI/RTD/TC / AQ 2xUI ST_1	0	4	PLC_1 [CPU 1513-1 PN]	-	-	Auto...
O	8	11	4 Bytes	AI 4xUI/RTD/TC / AQ 2xUI ST_1	0	4	PLC_1 [CPU 1513-1 PN]	-	-	Auto...
I	10	13	4 Bytes	DI 32x24VDC BA_1	0	3	PLC_1 [CPU 1513-1 PN]	-	-	Auto...
I	14	17	4 Bytes	DI 32x24VDC_1	0	4	Slave_1 [IM 153-1]	4	DP-Mastersystem [1]	Auto...
I	18	33	16 Bytes	AI 8x138IT_1	0	5	Slave_1 [IM 153-1]	4	DP-Mastersystem [1]	Auto...
O	12	13	2 Bytes	DO 16x24VDC/0.5A_1	0	6	Slave_1 [IM 153-1]	4	DP-Mastersystem [1]	Auto...

5 Logic program (OB/FC) showing you are using the correct Input and Output addresses :



Network 1:

the switch connected to the PLC controls an output on the distributed IO. The correct input you can already get from the Device configuration. Remember, the button is connected to the first bit of the first input byte right address for the switch (Addresses given in the table above). The correct output is any output on the first output byte of the distributed IO.

Diagram showing the connection between the input address **%I0.0** (labeled "INPUT DI") and the output address **%Q12.0** (labeled "OUTPUT").

6 Check that you have used the number corresponding to the first letter of your last name as the address of the digital input channel:

Name	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T	U	V	X	Y	Z	A	A	O
Digital IO	0	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24	25	26	27

Your last name : Koirala

Digital input card address from table above : 10