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Logical Controls Lab 2 Preliminary task return

10.03.2025

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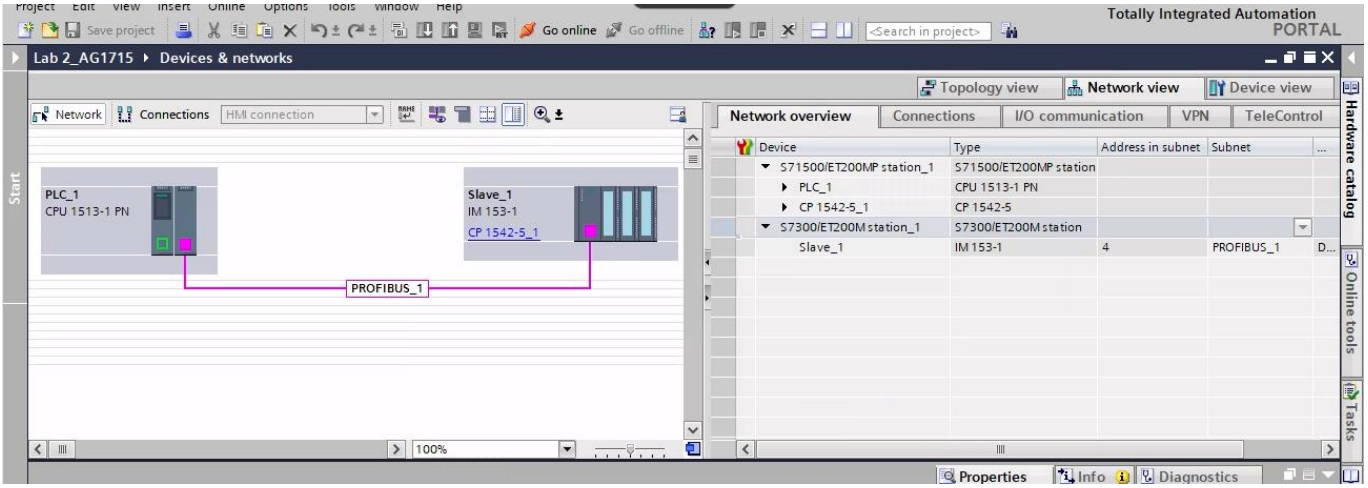
Change the screenshots below to match your own implementation, so that the things hidden in the boxes are readable.

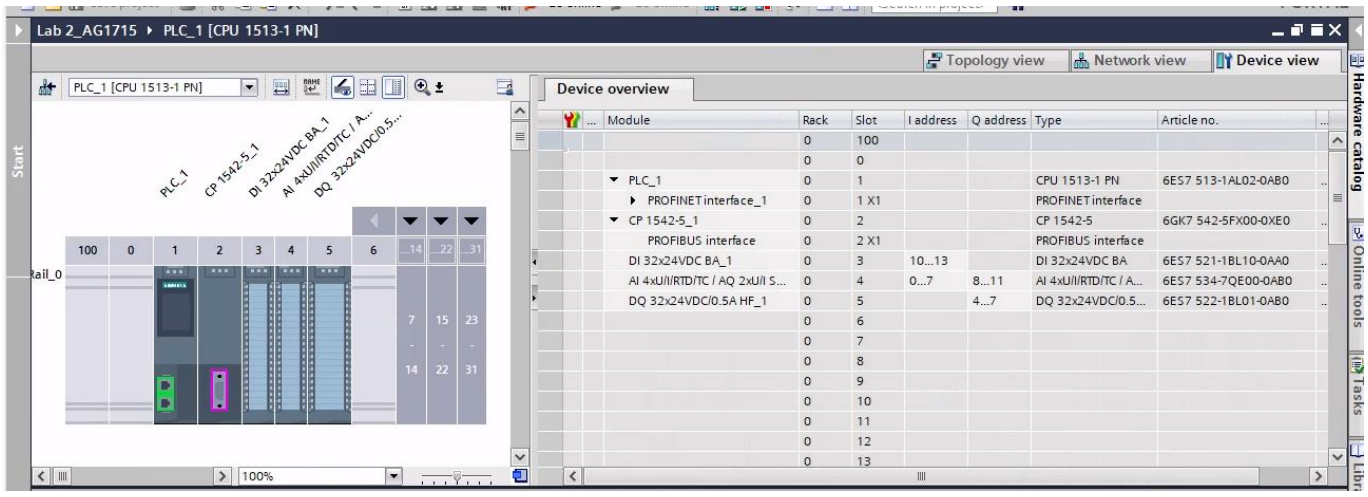
Please make sure that:

- Your Student ID is visible in screenshots (in a project name)
- Screenshot quality is good

1 Devices & networks view, to display CPU and ET200 Distributed IO :



2 Device configuration of **CPU** to show installed cards, including "Device overview" tab with addresses :



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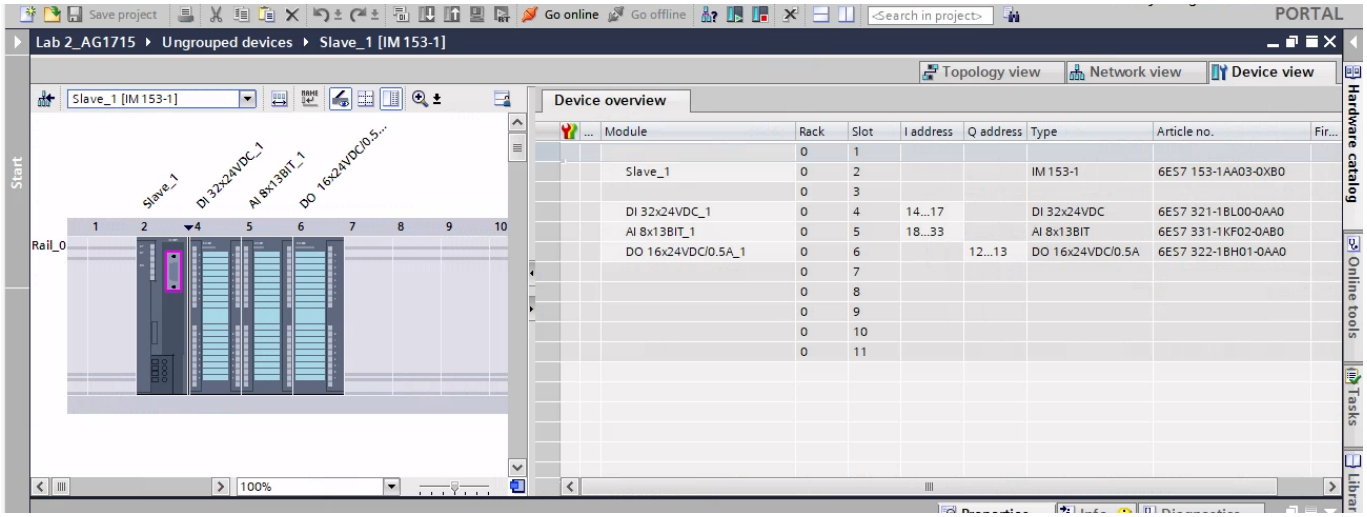
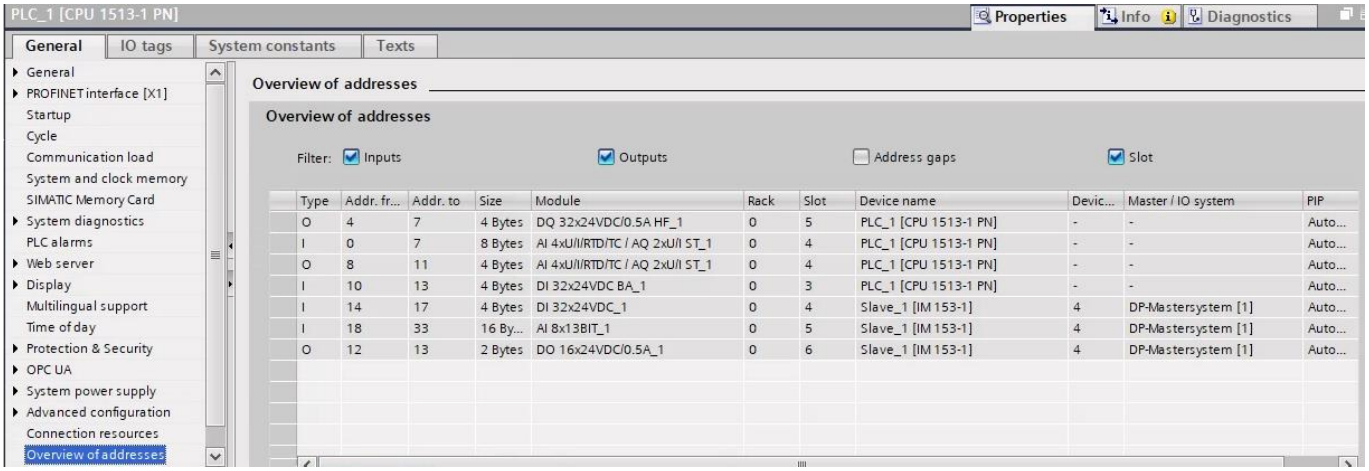
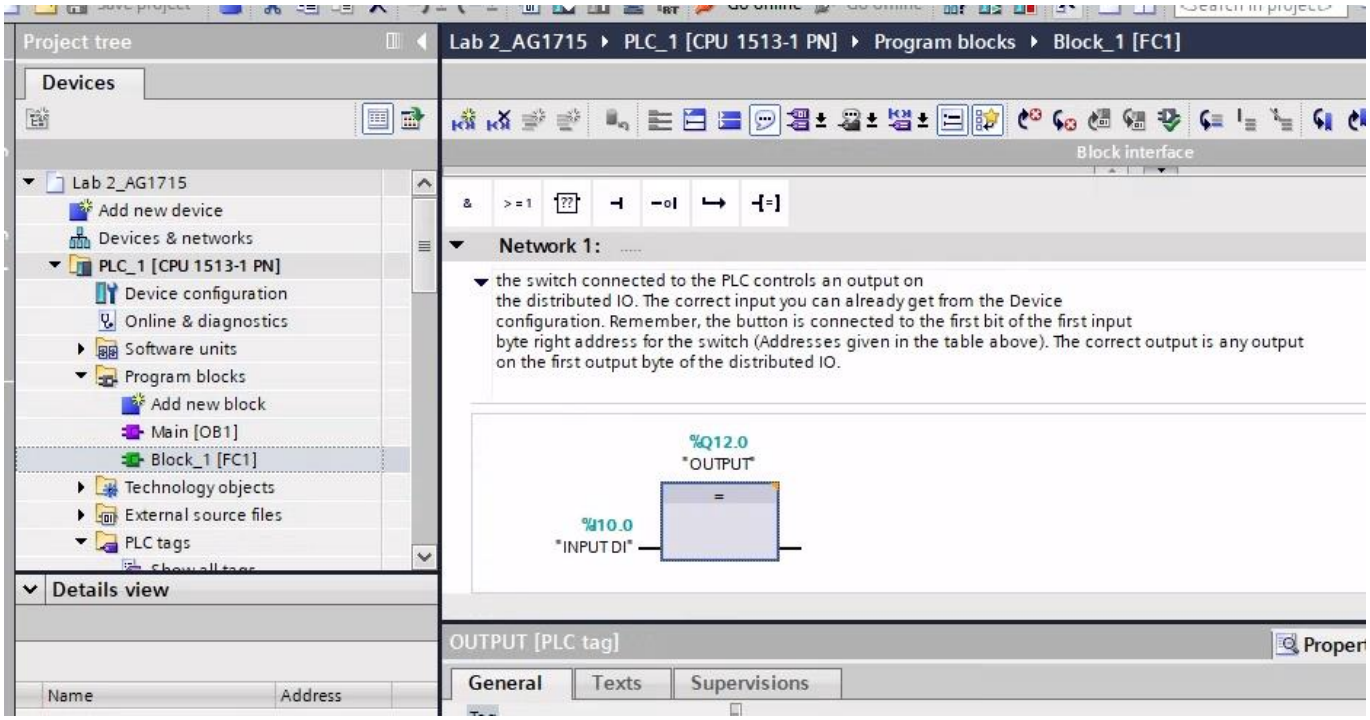
3 Device configuration of **ET200 Slave_1** to show installed cards, including "Device overview" tab with addresses :



4 CPU Properties -> Overview of addresses :



5 Logic program (OB/FC) showing you are using the correct Input and Output addresses :



6 Check that you have used the number corresponding to the first letter of your last name as the address of the digital input channel:

Name	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T	U	V	X	Y	Z	Å	Ä	Ö
Digital IO	0	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24	25	26	27

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Your last name	: Koirala
Digital input card address from table above	: 10