

## Logical Controls Lab 3 Preliminary task return

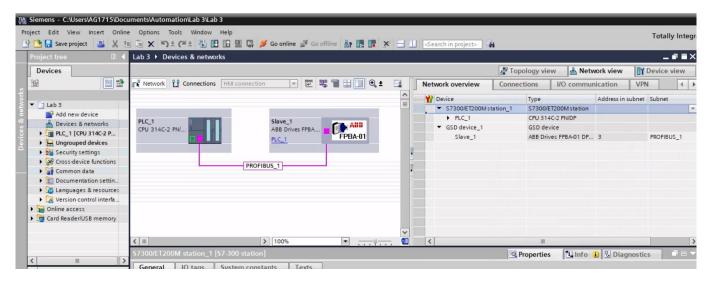
10.03.2025

Name : Student ID : Group :

Change the screenshots below to match your own implementation, so that the things hidden in the boxes are readable.

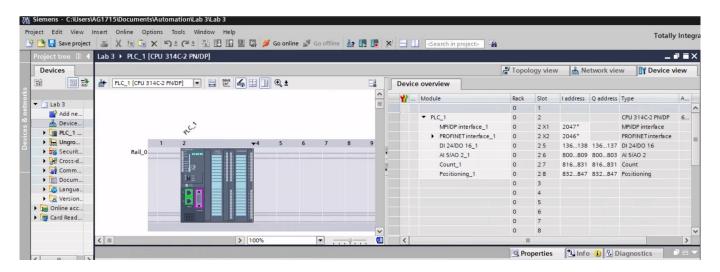
## Please make sure that:

- Your Student ID is visible in screenshots (in a project name)
- Screenshot quality is good
  - 1 Devices & networks view, to display CPU and Slave device ABB FPBA

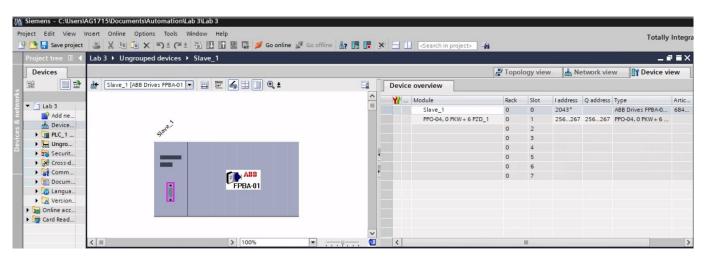




2 Device configuration of CPU including "Device overview" tab with addresses.

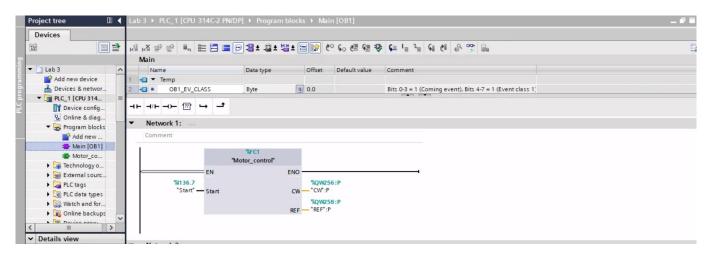


3 Device configuration of Slave\_1 (ABB) including "Device overview" tab with addresses.





4 Program OB1 with addresses



5 Motor Control function (2 Networks)

