

TECHNOLOGICAL INSTITUTE OF THE PHILIPPINES

WEEKLY LESSON ACTIVITY PLAN
(WLAP)

Grading Period : Prelim
Week : 4
No. of Hours :
Lecture : 2
Laboratory : 3

Course/Subject : CPE 132 / ELECTIVE 2 - ARCHITECTURE OF EMBEDDED SYSTEMS

Intended Learning Outcomes (ILOs)	Topics	Teaching and Learning Activities (TLAs) and Assessment Tasks (ATs)	Instructional Materials (Text, Ref. Media)	Remarks/ Suggestions for Improvement
1. Compare the different parallel ports, power supply and clock oscillator used in embedded system. 2. Evaluate the importance of the clock processor 3. Test the functionalities of each computer ports.	LECTURE 1. Parallel Ports, power supply and the clock oscillator a. Overview b. Technical Challenges c. Connections d. PIC 16F84A parallel ports e. Clock Oscillator types f. Power supply LABORATORY Experiment #2: Dancing LED	Teaching and Learning Activities (TLAs): Lecture Group discussion Assignment Assessment Tasks (ATs): Recitation Short Quiz	Reference book Power point presentation	

Prepared by: <u>ENGR. RONNIE M. DYSANGCO</u> Faculty Member's Printed Name and Signature	Noted by: <u>ENGR. MARIA CECILIA A. VENAL</u> Department Chair	Approved by: <u>DR. JESUSA N. PADILLA /</u> <u>DR. CYNTHIA C. LLANES</u> Dean/VPAA
Date:	Date:	Date: