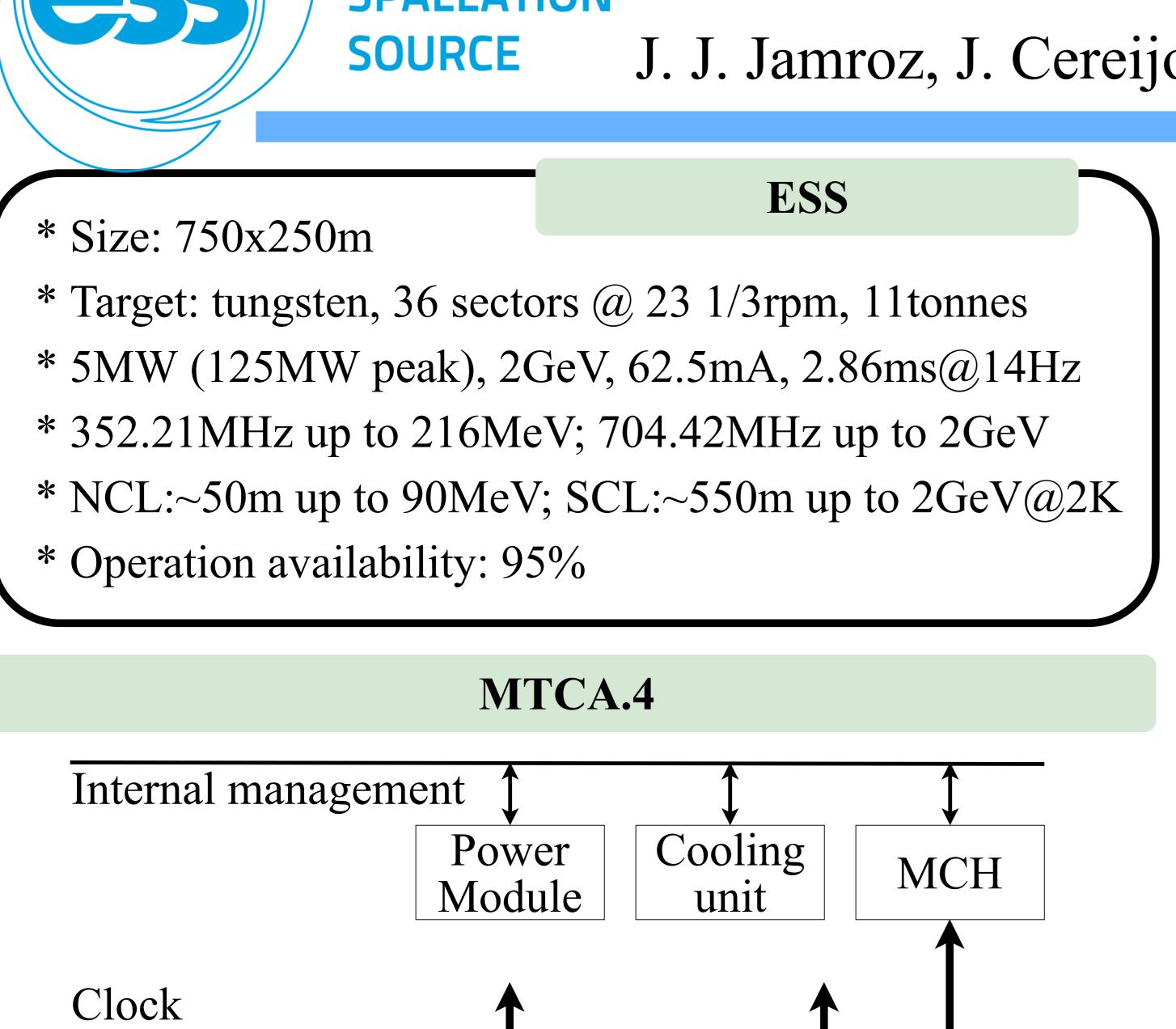
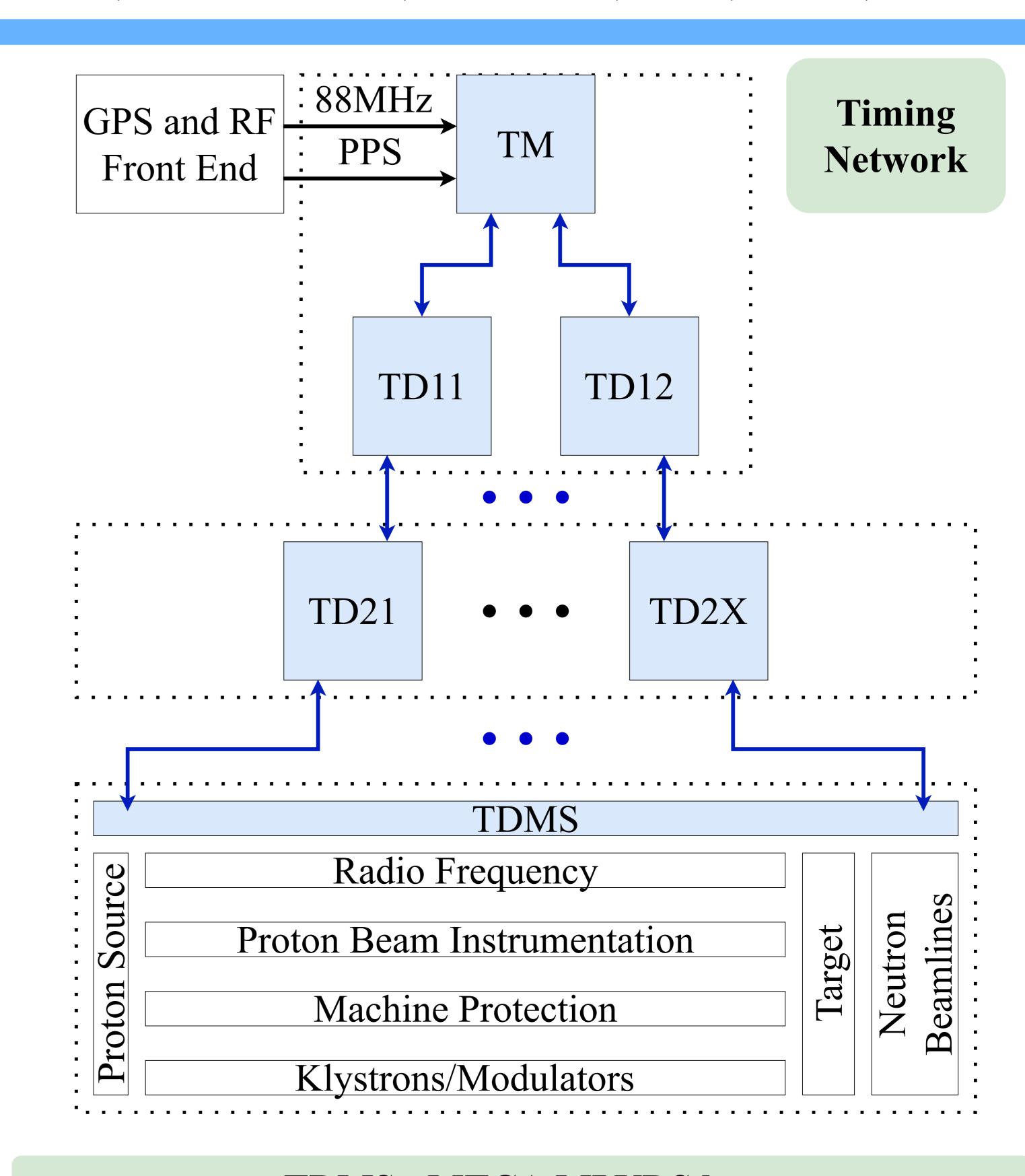


Timing System Integration with MTCA at ESS

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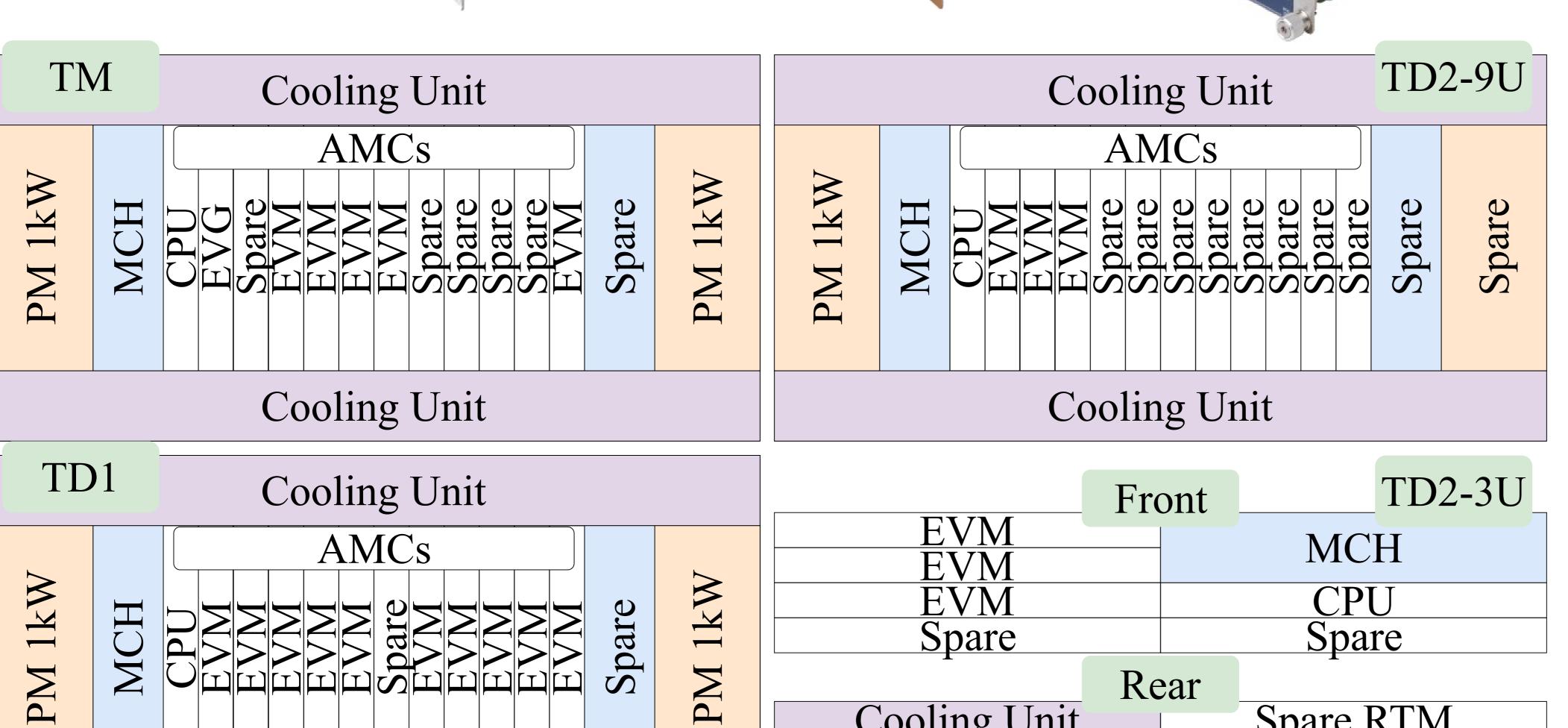
distribution Managment interface **IPMI** Base interface GbE Fabric interface PCIe/10GbE/SRIO Point to Point connections MLVDS bus: triggers, clocks and interlock AMC AMC RTM RTM Optional bus EVM/EVG EVR

TDMS - MTCA MLVDS bus Trigger I/O Line Function Frequency RF CLK 88MHz@RF OUT N/A RF CLK OUT N/A TCLKB **RX17** Trigger OUT 14HZ 14Hz@RF Trigger BPULSE CM <=14Hz(a)RFTX17 OUT **BPULSE** <=14Hz@RF**RX18** OUT Trigger 1Hz@GPS Trigger TX18 OUT PPS Trigger Asynchronous **RX19** OUT **PMORTEM** Trigger TX19 **PMORTEMI** IN Asynchronous Trigger **RX20** OUT DOD Asynchronous Trigger TX20 Asynchronous DODI IN MCH



Cooling Unit





Cooling Unit

PM 600W

N/A

Spare RTM

Spare RTM

Spare RTM

Spare RTM

Timing Data Buffer

Offset	Byte	Data Type
0	1	Protocol number
1	1	Reserved
2	1	Protocol version
3	1	Reserved
4	8	Linac cycle counter/ID
12	1	Beam on/off
13	1	Beam destination (last point
14	1	Beam mode
15	1	Reserved
16	4	Beam length: float [ms]
20	4	Beam energy: float [MeV]
24	4	Beam current: float [mA]
28	1	Raster pattern: 14 beam slot
29	1	Target segment (1-36)
30	1	Reserved
31	1	Reserved