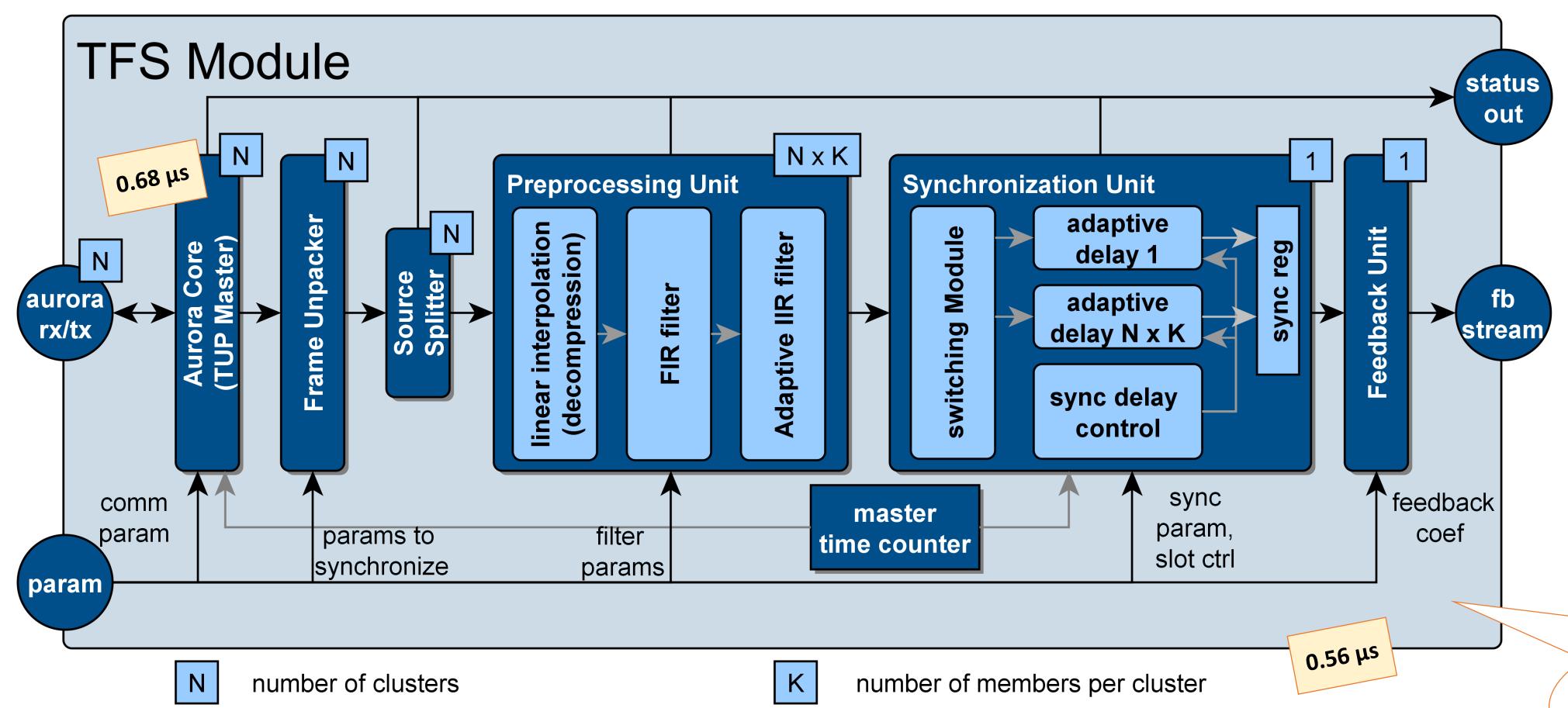
Realization of a Transverse Feedback System for SIS18/100 using FPGA TECHNISCHE

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✓ Distributed

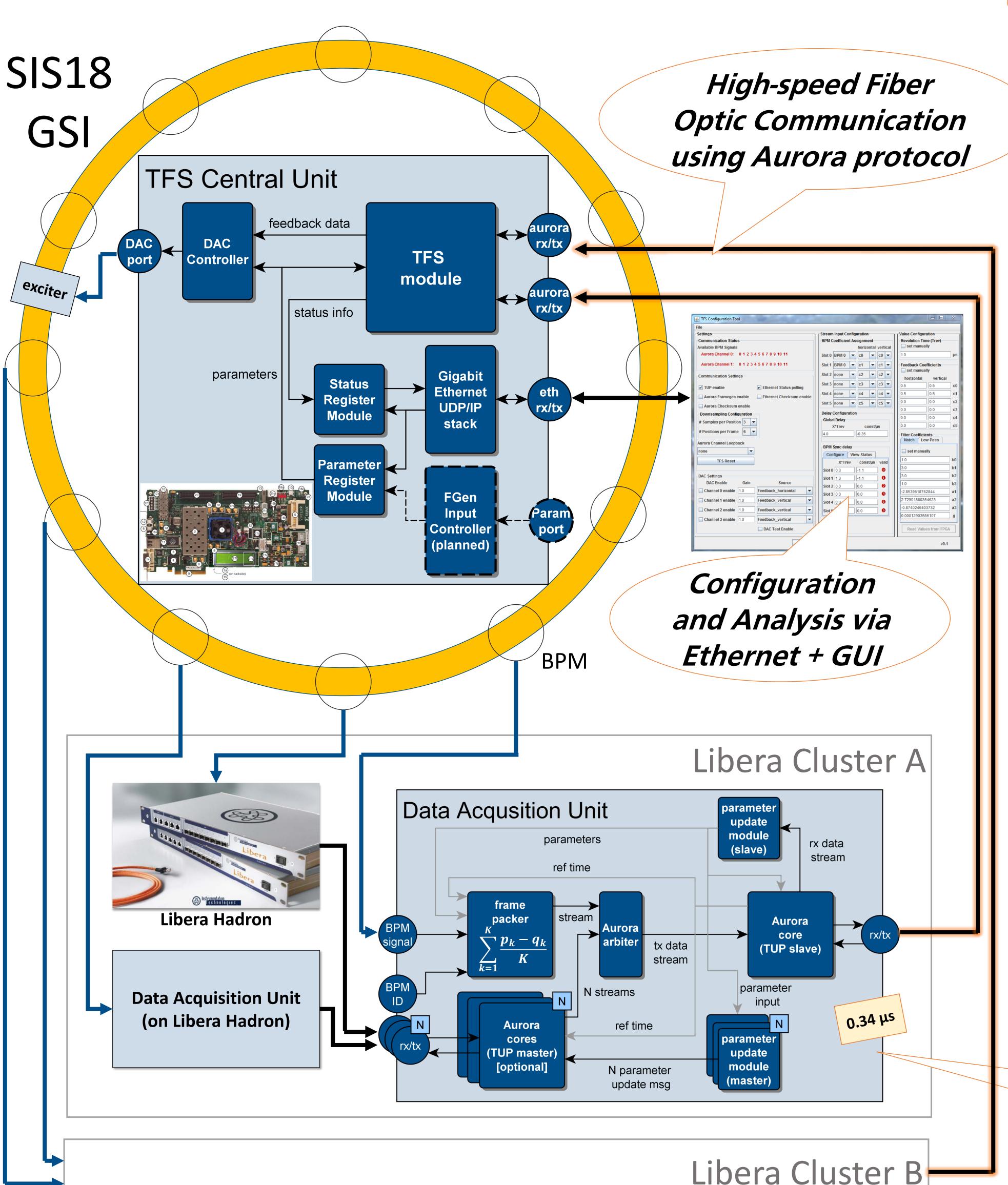
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DARMSTADT

- ✓ Scalable
- **√** Configurable
- ✓ Fast

TUP Slave

Low Latency design, adaptive and well controllable

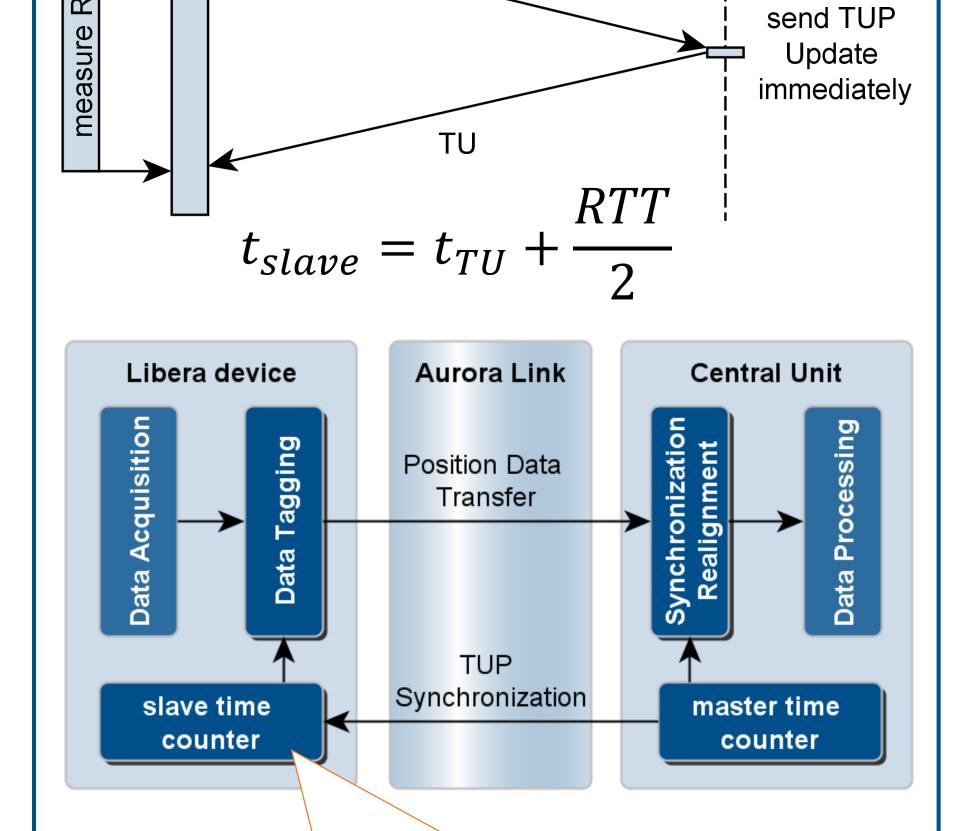


Synchronization

Constrained NTP
Synchronization
called Time Update
Protocol (TUP)

TR

TUP Master



Cycle accurate Timer Synchronization

Distributed System, Plug&Play extendable