

Figure 1: DAC05\_2\_subproblem\_1

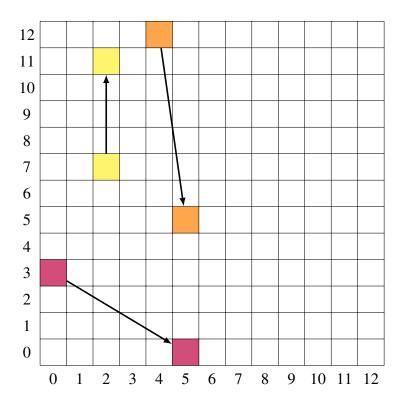


Figure 2: DAC05\_2\_subproblem\_2

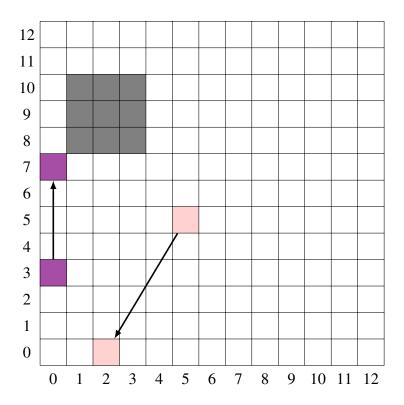


Figure 3: DAC05\_2\_subproblem\_3

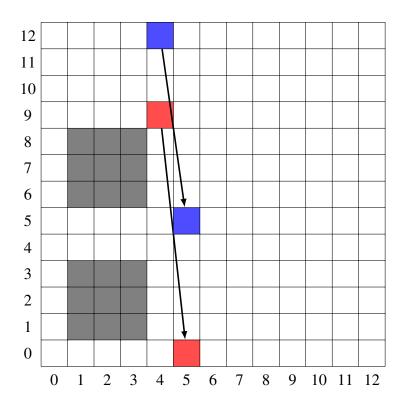


Figure 4: DAC05\_2\_subproblem\_4

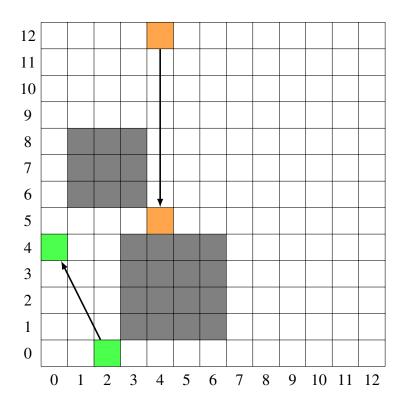


Figure 5: DAC05\_2\_subproblem\_5

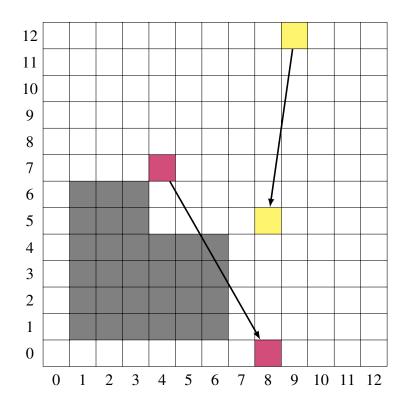


Figure 6: DAC05\_2\_subproblem\_6

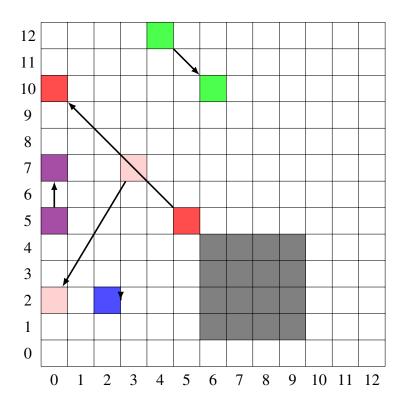


Figure 7: DAC05\_2\_subproblem\_7

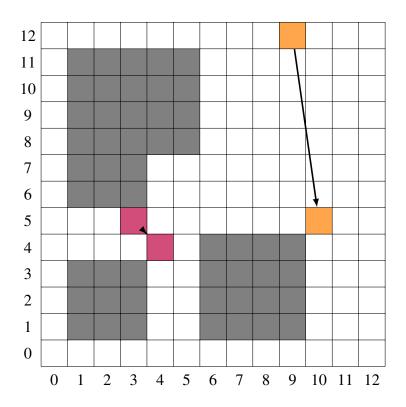


Figure 8: DAC05\_2\_subproblem\_8

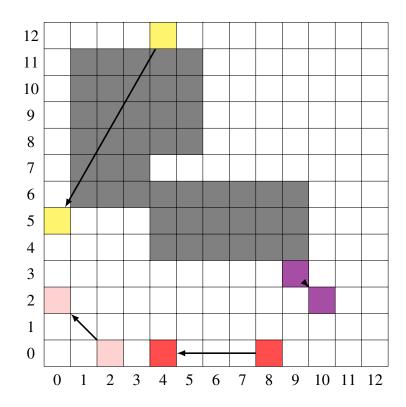


Figure 9: DAC05\_2\_subproblem\_9

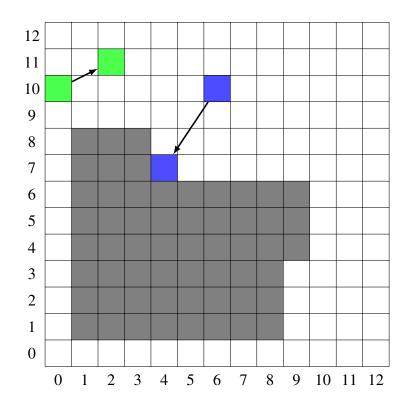


Figure 10: DAC05\_2\_subproblem\_10

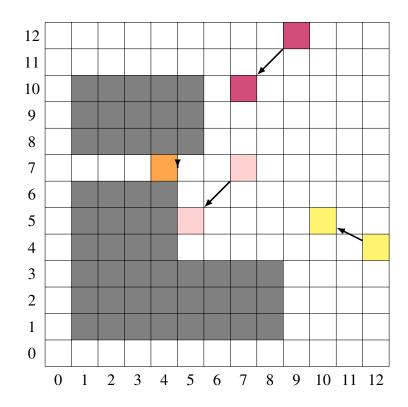


Figure 11: DAC05\_2\_subproblem\_11

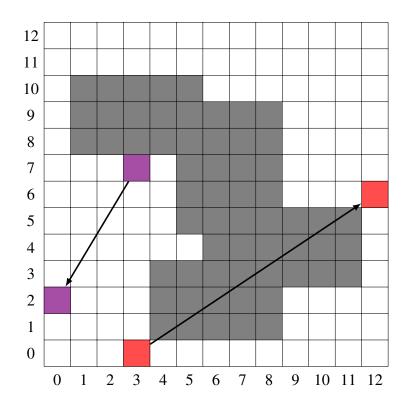


Figure 12: DAC05\_2\_subproblem\_12

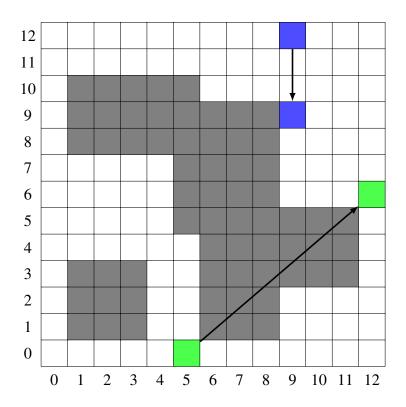


Figure 13: DAC05\_2\_subproblem\_13

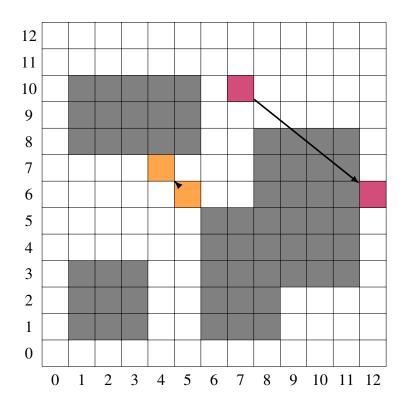


Figure 14: DAC05\_2\_subproblem\_14

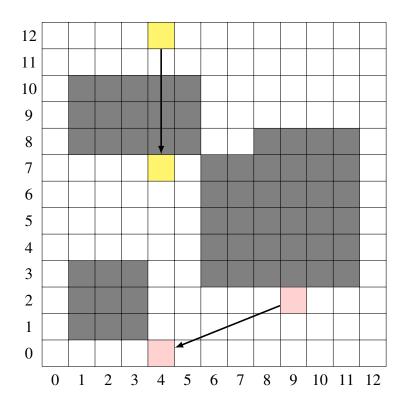


Figure 15: DAC05\_2\_subproblem\_15

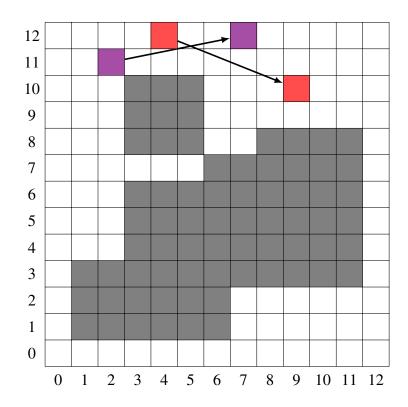


Figure 16: DAC05\_2\_subproblem\_16

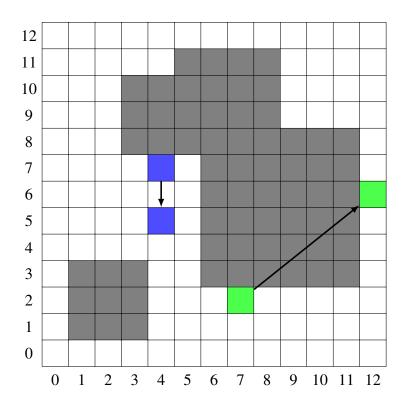


Figure 17: DAC05\_2\_subproblem\_17

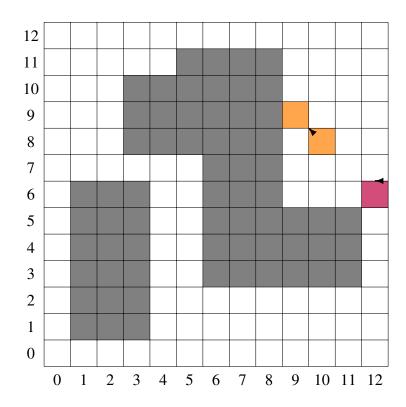


Figure 18: DAC05\_2\_subproblem\_18

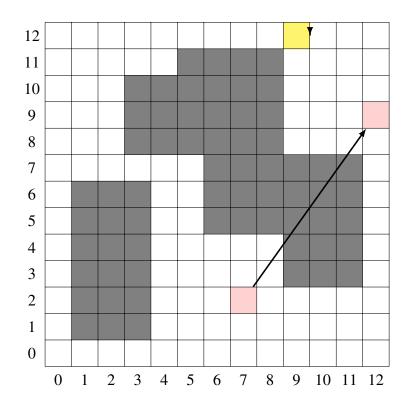


Figure 19: DAC05\_2\_subproblem\_19

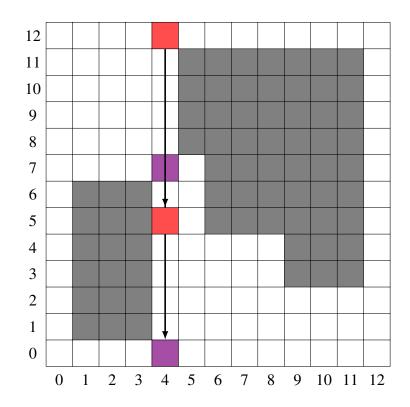


Figure 20: DAC05\_2\_subproblem\_20

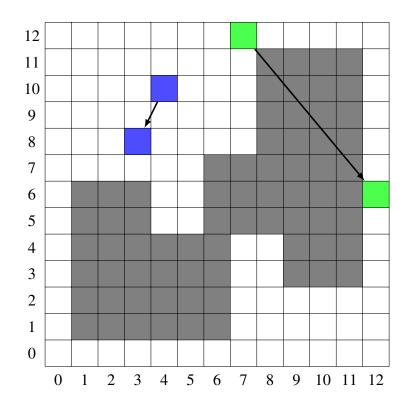


Figure 21: DAC05\_2\_subproblem\_21

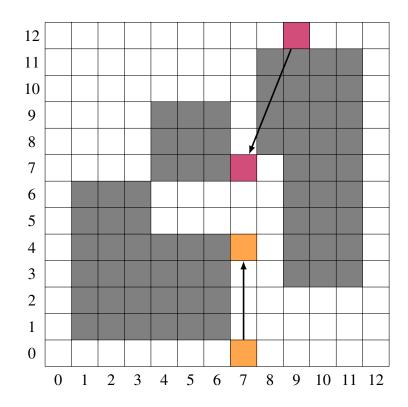


Figure 22: DAC05\_2\_subproblem\_22

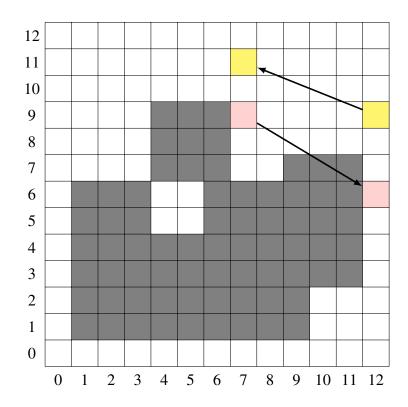


Figure 23: DAC05\_2\_subproblem\_23

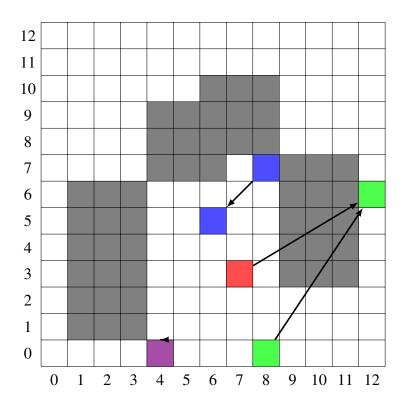


Figure 24: DAC05\_2\_subproblem\_24

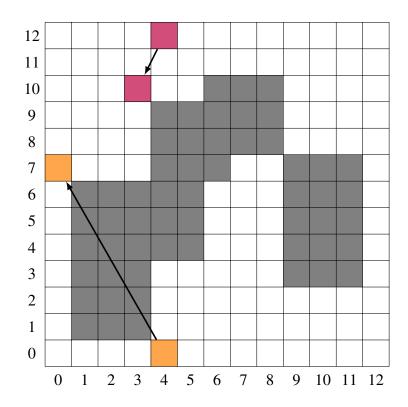


Figure 25: DAC05\_2\_subproblem\_25

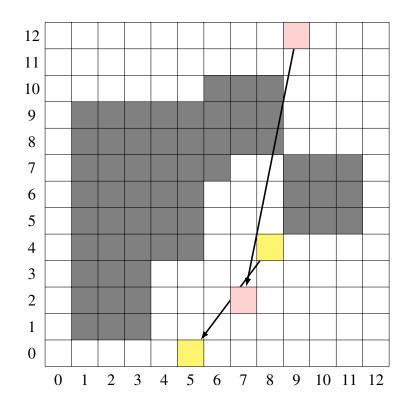


Figure 26: DAC05\_2\_subproblem\_26

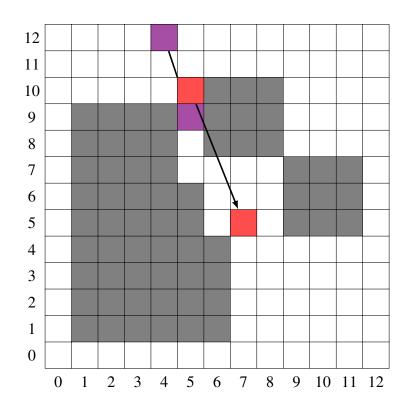


Figure 27: DAC05\_2\_subproblem\_27

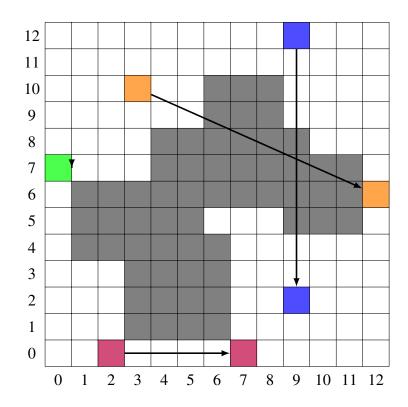


Figure 28: DAC05\_2\_subproblem\_28

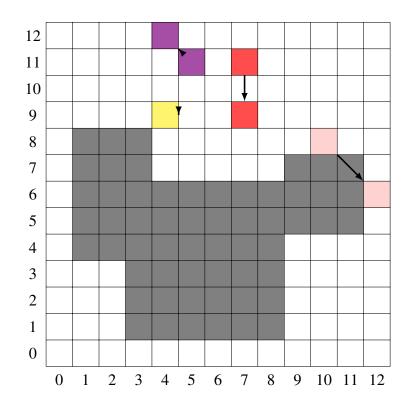


Figure 29: DAC05\_2\_subproblem\_29

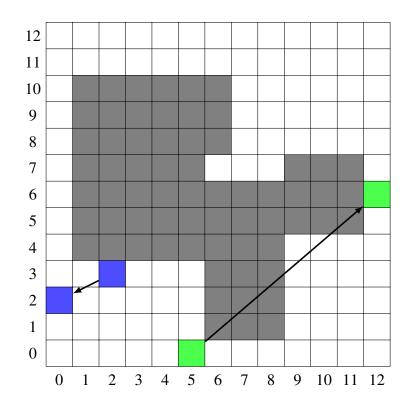


Figure 30: DAC05\_2\_subproblem\_30

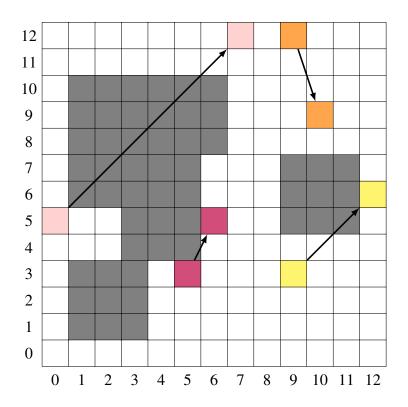


Figure 31: DAC05\_2\_subproblem\_31

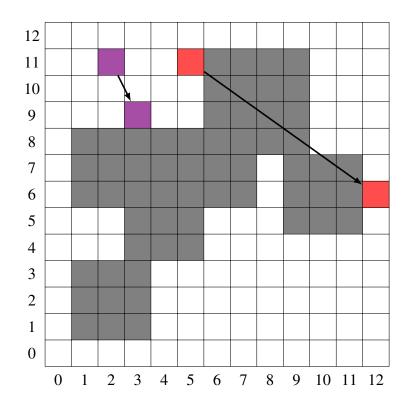


Figure 32: DAC05\_2\_subproblem\_32

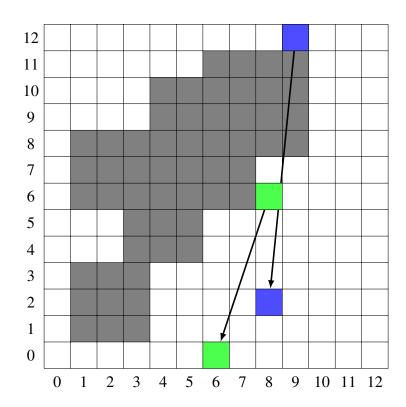


Figure 33: DAC05\_2\_subproblem\_33

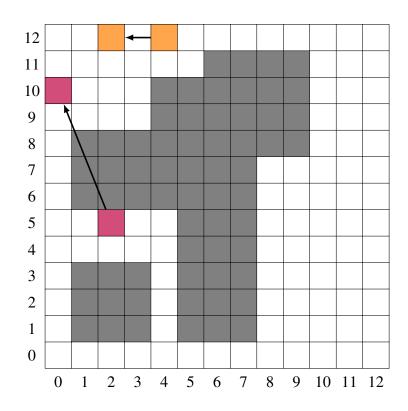


Figure 34: DAC05\_2\_subproblem\_34

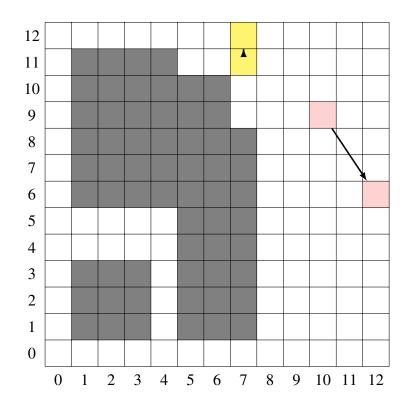


Figure 35: DAC05\_2\_subproblem\_35

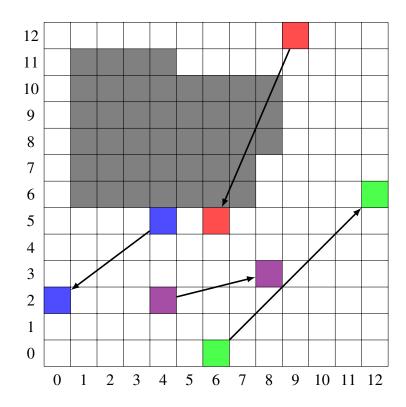


Figure 36: DAC05\_2\_subproblem\_36

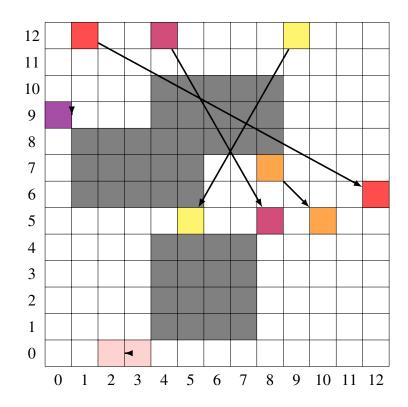


Figure 37: DAC05\_2\_subproblem\_37

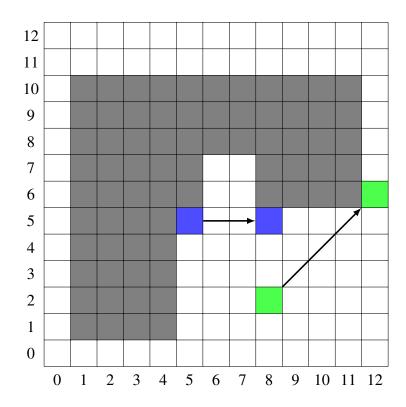


Figure 38: DAC05\_2\_subproblem\_38

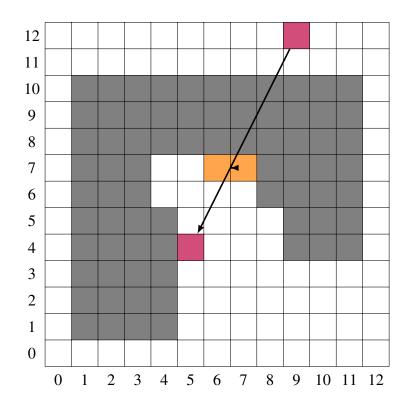


Figure 39: DAC05\_2\_subproblem\_39

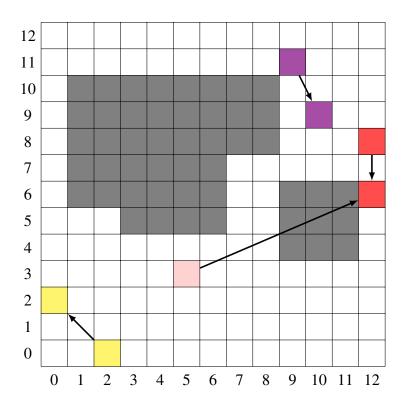


Figure 40: DAC05\_2\_subproblem\_40

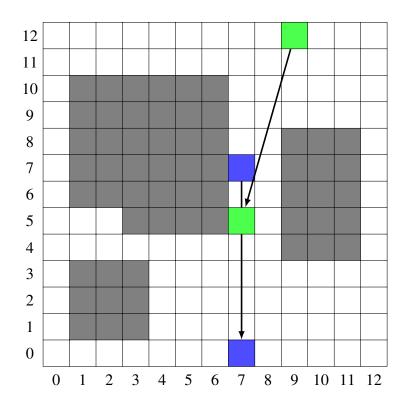


Figure 41: DAC05\_2\_subproblem\_41

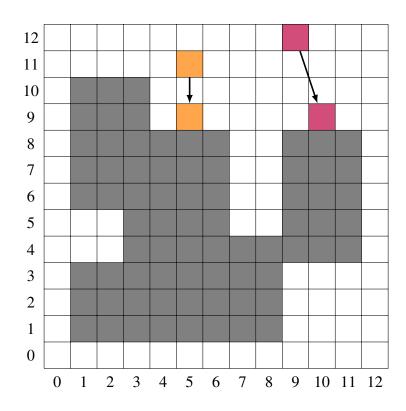


Figure 42: DAC05\_2\_subproblem\_42

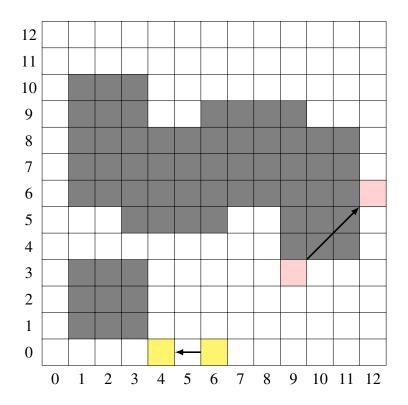


Figure 43: DAC05\_2\_subproblem\_43

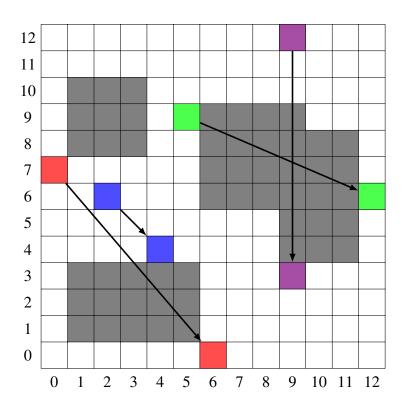


Figure 44: DAC05\_2\_subproblem\_44

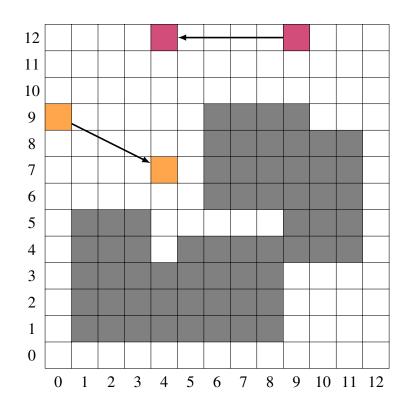


Figure 45: DAC05\_2\_subproblem\_45

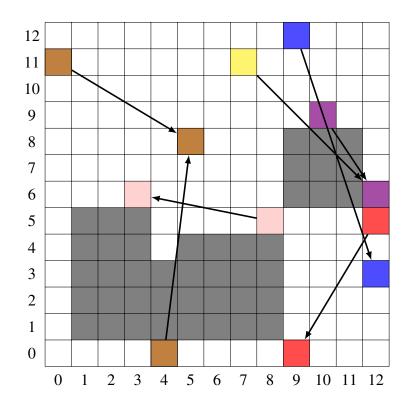


Figure 46: DAC05\_2\_subproblem\_46

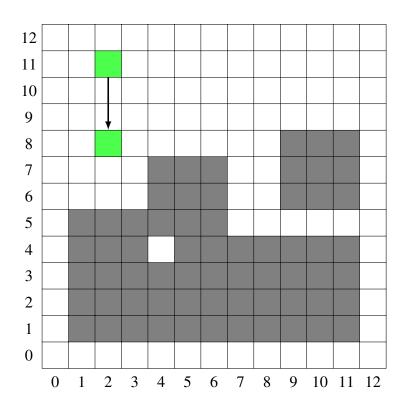


Figure 47: DAC05\_2\_subproblem\_47

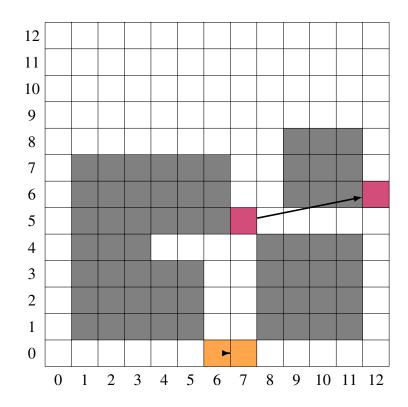


Figure 48: DAC05\_2\_subproblem\_48

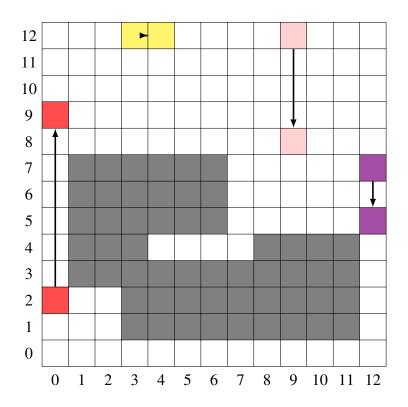


Figure 49: DAC05\_2\_subproblem\_49

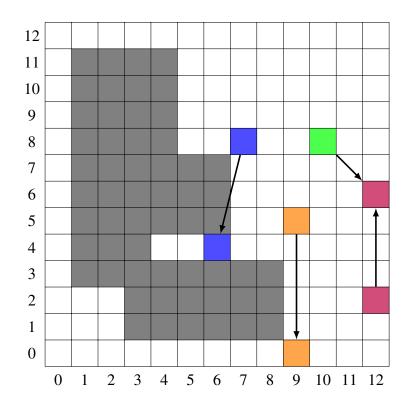


Figure 50: DAC05\_2\_subproblem\_50

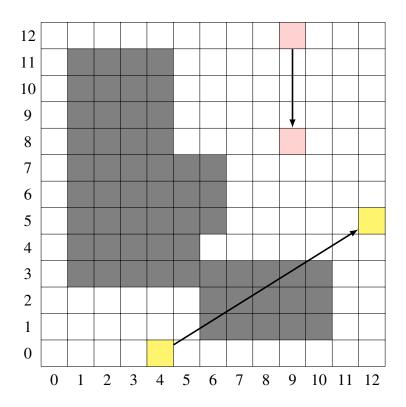


Figure 51: DAC05\_2\_subproblem\_51

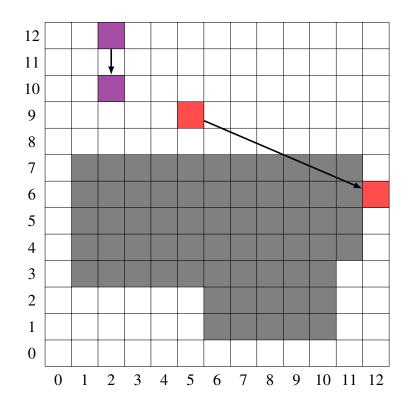


Figure 52: DAC05\_2\_subproblem\_52

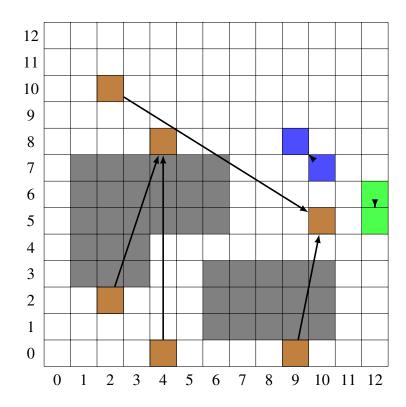


Figure 53: DAC05\_2\_subproblem\_53

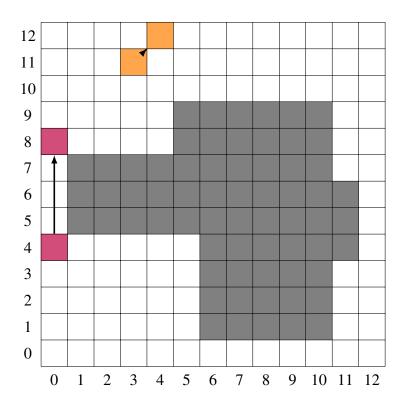


Figure 54: DAC05\_2\_subproblem\_54

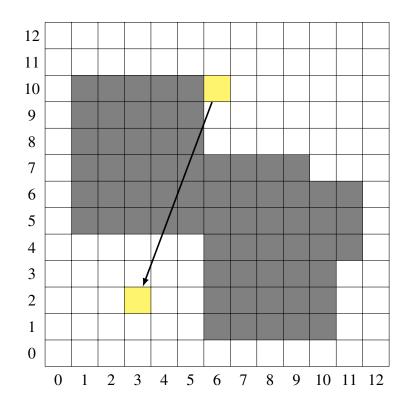


Figure 55: DAC05\_2\_subproblem\_55

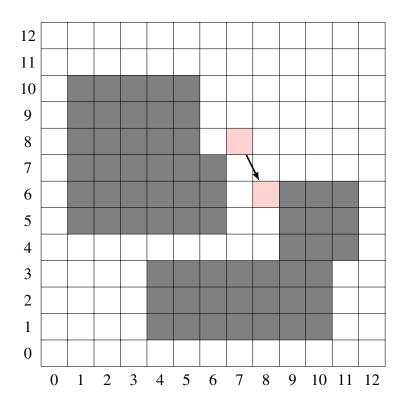


Figure 56: DAC05\_2\_subproblem\_56

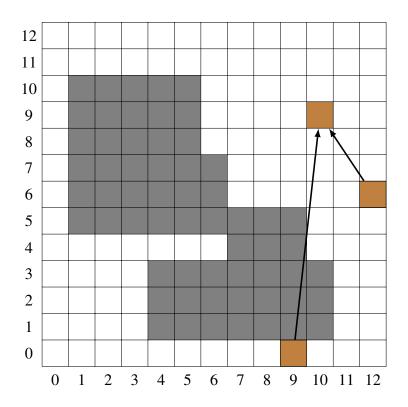


Figure 57: DAC05\_2\_subproblem\_57

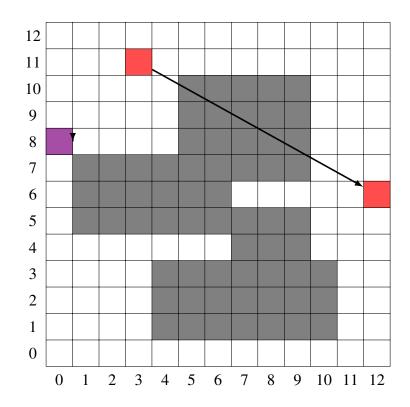


Figure 58: DAC05\_2\_subproblem\_58

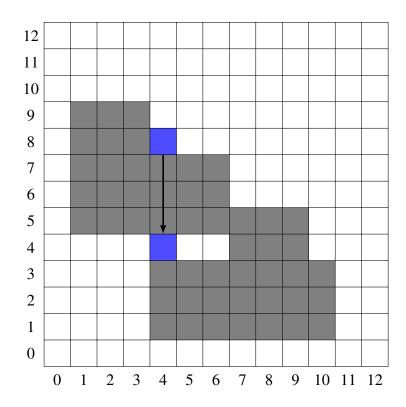


Figure 59: DAC05\_2\_subproblem\_59

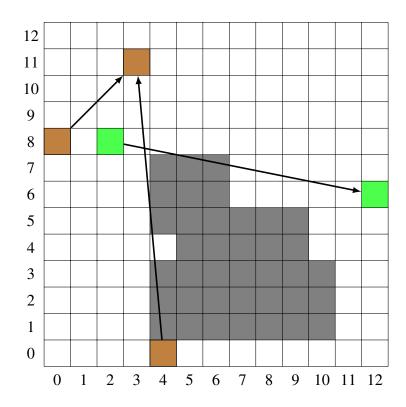


Figure 60: DAC05\_2\_subproblem\_60

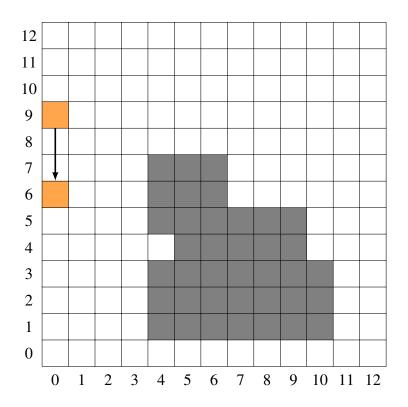


Figure 61: DAC05\_2\_subproblem\_61

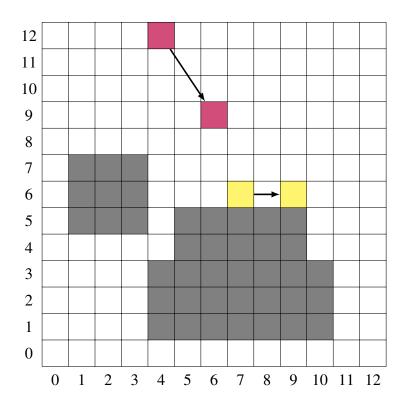


Figure 62: DAC05\_2\_subproblem\_62

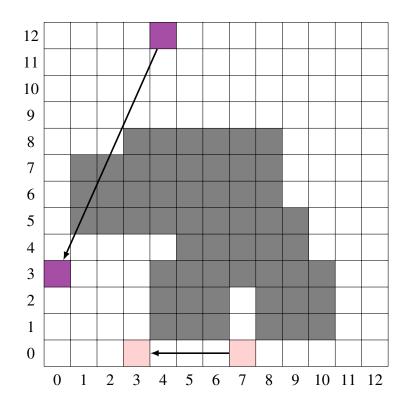


Figure 63: DAC05\_2\_subproblem\_63

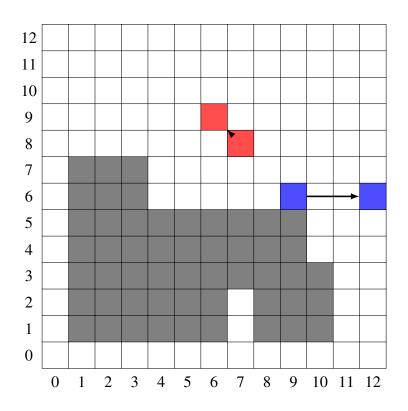


Figure 64: DAC05\_2\_subproblem\_64

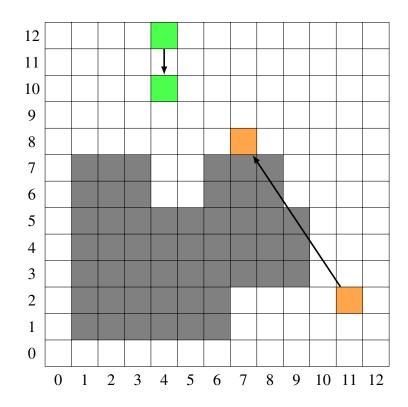


Figure 65: DAC05\_2\_subproblem\_65

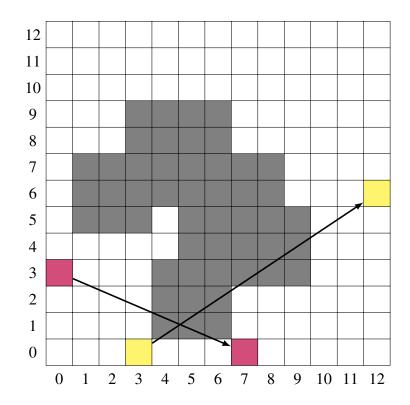


Figure 66: DAC05\_2\_subproblem\_66

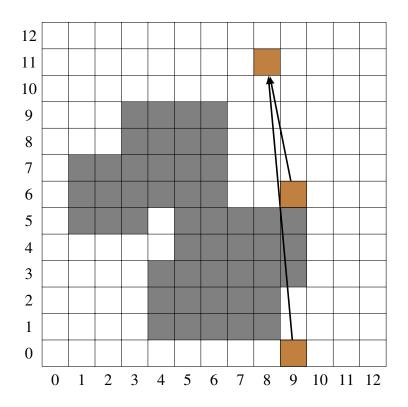


Figure 67: DAC05\_2\_subproblem\_67

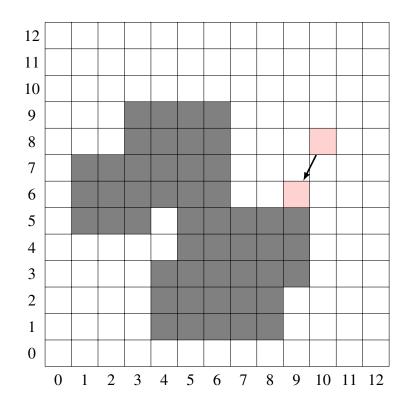


Figure 68: DAC05\_2\_subproblem\_68

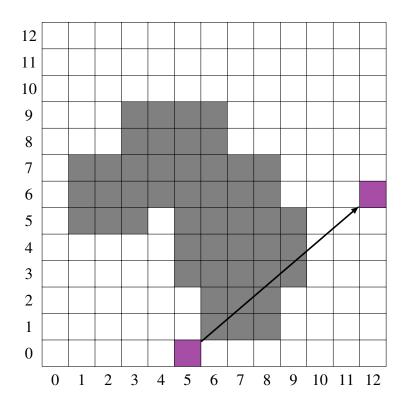


Figure 69: DAC05\_2\_subproblem\_69

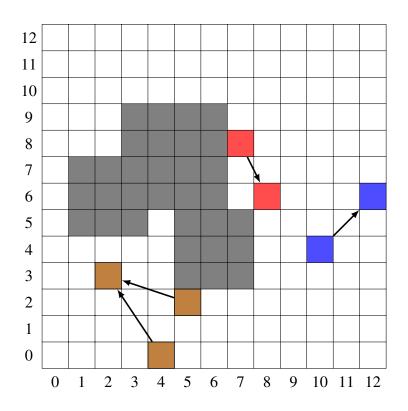


Figure 70: DAC05\_2\_subproblem\_70

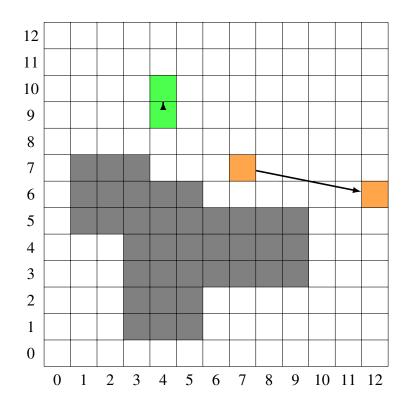


Figure 71: DAC05\_2\_subproblem\_71

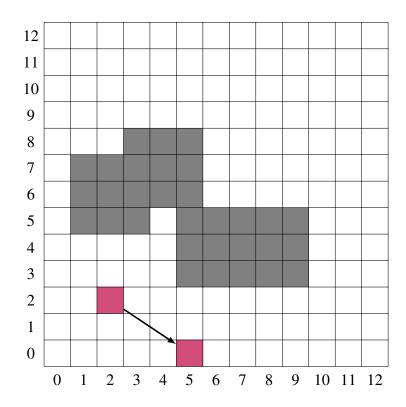


Figure 72: DAC05\_2\_subproblem\_72

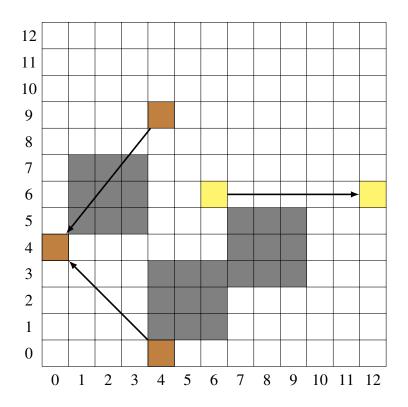


Figure 73: DAC05\_2\_subproblem\_73

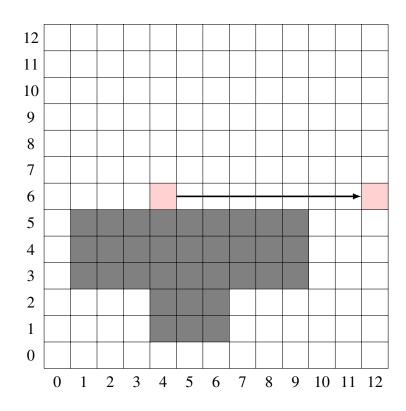


Figure 74: DAC05\_2\_subproblem\_74

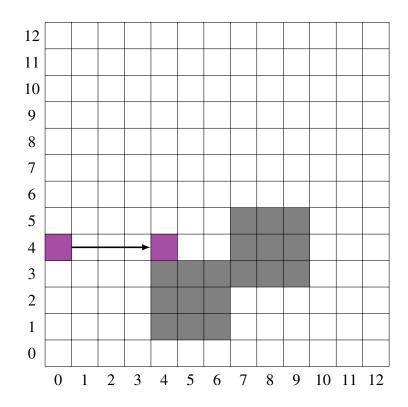


Figure 75: DAC05\_2\_subproblem\_75

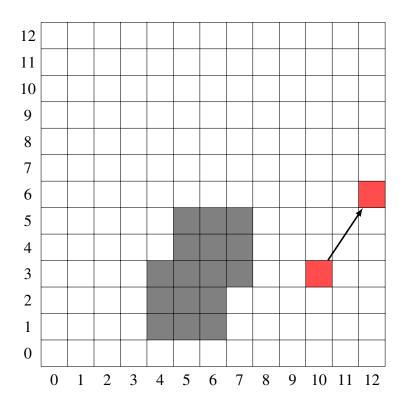


Figure 76: DAC05\_2\_subproblem\_76

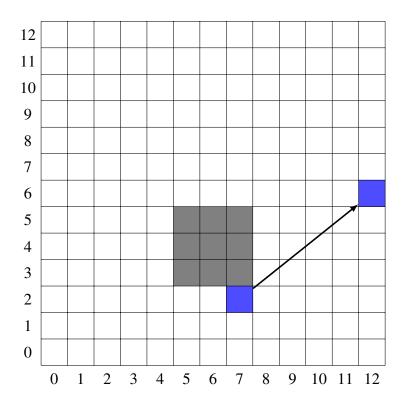


Figure 77: DAC05\_2\_subproblem\_77

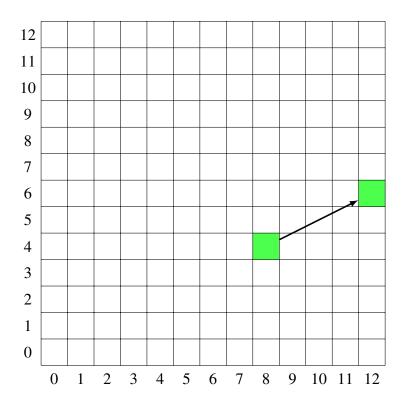


Figure 78: DAC05\_2\_subproblem\_78