

Figure 1: DAC05_subproblem_1

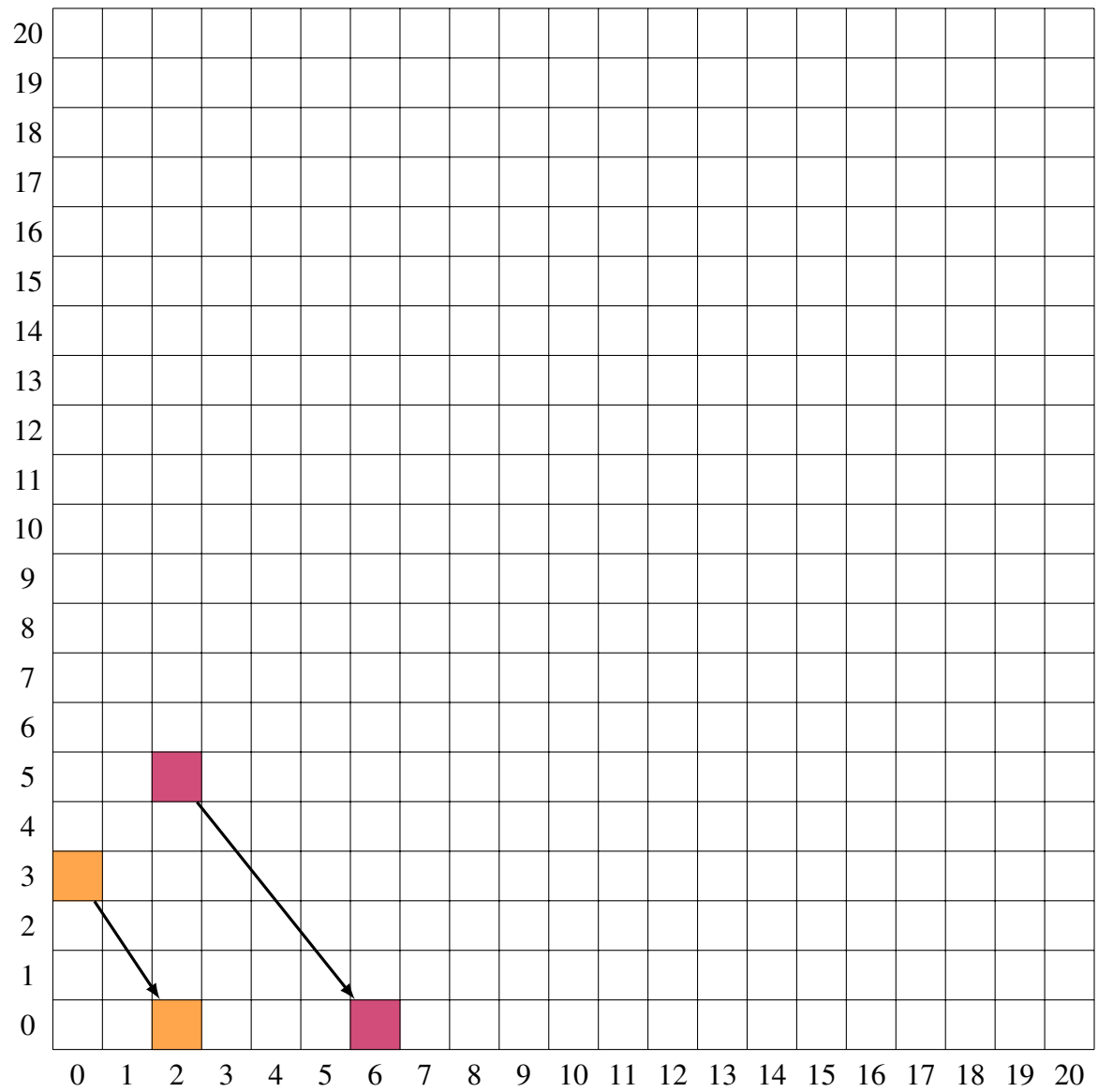


Figure 2: DAC05_subproblem_2

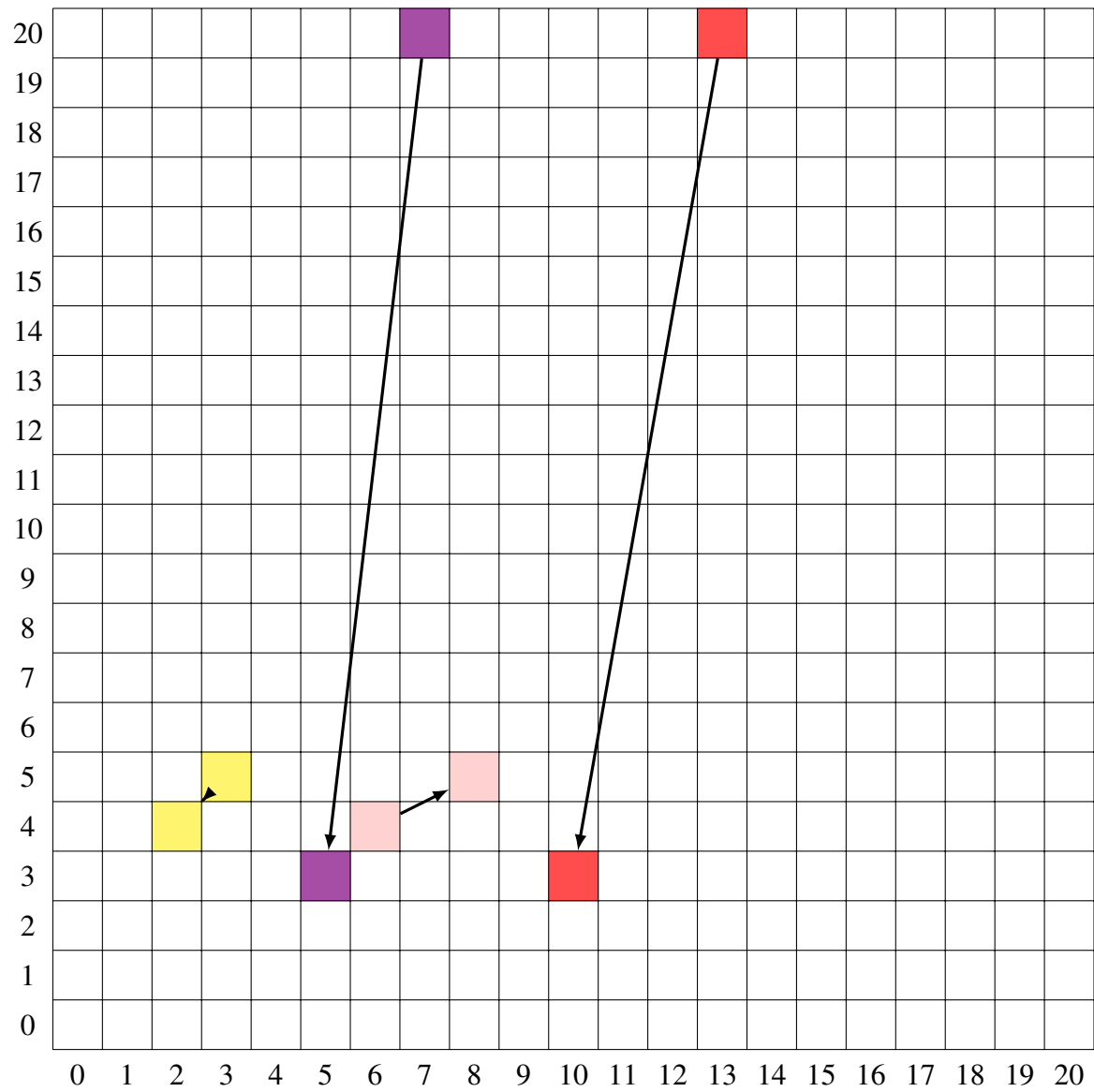


Figure 3: DAC05_subproblem_3

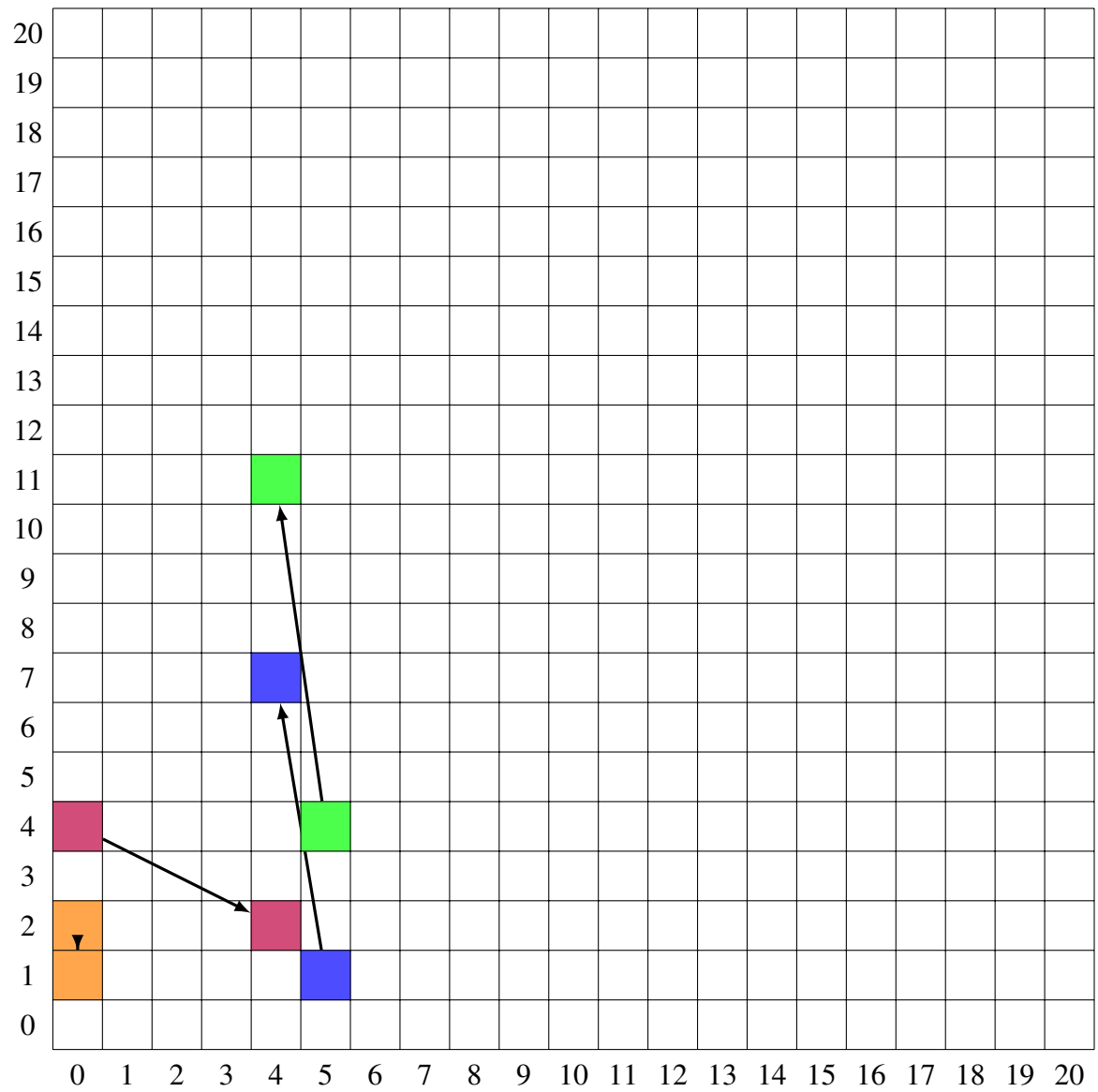


Figure 4: DAC05_subproblem_4

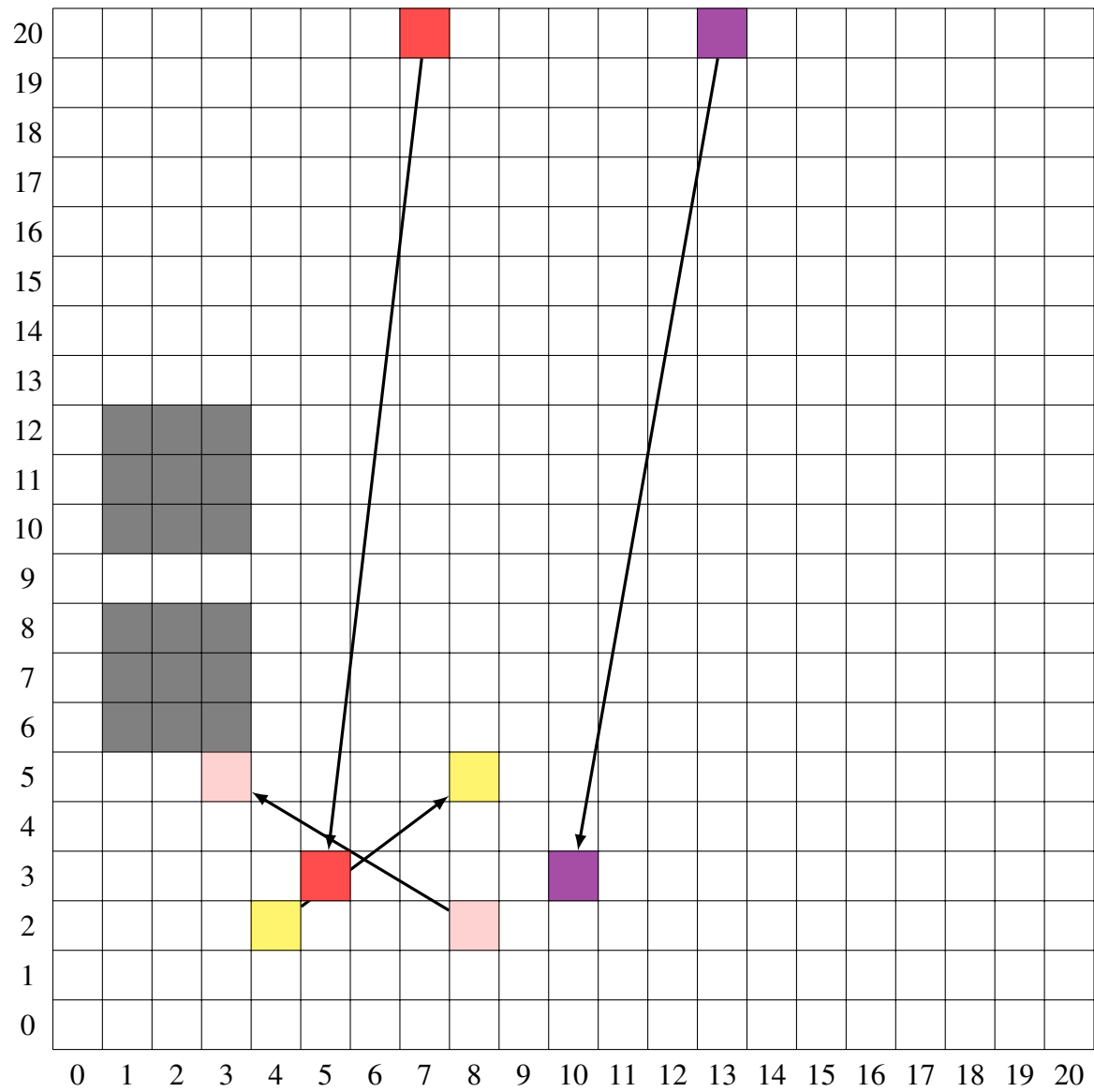


Figure 5: DAC05_subproblem_5

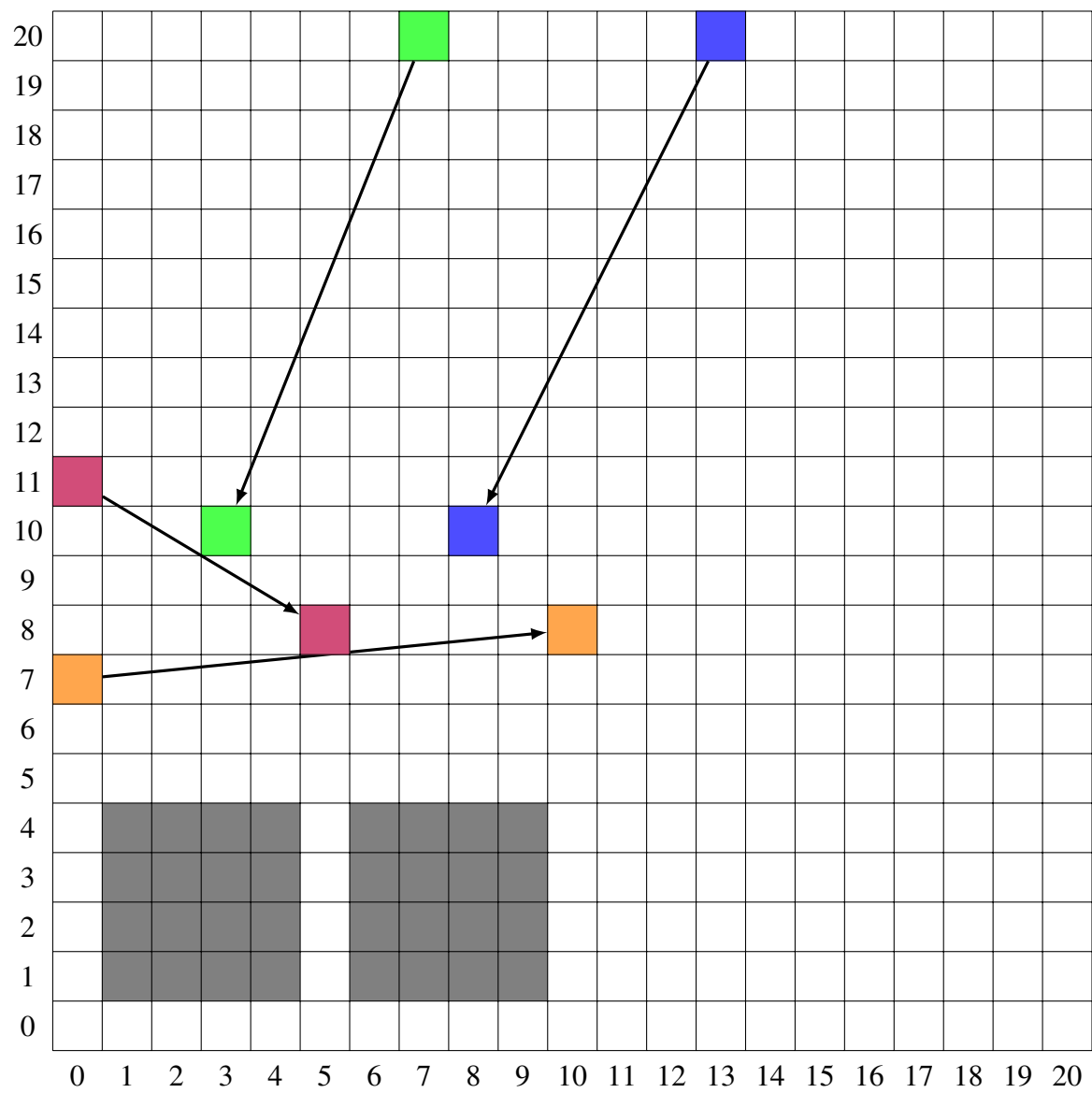


Figure 6: DAC05_subproblem_6

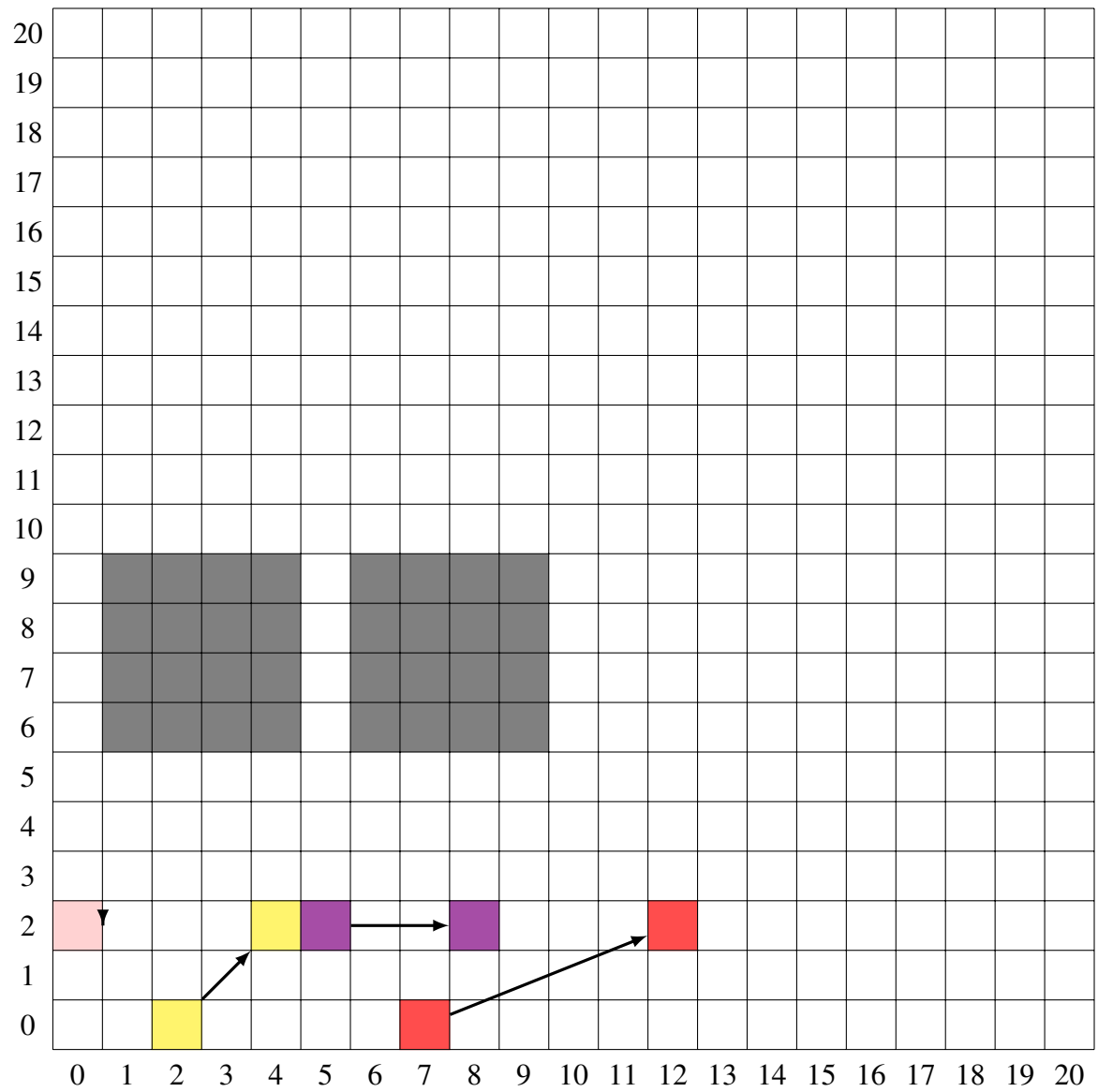


Figure 7: DAC05_subproblem_7

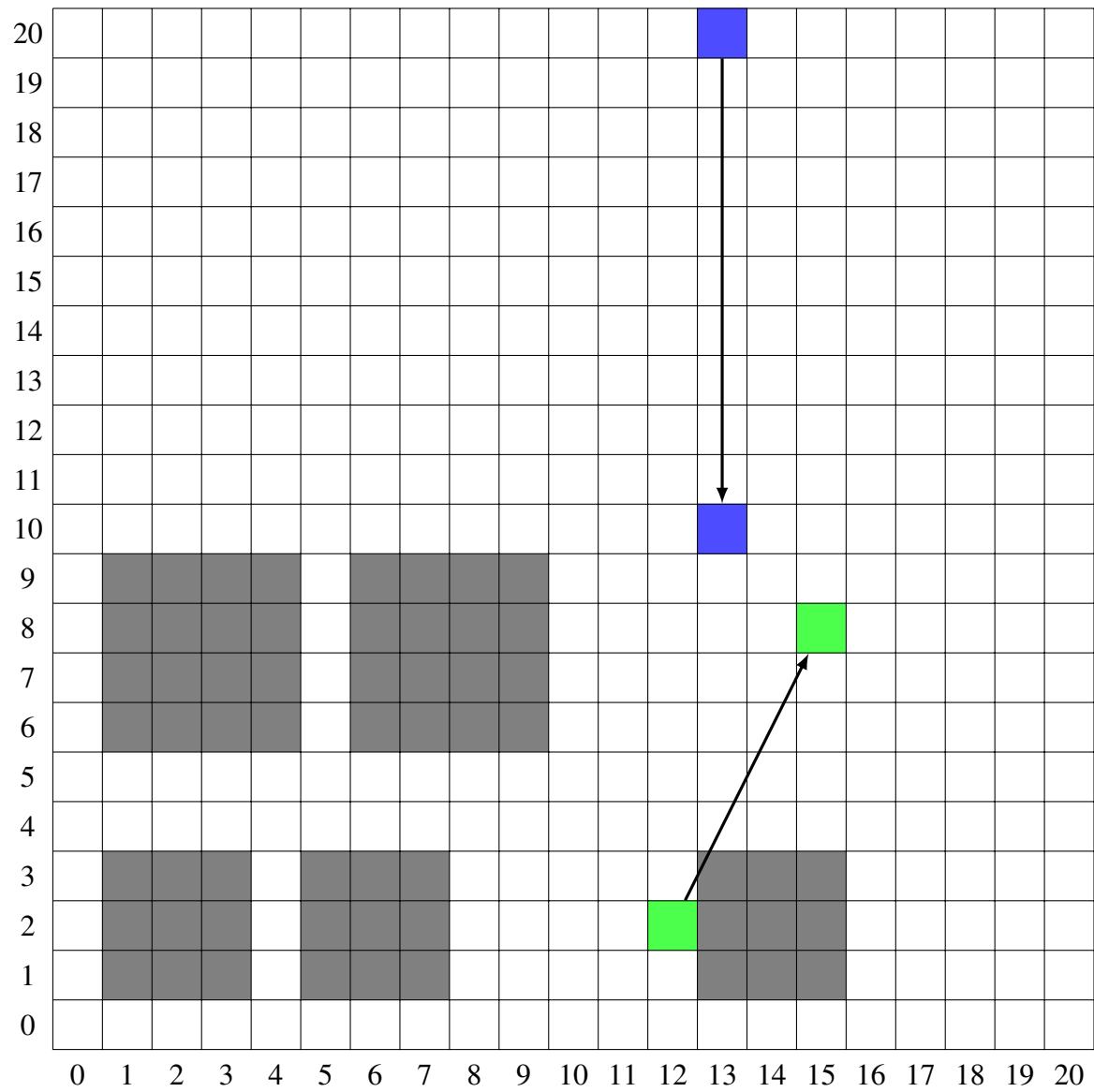


Figure 8: DAC05_subproblem_8

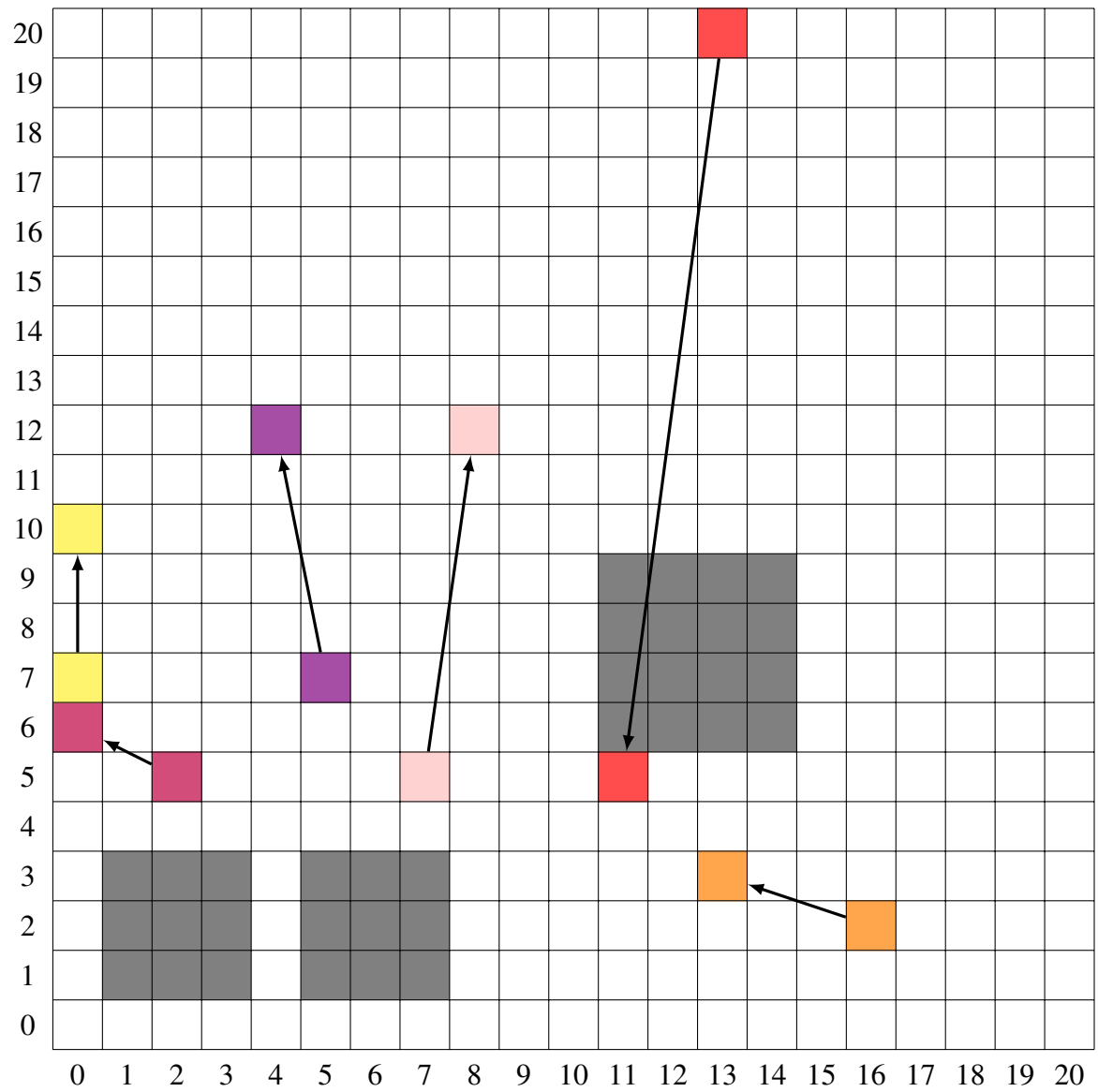


Figure 9: DAC05_subproblem_9

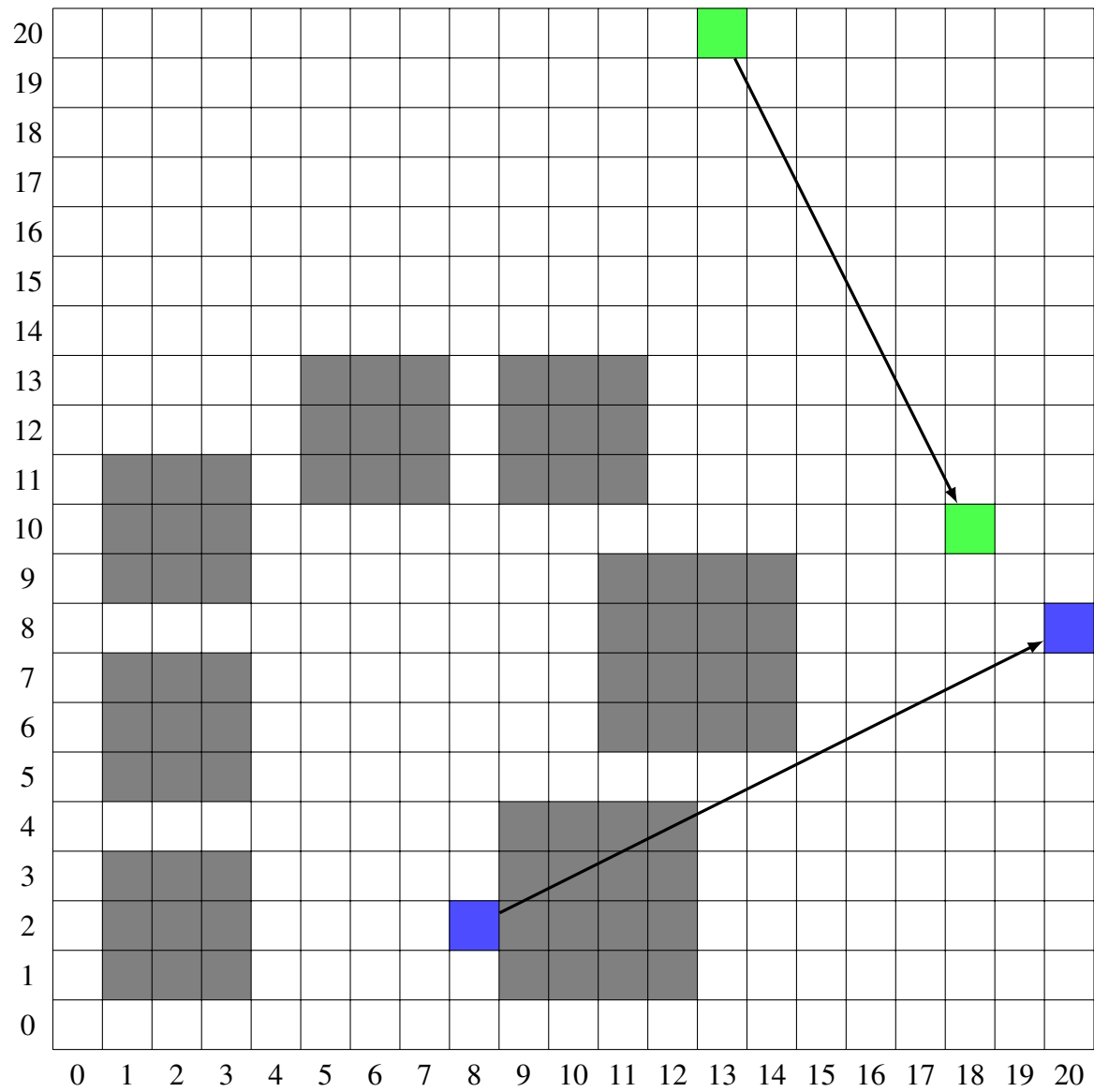


Figure 10: DAC05_subproblem_10

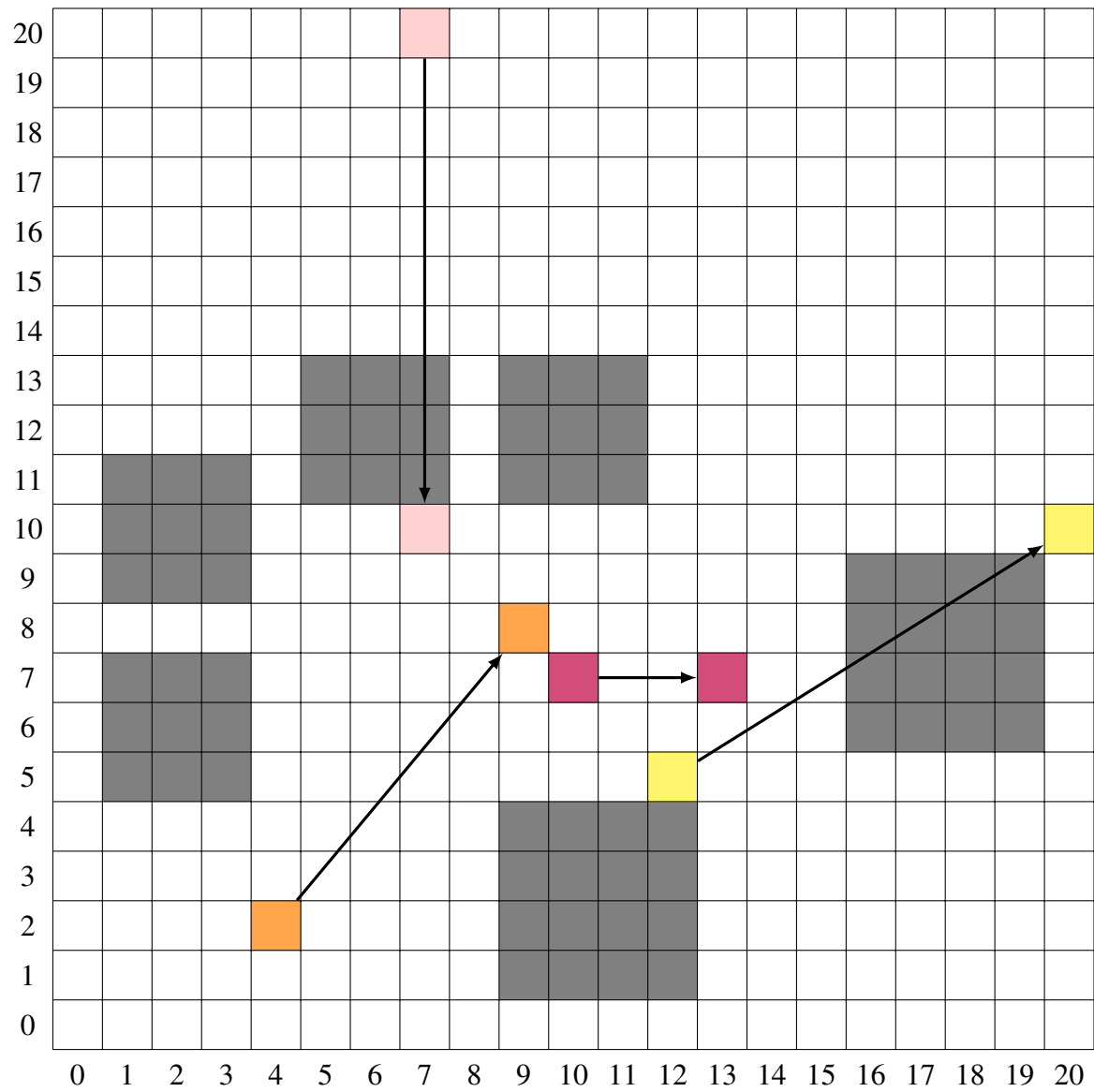


Figure 11: DAC05_subproblem_11

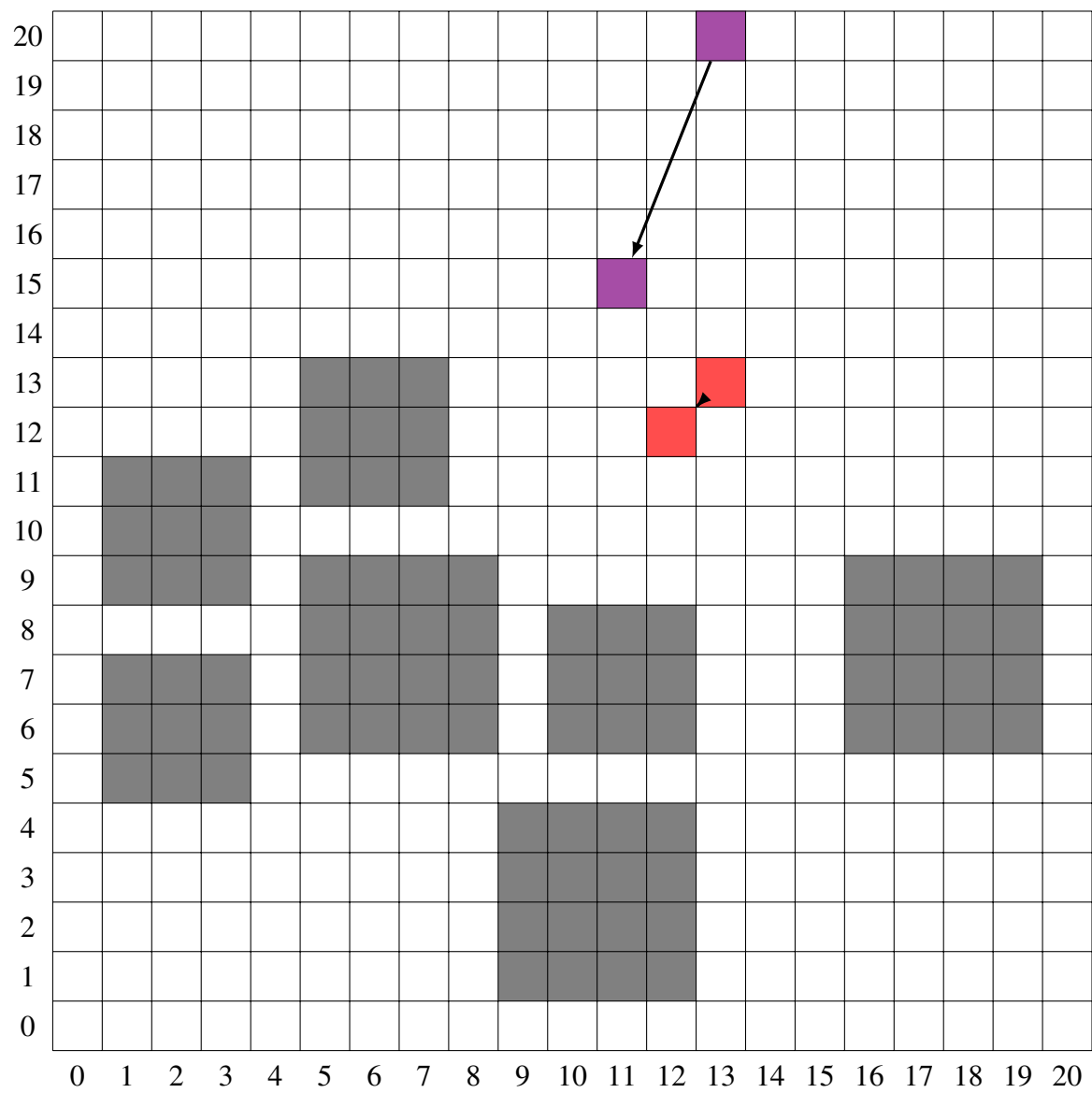


Figure 12: DAC05_subproblem_12

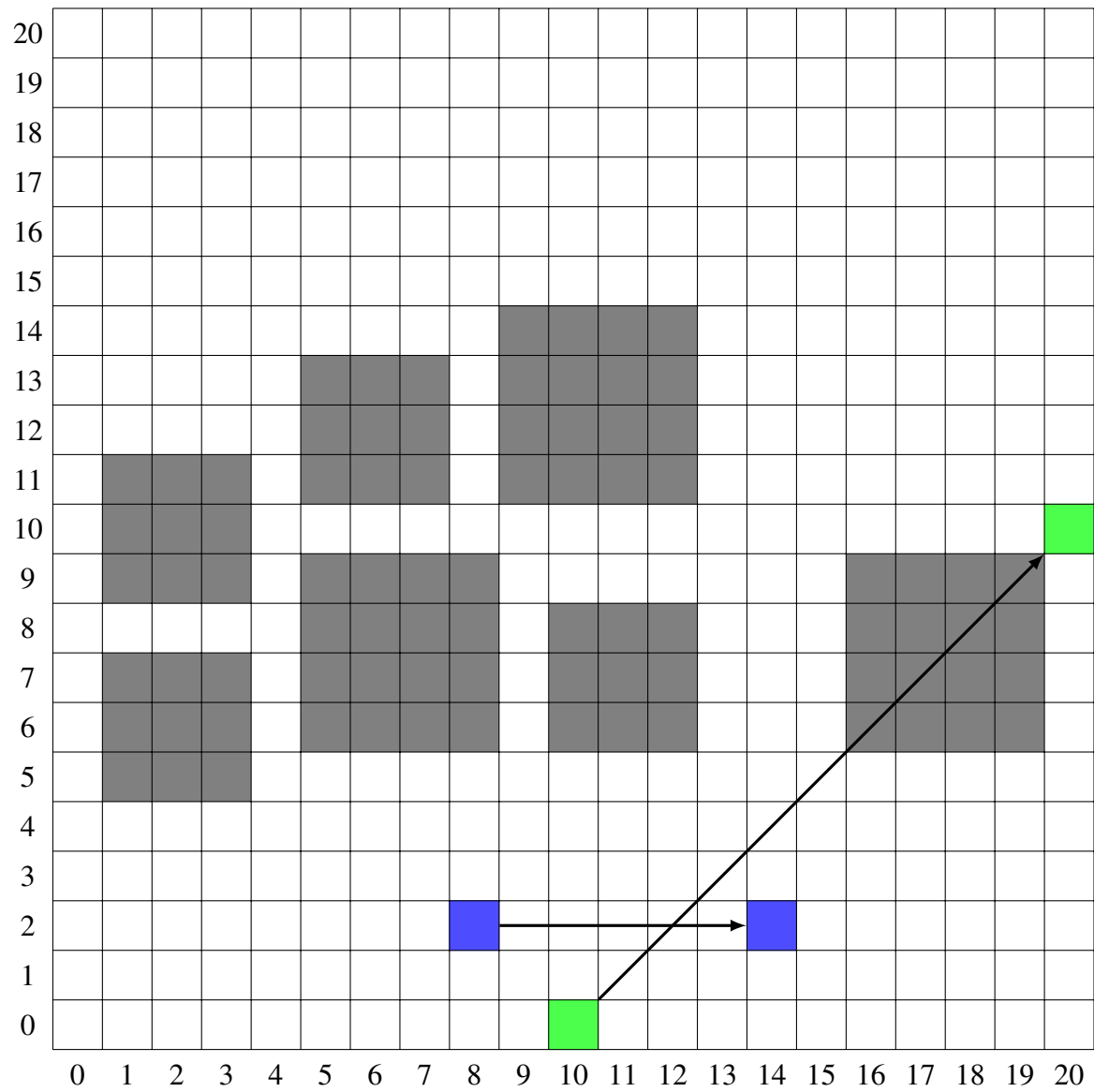
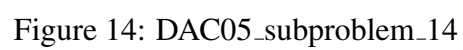


Figure 13: DAC05_subproblem_13



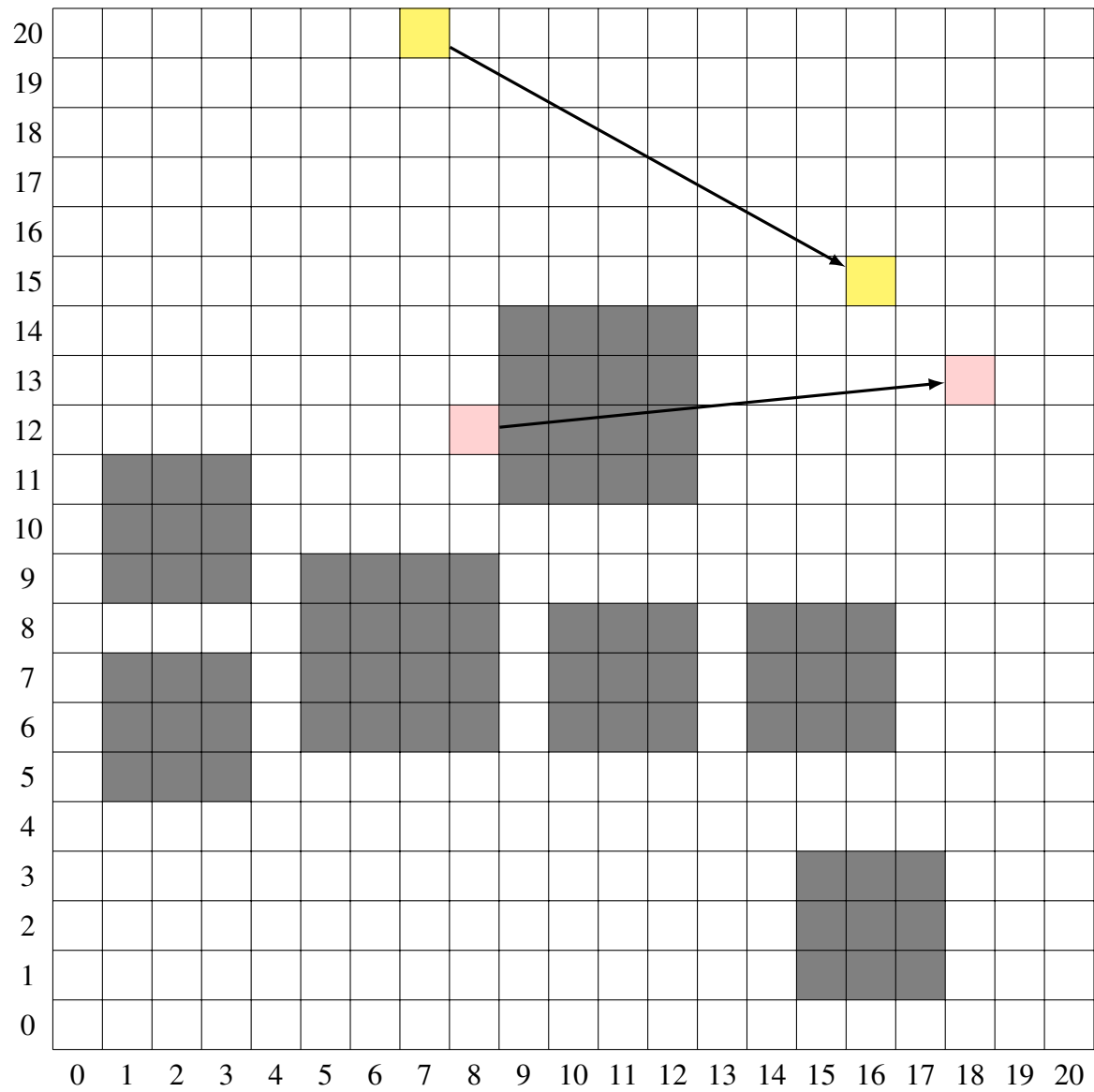


Figure 15: DAC05_subproblem_15

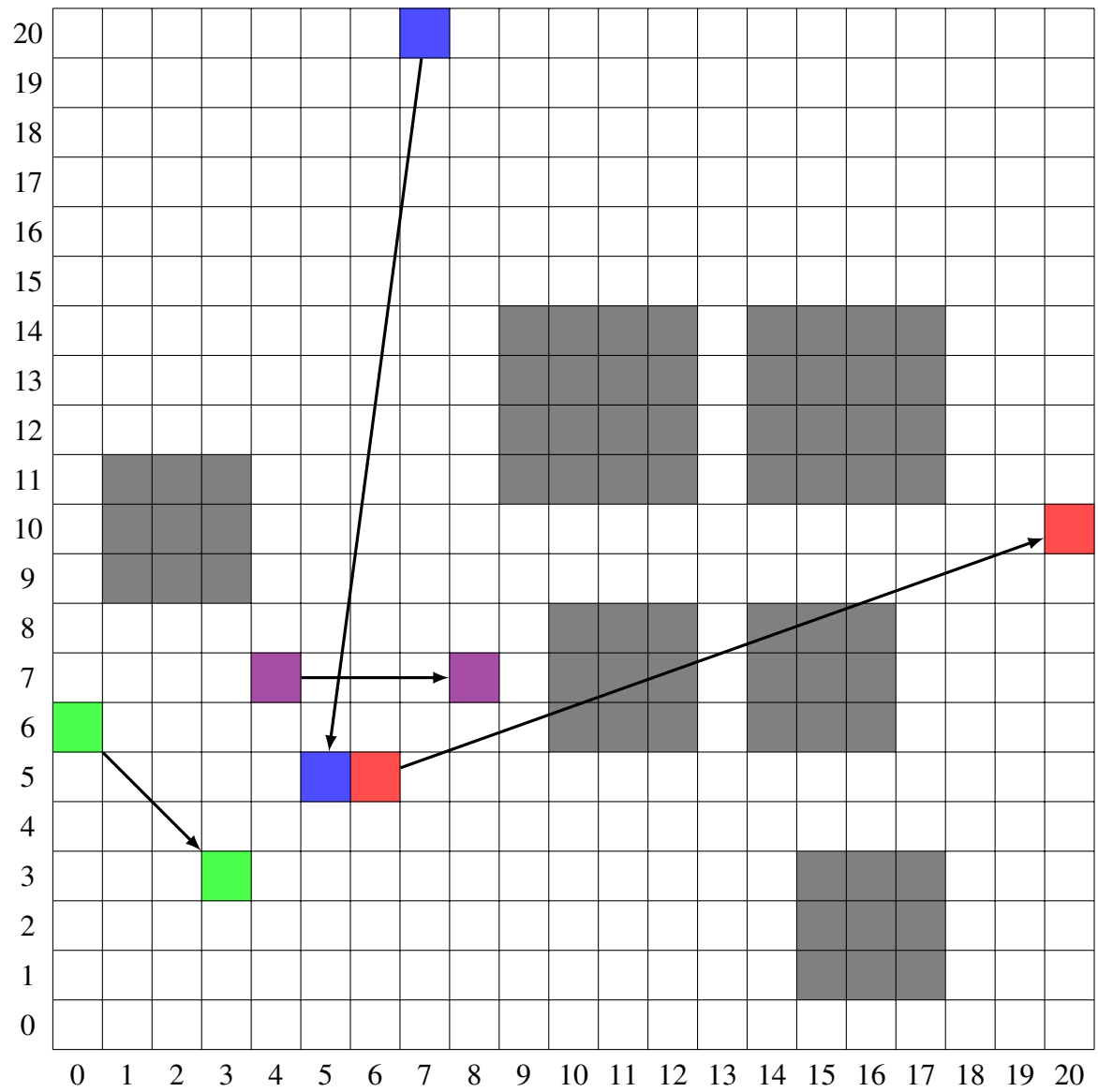


Figure 16: DAC05_subproblem_16

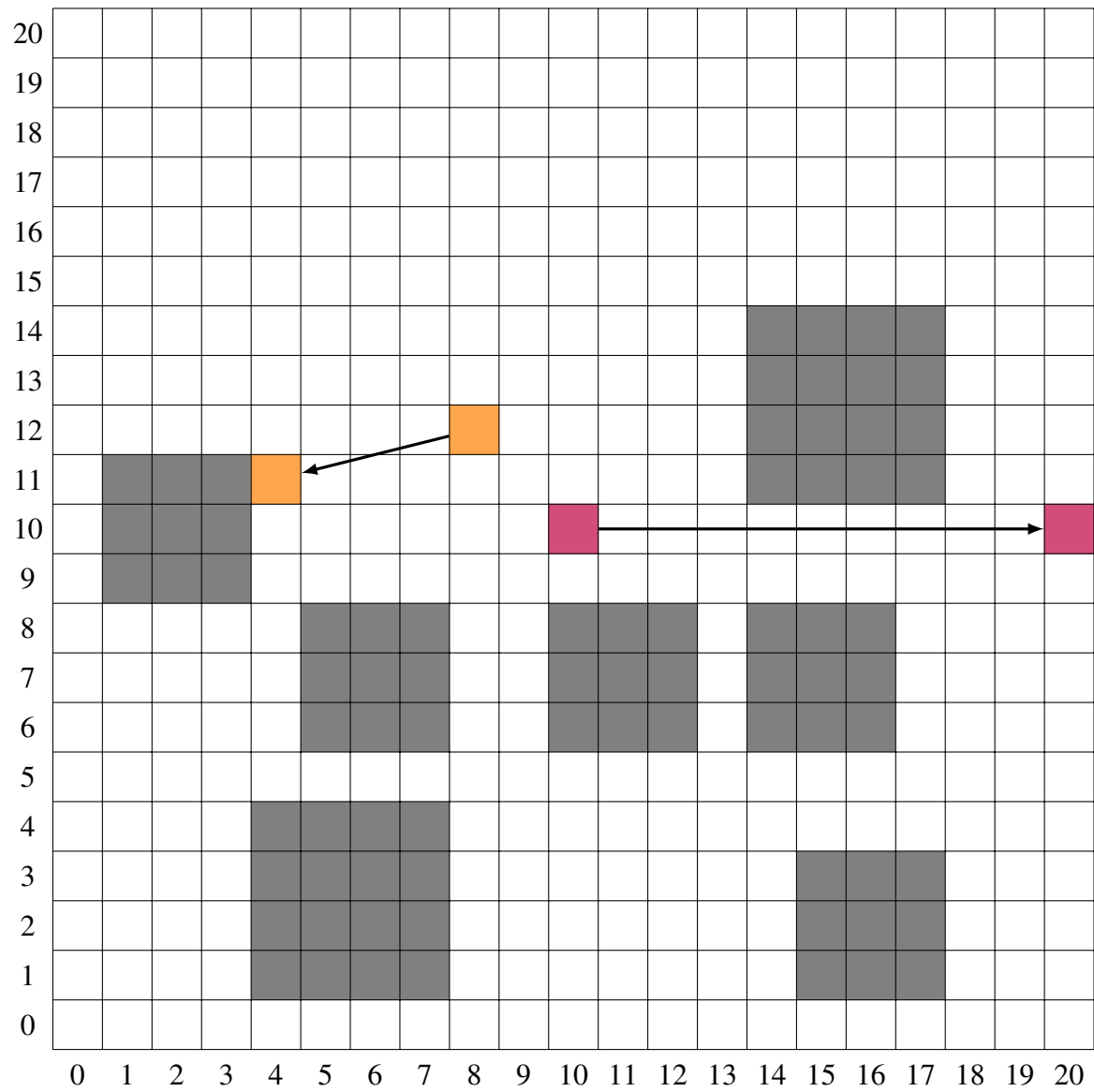


Figure 17: DAC05_subproblem_17

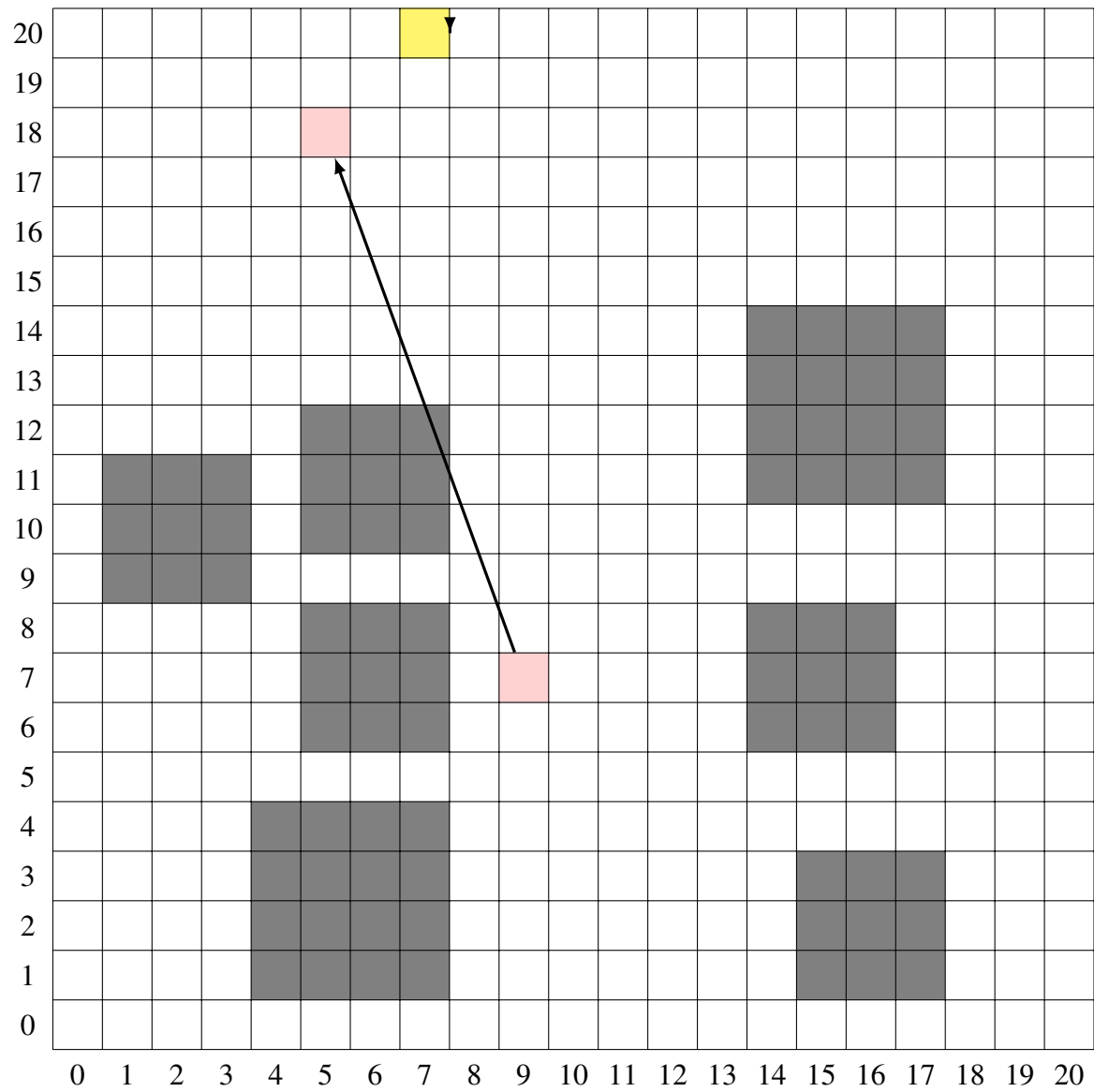


Figure 18: DAC05_subproblem_18

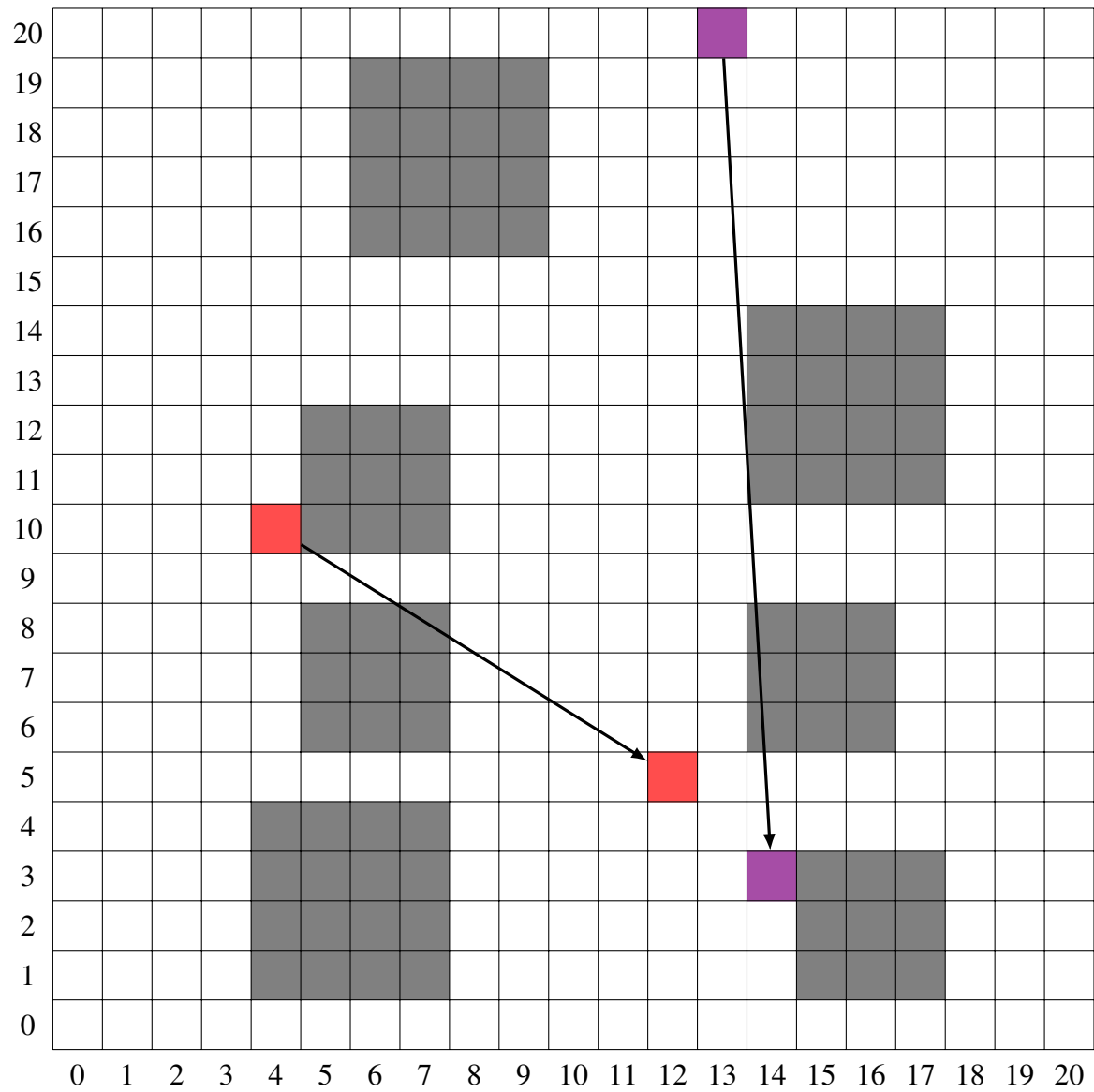


Figure 19: DAC05_subproblem_19

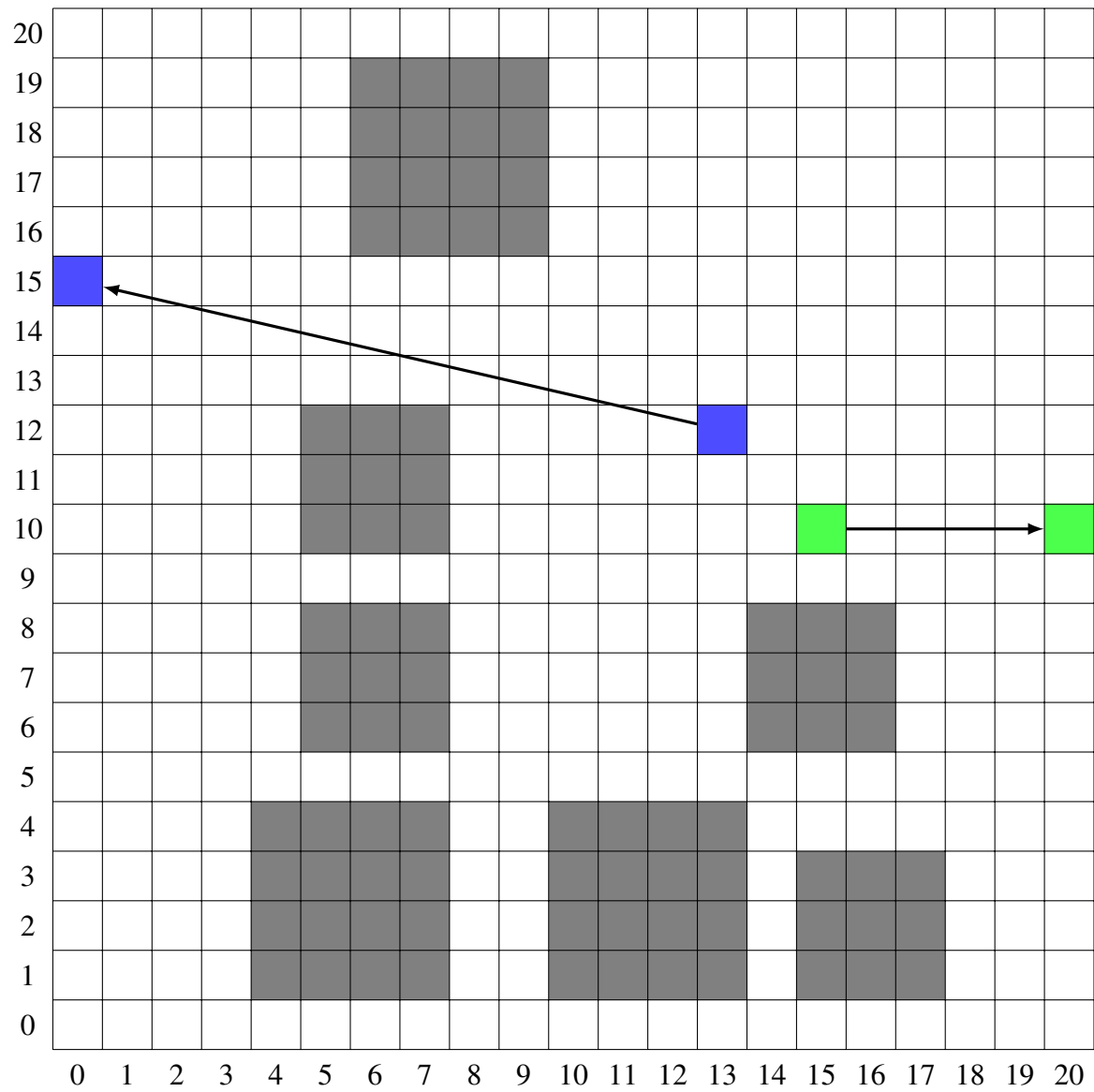


Figure 20: DAC05_subproblem_20

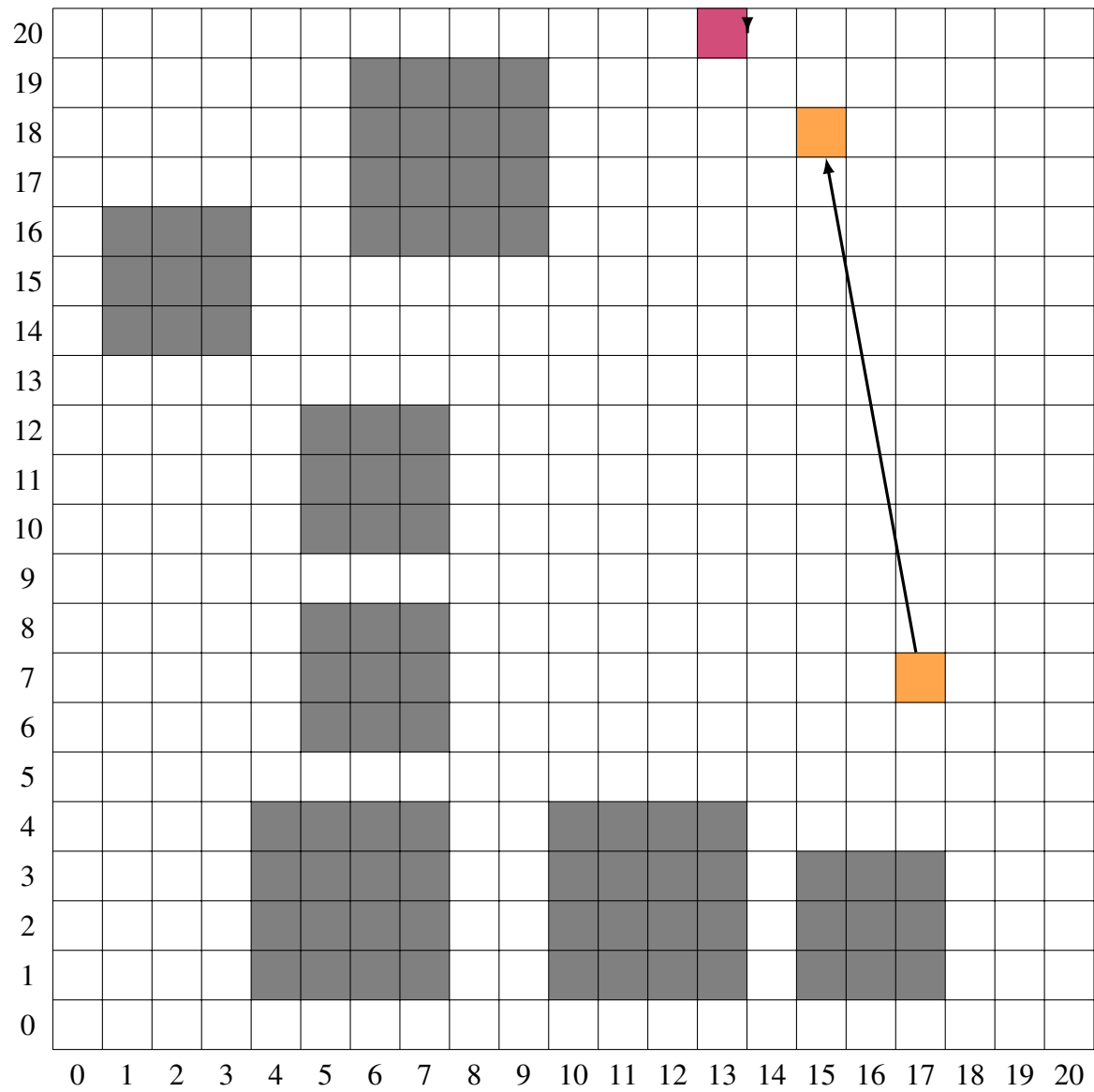


Figure 21: DAC05_subproblem_21

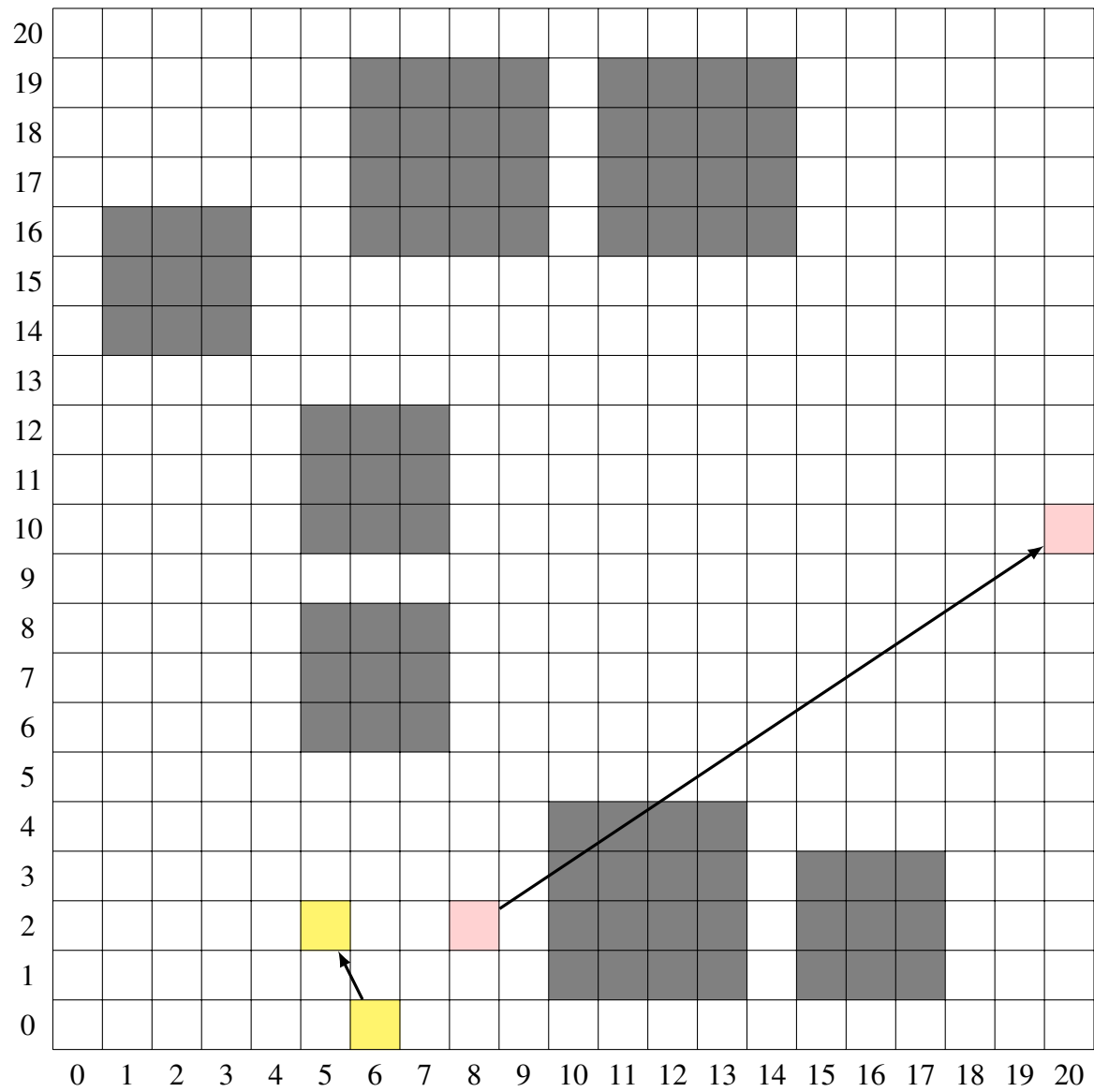


Figure 22: DAC05_subproblem_22

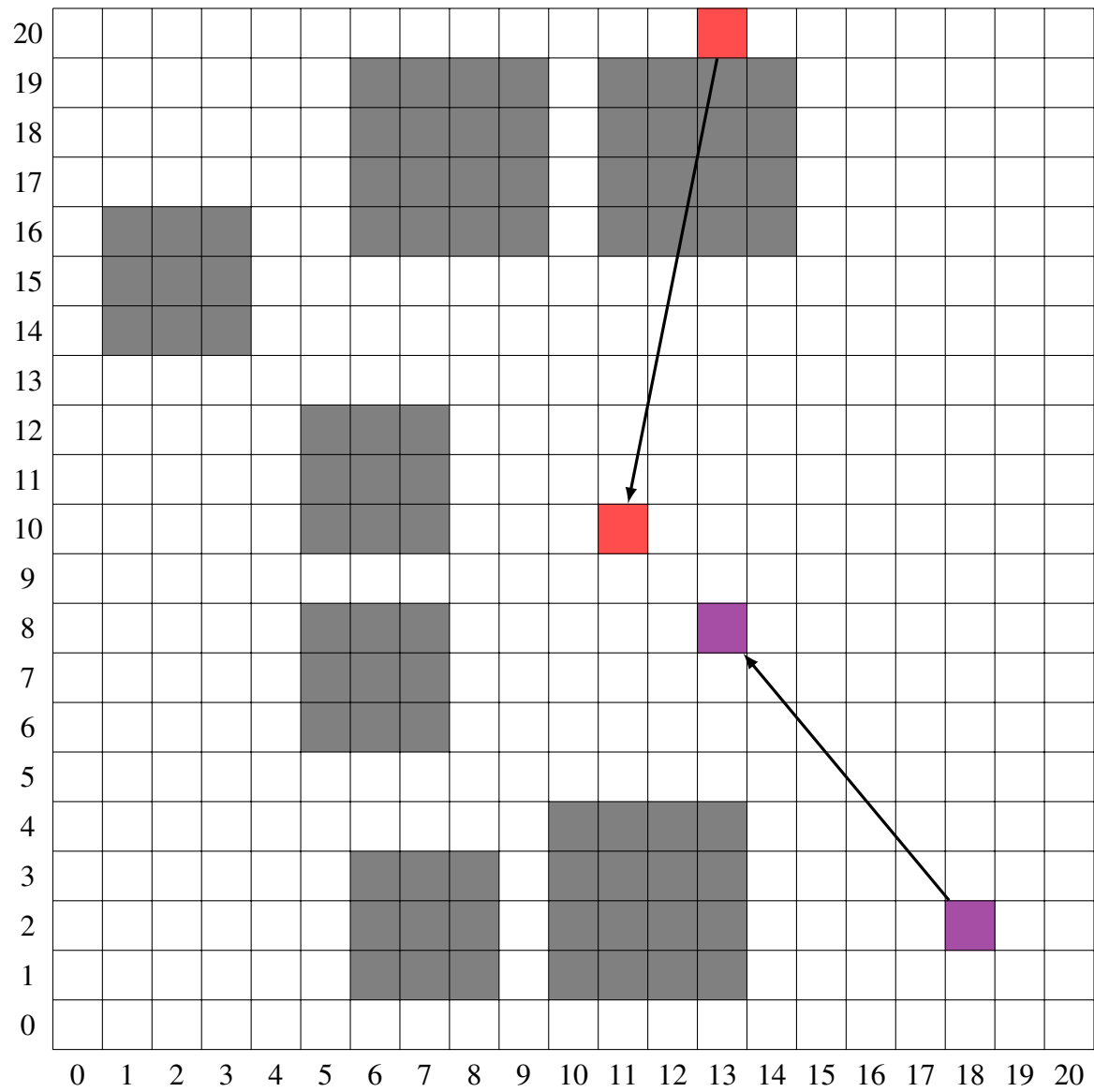


Figure 23: DAC05_subproblem_23

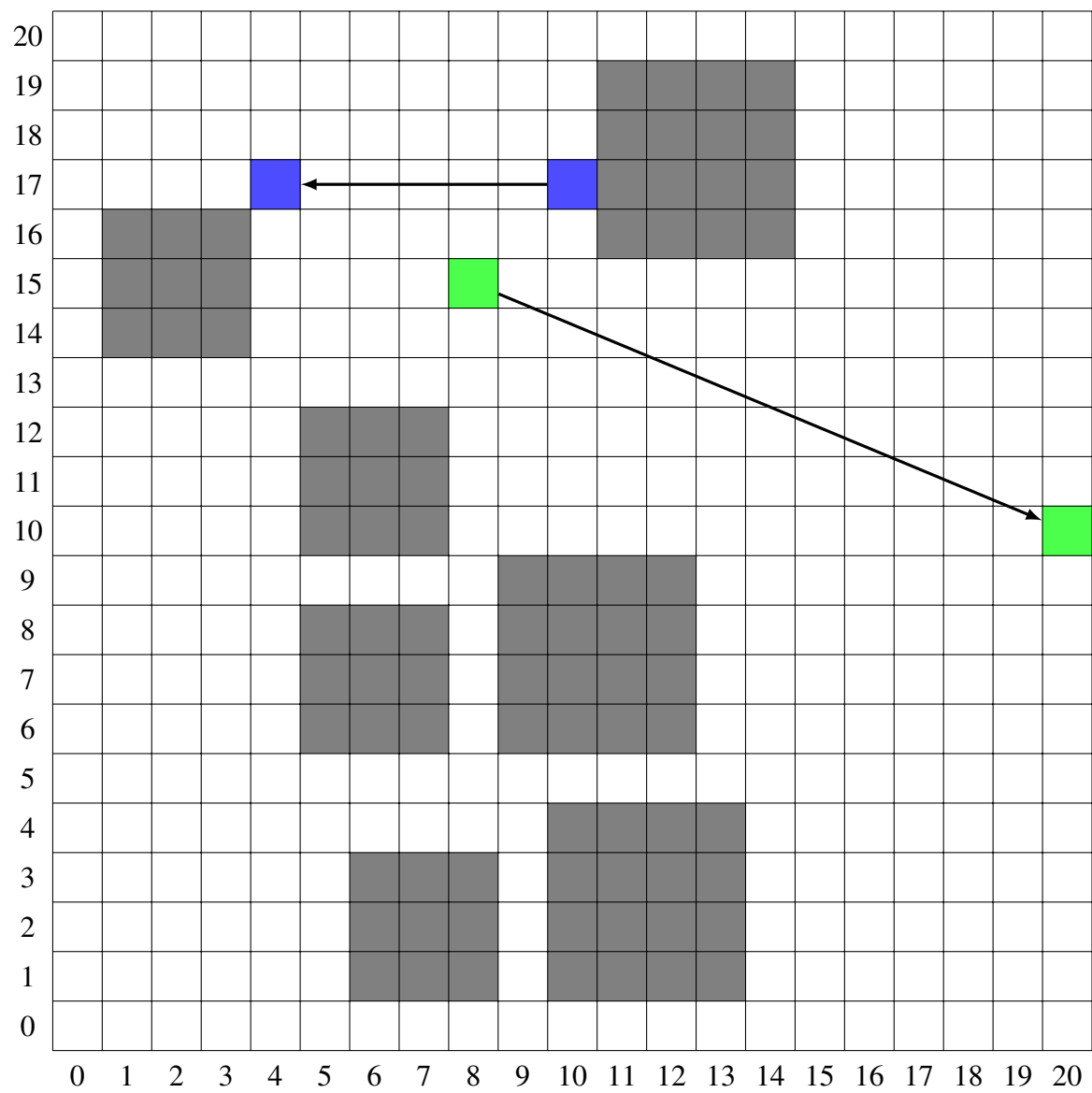


Figure 24: DAC05_subproblem_24

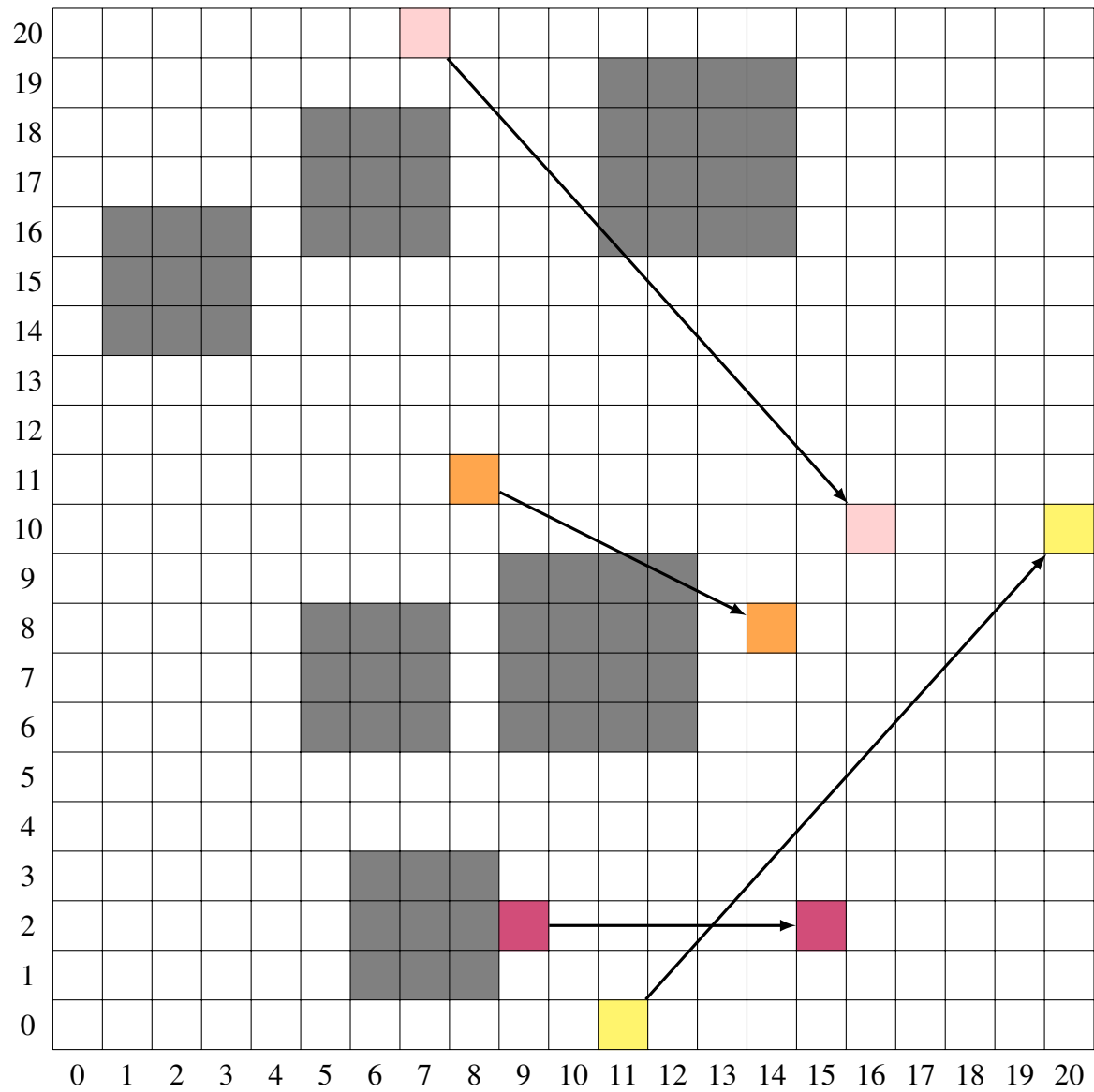


Figure 25: DAC05_subproblem_25

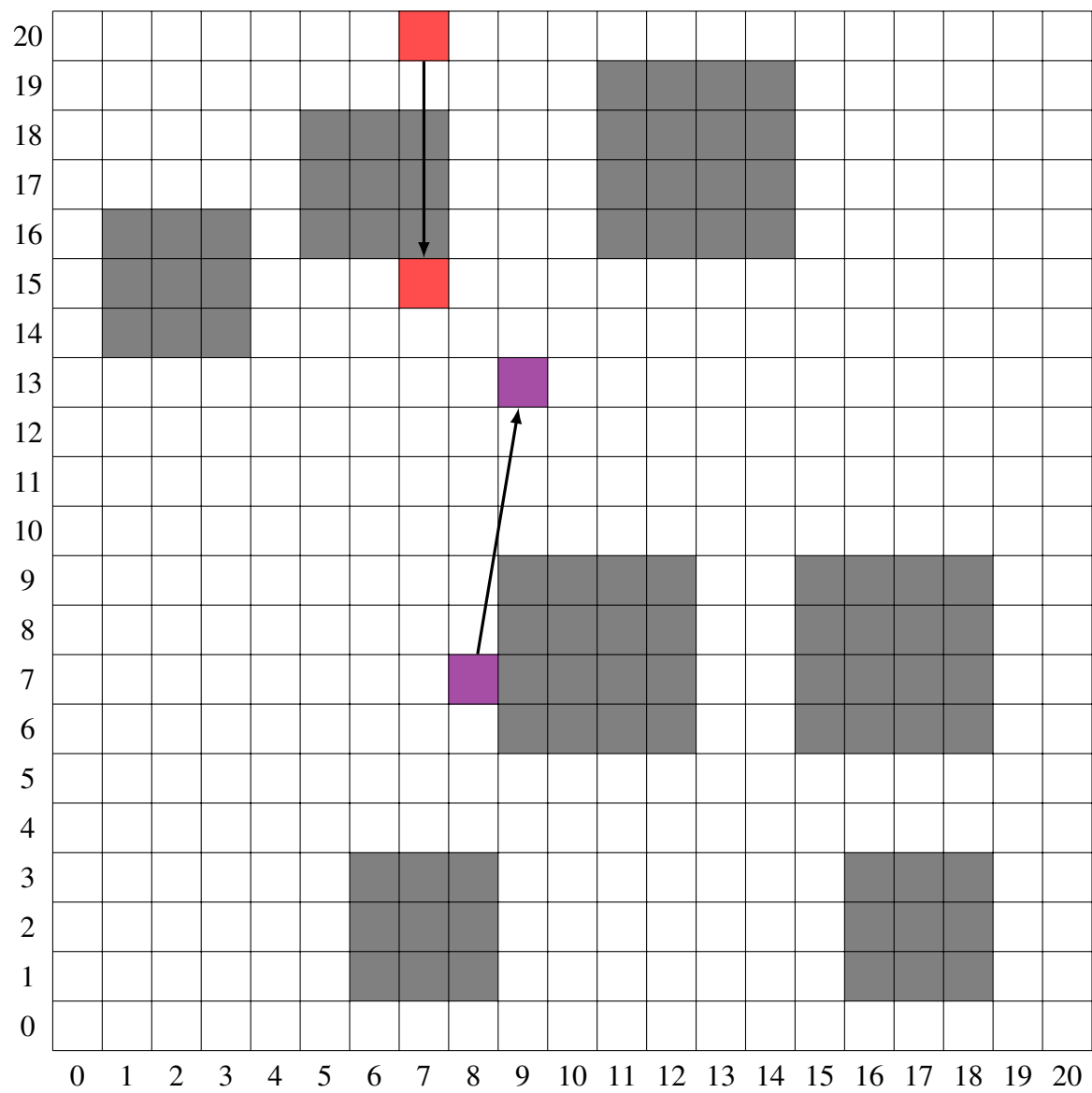


Figure 26: DAC05_subproblem_26

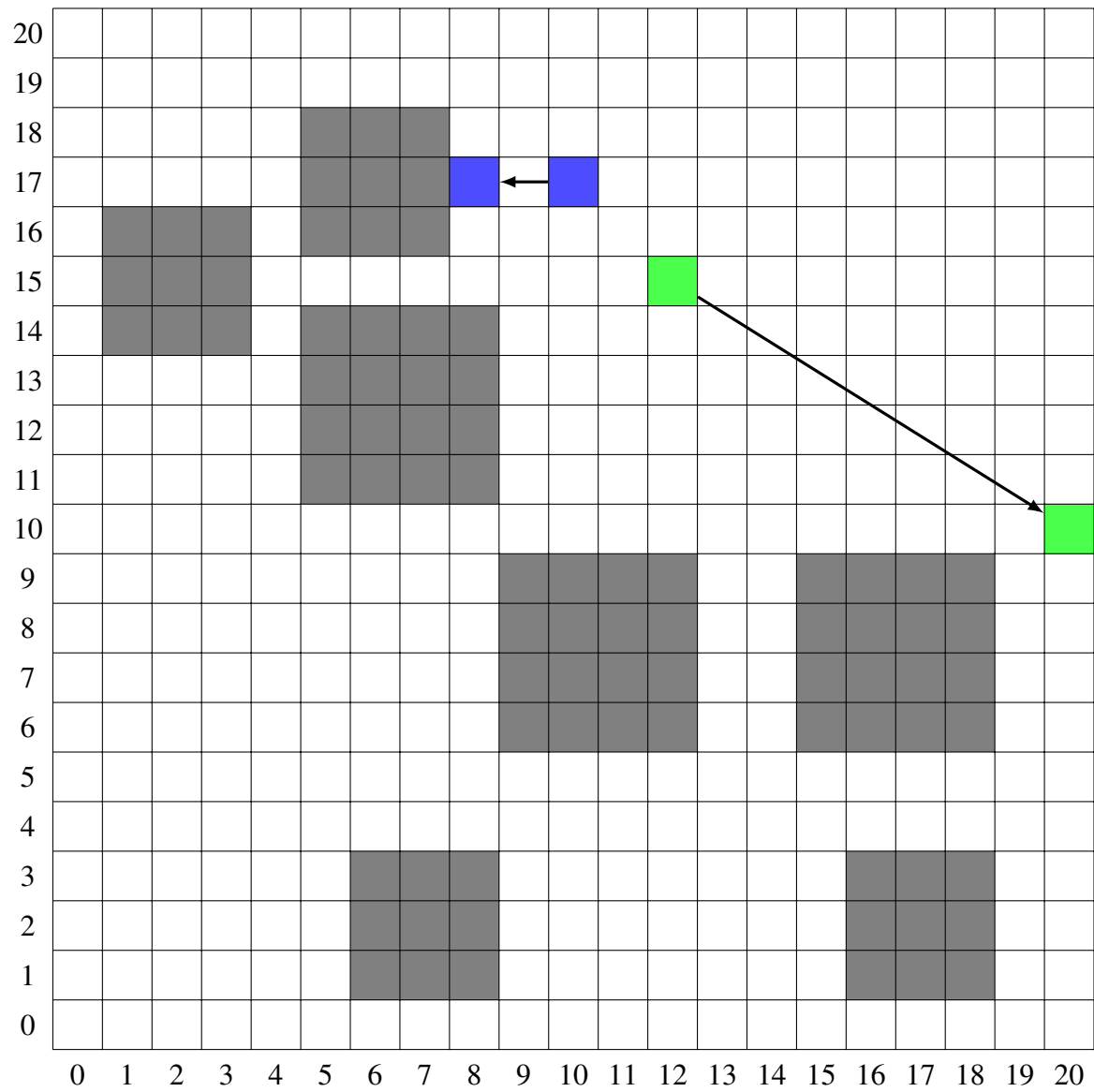


Figure 27: DAC05_subproblem_27

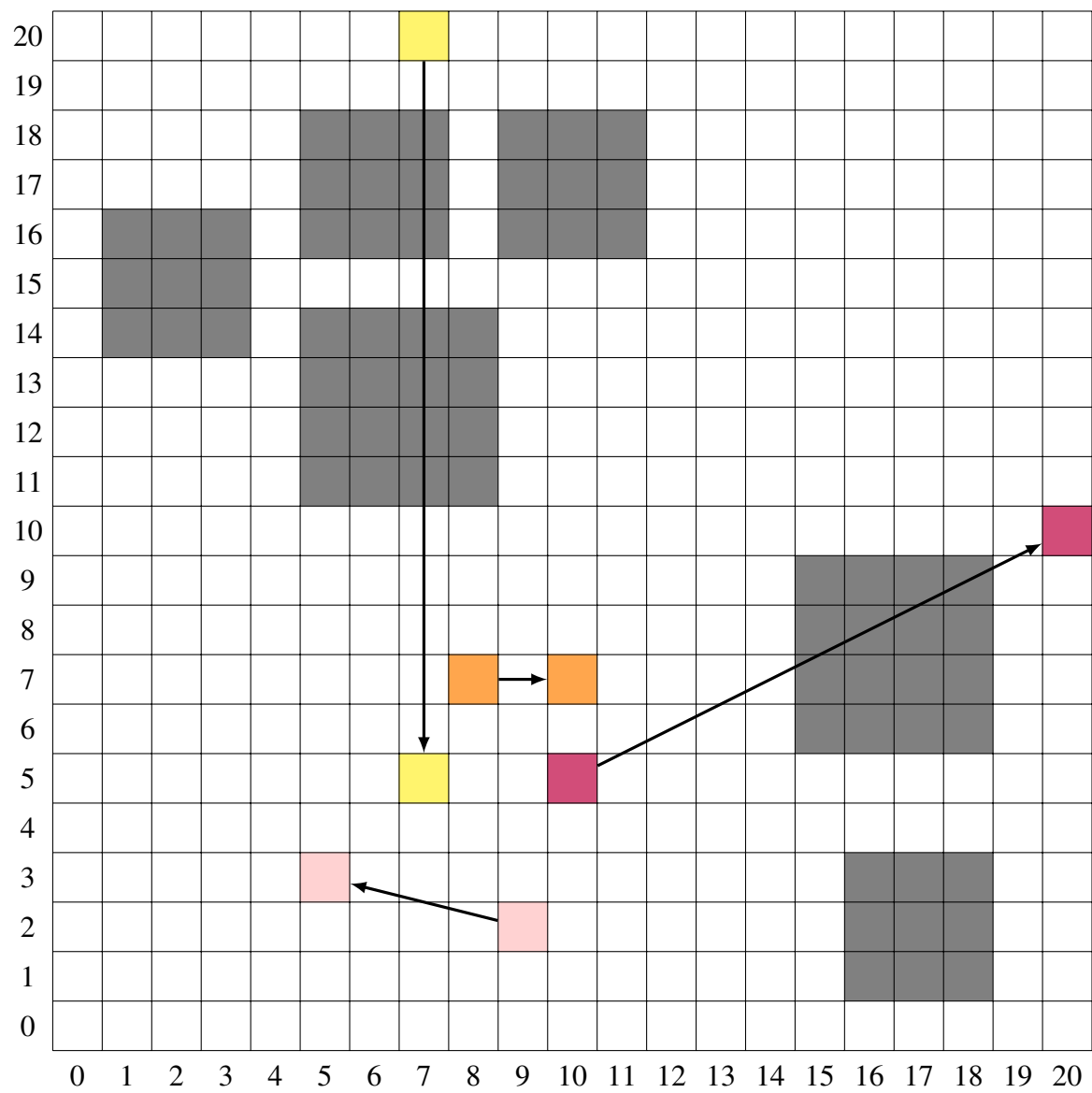


Figure 28: DAC05_subproblem_28

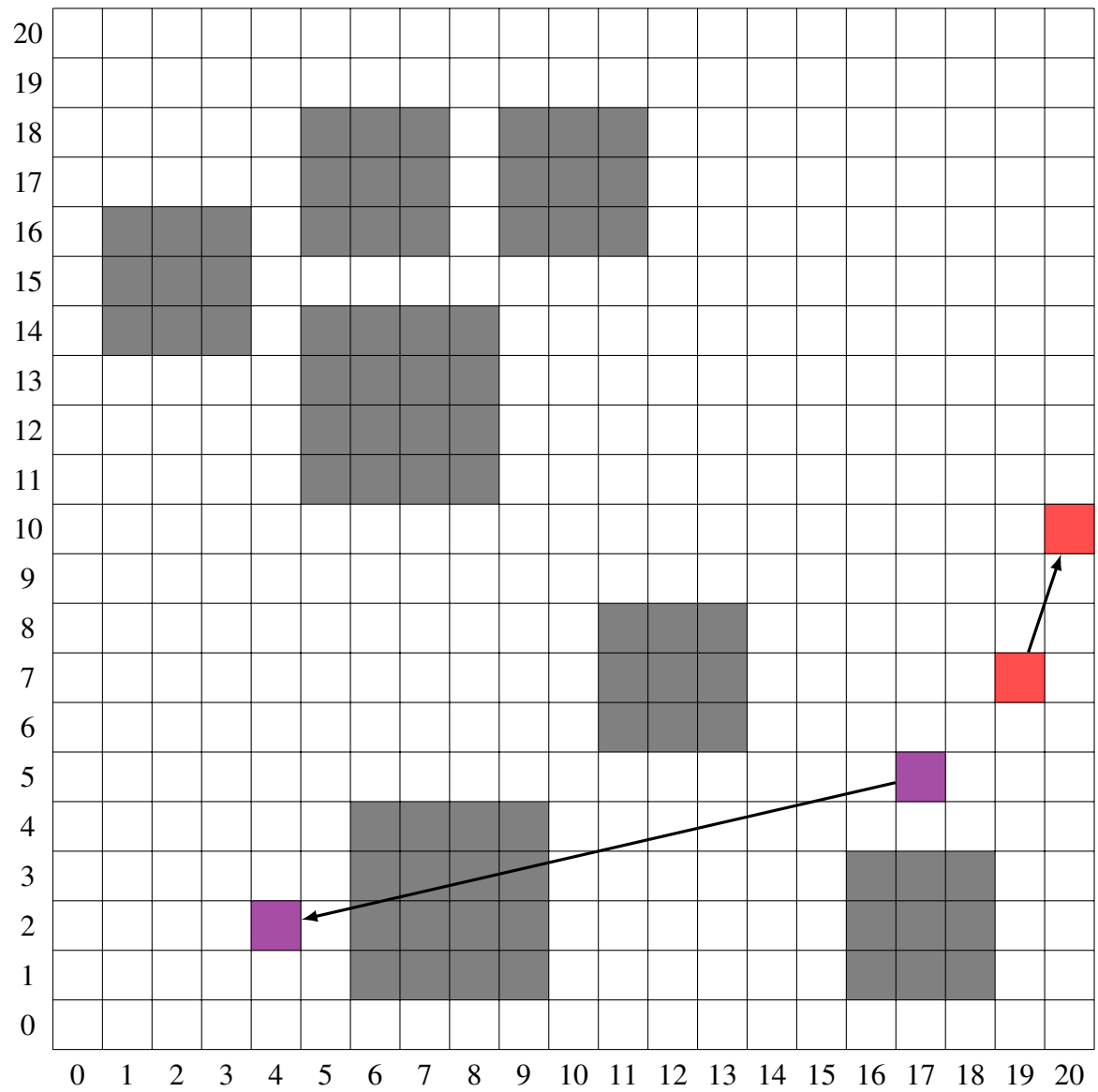


Figure 29: DAC05_subproblem_29

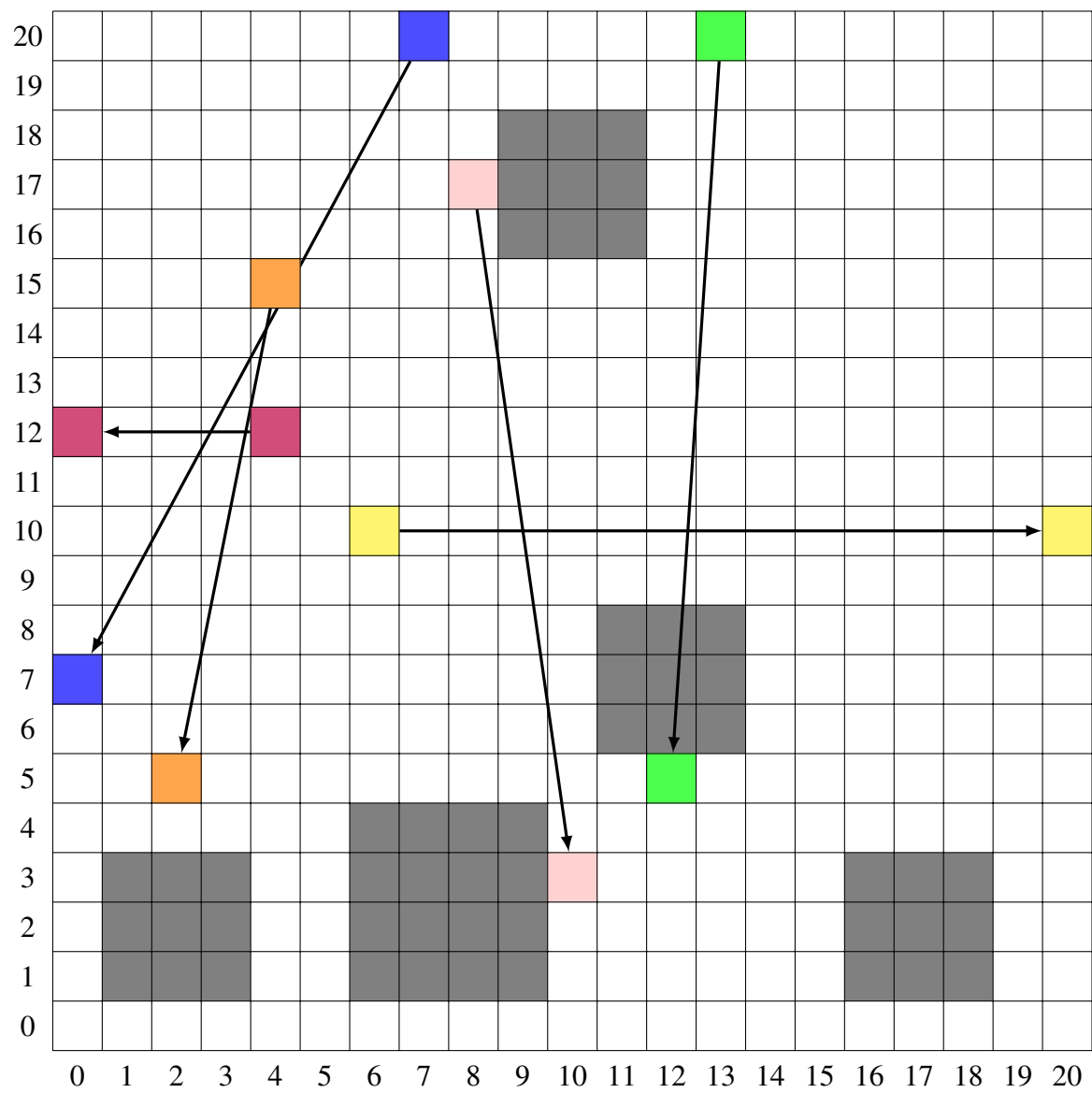


Figure 30: DAC05_subproblem_30

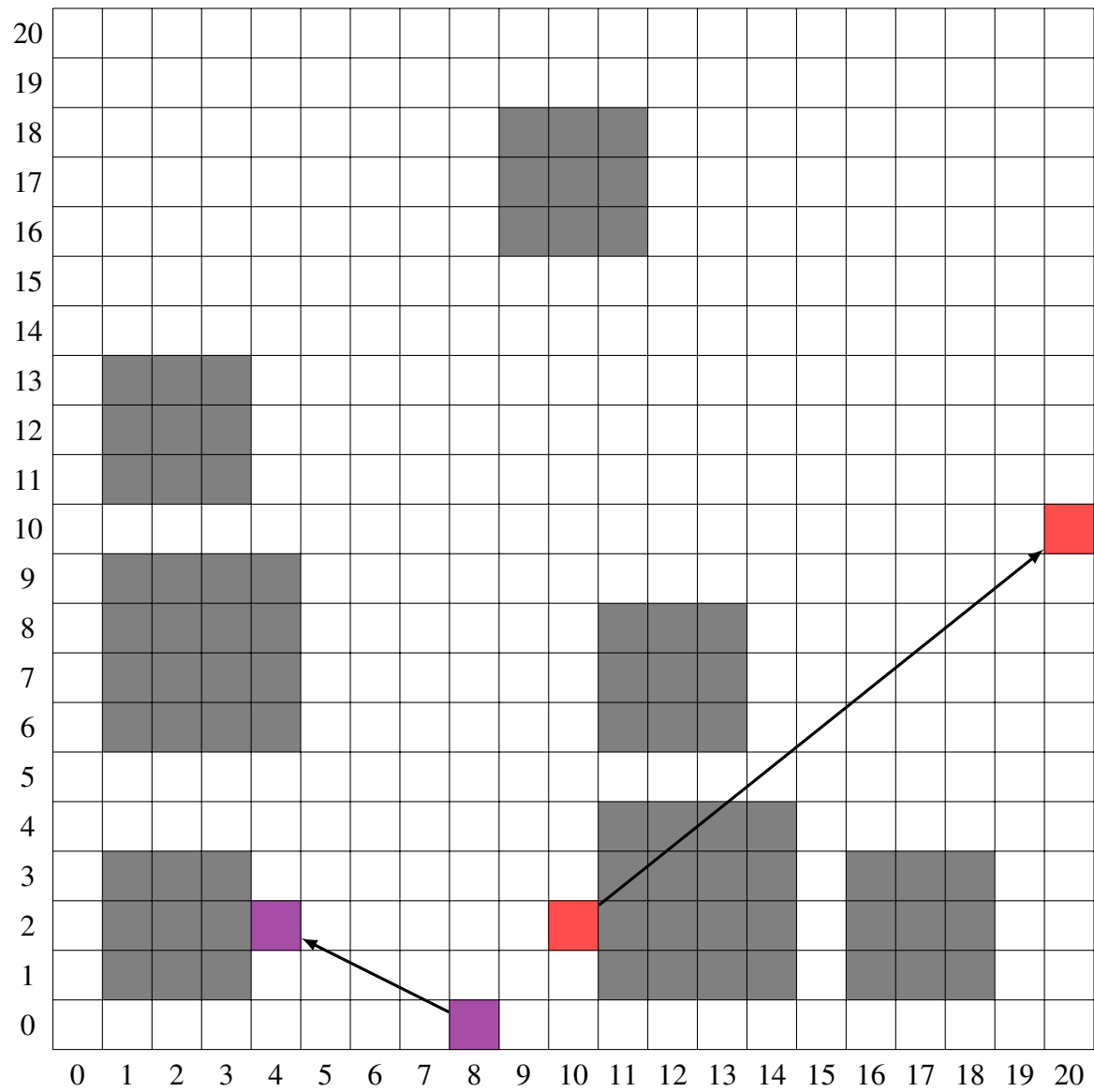


Figure 31: DAC05_subproblem_31

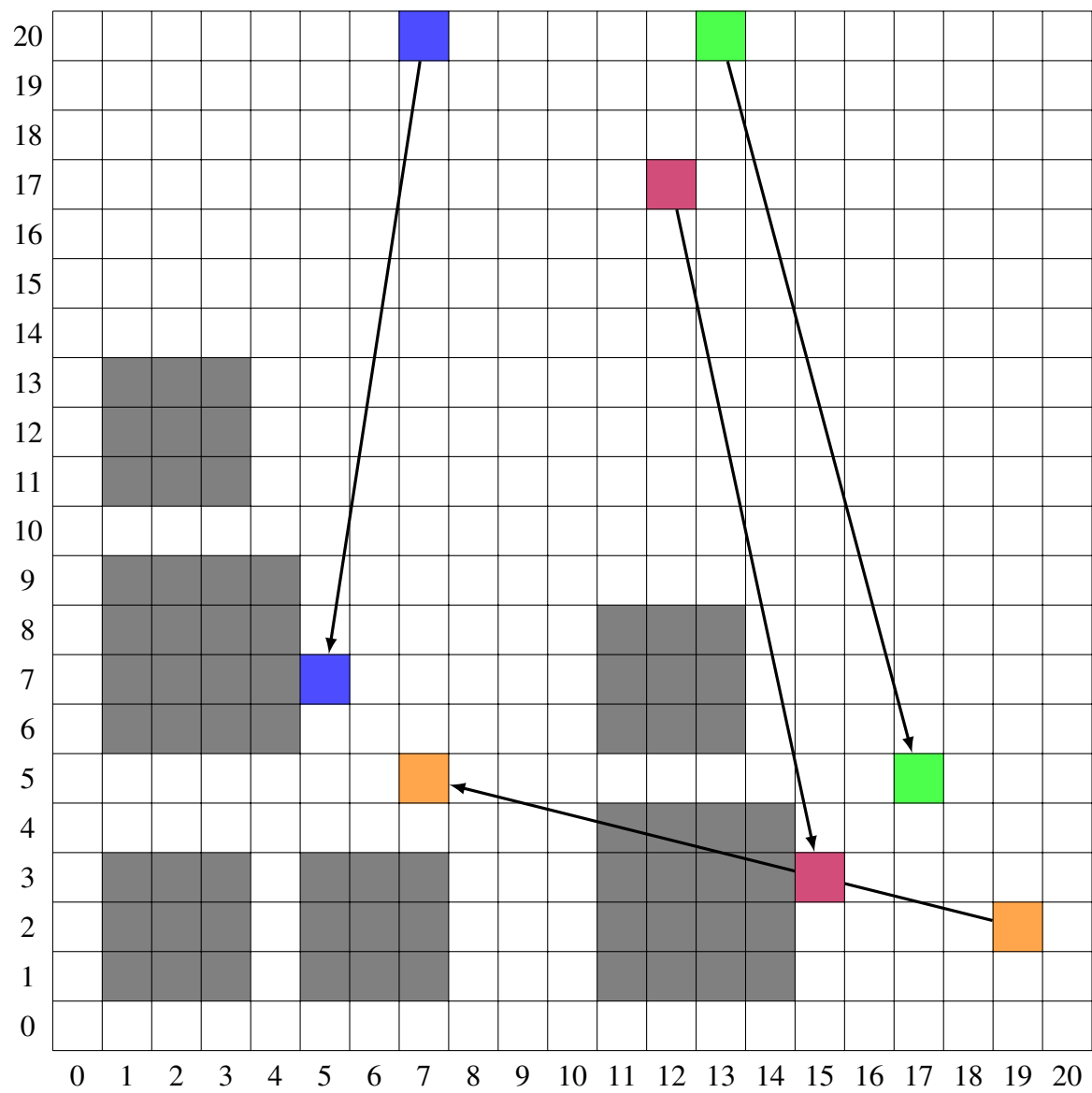


Figure 32: DAC05_subproblem_32

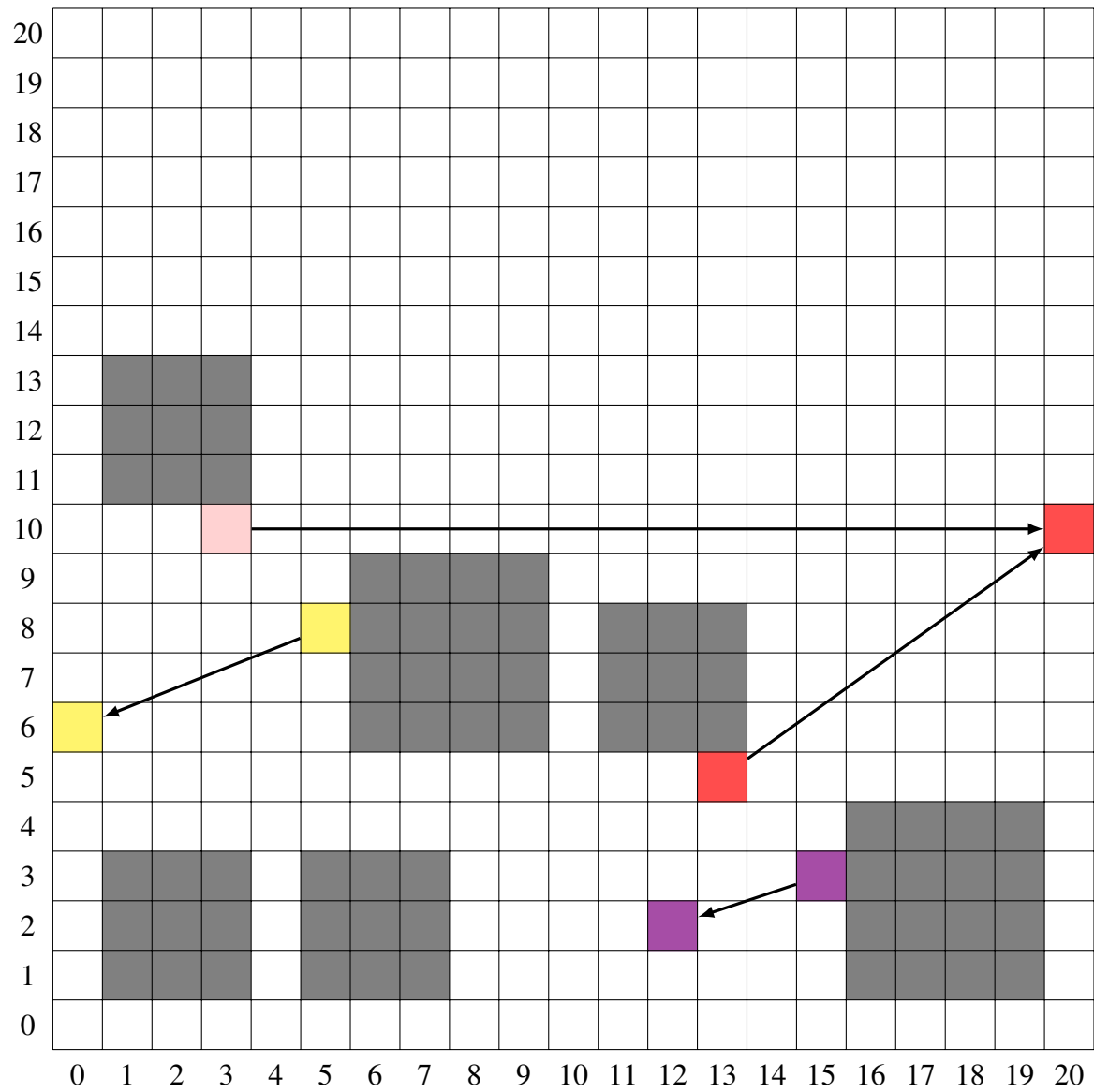


Figure 33: DAC05_subproblem_33

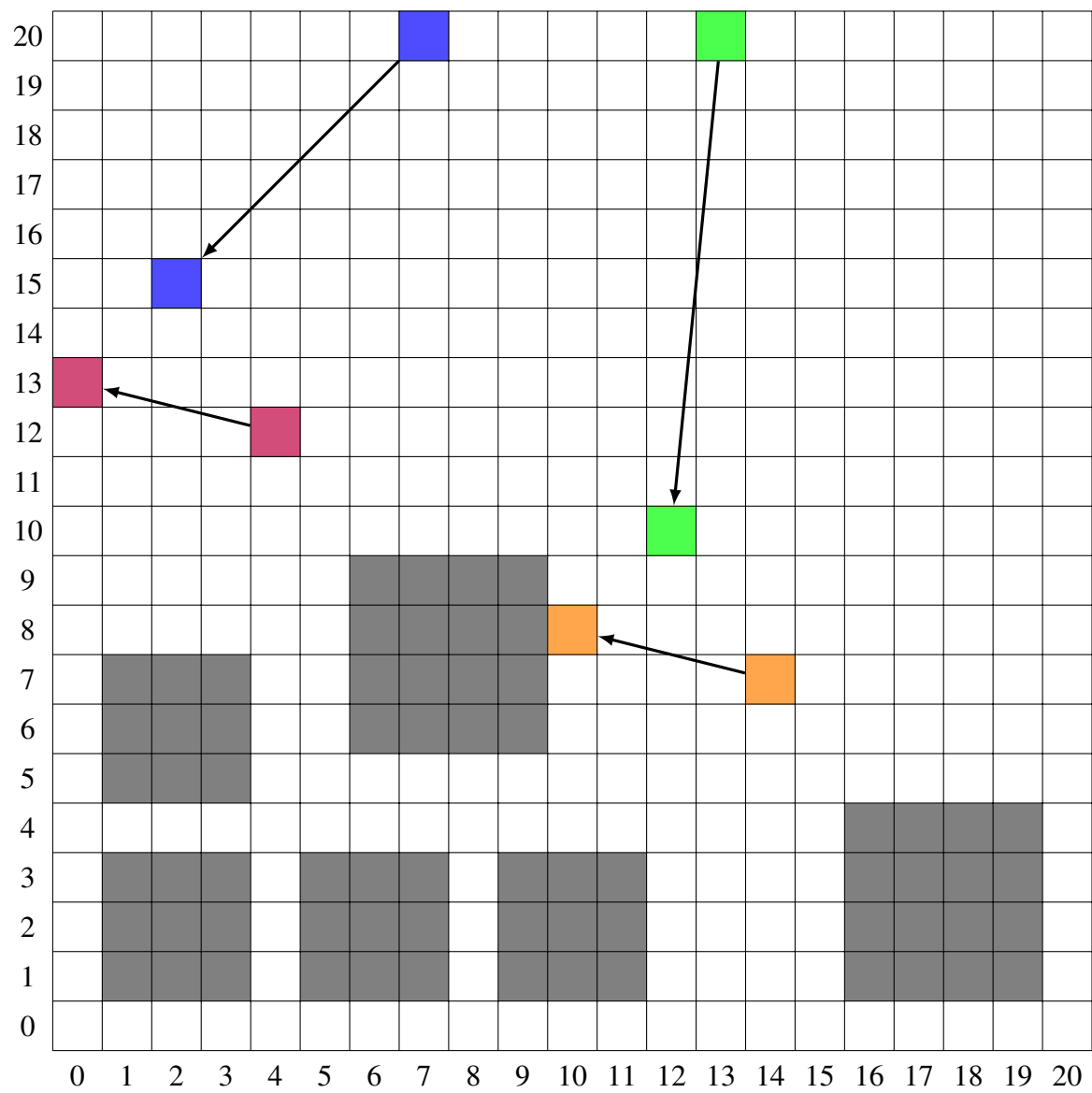


Figure 34: DAC05_subproblem_34

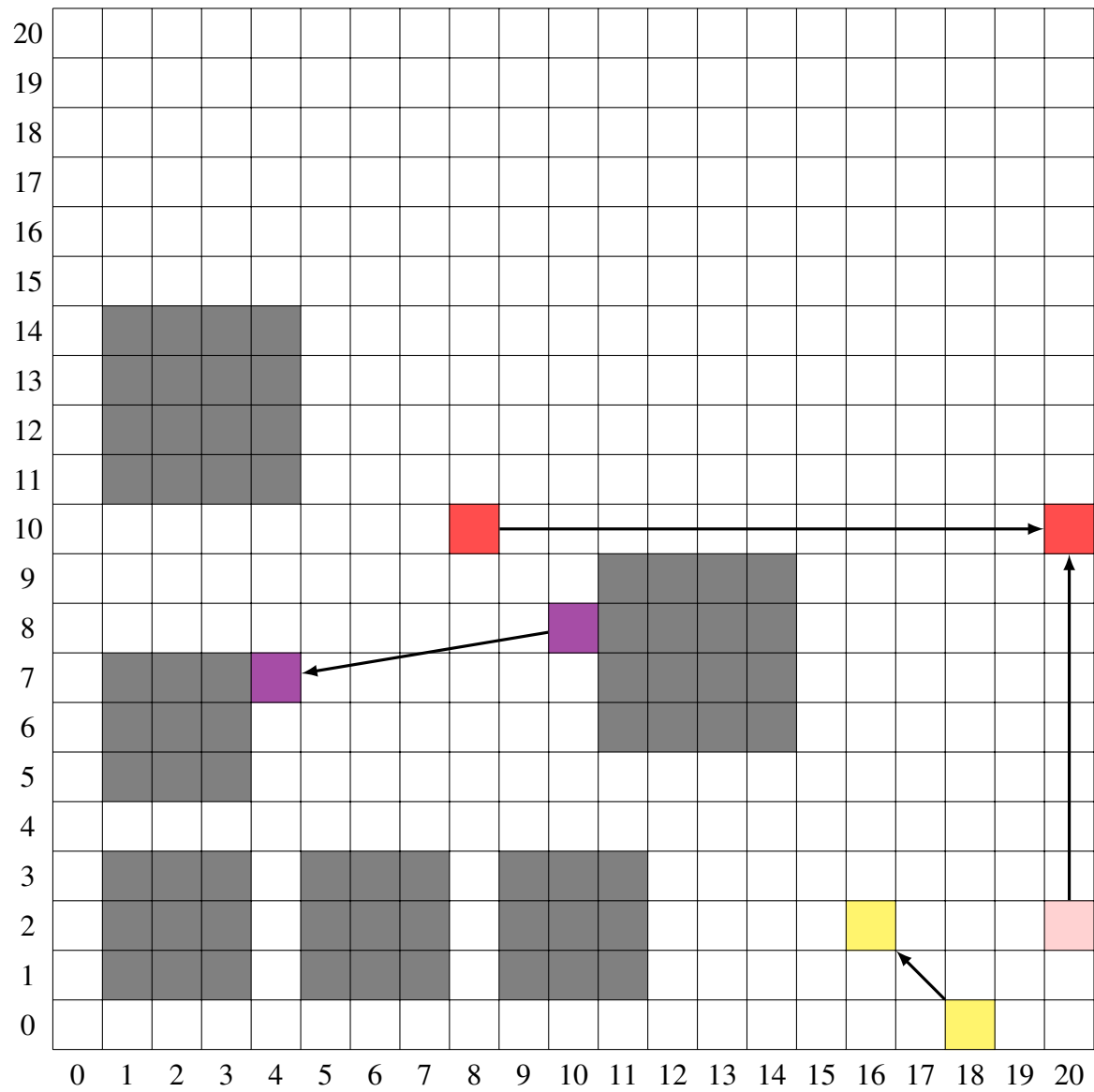


Figure 35: DAC05_subproblem_35

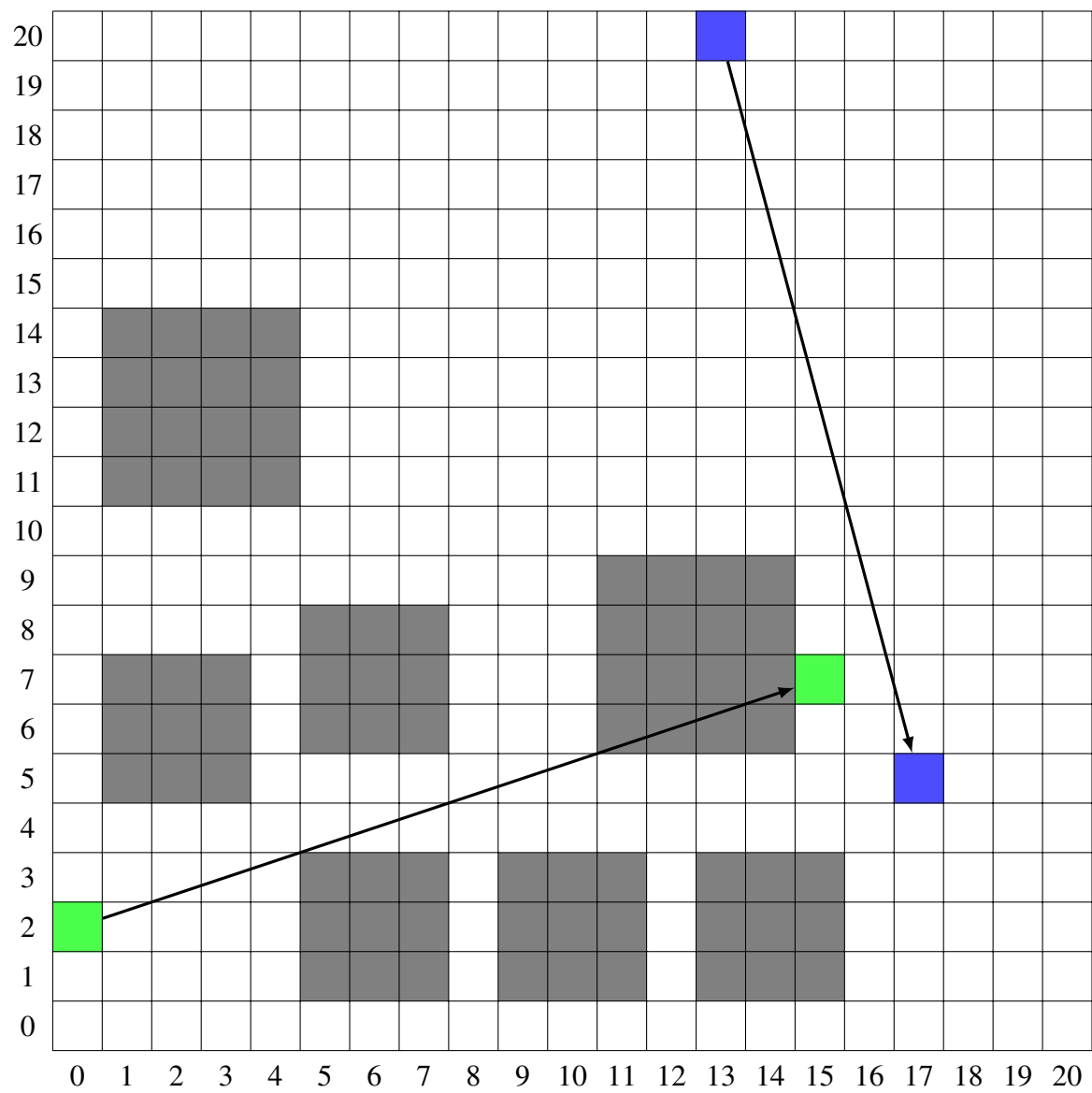


Figure 36: DAC05_subproblem_36

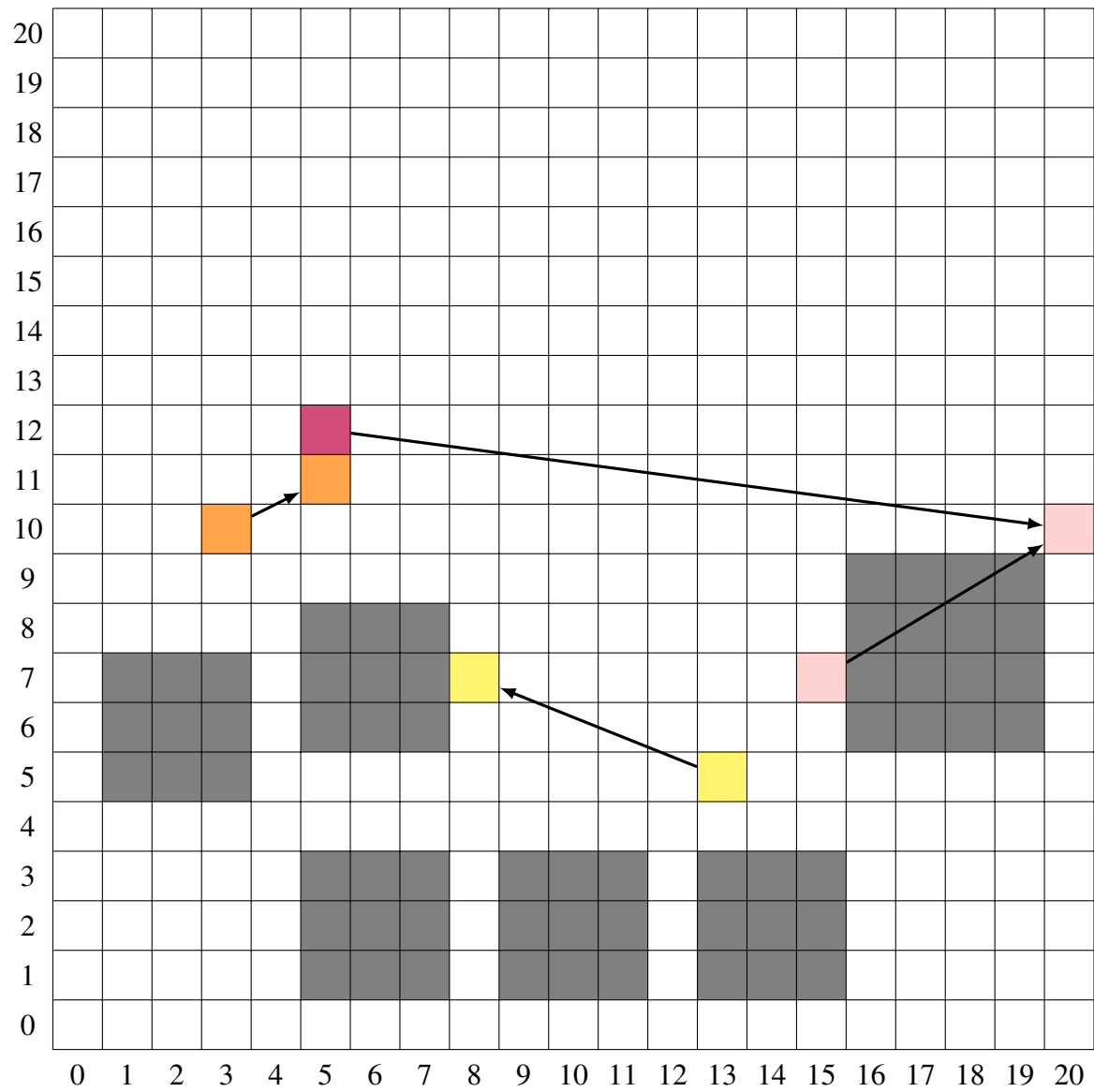


Figure 37: DAC05_subproblem_37

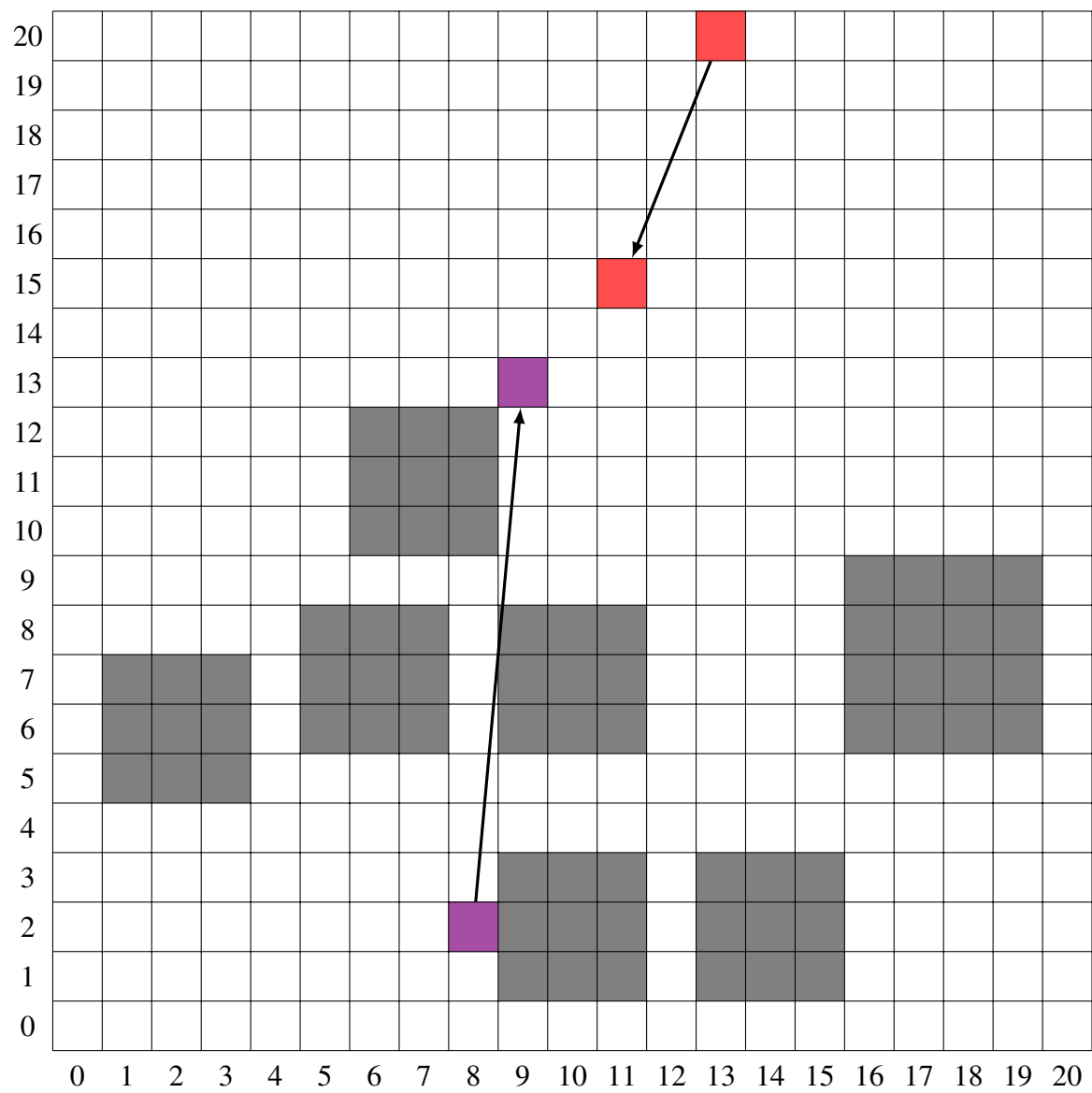


Figure 38: DAC05_subproblem_38

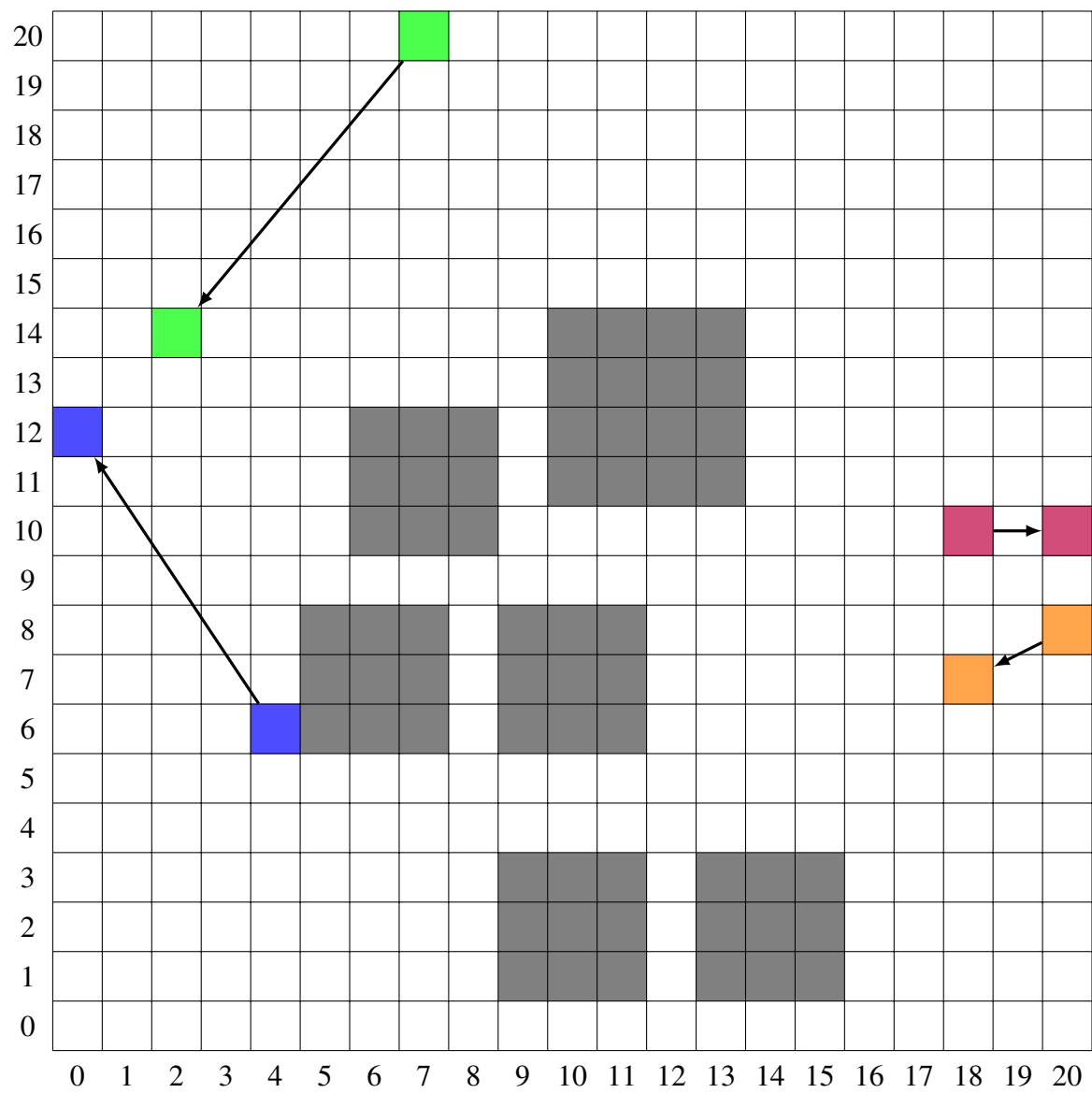


Figure 39: DAC05_subproblem_39

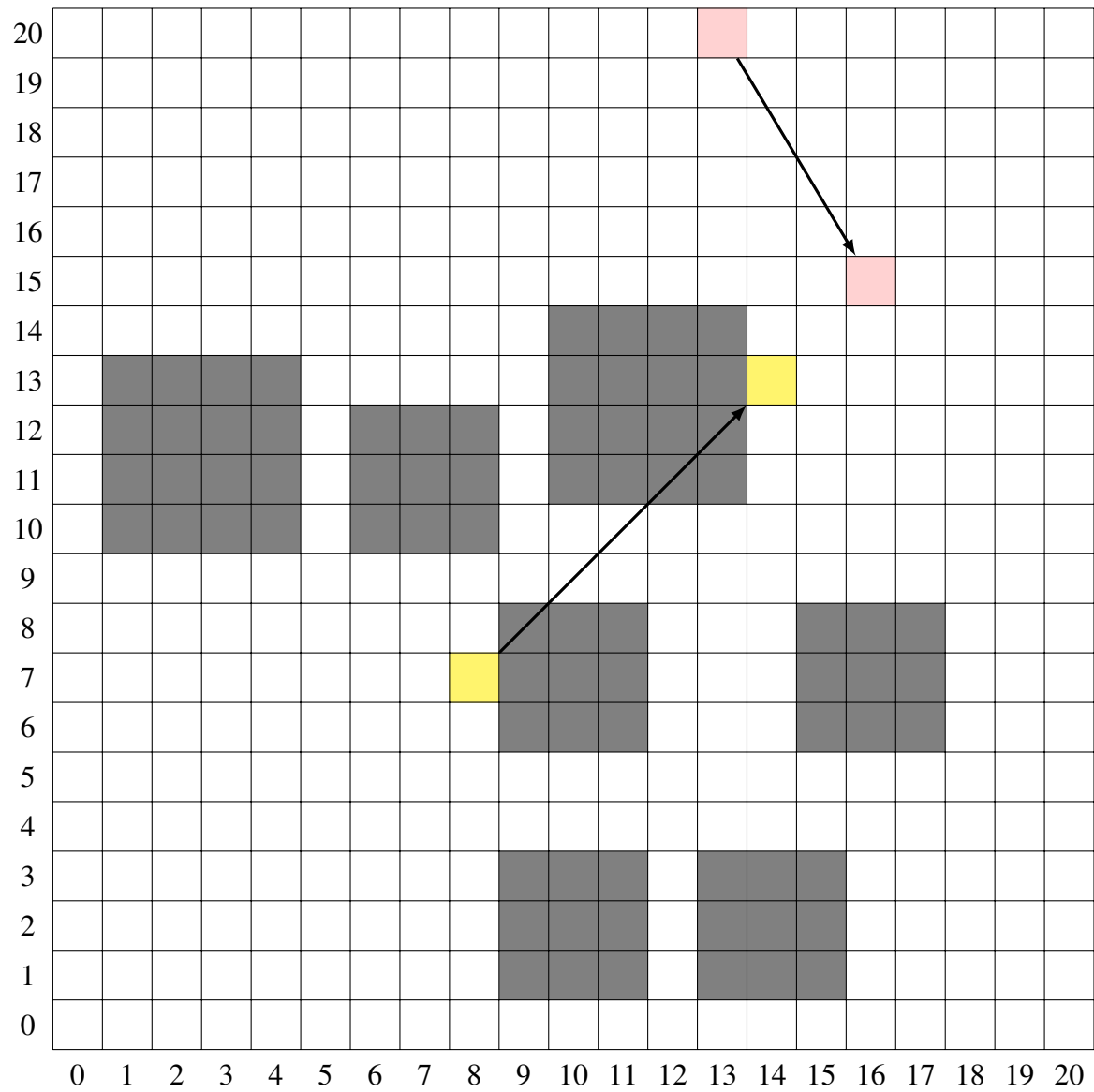


Figure 40: DAC05_subproblem_40

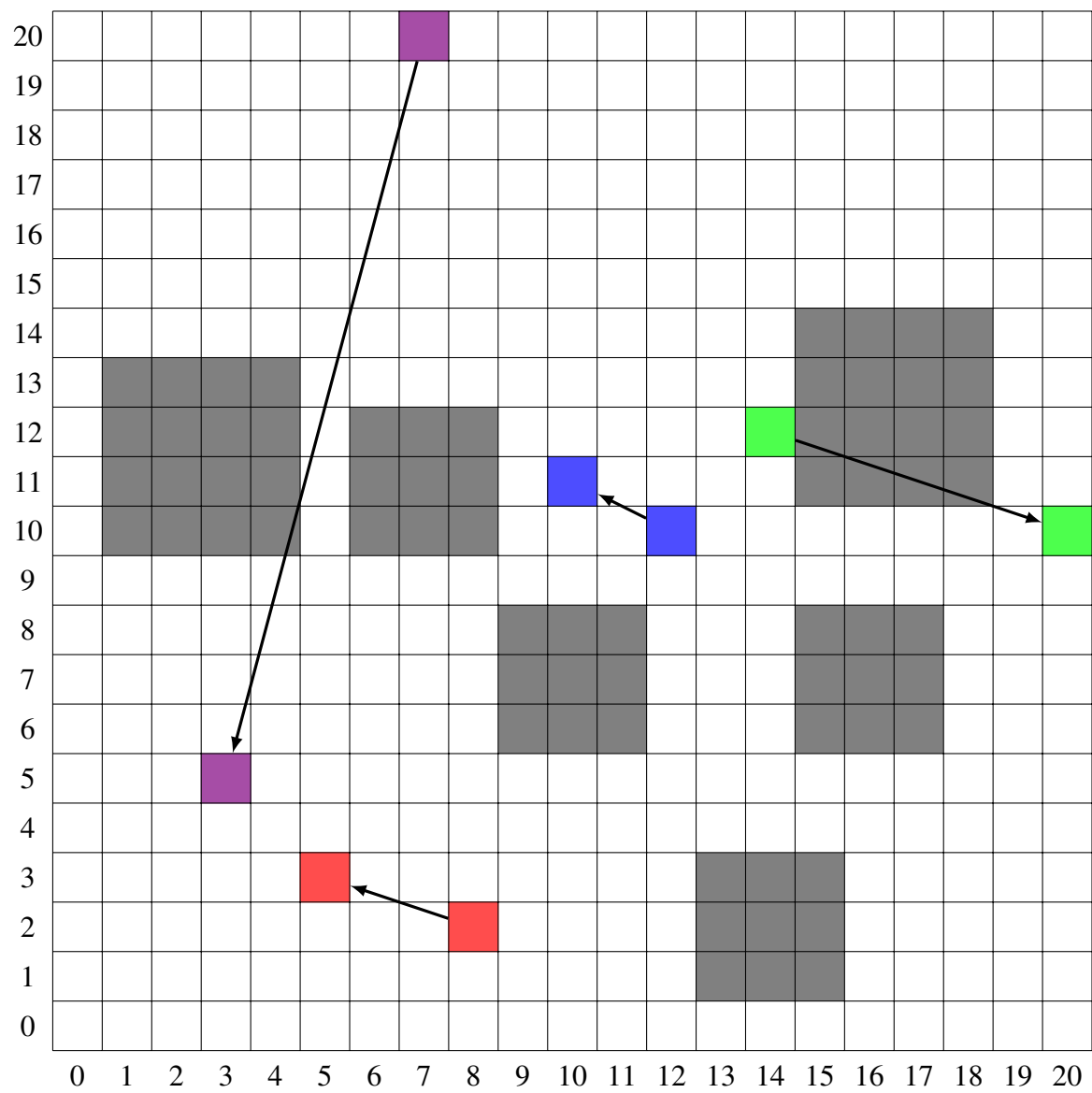


Figure 41: DAC05_subproblem_41

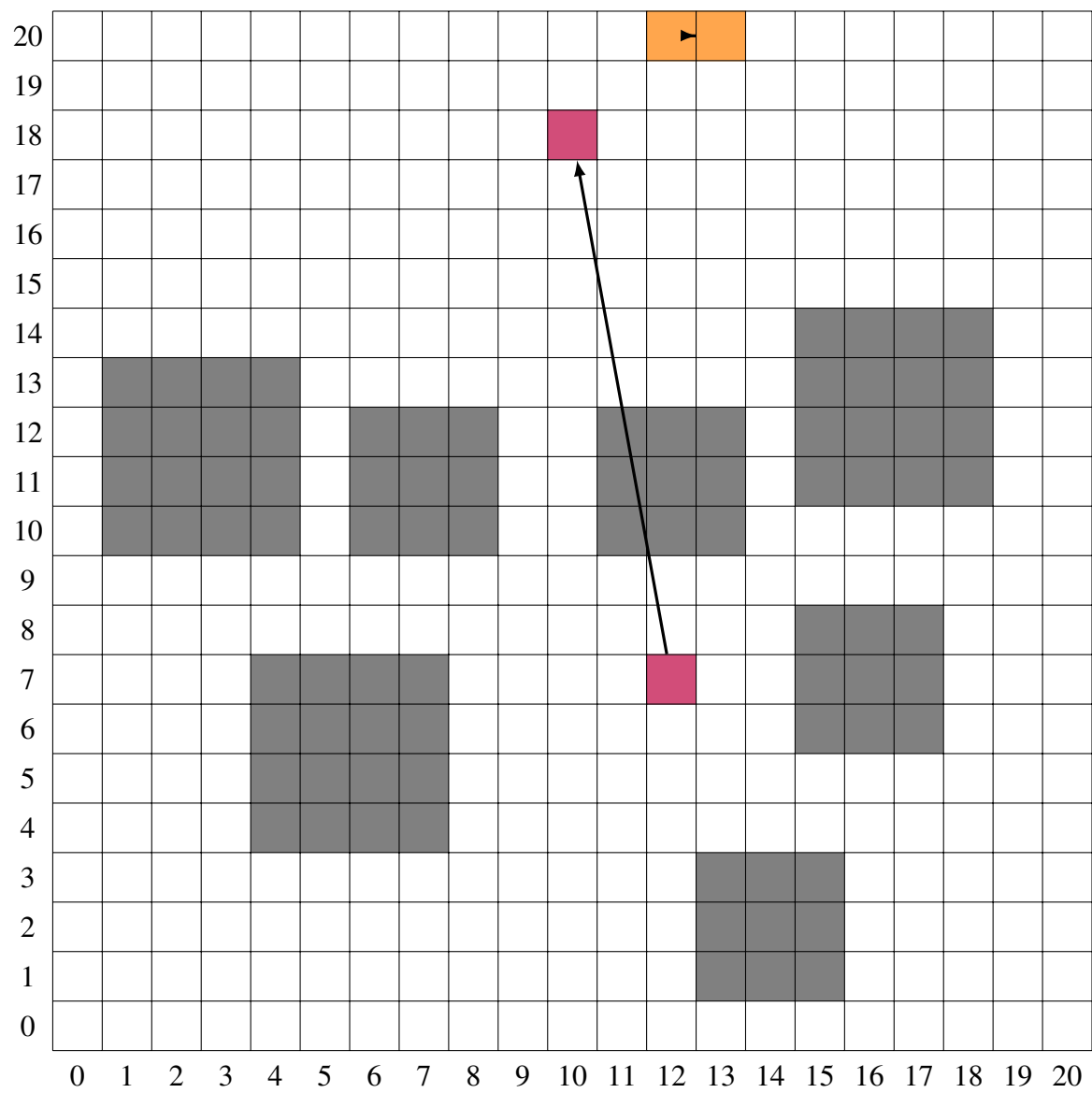
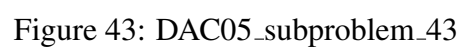


Figure 42: DAC05_subproblem_42



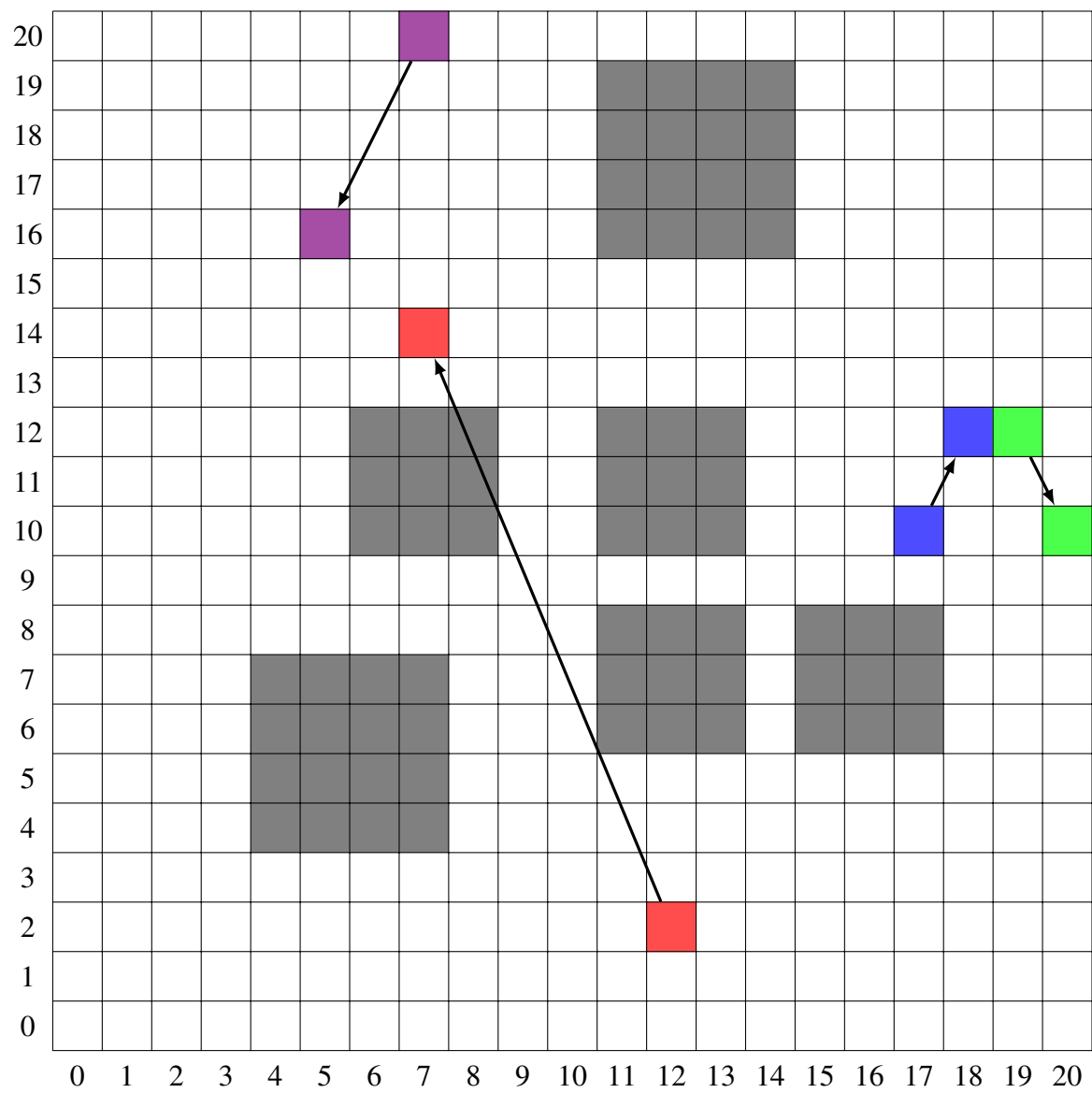


Figure 44: DAC05_subproblem_44

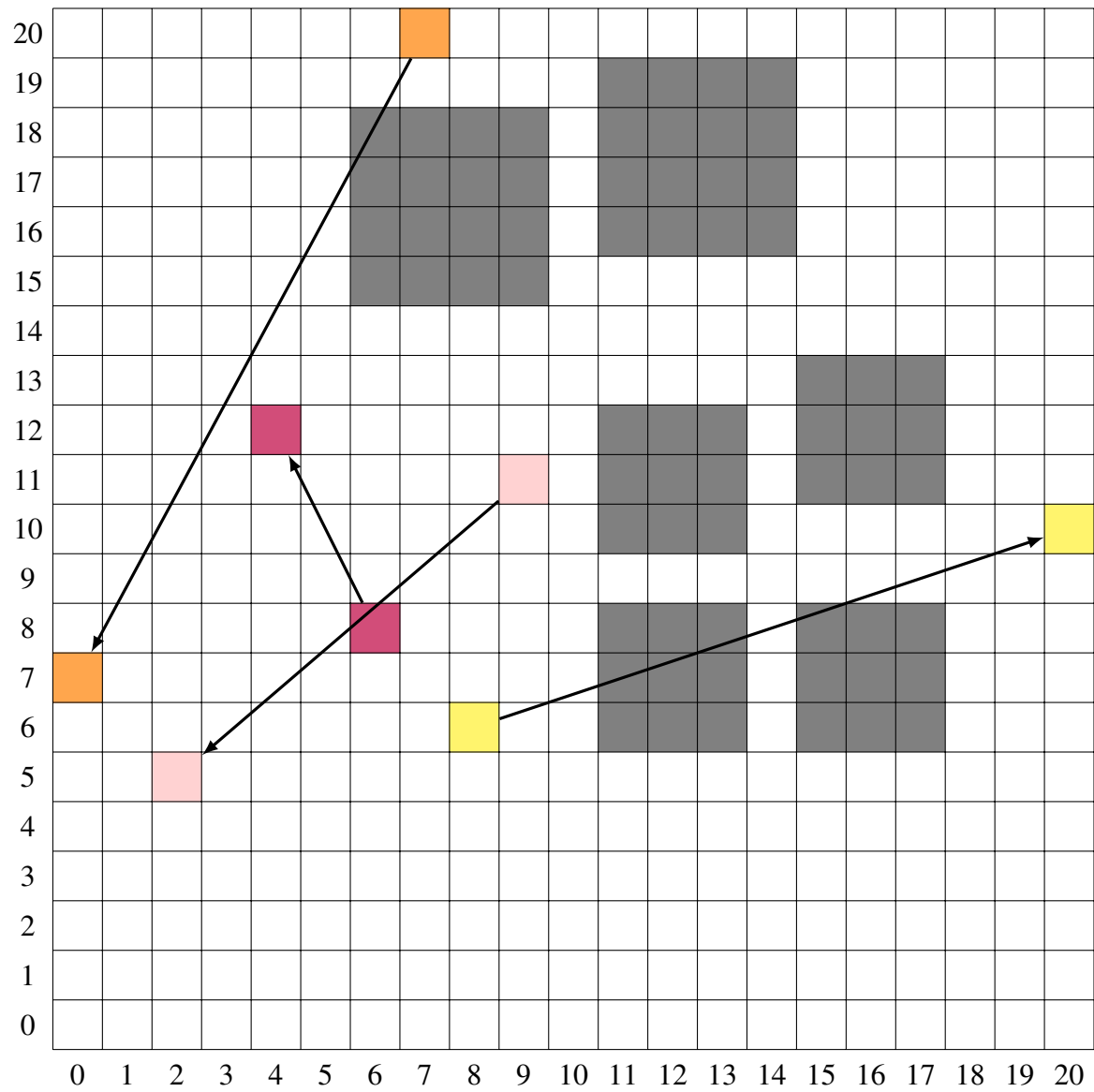


Figure 45: DAC05_subproblem_45

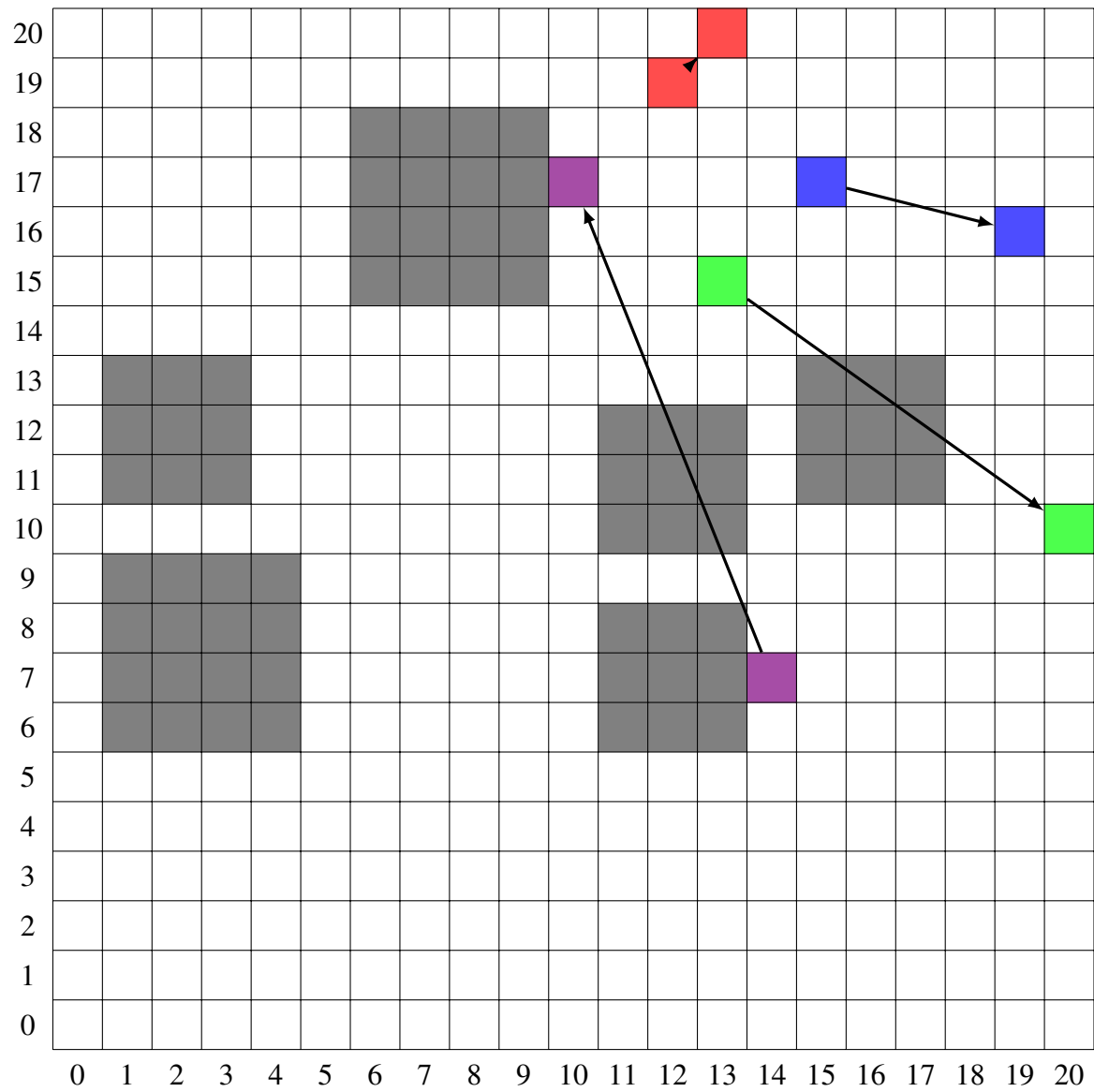


Figure 46: DAC05_subproblem_46

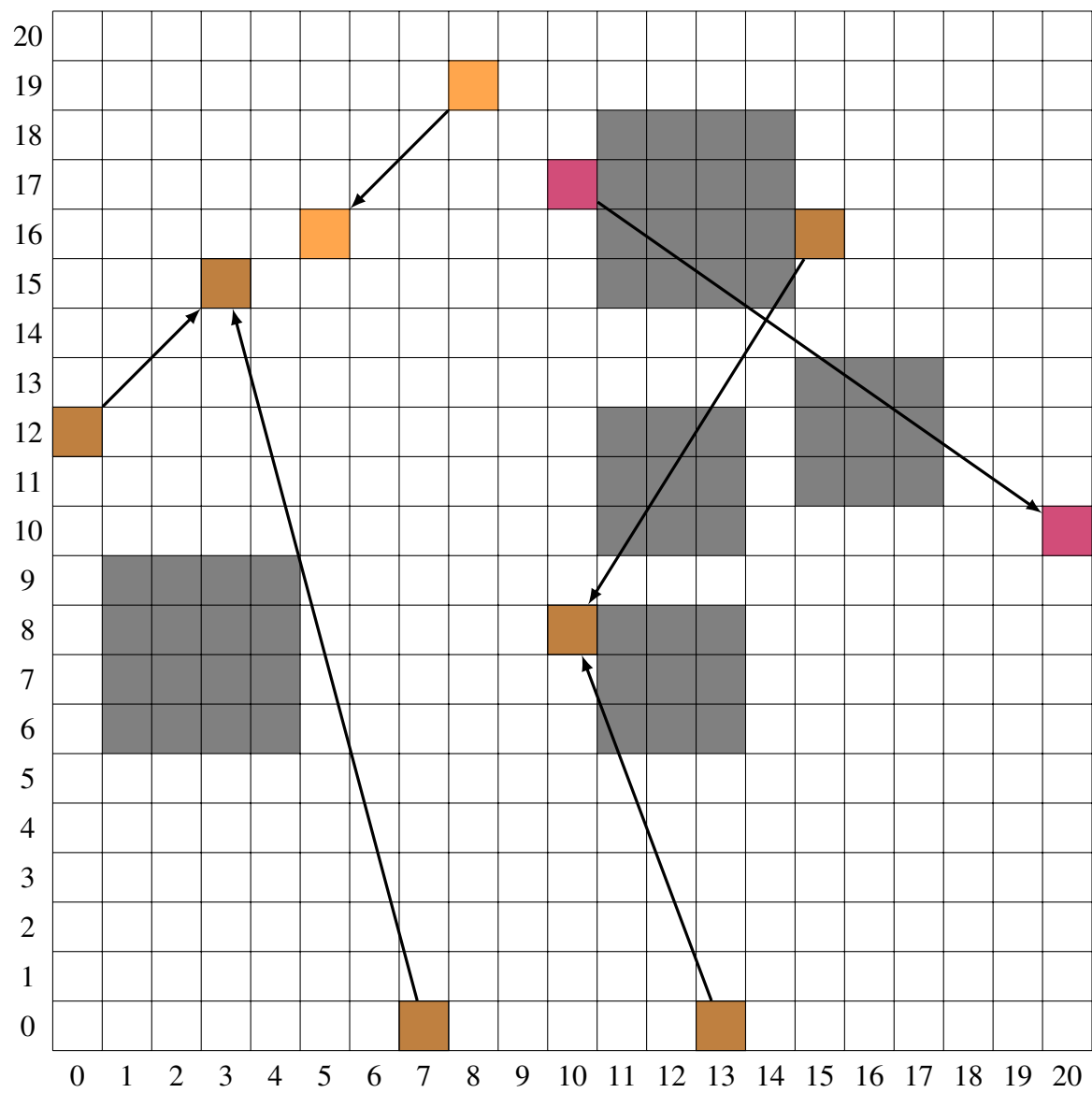


Figure 47: DAC05_subproblem_47

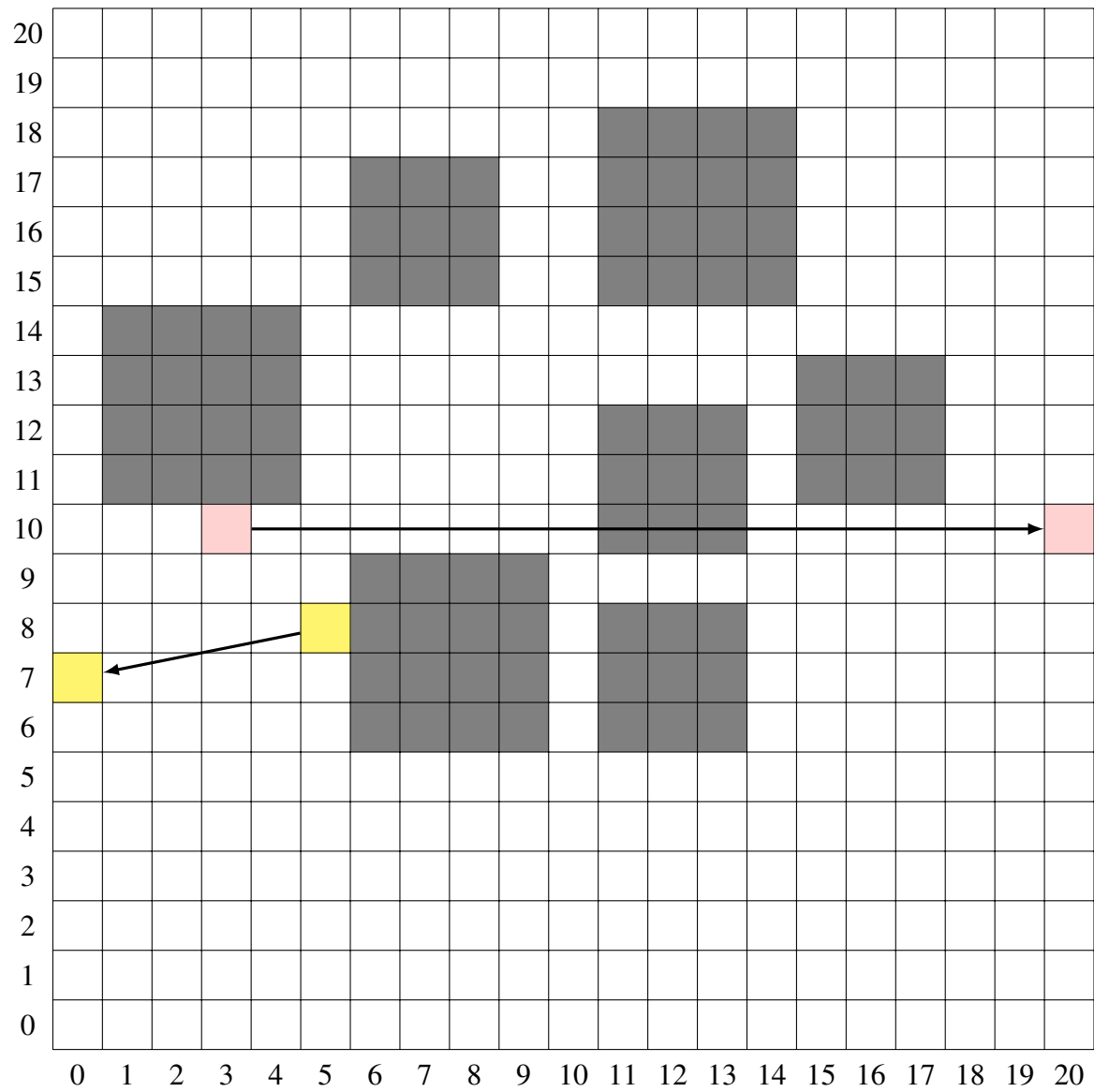


Figure 48: DAC05_subproblem_48

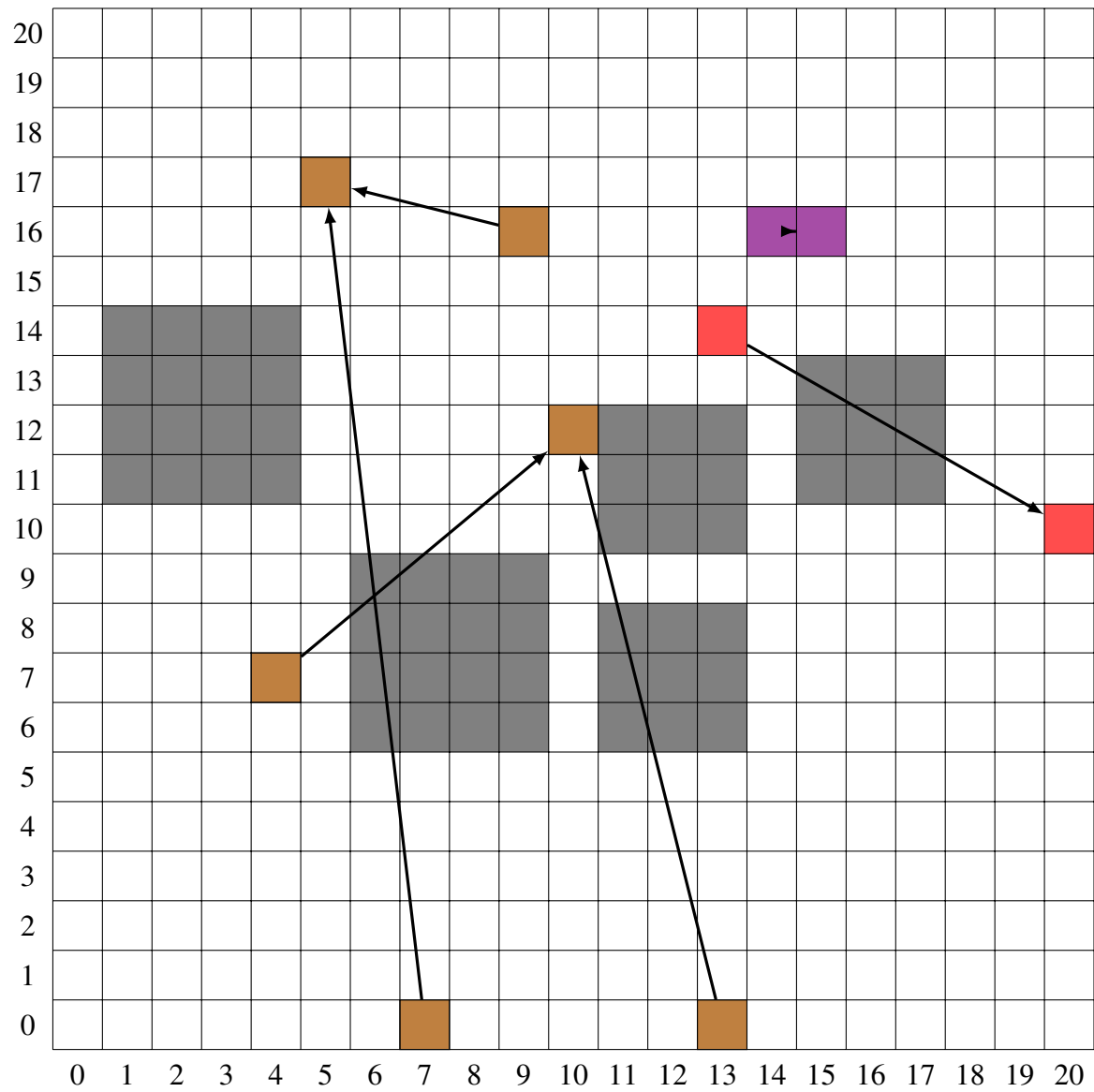


Figure 49: DAC05_subproblem_49

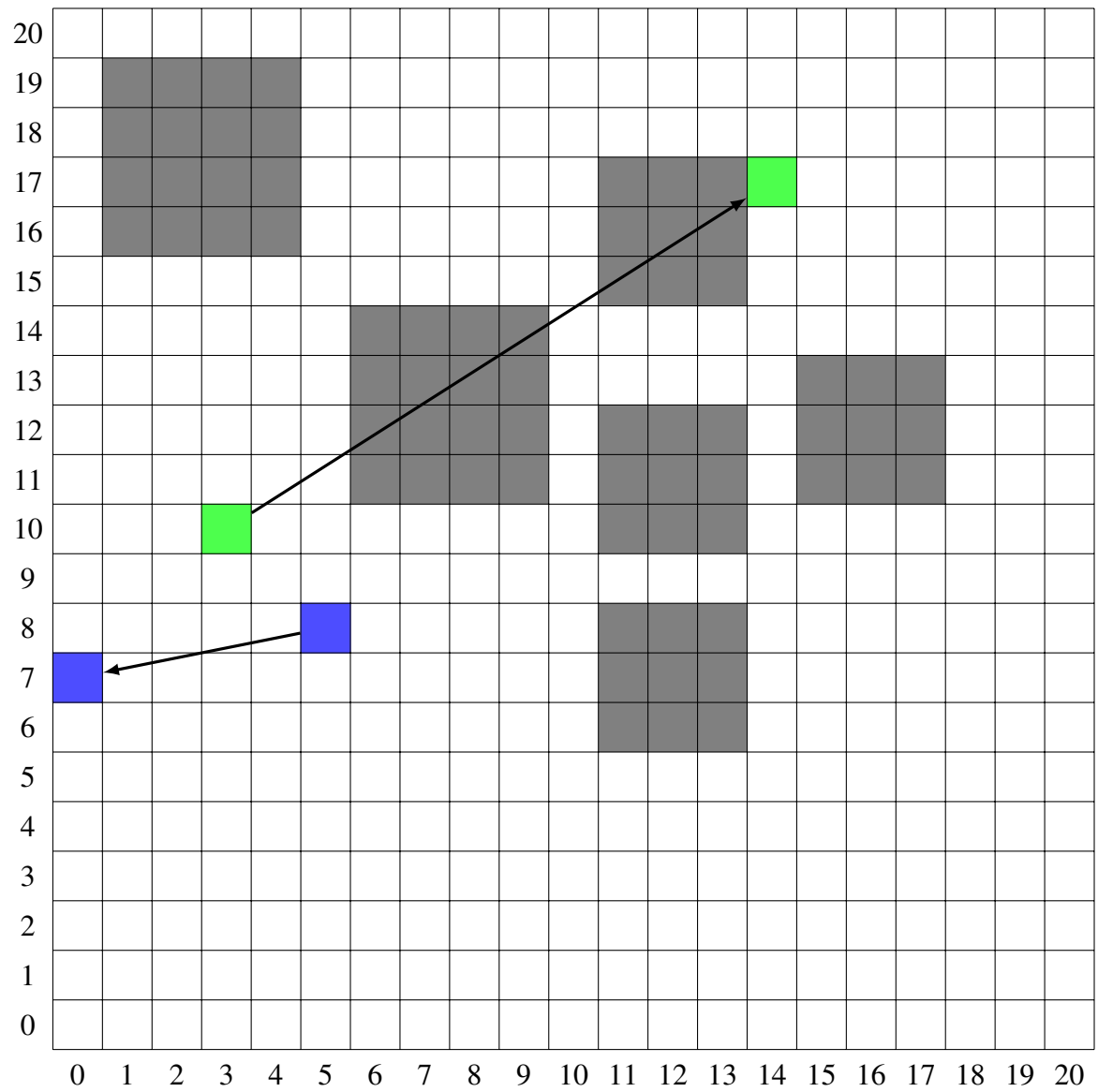


Figure 50: DAC05_subproblem_50

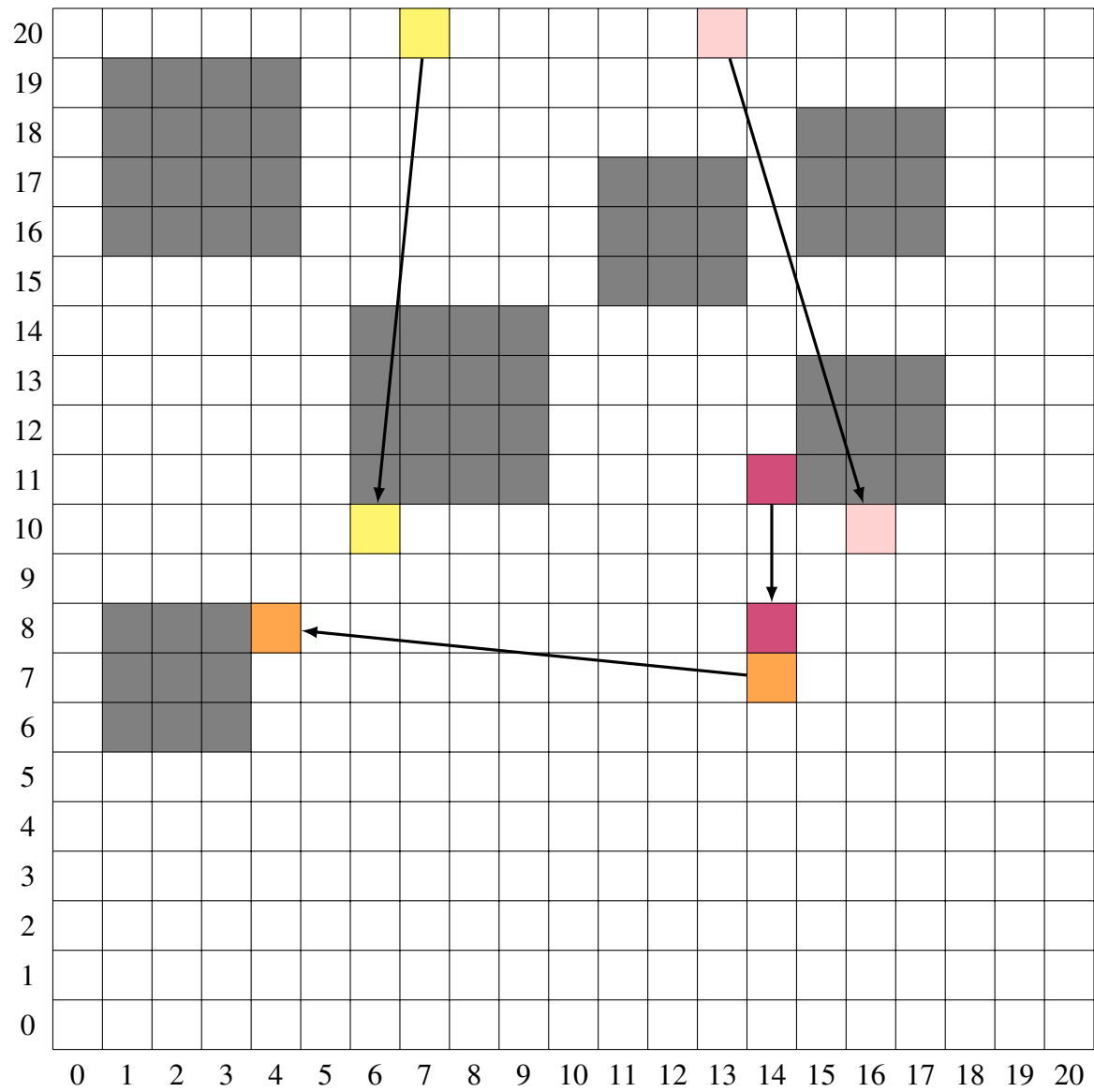


Figure 51: DAC05_subproblem_51

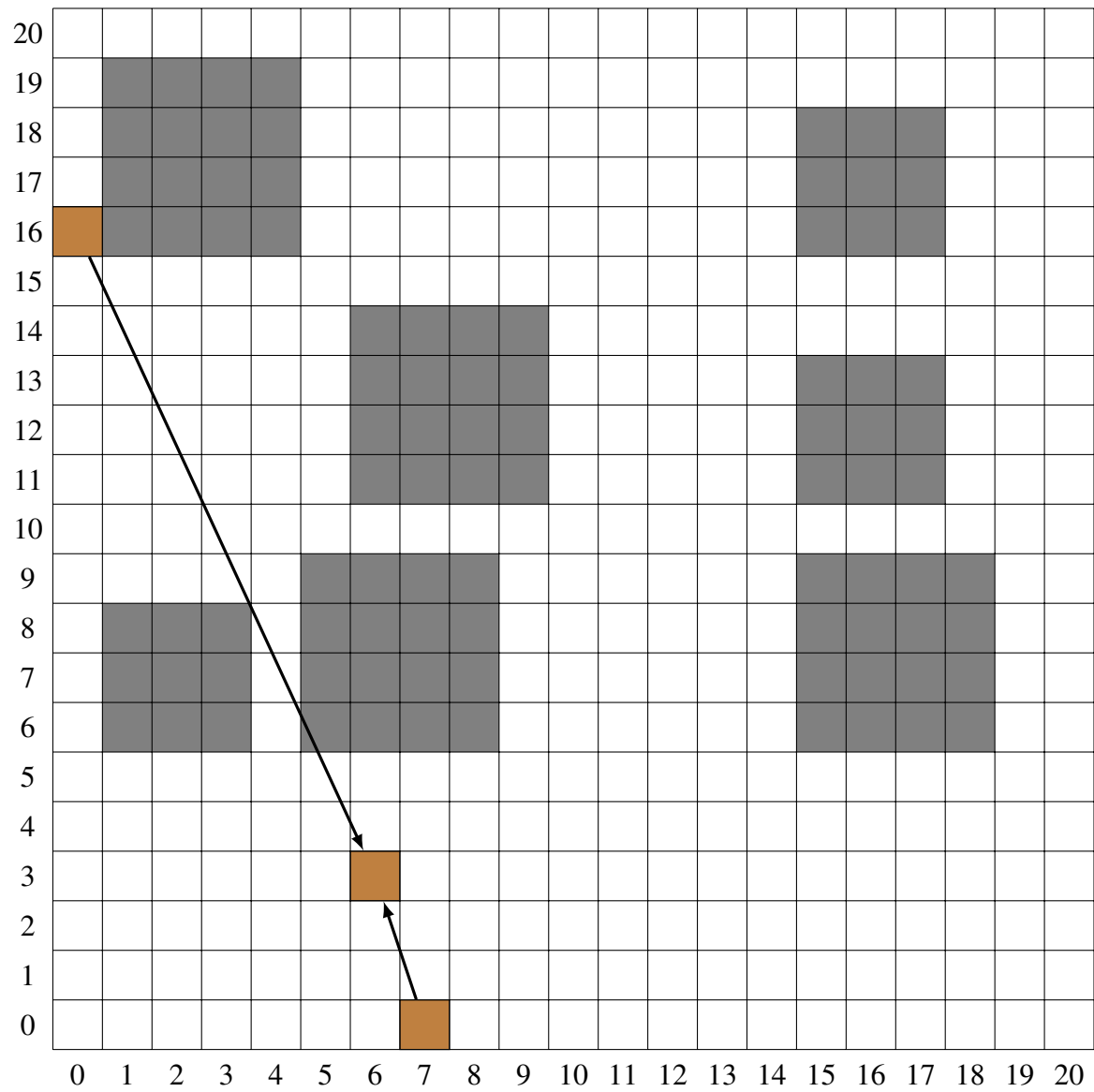


Figure 52: DAC05_subproblem_52

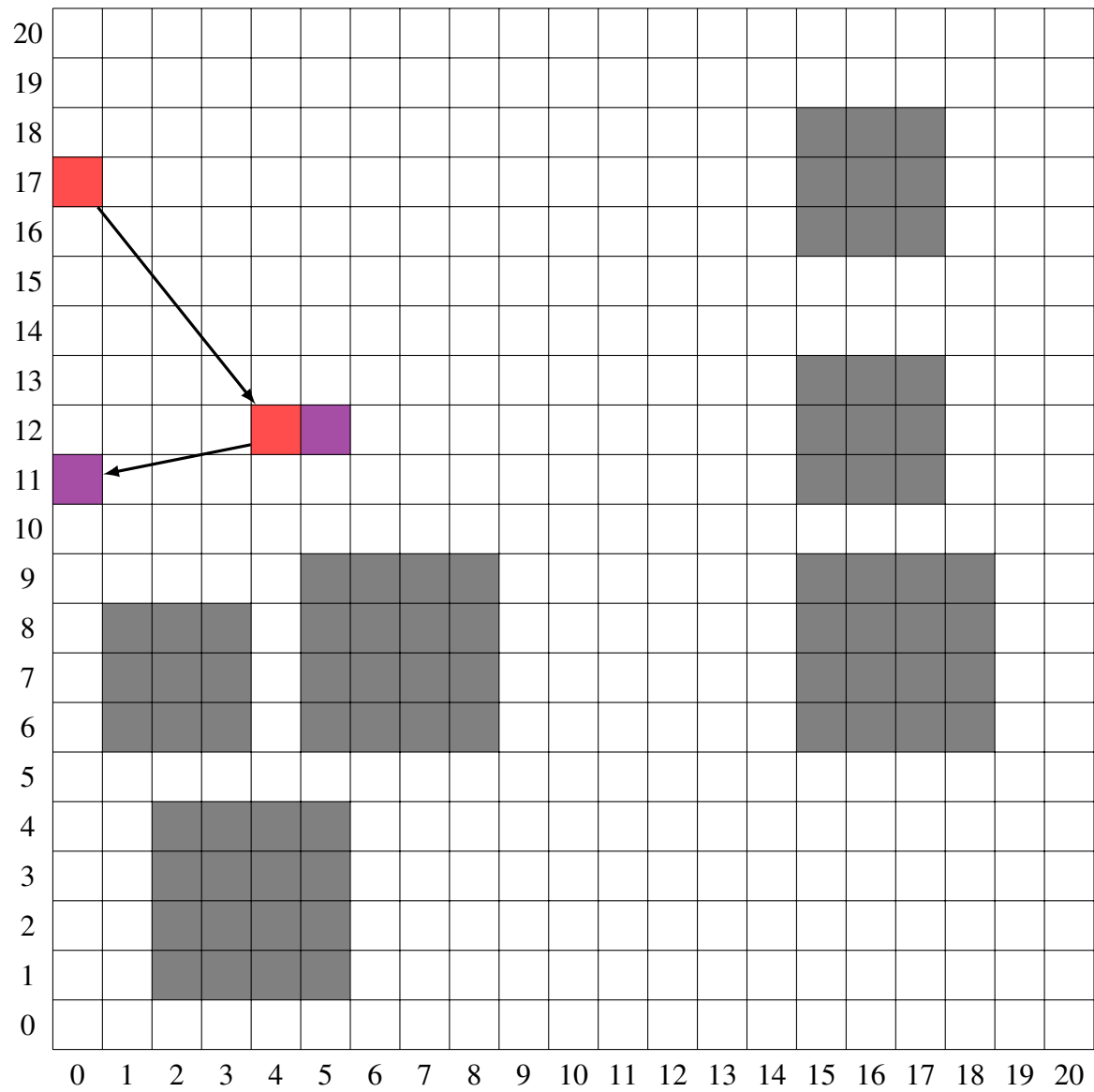


Figure 53: DAC05_subproblem_53

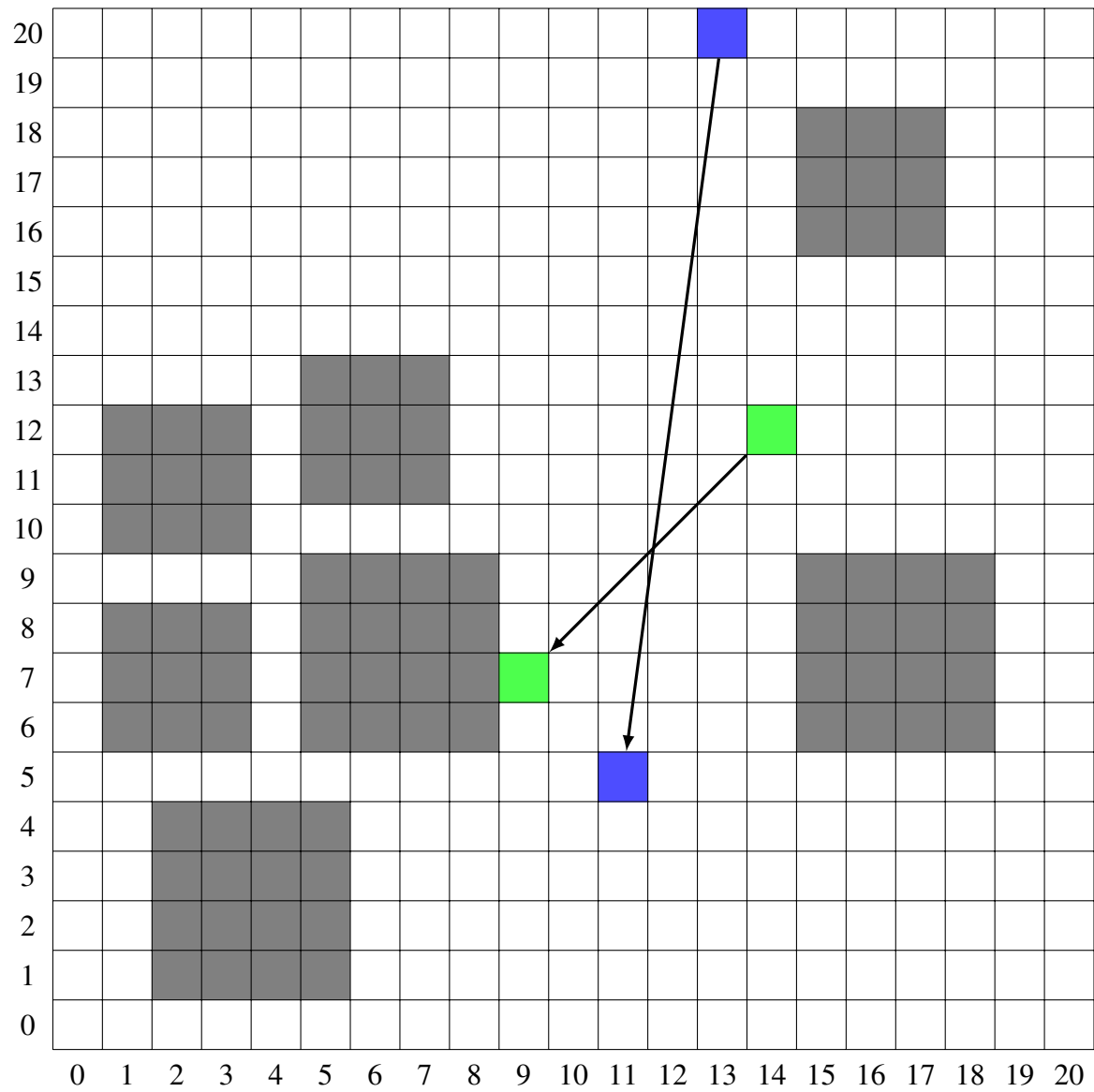


Figure 54: DAC05_subproblem_54

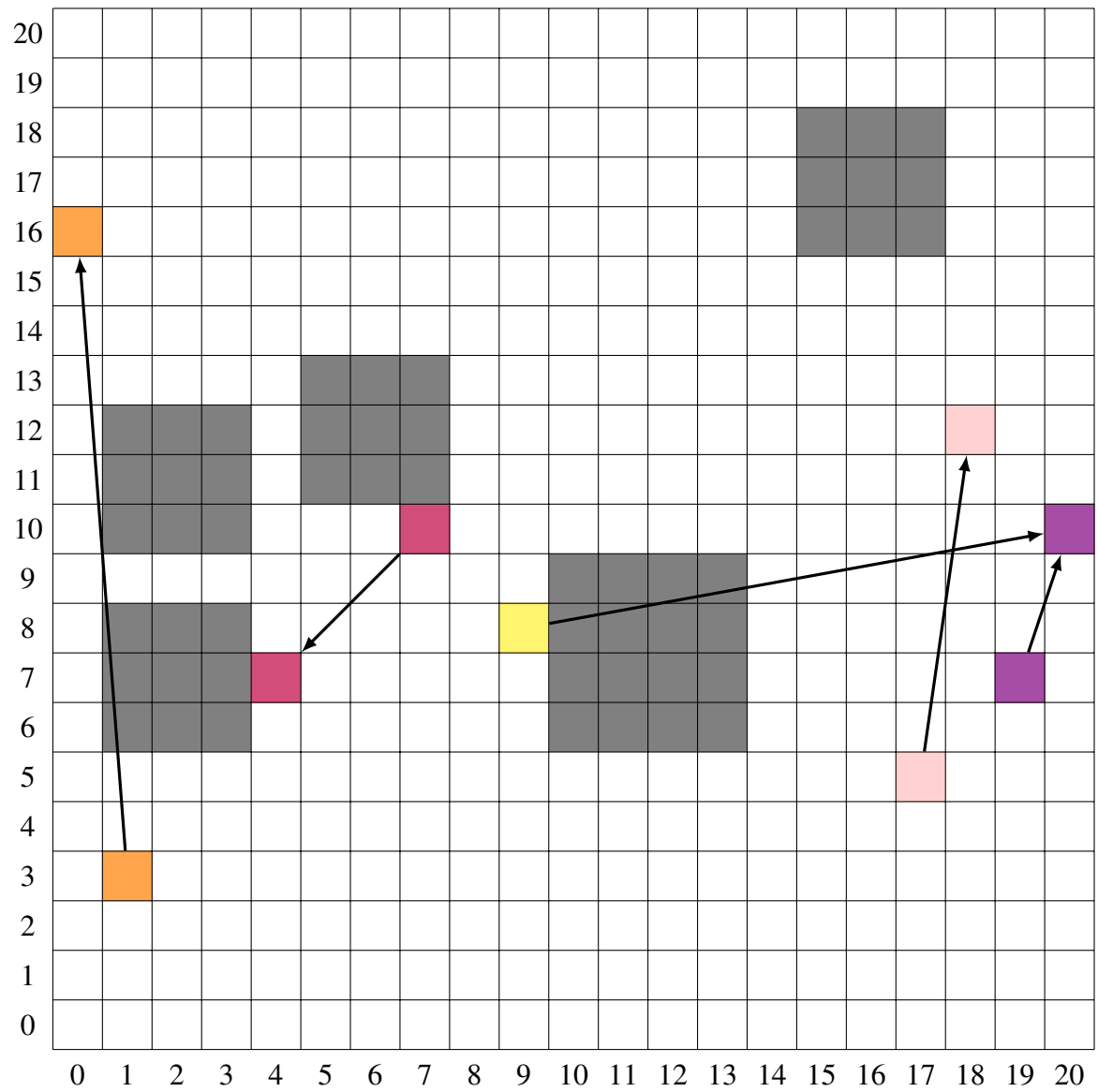


Figure 55: DAC05_subproblem_55

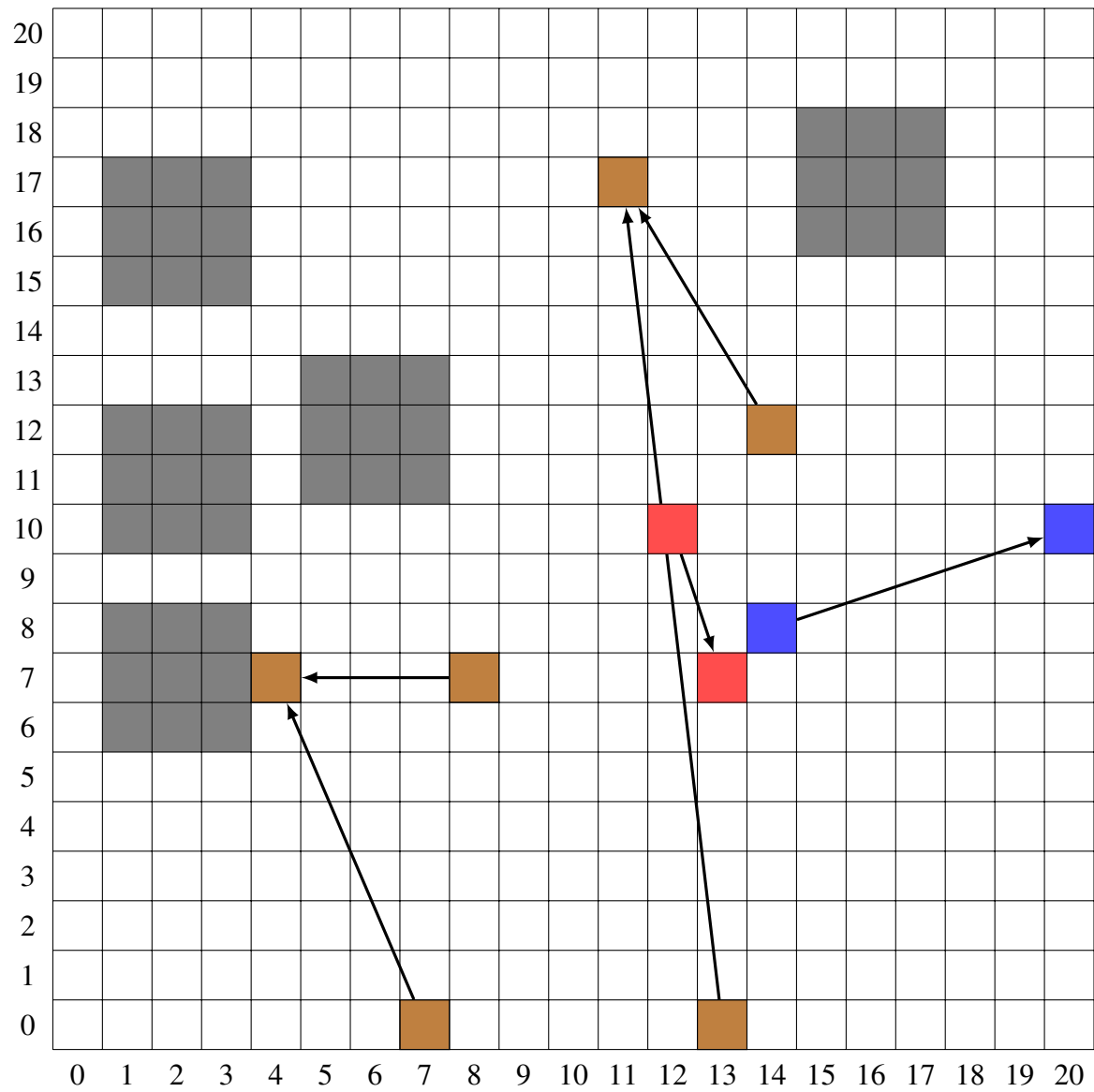


Figure 56: DAC05_subproblem_56

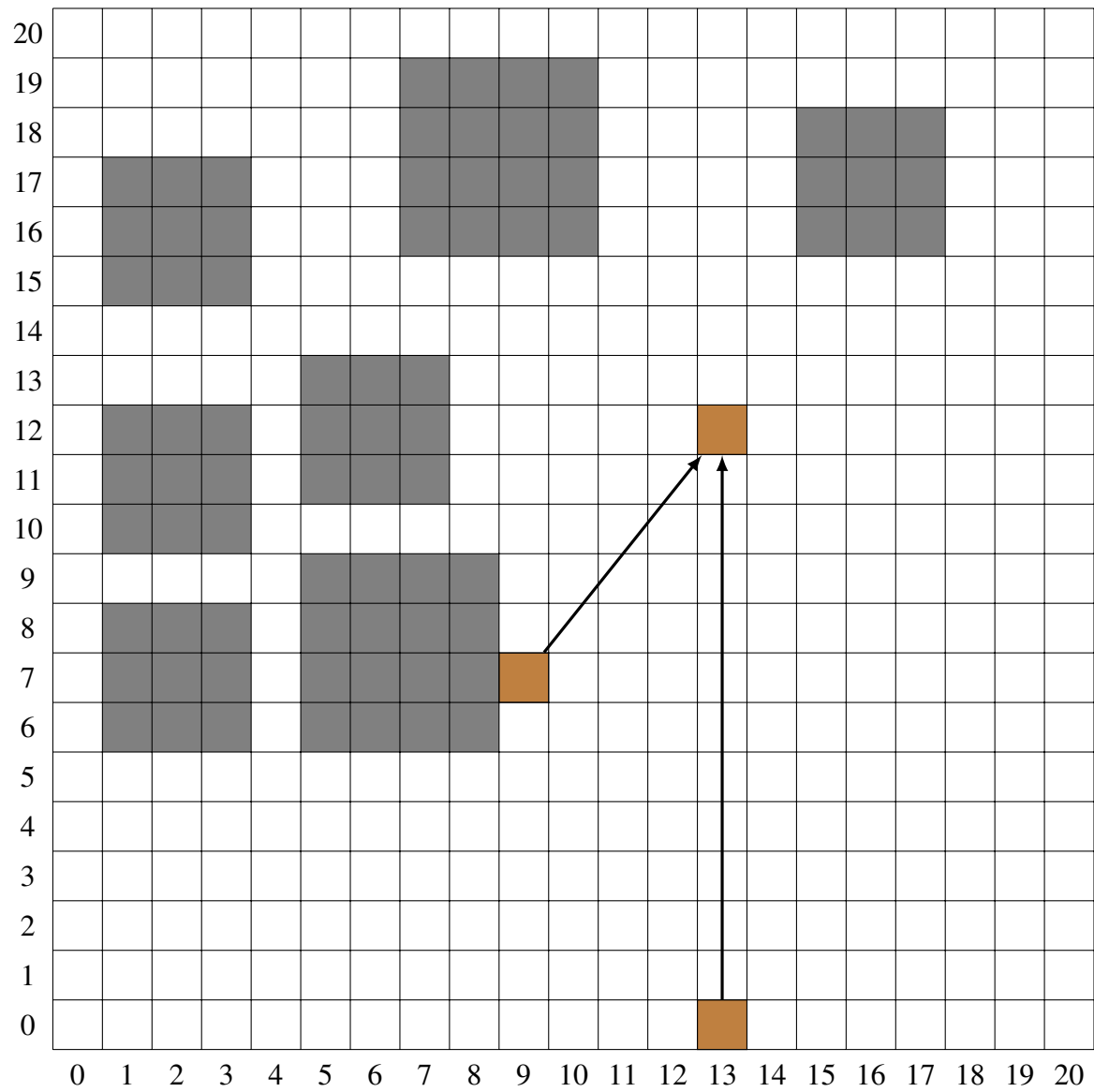


Figure 57: DAC05_subproblem_57

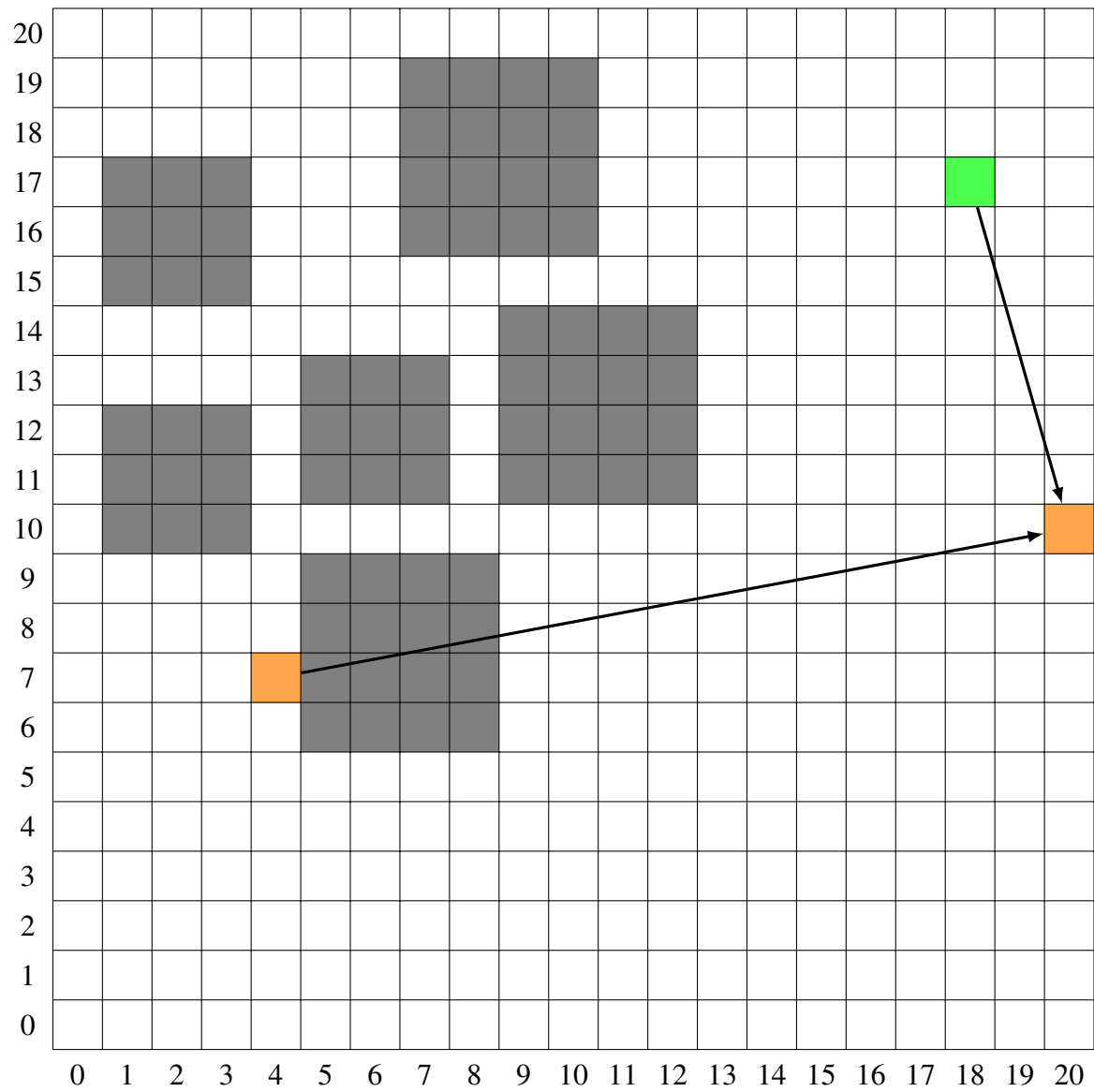


Figure 58: DAC05_subproblem_58

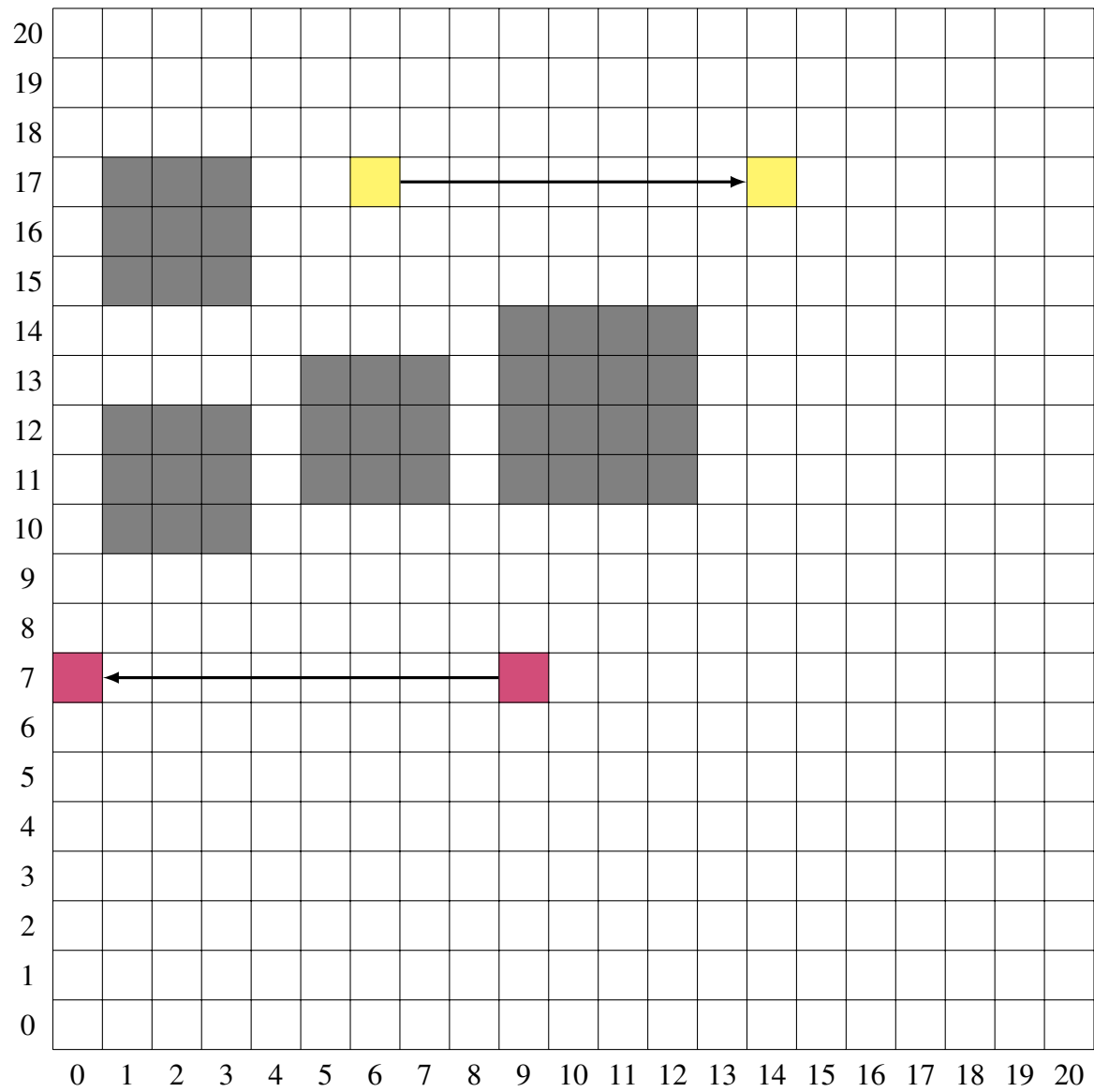


Figure 59: DAC05_subproblem_59

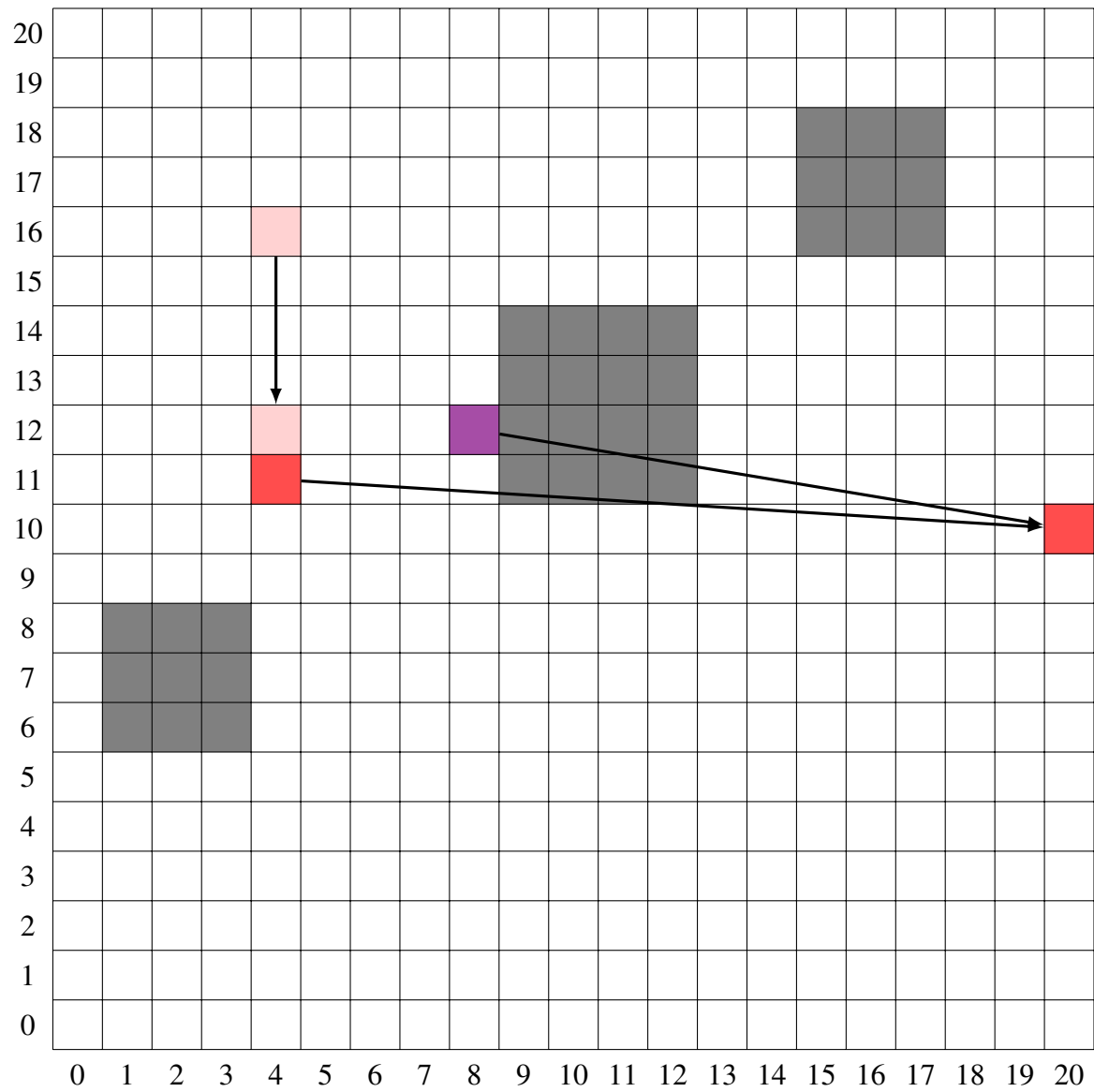


Figure 60: DAC05_subproblem_60

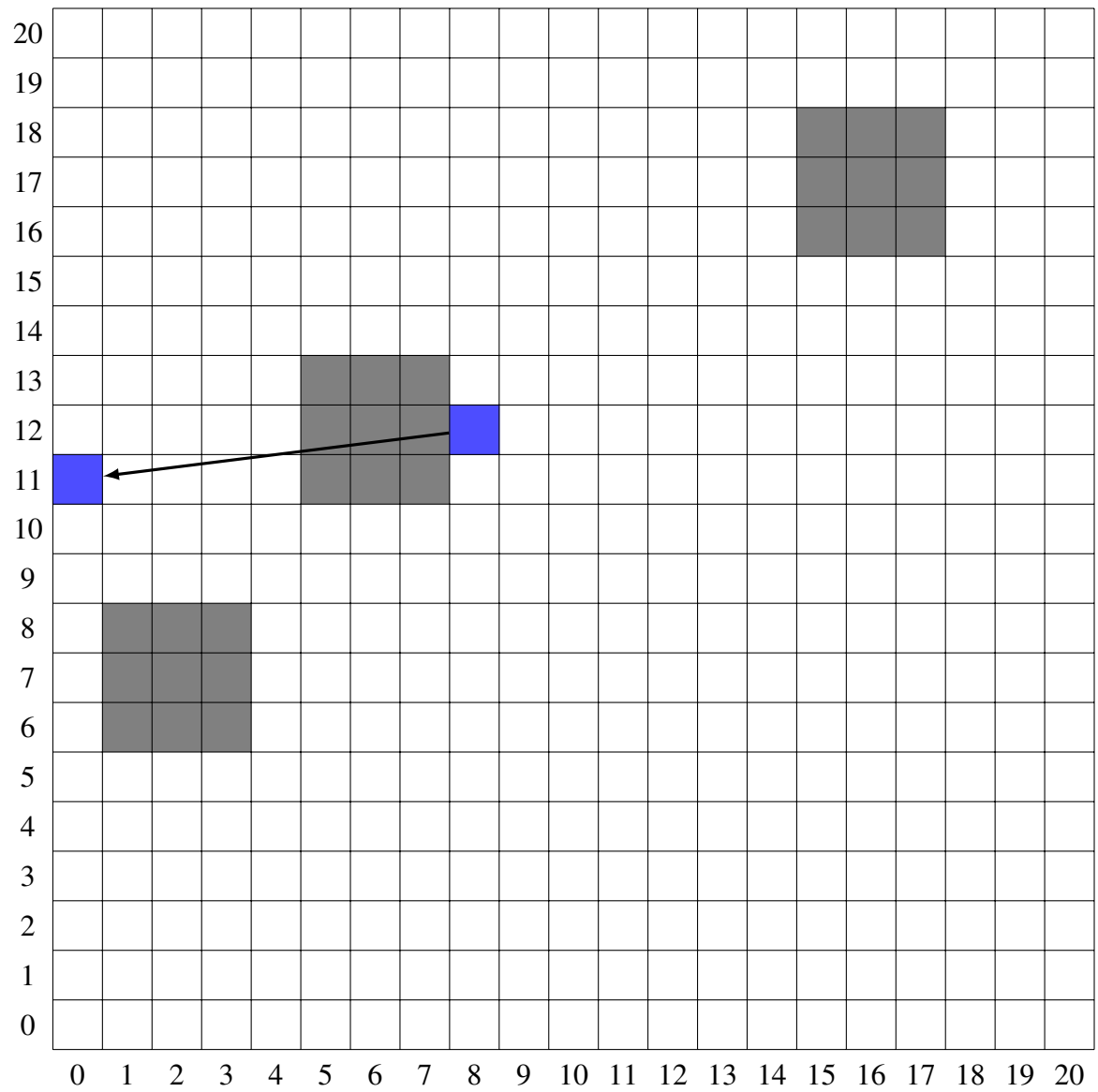


Figure 61: DAC05_subproblem_61

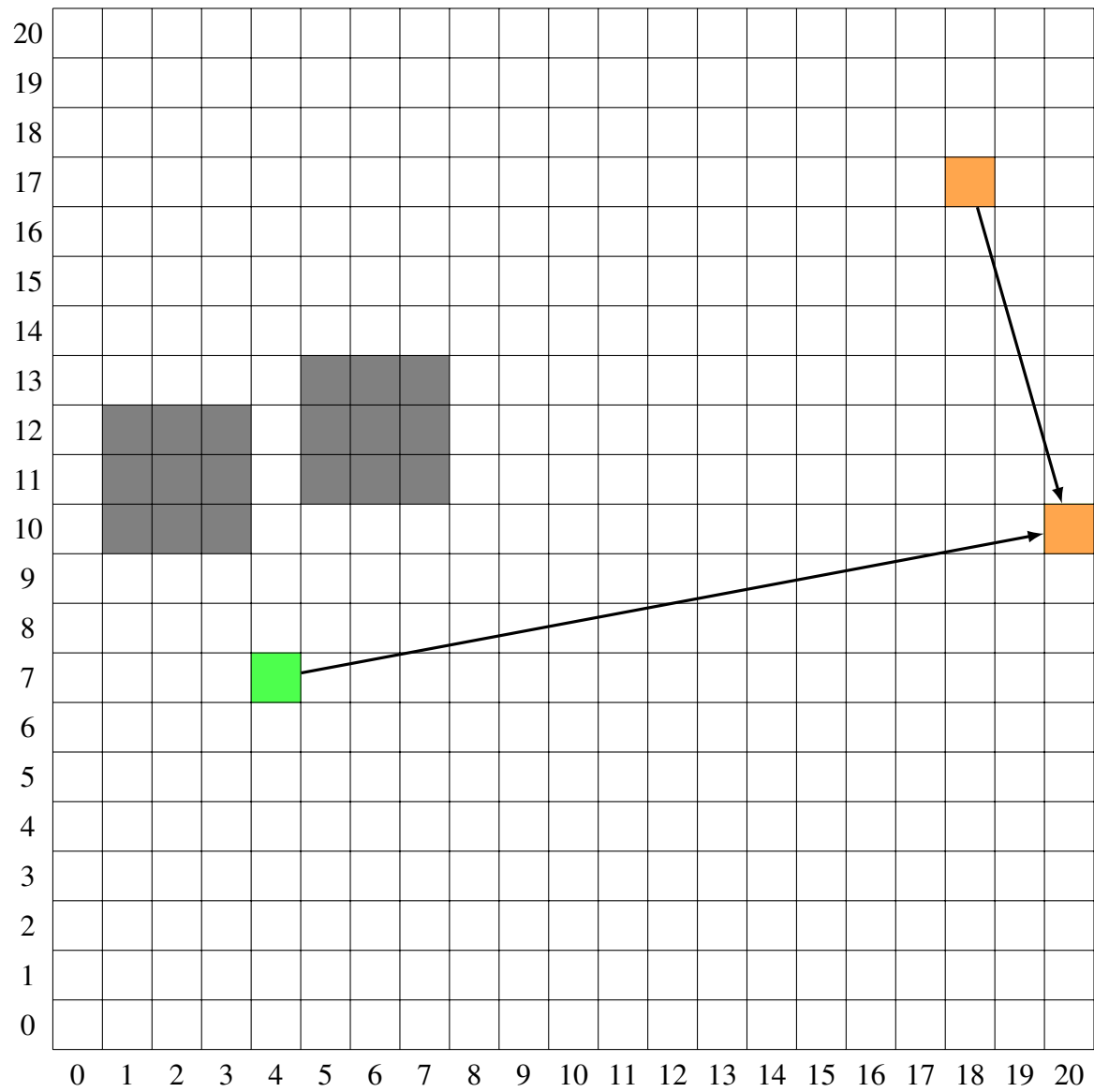


Figure 62: DAC05_subproblem_62

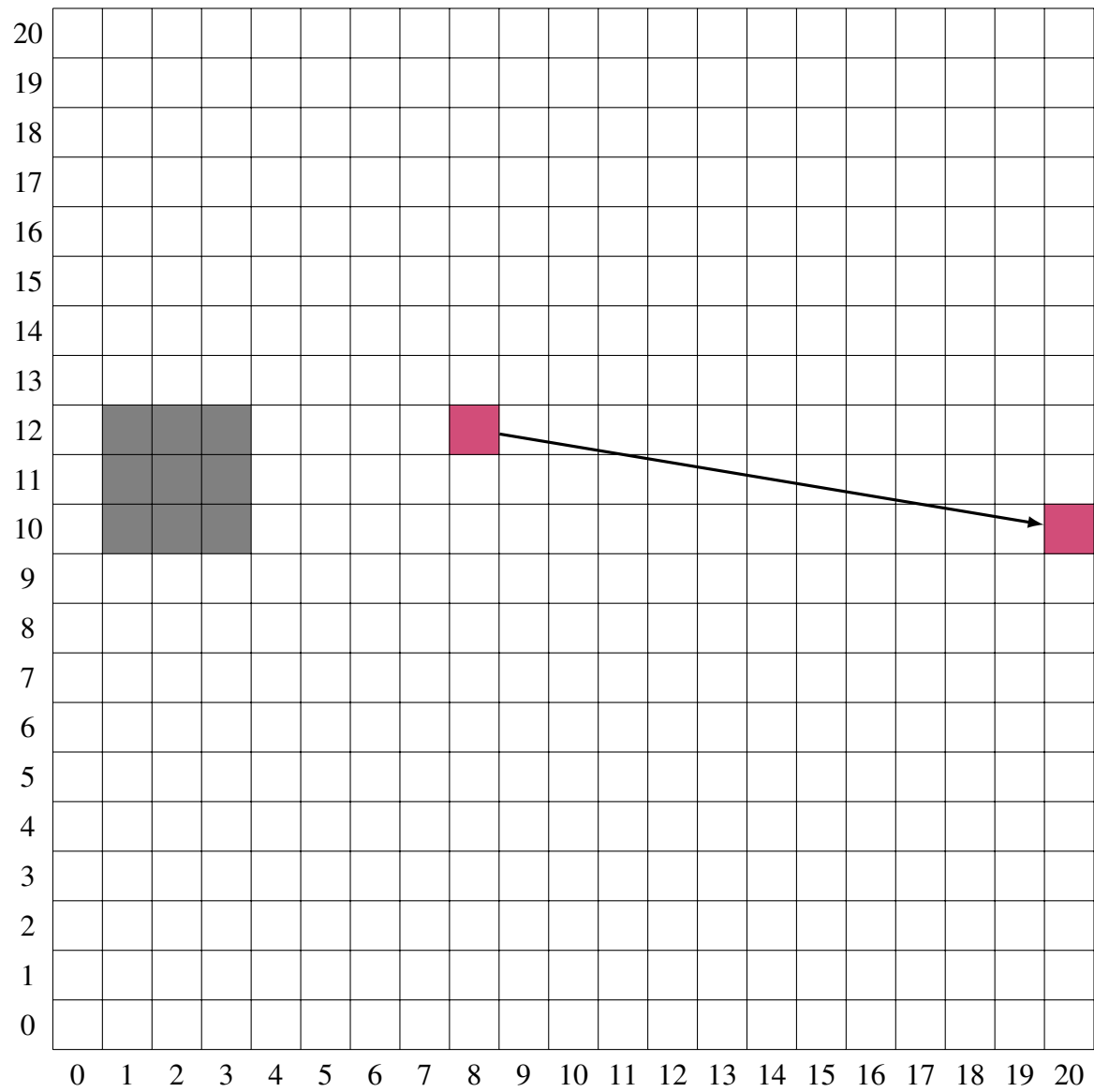


Figure 63: DAC05_subproblem_63

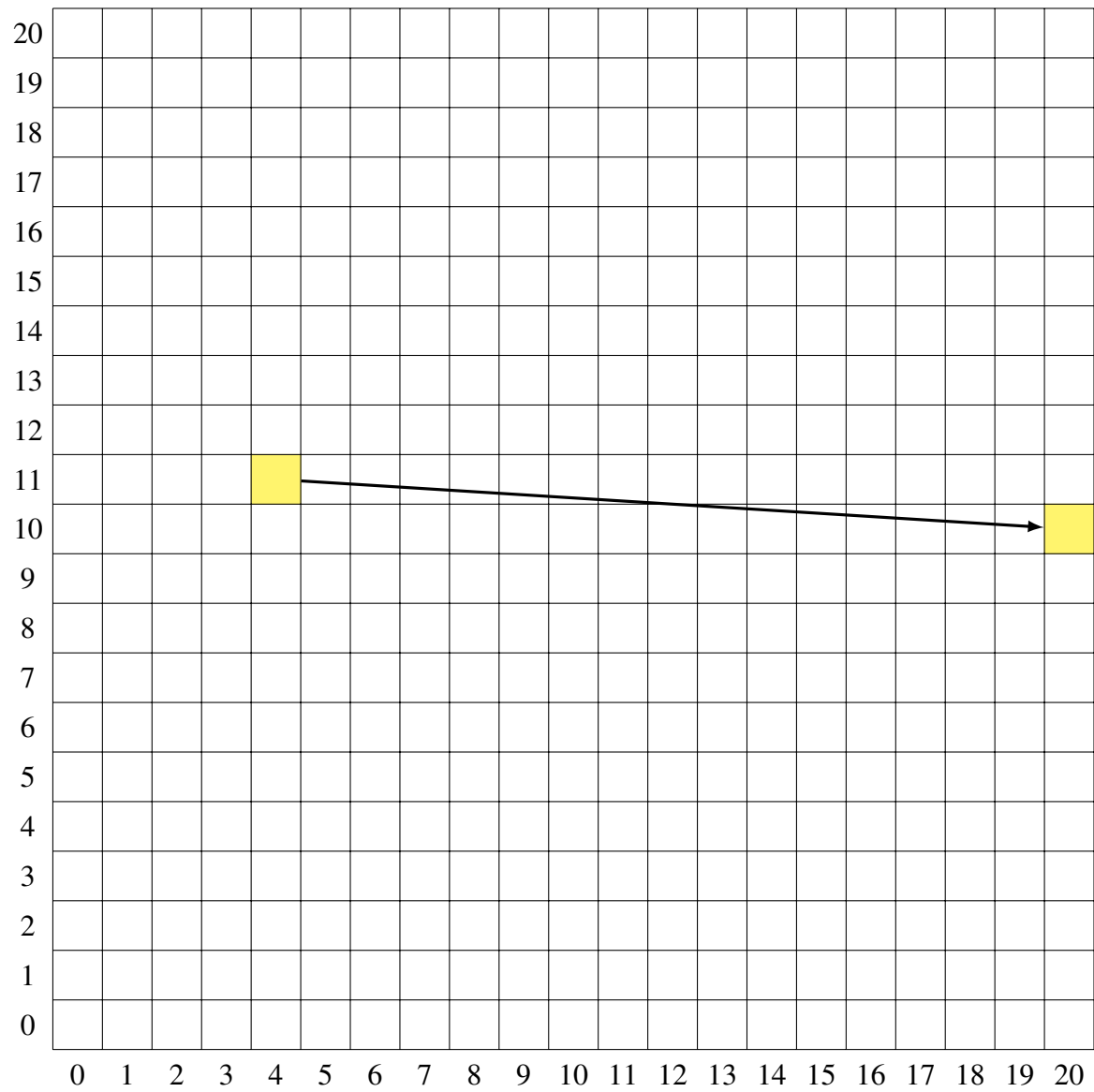


Figure 64: DAC05_subproblem_64