Project Jenny Bus Design

							Gap	250
FREE	1	0	0	Α	FREE	2x4	·	400
ANO	2	0	0	В	AN1		AN Bus	
AN2 PH0		0	0	С	FPH0 PH1		Fast Clock Unbuffered Clocks	
PHU	4	U	U	D	LUI		Gap	300
SYNC	1	0	0	Α	/NMI	2x13	Missing CPU Signals	1300
/D0xx		0	0	В	/CI		I/O / MMU Signals	
/D2xx	3	0	0	С	/D3xx		-	
/D4xx	4	0	0	D	/D5xx			
/D6xx		0	0	Ε	/D7xx			
/BASIC	6	0	0	F	/OS			
REN PB0	7	0	0	Η	PB1 /BE			
PB2 PB4	8 9	0	0	J K	PB3 PB5			
PB6		0	0	L	PB7 /MAP			
POKEY LEFT		0	0	М	POKEY RIGHT		Audio Signals	
GTIA BUZZER		0	0	N	+12V		Copy of +12V	
FREE		0	0	Р	FREE		17	
							Gap (room for cart shell)	800
/S4		0	0	Α	RD4	2x15	Cartridge port	1500
A3	2	0	0	В	GND			
A2 A1	3 4	0	0	С	A4 A5			
A1 A0	4 5	0	0	D E	A6			
D4	6	0	0	F	A7			
D5	7	0	0	Н	A8			
D2	8	0	0	J	A9			
D1	9	0	0	Κ	A12			
D0	10	0	0	L	D3			
D6		0	0	M	D7			
/S5		0	0	N	A11			
+5V RD5		0	0	P R	A10 R/W			
/CCTL								
,							Gap by CART/ECI spec	900
/EXT	1	0	0	Α	RESERVED	2x7	ECI	700
/RST		0	0	В	/IRQ			
/D1xx		0	0	С	/HALT			
/MPD		0	0	D	A13			
ECI AUDIO IN /REF	5 6	0	0	E F	A14 A15			
+5V		0	0	Н	GND			
101	•			••	OND		Gap	800
CLOCK IN	1	0	0	Α	CLOCK OUT	2x6	SIO Bus	600
DATA IN	2	0	0	В	DATA OUT			
COMMAND		0	0	С	MOTOR			
PROCEED	4	0	0	D	INTERRUPT			
GND		0	0	E	+5V			
AUDIO IN	6	0	0	F	+12V		Gap	250
							TOTAL (in mil)	7800
							TOTAL (in cm)	19.812
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