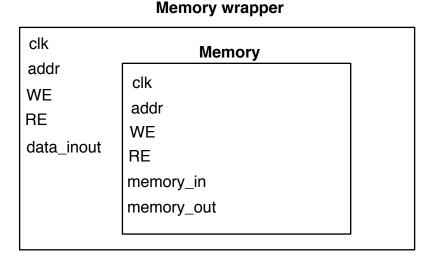
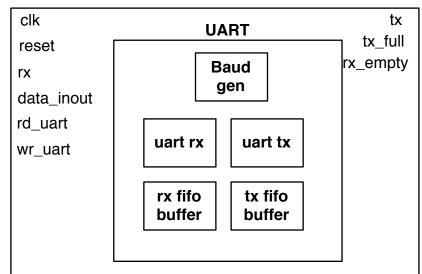
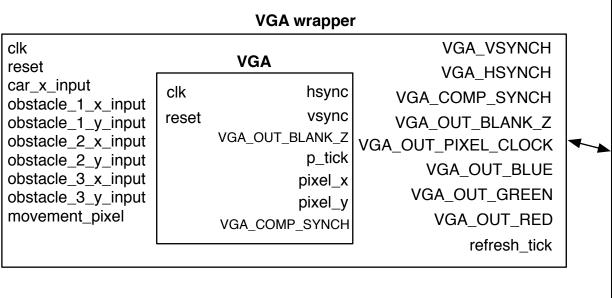
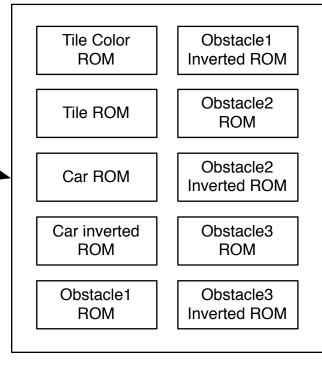
UART wrapper





VGA ROM





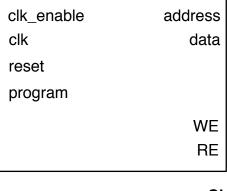
Complete System

clk100	tx
rx	led
sw_i	ledg
btn_i	seg
mb_sw_i	an
mb_btn_i	mb_led_o
SDATA1	vsync_out
	hsync_out
	comp_synch
	blank_out
	pixelclk_out
	b_out
	g_out
	r_out

nCS SCLK

7seg hex mux

clk an reset sseg hex3 hex2 hex1 hex0 dp_in



CPU

Steering Wheel A/D Conv.

clk sclk
rst ncs
sdata1 data1
start done

ClockDivider

clkIn clkOut