





















PS Power Sequence
VCCPINT -> (VCCPAUX + VCCPLL) -> (VCC_MIO0 + VCCO_MIO1 + VCCO_DDR)

PL Power Sequence
VCCINT -> VCCBIRAM -> VCCAUX + VCCO

Go4A		
TITLE :		
Video Rx for Static Applications - FPGA POWER		
SIZE		REV
Custom		A
DATE: Saturday, March 04, 2023	SHEET 10	OF 15







