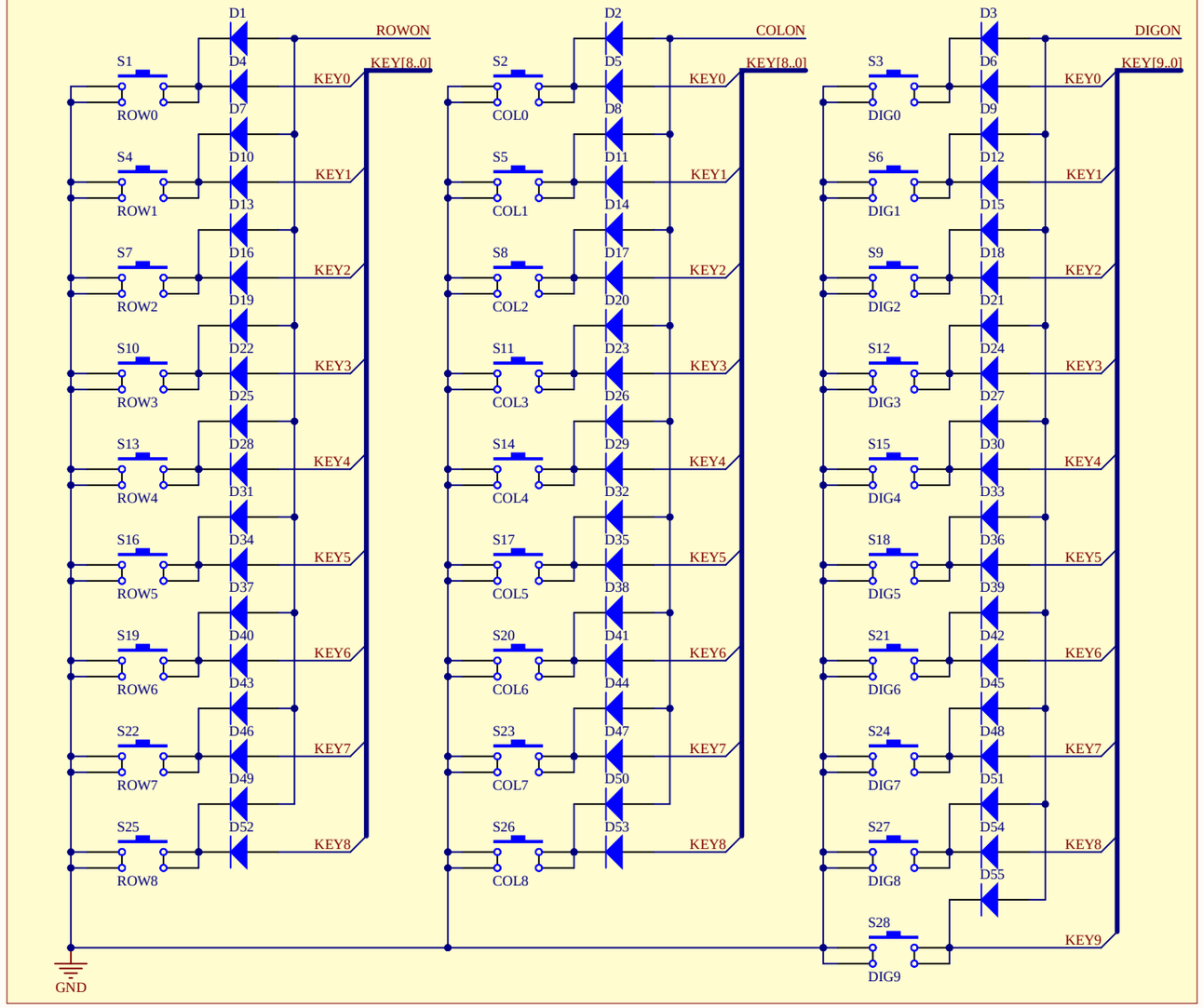
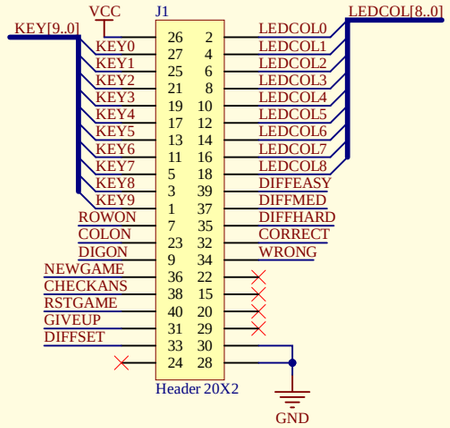
Keyboard matrix



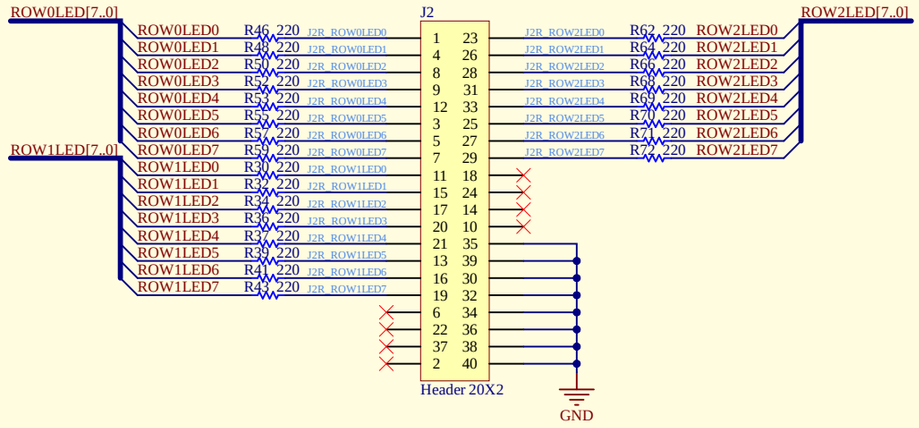
Title: FPGA-based Sudoku - keyboard		
Size: B	Number:	Revision: v1.0
Date: 8/15/2021		Sheet 1 of 4
File: C:\Users\..\keyboard.SchDoc		Drawn By: Justin Silver

FPGA connectors

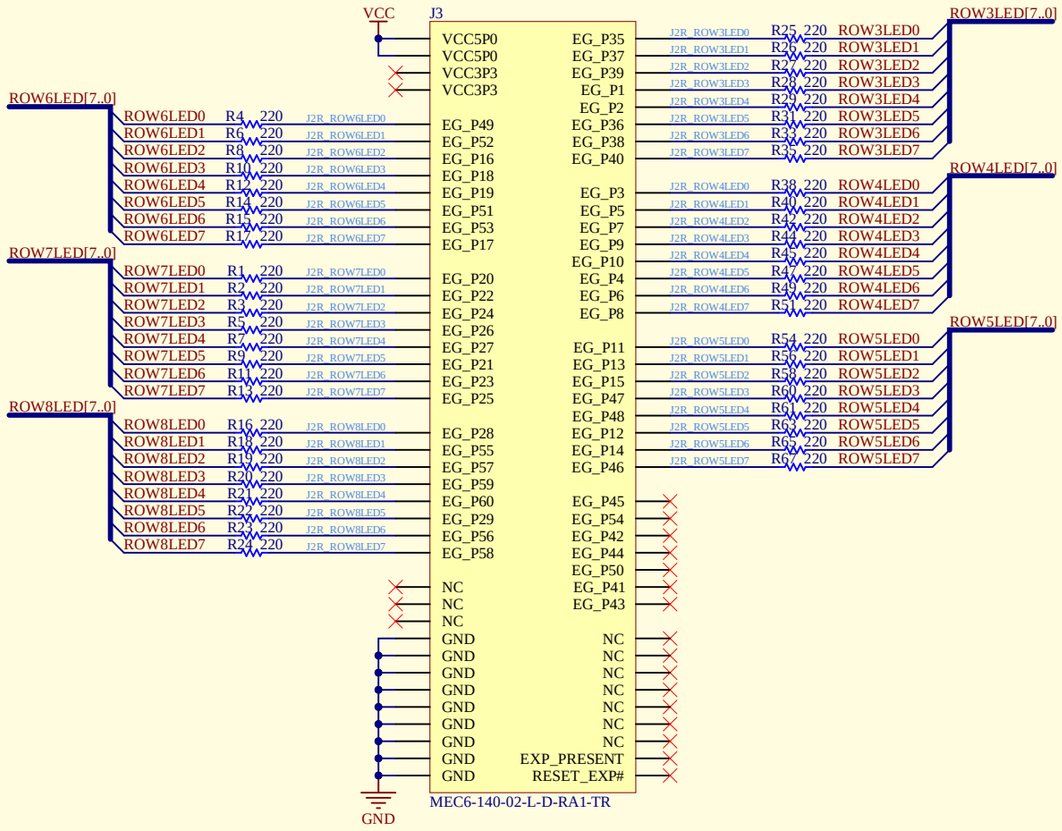
Connection to J5 on BeMicro MAX10



Connection to J4 on BeMicro MAX10

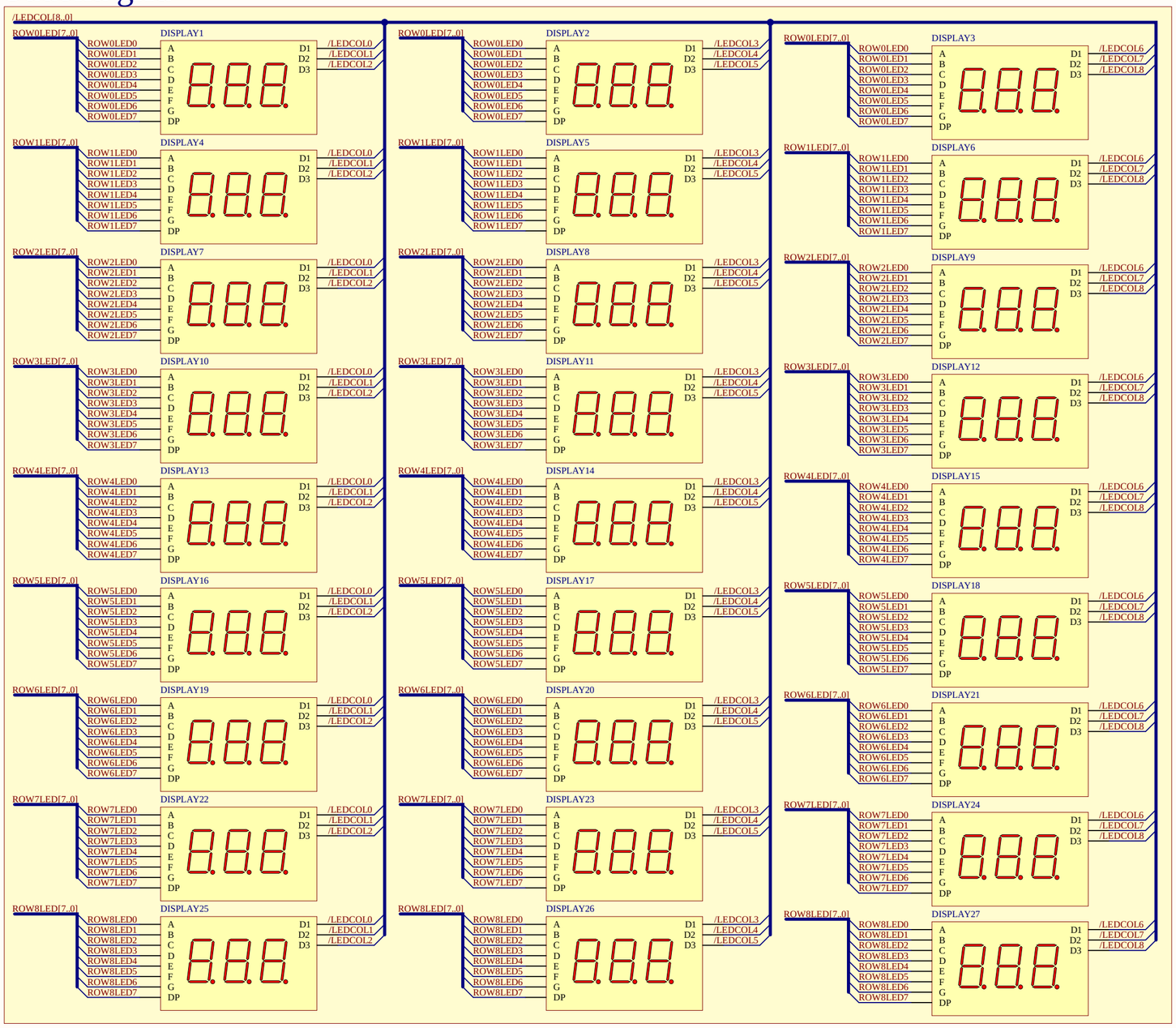


Connection to J2 on BeMicro MAX10



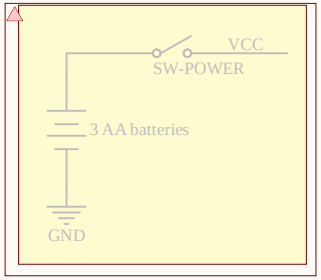
Title: FPGA-based Sudoku - connectors		
Size: B	Number:	Revision: v1.0
Date: 8/15/2021		Sheet 2 of 4
File: C:\Users\..\connectors.SchDoc		Drawn By: Justin Silver

Sudoku grid

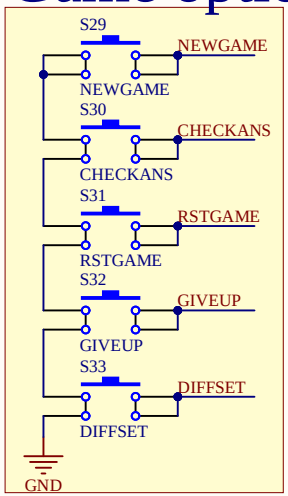


Title	FPGA-based Sudoku - display	
Size: C	Number	Revision: v1.0
Date: 8/15/2021	Sheet 3 of 4	
File: C:\Users\..\display.SchDoc	Drawn By: Justin Silver	

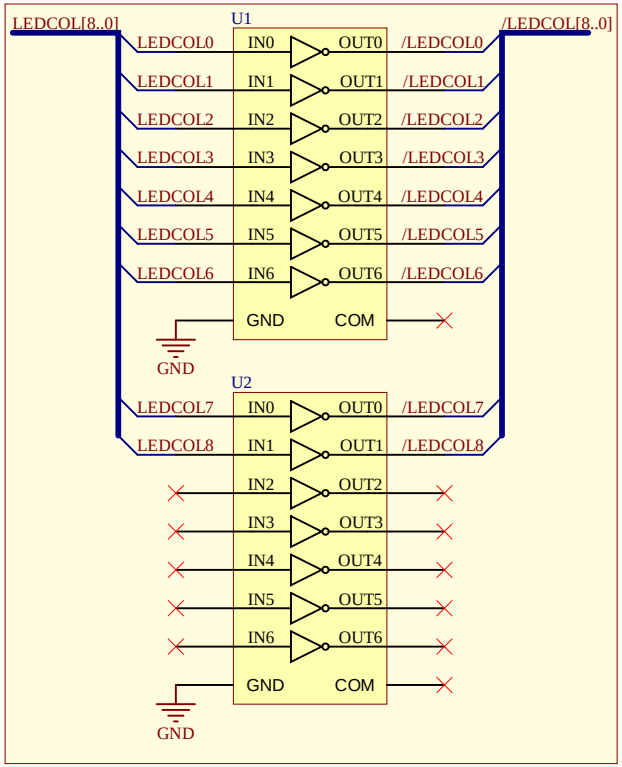
Power



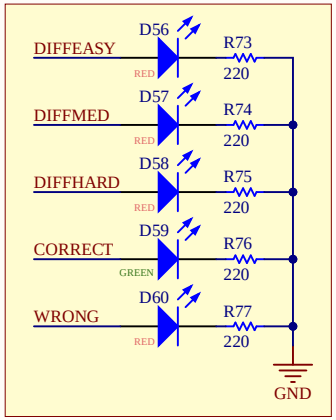
Game options



LED drivers



Game indicators



Title: FPGA-based Sudoku - miscellaneous		
Size: A	Number:	Revision: v1.0
Date: 8/15/2021		Sheet 4 of 4
File: C:\Users\..\miscellaneous.SchDoc		Drawn By: Justin Silver