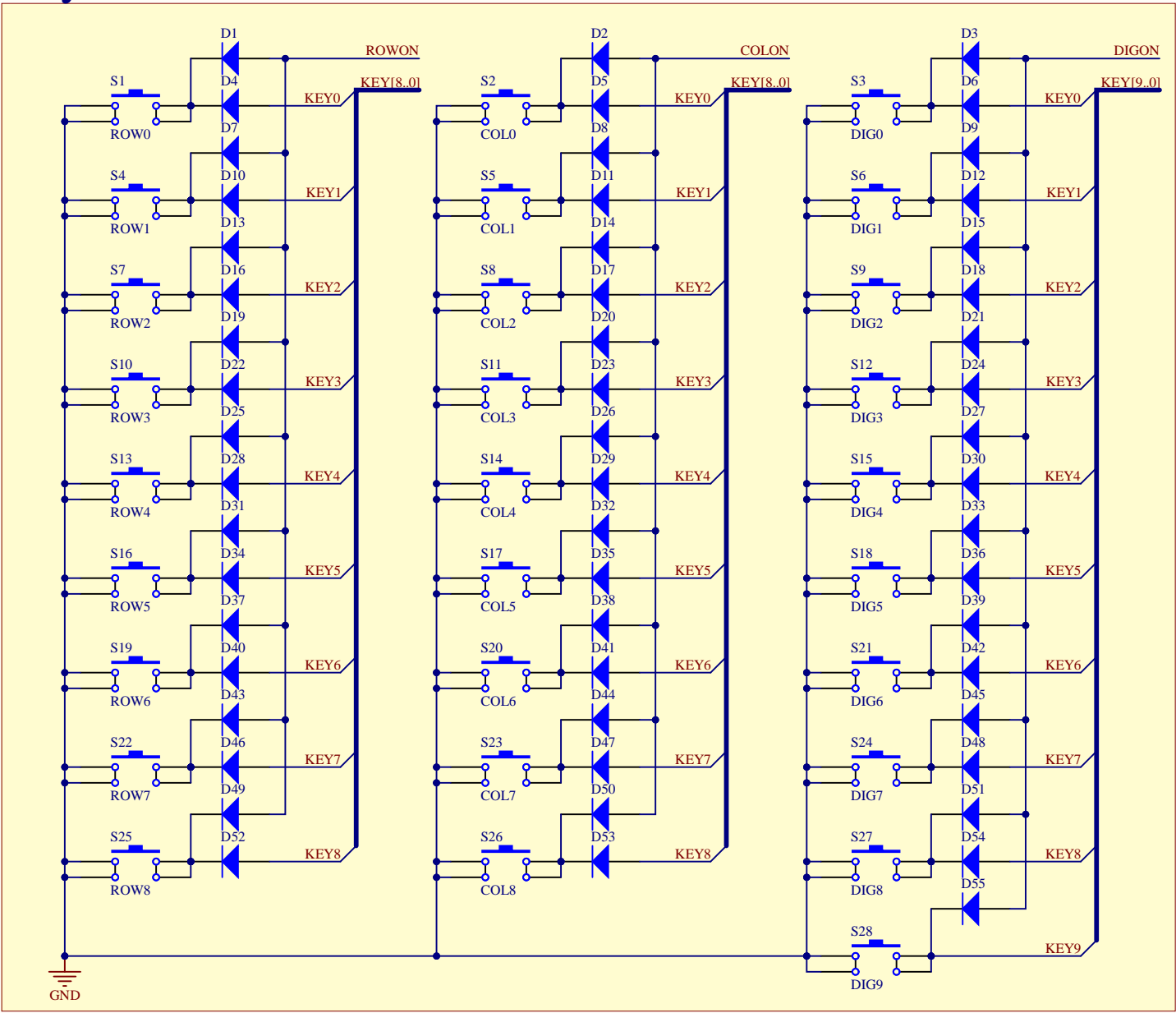
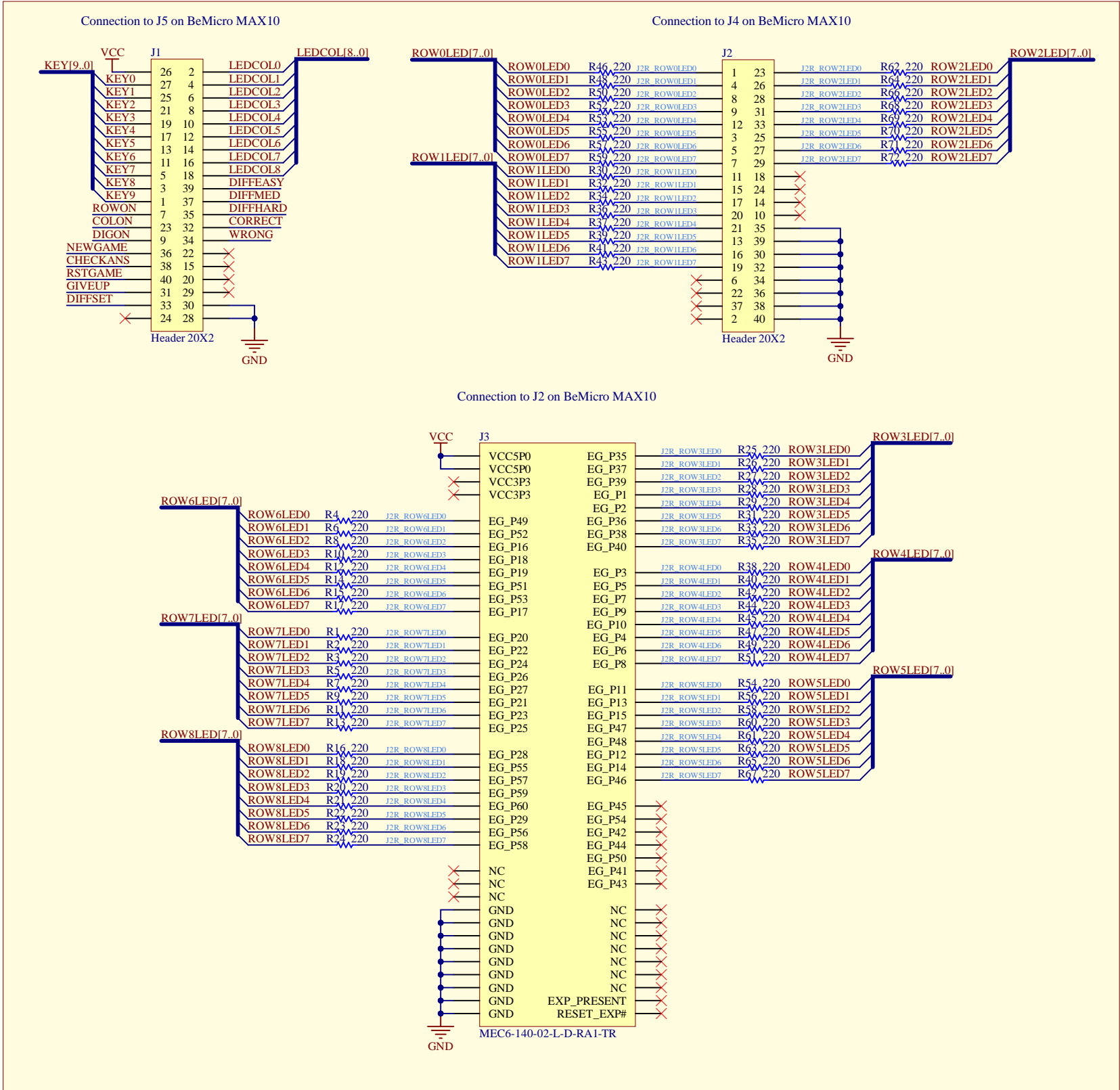


# Keyboard matrix



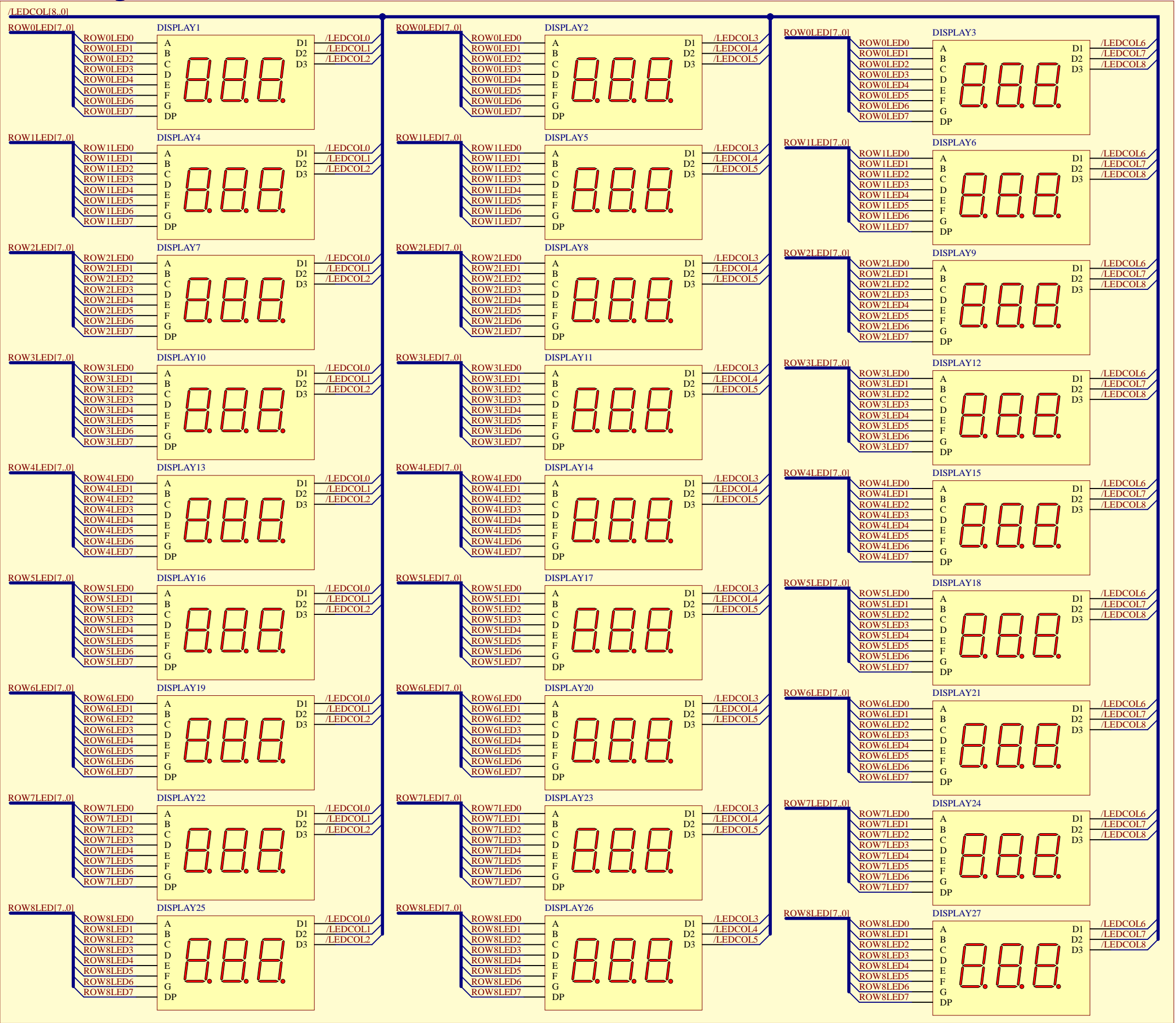
Title		
FPGA-based Sudoku - keyboard		
Size	Number	Revision
B		v1.0
Date:	8/14/2021	Sheet 1 of 4
File:	C:\Users\...\keyboard.SchDoc	Drawn By:

FPGA connectors



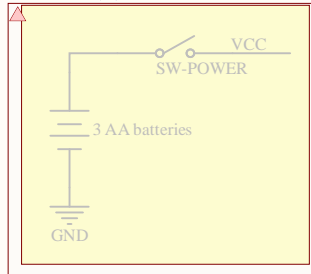
Title		
FPGA-based Sudoku - connectors		
Size	Number	Revision
B		v1.0
Date:	8/14/2021	Sheet 2 of 4
File:	C:\Users\...\connectors.SchDoc	Drawn By:

Sudoku grid

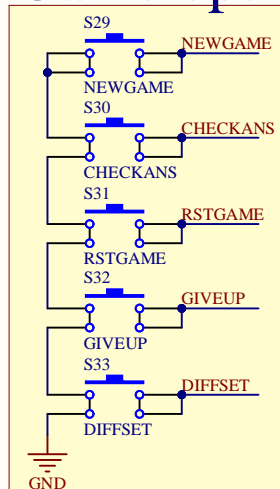


Title FPGA-based Sudoku - display		
Size C	Number	Revision v1.0
Date:	8/14/2021	Sheet3 of 4
File:	C:\Users\...\display.SchDoc	Drawn By:

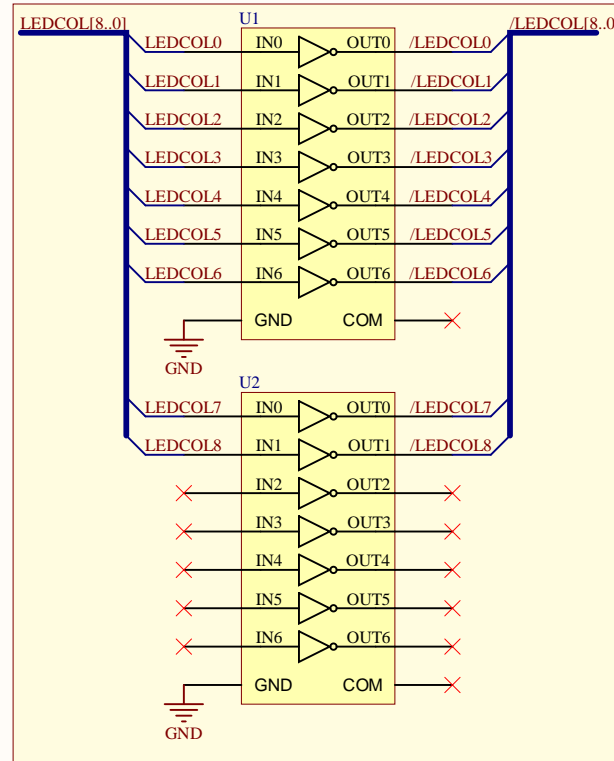
## Power



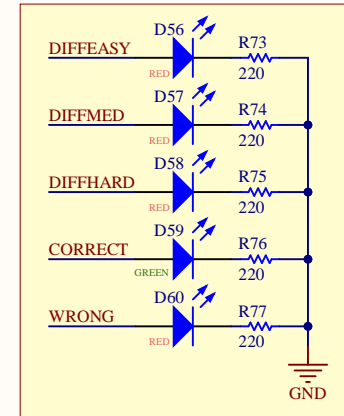
## Game options



## LED drivers



## Game indicators



Title FPGA-based Sudoku - miscellaneous		
Size A	Number	Revision v1.0
Date:	8/14/2021	Sheet 4 of 4
File:	C:\Users\...\miscellaneous.SchDoc	Drawn By: