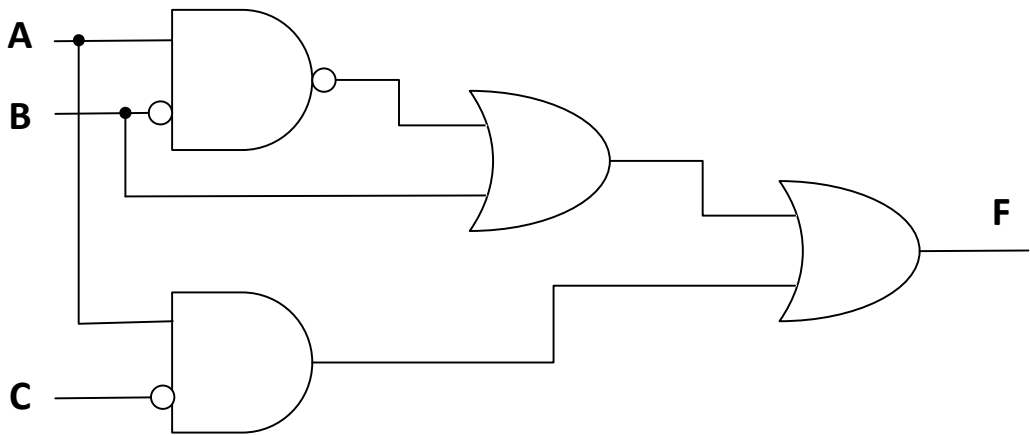


Name \_\_\_\_\_



1. Create a Boolean equation that represents the circuit above (any style). (2 points):
2. Complete the truth table for the above circuit (use scratch area for intermediate signals). (5 points):

A	B	C	Scratch area	F
0	0	0		
0	0	1		
0	1	0		
0	1	1		
1	0	0		
1	0	1		
1	1	0		
1	1	1		

3. DeMorgan's Law, draw equivalent gates below each of those illustrated (3 points):

