

COE758 Project 2 Marking Scheme

Student Name(s): _____

Student ID(s): _____

STYLE/FORMATting: [/10]

- Appropriate font/typesetting/layout for an academic report
- Past passive tense/writing style
- Figures, graphs, tables: titled, numbered and referenced in text

ABSTRACT: [/5]

- Summarizes background, objective, method, and results

INTRODUCTION: [/5]

- Motivation (why a VGA controller?)
- Objectives (What are the goals of this project? What is the perfect implementation?)
- Background theory (includes definitions; external citations cited IEEE style and referenced)
- Any specifications you must comply with

DEVICE DESCRIPTION: [/45]

- Symbol diagram for device and all custom components [5]
- Block Diagram [5]
- Process diagrams [15]
- Theoretical timings [15]
- Written description of system function [5]

RESULTS: [/15]

- Discussion of functionality. Does it work? [10]
- Compare to goals/specification originally set out. [5]

CONCLUSION: [/5]

- What was effective, what wasn't.
- Future work? Improvements? Alternatives?

APPENDIX: [/15]

- VHDL code organized and presented readably. Functionality commented.
- Correspondence between component symbols and code

REPORT TOTAL: [/100]