Hardware Information	☐ Input	☐ Output	
	Source / Sink IC		
	Number of video channels per stream:		
Sideband Communication	HDCP:	☐ Yes	☐ No
	I^{2}C:	☐ Yes	☐ No
	UART:	☐ Yes	☐ No
	SPI:	☐ Yes	☐ No
	MII:	☐ Yes	☐ No
	CAN:	☐ Yes	☐ No
Chip dependent Information	**Apix**		
	Apix Mode:	☐ Apix I ☐ Apix II ☐ Apix III	
	Additional Information:		