Hardware Information		
☐ Input	☐ Output	
Source / Sink IC		
Number of video channels per stream:		
HDCP:	☐ Yes	☐ No

Sideband Communication		
I^{2}C:	☐ Yes	☐ No
UART:	☐ Yes	☐ No
SPI:	☐ Yes	☐ No
MII:	☐ Yes	☐ No
CAN:	☐ Yes	☐ No

Chip dependent Information

Maxim

GMSL Version:	☐ GMSL I	☐ GMSL II	☐ GMSL III
Bus Width:	☐ 24 bit	☐ 32 bit	☐ 64 bit
Additional Information:			