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Parameter-Checklist for Automotive Video Projects

Please make sure that you select the right parameters depending on your device, not all video devices support them.

o U			
General Information	Technical contact person (with e-mail address):		
	Project title:		
	Expected use case (What is the application like?):		
Technical basics	Planned quantity of hardware:		
	Desired test hardware format:	Box (GigE) PCIe	e Card* PXIe Card*
rech Dasi	Number of Video inputs on UUT:		
	Number of Video outputs on UUT:		
	UUT Video IN/OUT 1		
Hardware Information	Input	Output	
	Source/ Sink IC (de-/serializer type) of UUT		
	Video connector type (Coax (Fakra), STP, etc.) and vendor ID:		
	Pinning of video connector:		
	Power supply of UUT via video cable? If Yes - voltage and current consumption:	Yes	No
S	Pixel clock:		
eter	Image width:		
am	Image height:		
Video Parameters	Frame rate:		
	HorizontalSyncPolarity:	High	Low
	VerticalSyncPolarity:	High	Low
	DataEnablePolarity:	High	Low
Vide	VerticalSyncPolarity:	High	Low

Directors Alice Göpel Jörg Schneider Thomas Wenzel

Local court Jena, HR B 20 1550 VAT ID no DE 150520615 WEEE reg no DE 887416033 Bank information

Commerzbank Filiale Jena

BIN 820 400 00 Account no 2581 833 SWIFT COBA DE FF 821 IBAN DE57 8204 0000 0258 183300

Sparkasse Jena
BiN 830 530 30
Account no 260 525
SWIFT HELA DEF 1 JEN
IBAN DE24 8305 3030 0000 260525





Video Parameters	PixelClockPolarity:	High	Low		
	LockOutputEnable:	High	Low		
	LockPolarity:	High	Low		
	Video format (RGB888, YUV422, RAW12, etc.):				
	Number of video channels per stream:				
	Is HDCP used?	Yes	No		
Sideband Communication	I ² C:	Yes	No		
	UART:	Yes	No		
	SPI:	Yes	No		
deb	MII:	Yes	No		
ŠŠ	CAN:	Yes	No		
	Texas Instruments Chip	<u>'</u>			
tion	FPD Link:	FPD Link I FPC	C Link II FPD Link III		
mat	Backward compatible mode:	Yes	No		
nfor	Low frequency mode:	Yes	No		
nt II	FPD Link III Transfer Mode:	Single Lane	Dual Lane		
Chip dependent Information	FPD Link III Transfer Mode:				
ede	Additional Information:				
p d	Maxim Chip				
Chi	GMSL version:	GMSL I GM	SL II GMSL III		
	Bus width/ Bus mode:	24 bit 32 l	bit 64 bit		
	UUT Video IN/OUT 2				
	Input	Output			
on	Source/ Sink IC (de-/serializer type) of UUT:				
Hardware Information	Video connector type (Coax (Fakra), STP, etc.) and				
forr	vendor ID:				
e In	Pinning of video connector:				
war					
lard					
Τ.	D. I. CHUT I II I				
	Power supply of UUT via video cable? If Yes - voltage and current consumption:	Yes	No		
	Pixel clock:				
Video Parameters	Image width:				
	Image height:				
	Frame rate:				
eo_	Frame rate: HorizontalSyncPolarity:	High	Low		
Video	Frame rate: HorizontalSyncPolarity: VerticalSyncPolarity:	High High	Low		



Video Parameters	PixelClockPolarity:	High		Low	
	LockOutputEnable:	High		Low	
	LockPolarity:	High		Low	
	Video format (RGB888, YUV422, RAW12, etc.):				
	Number of video channels per stream:				
>	Is HDCP used?	Yes		☐ No	
Sideband Communication	l²C:	Yes		☐ No	
	UART:	Yes		□ No	
	SPI:	Yes		☐ No	
deb	MII:	Yes		☐ No	
က်လ	CAN:	Yes		No	
	Texas Instruments Chip	·		'	
tion	FPD Link:	FPD Link I	FPD	Link II	FPD Link III
Chip dependent Information	Backward compatible mode:	Yes	,	No	
	Low frequency mode:	Yes		No	
nt =	FPD Link III Transfer Mode	Single Lane		Dual La	ane
nde	APIX Chip				
ebei	APIX version:	APIX I	APIX		APIX III
p di	Maxim Chip				
Chi	GMSL version:	GMSLI	GMS	il II	GMSL III
	Bus width/ Bus mode:	24 bit	32 b	it	64 bit
	UUT Video IN/OUT 3				
	☐ Input	Output			
ion	Source/ Sink IC (de-/serializer type) of UUT:				
rmation	Video connector type (Coax (Fakra), STP, etc.) and				
fori	vendor ID:				
Hardware Info	Pinning of video connector:				
wa!					
lard					
_	Power supply of UUT via video cable?				
	If Yes - voltage and current consumption:	Yes		No	
	Pixel clock:				
ters	Image width:				
Video Parameters	Image height:				
	Frame rate:				
	HorizontalSyncPolarity:	High		Low	
	VerticalSyncPolarity:	High		Low	
	DataEnablePolarity:	High		Low	



PixelClockPolarity:	High		Low	
LockOutputEnable:	High		Low	
LockPolarity:	High		Low	
Video format (RGB888, YUV422, RAW12, etc.):				
Number of video channels per stream:				
Is HDCP used?	Yes		No	
I ² C:	Yes		No	
UART:	Yes		No	
SPI:	Yes		No	
MII:	Yes		No	
CAN:	Yes		No	
Texas Instruments Chip	,			
FPD Link:	FPD Link I	FPD L	ink II	FPD Link III
Backward compatible mode:	Yes		No	
Low frequency mode:	Yes		□ No	
FPD Link III Transfer Mode:	Single Lane		Dual L	ane
APIX Chip				
APIX version:	APIX I	APIX I	II	APIX III
Maxim Chip				
GMSL version:	GMSL I	GMSL	. II	GMSL III
Bus width/ Bus mode:	24 bit	32 bit	t	64 bit
Are you interested in th Oragon Suite Advanced	Vos		No	
	l Tes		NO	
Remarks:				
	LockOutputEnable: LockPolarity: Video format (RGB888, YUV422, RAW12, etc.): Number of video channels per stream: Is HDCP used? I²C: UART: SPI: MII: CAN: Texas Instruments Chip FPD Link: Backward compatible mode: Low frequency mode: FPD Link III Transfer Mode: APIX Chip APIX version: Maxim Chip GMSL version:	LockOutputEnable: LockPolarity: Video format (RGB888, YUV422, RAW12, etc.): Number of video channels per stream: Is HDCP used? PC: Ves Ves UART: SPI: Yes MII: CAN: Texas Instruments Chip FPD Link: Backward compatible mode: Low frequency mode: Yes FPD Link III Transfer Mode: APIX Chip APIX version: Maxim Chip GMSL version: Bus width/ Bus mode: Are you interested in the Dragon Suite Advanced software? ** Do you need start-up support? *** Pyes High Yes Divided High Yes Divided High Yes Lore High High High High High High High High High	LockOutputEnable: LockPolarity: Video format (RGB888, YUV422, RAW12, etc.): Number of video channels per stream: Is HDCP used? PC: Ves Ves Ves Ves MII: Yes CAN: Texas Instruments Chip FPD Link: Backward compatible mode: Low frequency mode: FPD Link III Transfer Mode: APIX Chip APIX version: Maxim Chip GMSL version: Bus width/ Bus mode: Are you interested in th@ragon Suite Advanced software? ** Do you need start-up support? *** Wideo freque. High Yes Diani High Hi	LockOutputEnable: LockPolarity: High Low Video format (RGB888, YUV422, RAW12, etc.): Number of video channels per stream: Is HDCP used? PC: Yes No UART: Yes No SPI: Yes No MII: Yes No CAN: Yes No Texas Instruments Chip FPD Link: Backward compatible mode: Low frequency mode: PFD Link III Transfer Mode: APIX Chip APIX Version: APIX I Maxim Chip GMSL version: GMSL Version: Bus width/ Bus mode: Are you interested in the Dragon Suite Advanced software? *** No Do you need start-up support? **** Yes No Low Yes No No No No No No No No No N

Some parameters are not supported by all our video devices. Please be not confused when unnecessary information is requested for your project. Please give us as much information as possible about your project. Only in this way we can create an offer that fits your application.

Please add a detailed schematic of your application to the checklist.

- * Only available for Series 62 (Video Dragon 2)
- ** The **Dragon Suite** software is provided free of charge to our customers to help them work with their Göpel video devices. This includes configuring the device, generating and capturing images and videos, sideband communication as well as various IO and CAN functions.

In addition, the tool offers additional features that are available as a paddagorg Sui Gel Add vadaced (Suich as Script Interface and Raw Data Recording) and are constantly being further developed. If you have any additional expansion requests, please contact our support team (assupport@gepes) and are

*** We are delighted to support our customers in the start-up and development of their applications. In case of increased effort, we will offer you a fee-based support.