

Hardware Information				
	<input type="checkbox"/> Input	<input type="checkbox"/> Output		
	Source / Sink IC			
	Number of video channels per stream:			
	HDCP:	<input type="checkbox"/> Yes	<input type="checkbox"/> No	
Sideband Communication	I <sup>2</sup> C:	<input type="checkbox"/> Yes	<input type="checkbox"/> No	
	UART:	<input type="checkbox"/> Yes	<input type="checkbox"/> No	
	SPI:	<input type="checkbox"/> Yes	<input type="checkbox"/> No	
	MII:	<input type="checkbox"/> Yes	<input type="checkbox"/> No	
	CAN:	<input type="checkbox"/> Yes	<input type="checkbox"/> No	
Chip dependent Information	<b>Texas Instruments</b>			
	FPD Link:	<input type="checkbox"/> FPD Link II	<input type="checkbox"/> FPD Link III	<input type="checkbox"/> FPD Link IV
	Backward compatible mode:	<input type="checkbox"/> Yes	<input type="checkbox"/> No	
	Low frequency mode:	<input type="checkbox"/> Yes	<input type="checkbox"/> No	
	FPD Link III Transfer Mode:	<input type="checkbox"/> Single Lane	<input type="checkbox"/> Dual Lane	
	Additional Information:			