

Hardware Information	<input type="checkbox"/> Input		<input type="checkbox"/> Output		
	Source / Sink IC				
	Video connector type				
	Pinning of video connector:				
	Power supply of UUT via video cable:	Yes <input type="checkbox"/>	No <input type="checkbox"/>		
Video Parameters	Pixel clock:				
	Image width:				
	Image height:				
	Frame rate:				
	Horizontal Sync Polarity:	<input type="checkbox"/> High	<input type="checkbox"/> Low		
	Vertical Sync Polarity:	<input type="checkbox"/> High	<input type="checkbox"/> Low		
	Data Enable Polarity:	<input type="checkbox"/> High	<input type="checkbox"/> Low		
	Pixel Clock Polarity:	<input type="checkbox"/> High	<input type="checkbox"/> Low		
	Lock Output Enable:	<input type="checkbox"/> High	<input type="checkbox"/> Low		
	Lock Polarity:	<input type="checkbox"/> High	<input type="checkbox"/> Low		
	Video format:				
	Number of video channels per stream:				
Sideband Communication	HDCP:	<input type="checkbox"/> Yes	<input type="checkbox"/> No		
	I ² C:	<input type="checkbox"/> Yes	<input type="checkbox"/> No		
	UART:	<input type="checkbox"/> Yes	<input type="checkbox"/> No		
	SPI:	<input type="checkbox"/> Yes	<input type="checkbox"/> No		
	MII:	<input type="checkbox"/> Yes	<input type="checkbox"/> No		
	CAN:	<input type="checkbox"/> Yes	<input type="checkbox"/> No		
Chip dependent Information	Apix				
	Apix Mode:	<input type="checkbox"/> Apix I	<input type="checkbox"/> Apix II	<input type="checkbox"/> Apix III	
	Additional Information:				