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Parameter-Checklist for Automotive Video Projects

Please fill in the table as detailed as possible and send it to **ats.sales@goepel.com**:

General Information	Customer name:			
	Technical contact person (with e-mail address):			
	Project title:			
	Expected use case (What is the application like?):			
Technical basics	Planned quantity of hardware:			
	Desired test hardware format:	☐ Box (GigE)	☐ PCIe Card*	☐ PXIe Card*
	Number of Video inputs on UUT:			
	Number of Video outputs on UUT:			

Please fill in a new table block for each input/output:

Hardware Information	UUT Video IN/OUT 1		
	☐ Input	☐ Output	
	Source/ Sink IC (de-/serializer type) of UUT		
	Video connector type (Coax (Fakra), STP, etc.) and vendor ID:		
	Pinning of video connector:		
	Power supply of UUT via video cable? If Yes - voltage and current consumption:	☐ Yes	☐ No
Video Parameters	Pixel clock:		
	Image width:		
	Image height:		
	Frame rate:		
	HorizontalSyncPolarity:	☐ High	☐ Low
	VerticalSyncPolarity:	☐ High	☐ Low
	DataEnablePolarity:	☐ High	☐ Low

Video Parameters	PixelClockPolarity:	☐ High	☐ Low	
	LockOutputEnable:	☐ High	☐ Low	
	LockPolarity:	☐ High	☐ Low	
	Video format (RGB888, YUV422, RAW12, etc.):			
	Number of video channels per stream:			
	Is HDCP used?	☐ Yes	☐ No	
Sideband Communication	I²C:	☐ Yes	☐ No	
	UART:	☐ Yes	☐ No	
	SPI:	☐ Yes	☐ No	
	MII:	☐ Yes	☐ No	
	CAN:	☐ Yes	☐ No	
Chip dependent Information	**Texas Instruments Chip**			
	FPD Link:	☐ FPD Link I	☐ FPD Link II	☐ FPD Link III
	Backward compatible mode:	☐ Yes	☐ No	
	Low frequency mode:	☐ Yes	☐ No	
	FPD Link III Transfer Mode:	☐ Single Lane	☐ Dual Lane	
	APIX Chip			
	APIX version:	☐ APIX I	☐ APIX II	☐ APIX III
	Maxim Chip			
	GMSL version:	☐ GMSL I	☐ GMSL II	☐ GMSL III
	Bus width/ Bus mode:	☐ 24 bit	☐ 32 bit	☐ 64 bit

Hardware Information	UUT Video IN/OUT 2		
	☐ Input	☐ Output	
	Source/ Sink IC (de-/serializer type) of UUT:		
	Video connector type (Coax (Fakra), STP, etc.) and vendor ID:		
	Pinning of video connector:		
	Power supply of UUT via video cable? If Yes - voltage and current consumption:	☐ Yes	☐ No
Video Parameters	Pixel clock:		
	Image width:		
	Image height:		
	Frame rate:		
	HorizontalSyncPolarity:	☐ High	☐ Low
	VerticalSyncPolarity:	☐ High	☐ Low
	DataEnablePolarity:	☐ High	☐ Low

Video Parameters	PixelClockPolarity:	☐ High	☐ Low	
	LockOutputEnable:	☐ High	☐ Low	
	LockPolarity:	☐ High	☐ Low	
	Video format (RGB888, YUV422, RAW12, etc.):			
	Number of video channels per stream:			
	Is HDCP used?	☐ Yes	☐ No	
Sideband Communication	I²C:	☐ Yes	☐ No	
	UART:	☐ Yes	☐ No	
	SPI:	☐ Yes	☐ No	
	MII:	☐ Yes	☐ No	
	CAN:	☐ Yes	☐ No	
Chip dependent Information	**Texas Instruments Chip**			
	FPD Link:	☐ FPD Link I	☐ FPD Link II	☐ FPD Link III
	Backward compatible mode:	☐ Yes	☐ No	
	Low frequency mode:	☐ Yes	☐ No	
	FPD Link III Transfer Mode	☐ Single Lane	☐ Dual Lane	
	APIX Chip			
	APIX version:	☐ APIX I	☐ APIX II	☐ APIX III
	Maxim Chip			
	GMSL version:	☐ GMSL I	☐ GMSL II	☐ GMSL III
	Bus width/ Bus mode:	☐ 24 bit	☐ 32 bit	☐ 64 bit

Hardware Information	UUT Video IN/OUT 3		
	☐ Input	☐ Output	
	Source/ Sink IC (de-/serializer type) of UUT:		
	Video connector type (Coax (Fakra), STP, etc.) and vendor ID:		
	Pinning of video connector:		
	Power supply of UUT via video cable? If Yes - voltage and current consumption:	☐ Yes	☐ No
Video Parameters	Pixel clock:		
	Image width:		
	Image height:		
	Frame rate:		
	HorizontalSyncPolarity:	☐ High	☐ Low
	VerticalSyncPolarity:	☐ High	☐ Low
	DataEnablePolarity:	☐ High	☐ Low

Video Parameters	PixelClockPolarity:	☐ High	☐ Low	
	LockOutputEnable:	☐ High	☐ Low	
	LockPolarity:	☐ High	☐ Low	
	Video format (RGB888, YUV422, RAW12, etc.):			
	Number of video channels per stream:			
	Is HDCP used?	☐ Yes	☐ No	
Sideband Communication	I²C:	☐ Yes	☐ No	
	UART:	☐ Yes	☐ No	
	SPI:	☐ Yes	☐ No	
	MII:	☐ Yes	☐ No	
	CAN:	☐ Yes	☐ No	
Chip dependent Information	**Texas Instruments Chip**			
	FPD Link:	☐ FPD Link I	☐ FPD Link II	☐ FPD Link III
	Backward compatible mode:	☐ Yes	☐ No	
	Low frequency mode:	☐ Yes	☐ No	
	FPD Link III Transfer Mode:	☐ Single Lane	☐ Dual Lane	
	APIX Chip			
	APIX version:	☐ APIX I	☐ APIX II	☐ APIX III
	Maxim Chip			
	GMSL version:	☐ GMSL I	☐ GMSL II	☐ GMSL III
	Bus width/ Bus mode:	☐ 24 bit	☐ 32 bit	☐ 64 bit

Any other Business	Are you interested in the **Dragon Suite Advanced** software? **	☐ Yes	☐ No
	Do you need start-up support? ***	☐ Yes	☐ No
	Remarks:		

Some parameters are not supported by all our video devices. Please be not confused when unnecessary information is requested for your project. Please give us as much information as possible about your project. Only in this way we can create an offer that fits your application.

Please add a detailed schematic of your application to the checklist.

* Only available for Series 62 (Video Dragon 2)
** The **Dragon Suite** software is provided free of charge to our customers to help them work with their Göpel video devices. This includes configuring the device, generating and capturing images and videos, sideband communication as well as various IO and CAN functions.
In addition, the tool offers additional features that are available as a paid **Dragon Suite Advanced** (such as Script Interface and Raw Data Recording) and are constantly being further developed.If you have any additional expansion requests, please contact our support team (**ats.support@goepel.com**).
*** We are delighted to support our customers in the start-up and development of their applications. In case of increased effort, we will offer you a fee-based support.