

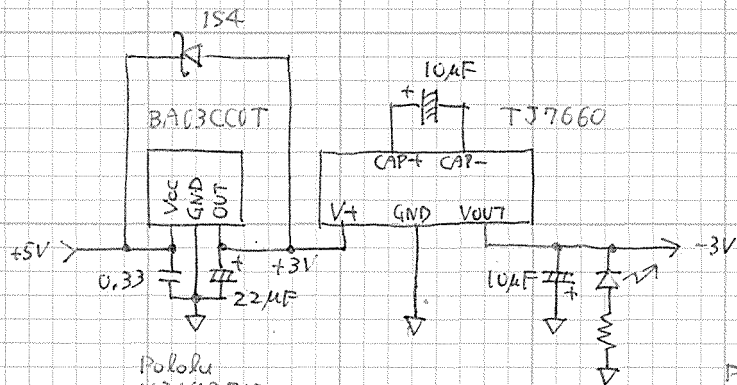
(A): D15..D8  
(B): D7..D0

\*: These logics are implemented by using a Microchip (formerly ATMEL) ATF22V10C.

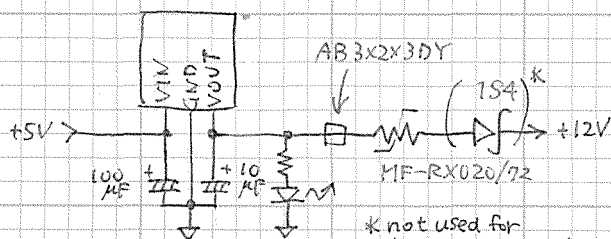
MUTIF-11 Project  
CP-1600 Computer Board

Date: 2021.12.30 Rev.1.0  
By Kasumi YOSHINO /JA1UM1

Sheet: 3/3



Pololu  
U3V12F12



\* not used for  
later experiments

Pololu Adjustable Regulator  
4-25V

