ENGR210: GSA 1

Group Number: 21	<u></u>		
Group Member #1 Name:	Kara Hageman	Case ID:kah157	
Group Member #2 Name:	Sophia Hestad	Case ID:sbh34	
Group Member #3 Name:	Jacob Alspaw	Case ID:	
Group Member #4 Name:	Jon Healy	Case ID:imh267	
Group Member #5 Name:	Sarah Breland	Case ID: sab196	

Beneficial

- 1. Communication: nothing can get done if we aren't talking to each other. This starts with our ability to communicate what needs to be done before we meet so that while we meet we will get done what needs to get done
- 2. Making an Agenda: this allows us to stay focused and make sure everything gets done
- 3. Time management: this way everything that needs to get done gets done. This also keeps the work load from becoming too much
- 4. Effort: we need to put effort into the time plan to put in so that the work gets done and so that the work is quality
- 5. Enthusiasm: we have to want to do the work in order to get it done well and in a timely manner

Destructive

- 1. Negative Attitude: this pulls everyone down because negative people aren't doing work as well and bring others down
- 2. Unwilling to Co-operate: this does not contribute to the group and furthermore hinders the group from moving forward. Creates an uneven divide of work load
- 3. Distractions: this decreases efficiency by breaking concentration and taking up time
- 4. Not being prepared: this holds the group back because prepared members then have to catch the other member up or take the un-prepared person's work load
- 5. Close-Minded: this inhibits learning. Even if something is wrong it should be talked out as to why it is wrong so that the experience can be meaningful

High Performance

- 1. Being prepared: This shows that a person cares about the class and is ready to participate fully in the work that needs to get done during the meeting
- 2. Attentive: this allows work to get done. It is also just respectful. It is the only way for collaborative learning to happen is to know what the group is working on
- 3. Efficient: this shows that the person is paying attention and focused on the work. It keeps the group on track and helps them stick to the agenda
- 4. Engaging with the Group: this allows collaborative learning by sparking discussion. It allows for good questions to be asked and mistakes to be explained and learned from
- 5. Willingness to learn: without this people will not want to enter into discussion. Going through the motions does not culminate in long term learning and understanding

Low Performance

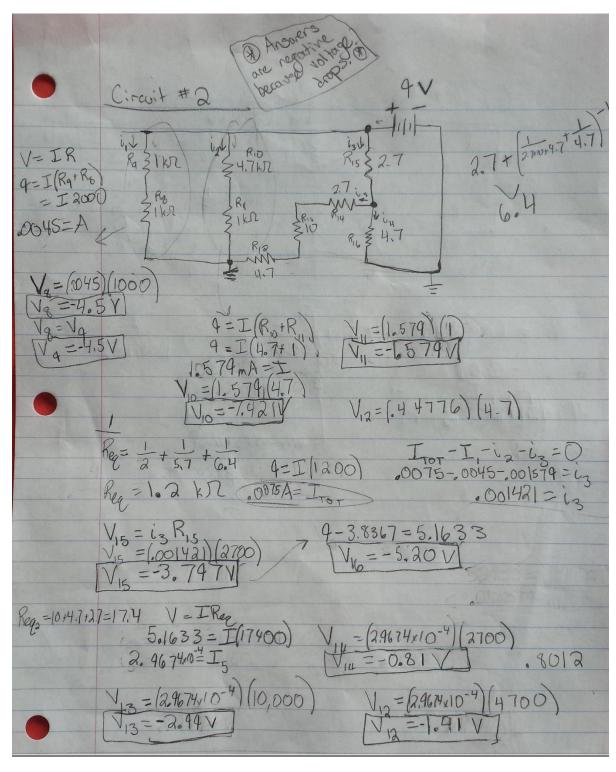
- 1. Too reserved: prevents good discussion, personal learning, and detracts from others ability to learn
- 2. Too arrogant: makes others uncomfortable asking questions. Others are unable to learn because they feel stupid. The arrogant one does not learn either because they don't acknowledge their mistakes or learn from them
- 3. Judgmental: this creates tension within the group because people are not able to express what they are thinking and if people can't do this then discussion can't happen and learning doesn't take place
- 4. Unmotivated: this takes up time and decreases the quality of the work. It slows everyone down and tends to rub off on members
- 5. Temperamental: prevents people from having good communication by decreasing the clarity of the what is being said

Meeting Time: Sunday night, late Monday night if needed

Part 1

Circuit 1

GSA #1	8/30/15
1. [Circuit 1] KCL : 2+2 = 0 KVL: V,+V2 = 0	
V= 1R TV1 9V \$R1 2.7ka R1 + R2 = 7.	
5 B2 47K2	
	2+ Ry + Rg+Rb) = 5.5 KSZ
Ry 3 2 Ro 10 KR R7 = 3.7	V= 1R 15.6KR T= av/15.16(103) R T= 0.000577A
-	
$R_1: 1 = V/R = aV/2.7(10^2) \Omega = 0$	
$R_{2} = \frac{9}{7.40} = \frac{900}{7.40} = 0.001$ $V_{1} = \frac{000577}{000577} = \frac{1.00}{1000}$	2,679 V
1005/12 (1000) =	

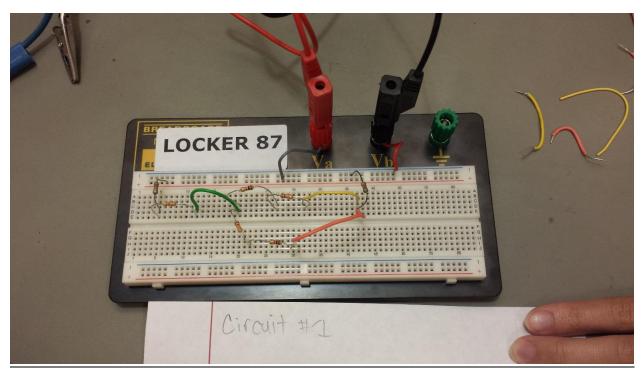


Open

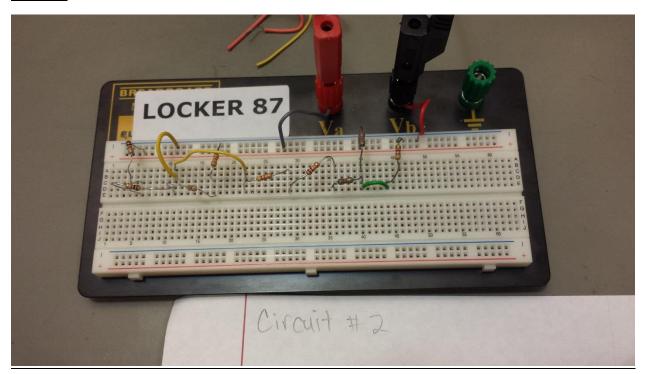
OPEN
R26 V= IR V= 9V because one path, one resistor
Root of circut for all clused loops
R=(10+2.7+4.7 + 1)=0.94542
R= 0.945 + = 1.945 \(\text{L} \)
R=(1.945 + 2.7) 1.13 \(\omega + 4.7 \(\omega \) = 5.83\(\omega \)
T= V/R = 9V/5.83 KSZ = 1.54nA
7.255V 7.255V
V ₂₅ = 9 - V ₂₃ = [1.745 V)
$I_{2\overline{5}} V_R = 1.745/2.7 \times \Omega = 0.646 \text{ mA}$ $I_{2\overline{5}} V_R = I_{22} - I_{3\overline{5}} = 1.54 - 0.646 \text{ mA} = 0.894 \text{ mA}$ $V_{21} R = .894(1) = [.894V]$
$V_{21} = 9V - (V_{22} + V_{23}) = [0.851V]$
$T_{a}^{-} V/R = 0.85^{1}/1 = 0.851 \text{ mA}$
$T_{20} = T_{21} = 0.894 - 0.851 = 0.043 \text{ mA}$ $V_{3} = IR = 0.043(4.7) = 0.202 \text{ V}$ $V_{19} = IR = .043(2.7) = 0.116 \text{ V}$
Which of Viz = OVit is not in a connected loop

1	Circuit #3 SWITCH CLOSED	
	Veza = 9V because only one restator along path. be full voltage drop.	
	VAZY = OV because both voltage sources have a potential across resistory, but are in opposite Therefore, no current, no Voltage drop.	direction.
	V _{R17} + V _{R12} - 9 = 0	
	$ \frac{V_{R_{2}3}}{R_{2}} = \frac{V_{R_{2}S}}{R_{2}} + \frac{V_{R_{2}1}}{R_{22}} $ $ \frac{V_{R_{2}3}}{V_{R_{2}1}} = \frac{V_{R_{2}}}{V_{R_{2}1}} + \frac{V_{R_{2}0}}{V_{R_{2}1}} + \frac{V_{R_{2}0}}{V_{R_{2}1}} $ $ \frac{V_{R_{2}1}}{V_{R_{2}1}} = \frac{V_{R_{2}1}}{V_{R_{2}1}} + \frac{V_{R_{2}0}}{V_{R_{2}1}} $ $ \frac{V_{R_{1}1}}{V_{R_{1}1}} = \frac{V_{R_{1}0}}{V_{R_{1}0}} + \frac{V_{R_{1}0}}{V_{R_{1}0}} $ $ \frac{V_{R_{1}1}}{V_{R_{1}1}} = \frac{V_{R_{2}0}}{V_{R_{1}0}} $ $ \frac{V_{R_{1}1}}{V_{R_{1}1}} = \frac{V_{R_{2}0}}{V_{R_{1}0}} $ $ \frac{V_{R_{1}1}}{V_{R_{1}1}} = \frac{V_{R_{2}0}}{V_{R_{1}0}} $	
4	Solving our system of equations:	
	$V_{R25} = 2.14 V$ $V_{19} = 2.18 V$ $V_{R26} = V_{R25} = 6.86 V$ $V_{13} = 7.45 V$ $V_{R24} = 0.67 V$ $V_{17} = 1.55 V$	
	V _{R21} = 1.47 V V _{R20} = 3.79 V	

Part 2

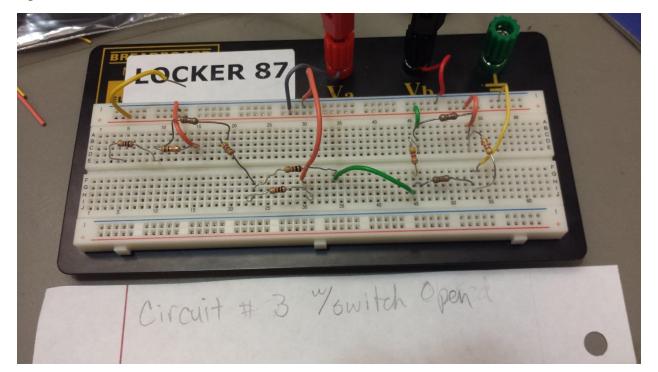


-	G 5A#1
Cir	cuit I Data
14tools	R, = -1.579 V
11/1	R2 = 2.724V
	R3=2,886V
	Ru=286 0.2861V
	R5=.2891V
	R6=2,882V
	R= 1,589V



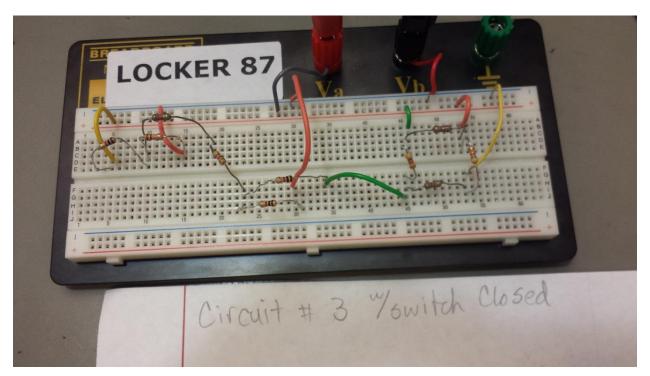
Re=4.513V Re=4.513V Re=7.45V Ri=1.574V Ri=5.18V Ri=5.18V Ri=0.8116V Riz=1.400V

Open



Deen .
Vollage R.z = OV Chuitch
W R18 = 0.489V
R19 = 0.134V
R20 = 0-2303V
R21=0.8532V
R2=0.8984V
R ₂₅ = 1,752V
R26=9.037V
Rzn=OV
R23=7.285V

Closed



P :	cavit 3 Data
C Nothorn	R1= 1/543V
14	R= 7.500V
	R19=2.216V
	R20 = 3.801V
	R= 1.478V
	Rez=0.671V
	R25=2,149V
THE RESERVE TO SERVE THE PARTY OF THE PARTY	226 = 9.04V
	R24 = 0.02(mV) = 0V
	R23=6.890V

The measured and calculated values for circuit 1 and 2 are similar, but are not exact. This could be for a multitude of reasons: experimental error, uncertainty in the magnitude of resistance, non-ideal wires, and carrying over significant figures. Our group is not absolute in our experiment. There will always be a source of error by some marginal value. It is possible our group measured over the wrong wire, or perhaps in series when we needed to measure in parallel. There are many possibilities that could spoil results. However, I believe the human error our group made is minimal because our measured results matched our calculations. Moreover, the resistors are not perfect resistors and, in actuality, have resistance within a range of their labeled value. A 1000 Ohm resistor could have an allowable uncertainty of 10% for the manufacturer to certify it, resulting in resistor that could be 100 Ohms more or less than its labeled value. This discrepancy in resistance magnitudes could be a large contributor to the differences in measurements and theoretical values. But it doesn't stop there; in conjunction with forgoing the imperfections in resistors into our calculations, neither are the resistances of the wires themselves. In calculations, we assume wires are ideal and supply no resistance, but this is simply not true. Every wire has some magnitude of resistance along its path. Lastly, throughout the calculations, our group was consistently rounding to match significant figures the best we could. Over the course of the totality of arithmetic involved in these calculations, the rounding errors compounded onto one another. When we compare these rounded values to the measured values, there will be a difference, even though slight, because the machines, the DMMs specifically, do not take the rounding of significant figures into consideration. The measurement tools do not limit themselves to a certain amount of digits.