













Copyright © Jaap Geurts 2021

This documentation describes Open Hardware and is licensed under the CERN OHL v. 1.1.
You may redistribute and modify this documentation under the terms of the CERN OHL v.1.1.
(http://ohwr.org/cernohl), This documentation is distributed WITHOUT ANY EXPRESS OR IMPLIED WARRANTY, INCLUDING OF MERCHANTABLITY, SATISFACTORY QUALITY AND FITNESS FOR A PARTICULAR PURPOSE.

Jaap Geurts Please see the CERN OHL v.1.1 for applicable conditions

Sheet: /CPU & Decode Logic/

File: cpu.sch

Title: Z80 CPU & Memor	y Logic
------------------------	---------

	Size: A4	Date: 2021-02-	02	Rev: 2
KiCad E.D.A. kicad 5.1.9-5.1.9			ld: 5/5	
	4		5	