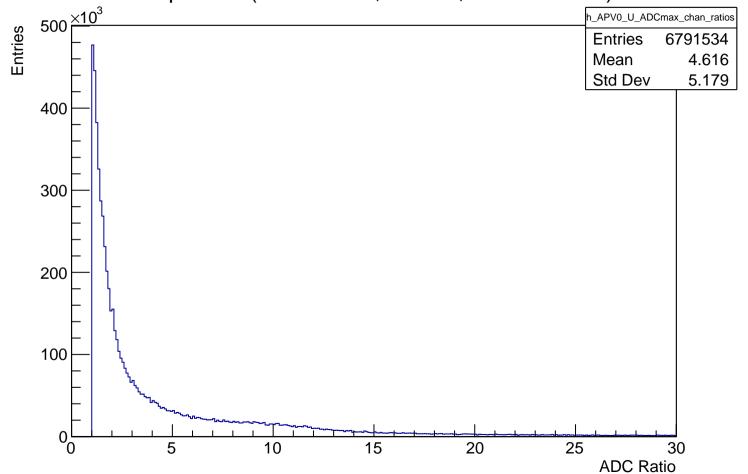
Strip ratios (Run: 13732, APV: 0, ADC cut: 100)



Strip ratios (Run: 13732, APV: 1, ADC cut: 100) $\times 10^3$ h_APV1_U_ADCmax_chan_ratios Entries 350 **Entries** 6681820 Mean 4.812 Std Dev 5.272 300 250 200 150 100 50 0, 5 10 15 20 25 30

Strip ratios (Run: 13732, APV: 2, ADC cut: 100) ×10³ h_APV2_U_ADCmax_chan_ratios Entries **Entries** 6964703 Mean 4.658 350 Std Dev 5.15 300 250 200 150 100 50 0,

15

20

25

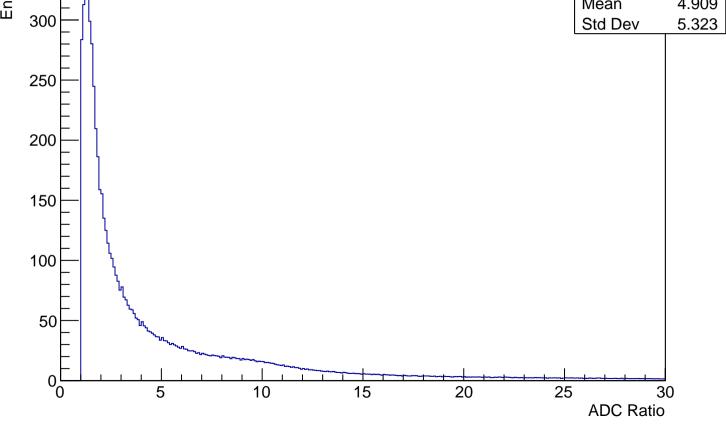
30

ADC Ratio

10

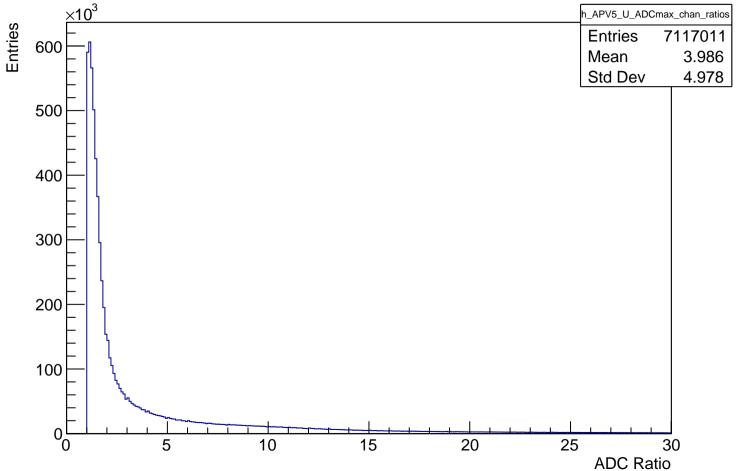
5

Strip ratios (Run: 13732, APV: 3, ADC cut: 100) ×10³ h_APV3_U_ADCmax_chan_ratios Entries **Entries** 6811715 Mean 4.909 300 Std Dev 5.323 250 200 150 100



Strip ratios (Run: 13732, APV: 4, ADC cut: 100) ×10³ h_APV4_U_ADCmax_chan_ratios Entries **Entries** 8034744 300 Mean 5.182 Std Dev 5.261 250 200 150 100 50 0, 25 5 10 15 20 30

Strip ratios (Run: 13732, APV: 5, ADC cut: 100)

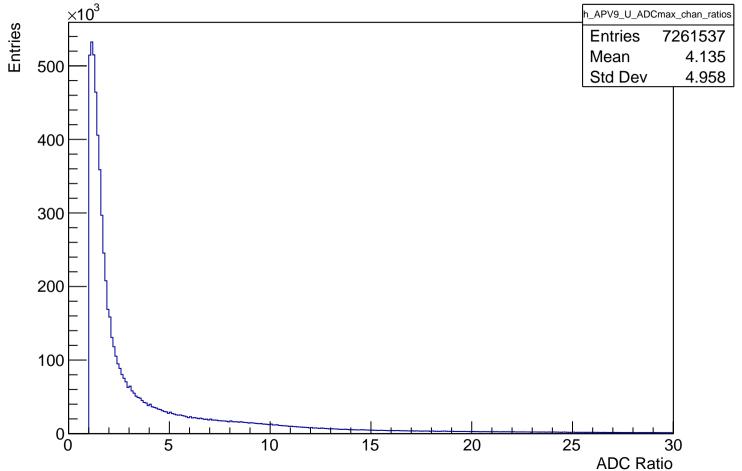


Strip ratios (Run: 13732, APV: 6, ADC cut: 100) $\times 10^3$ h_APV6_U_ADCmax_chan_ratios Entries **Entries** 7087592 Mean 5.337 300 Std Dev 5.728 250 200 150 100 50 0, 15 20 25 5 10 30

Strip ratios (Run: 13732, APV: 7, ADC cut: 100) ×10³ h_APV7_U_ADCmax_chan_ratios Entries **Entries** 7095185 Mean 4.048 Std Dev 5.009 500 400 300 200 100 0, 5 10 15 20 25 30

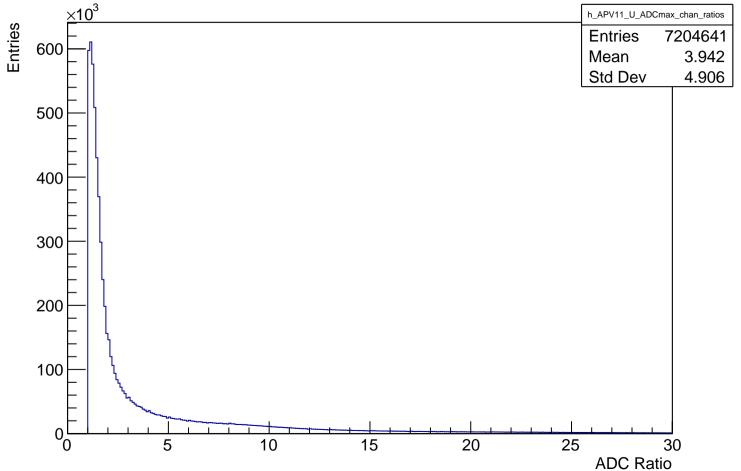
Strip ratios (Run: 13732, APV: 8, ADC cut: 100) ×10³ h_APV8_U_ADCmax_chan_ratios Entries **Entries** Mean 4.547 Std Dev 5.247

Strip ratios (Run: 13732, APV: 9, ADC cut: 100)

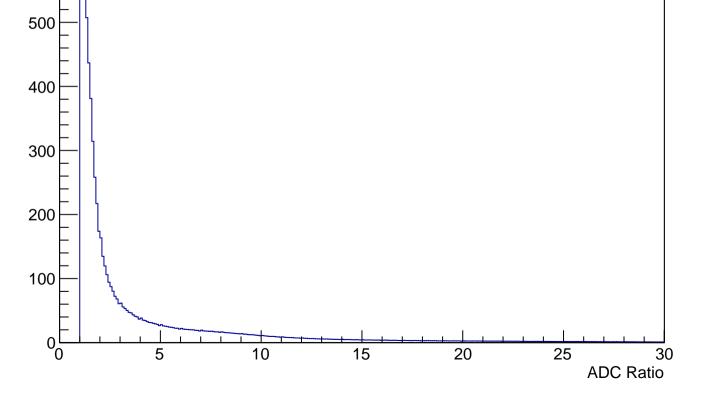


Strip ratios (Run: 13732, APV: 10, ADC cut: 100) ×10³ h_APV10_U_ADCmax_chan_ratios Entries 7381716 **Entries** 300 Mean 5.351 Std Dev 5.639 250 200 150 100 50 0, 20 25 5 10 15 30 **ADC Ratio**

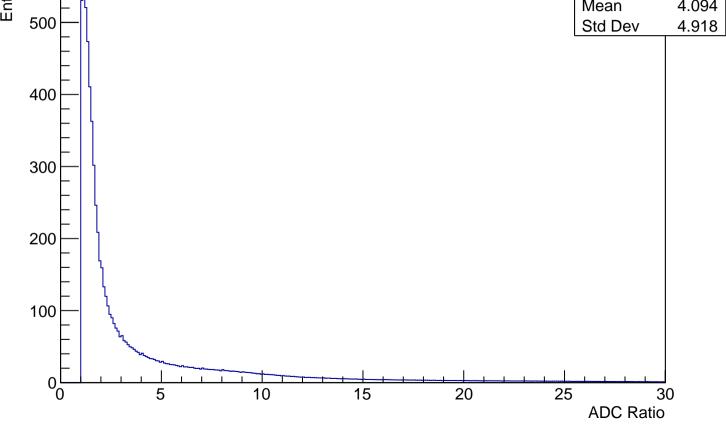
Strip ratios (Run: 13732, APV: 11, ADC cut: 100)



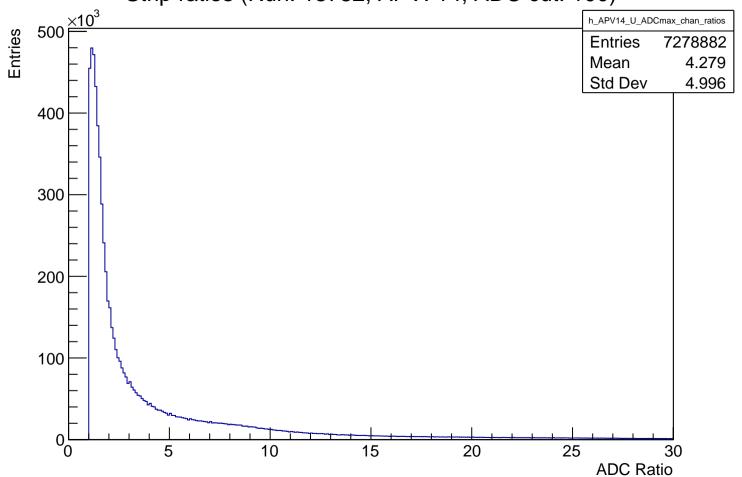
Strip ratios (Run: 13732, APV: 12, ADC cut: 100) $\times 10^3$ h_APV12_U_ADCmax_chan_ratios Entries **Entries** 7480983 600 Mean 3.854 Std Dev 4.727 500 400 300 200



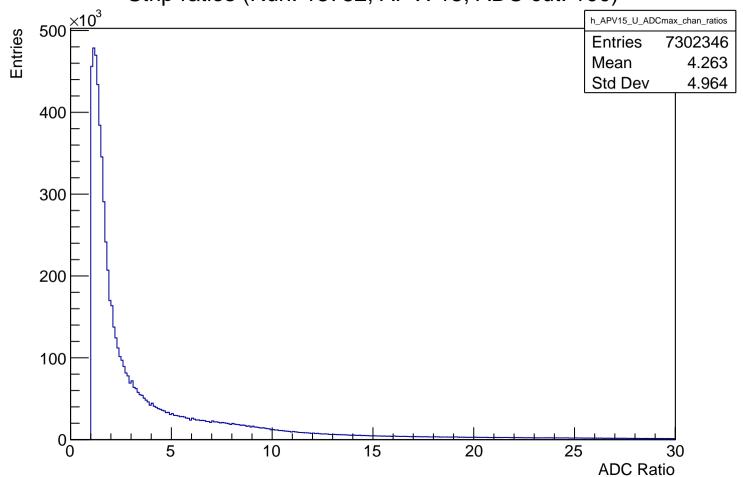
Strip ratios (Run: 13732, APV: 13, ADC cut: 100) ×10³ h_APV13_U_ADCmax_chan_ratios Entries 7337133 **Entries** Mean 4.094 500 Std Dev 4.918 400 300 200



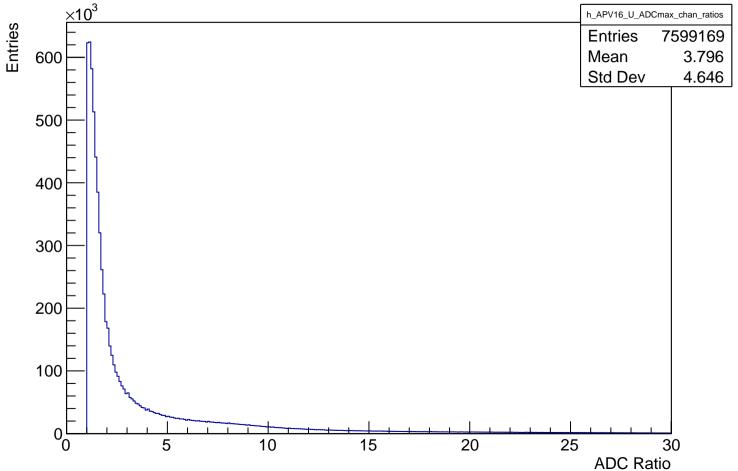
Strip ratios (Run: 13732, APV: 14, ADC cut: 100)



Strip ratios (Run: 13732, APV: 15, ADC cut: 100)

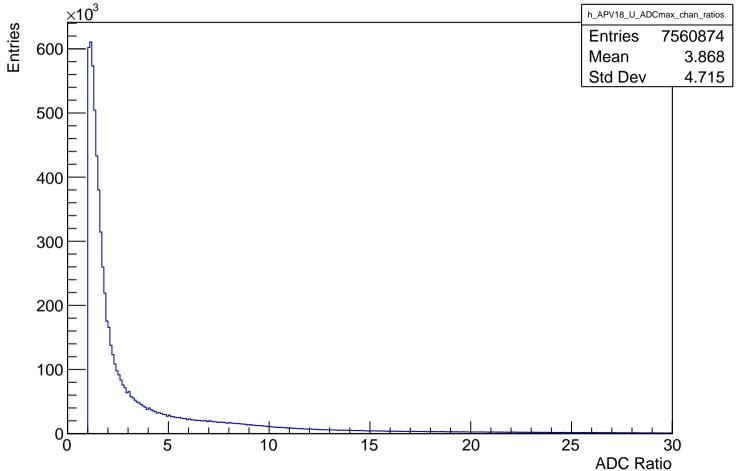


Strip ratios (Run: 13732, APV: 16, ADC cut: 100)



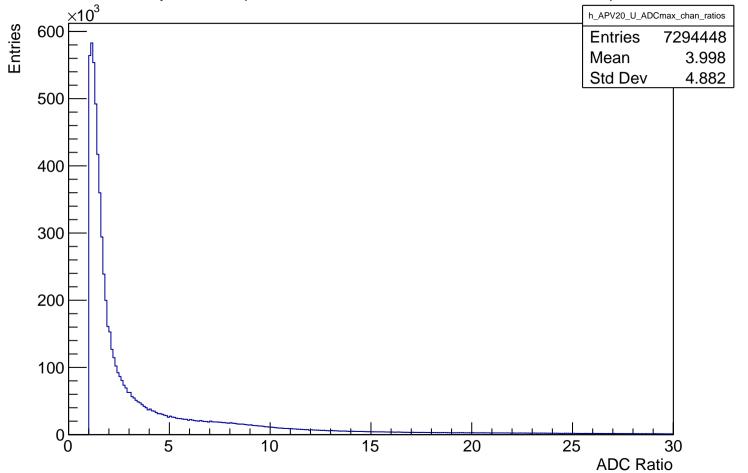
Strip ratios (Run: 13732, APV: 17, ADC cut: 100) ×10³ h_APV17_U_ADCmax_chan_ratios Entries 7401319 **Entries** 500 Mean 4.149 Std Dev 4.866 400 300 200 100 0 15 5 10 20 25 30

Strip ratios (Run: 13732, APV: 18, ADC cut: 100)

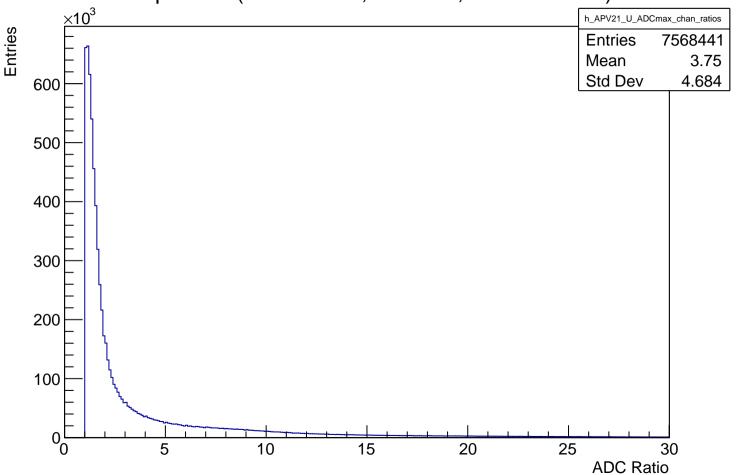


Strip ratios (Run: 13732, APV: 19, ADC cut: 100) <u>×10</u>³ h_APV19_U_ADCmax_chan_ratios Entries 700 **Entries** 7474413 Mean 3.693 Std Dev 4.64 600 500 400 300 200 100 0, 5 10 15 20 25 30 **ADC Ratio**

Strip ratios (Run: 13732, APV: 20, ADC cut: 100)



Strip ratios (Run: 13732, APV: 21, ADC cut: 100)

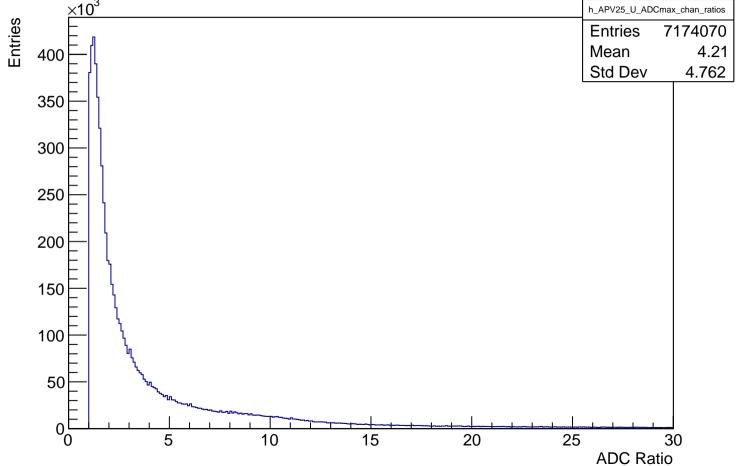


Strip ratios (Run: 13732, APV: 22, ADC cut: 100) $\times 10^3$ h_APV22_U_ADCmax_chan_ratios Entries 7442842 **Entries** Mean 3.969 Std Dev 4.787 500 400 300 200 100 0, 5 10 15 20 25 30

Strip ratios (Run: 13732, APV: 23, ADC cut: 100) ×10³ h_APV23_U_ADCmax_chan_ratios Entries **Entries** Mean 4.401 Std Dev 4.952

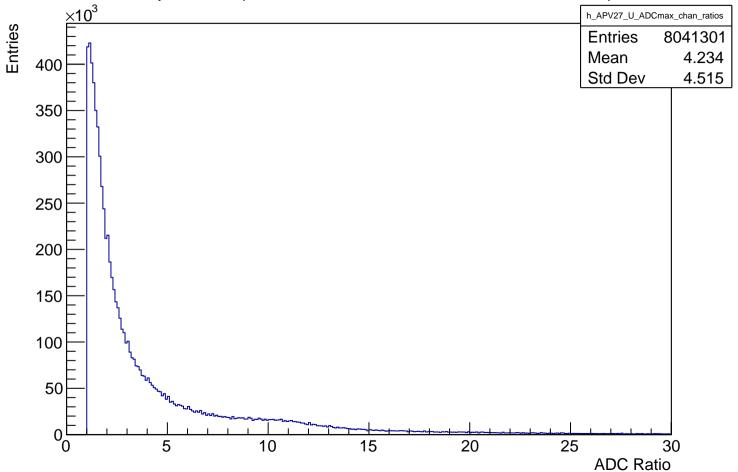
Strip ratios (Run: 13732, APV: 24, ADC cut: 100) ×10³ h_APV24_U_ADCmax_chan_ratios Entries **Entries** Mean 4.258 Std Dev 4.761

Strip ratios (Run: 13732, APV: 25, ADC cut: 100) ×10³

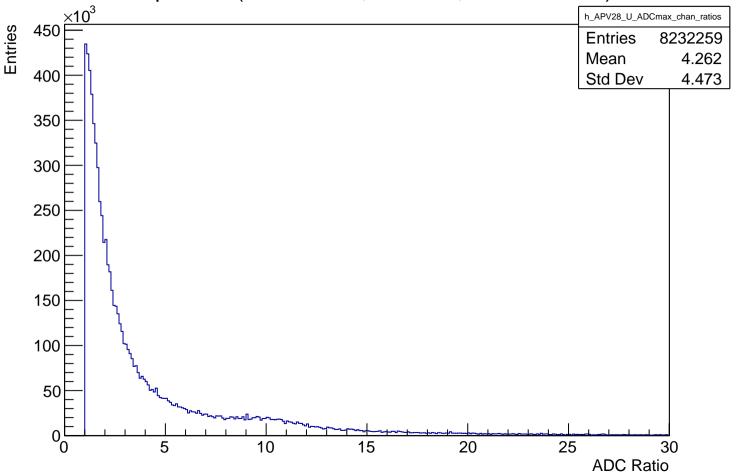


Strip ratios (Run: 13732, APV: 26, ADC cut: 100) ×10³ h_APV26_U_ADCmax_chan_ratios Entries **Entries** Mean 4.095 Std Dev 4.417

Strip ratios (Run: 13732, APV: 27, ADC cut: 100)



Strip ratios (Run: 13732, APV: 28, ADC cut: 100)



Strip ratios (Run: 13732, APV: 29, ADC cut: 100)

