Reg Address s	Name	Type	0x11 (10/ChipID)	Byte Size	Sequence Read Offset	multiplied by
0x00	Pitch(signed int)	Read	Low Byte/High Byte	2	0	100
0x01	Roll(signed int)	Read	Low Byte/High Byte	2	2	100
0x02	Yaw(unsigned int)	Read	Low Byte/High Byte	2	4	100
0x03	Temp(signed int)	Read	Low Byte/High Byte	2	6	100
0x04	Gyro(signed int)	Read	Low Byte/High Byte	2	8	
0x05	Ax_RAW(signed int)	Read	Low Byte/High Byte	2	10	
0x06	Ay_RAW(signed int)	Read	Low Byte/High Byte	2	12	
0x07	Az_RAW(signed int)	Read	Low Byte/High Byte	2	14	
0x08	Mx_RAW(unsigned int)	Read	Low Byte/High Byte	2	16	
0x09	My_RAW(unsigned int)	Read	Low Byte/High Byte	2	18	
0x0A	Mz_RAW(unsigned	Read	Low Byte/High	2	20	

	int)		Byte			
0x0B	Gyro_RAW(unsigned int)	Read	Low Byte/High Byte	2	22	
0x0C	Mx_max(signed int)	Read	Low Byte/High Byte	2	24	
0x0D	Mx_min(signed int)	Read	Low Byte/High Byte	2	26	
0x0E	My_max(signed int)	Read	Low Byte/High Byte	2	28	
0x0F	My_min(signed int)	Read	Low Byte/High Byte	2	30	
0x10	Mz_max(signed int)	Read	Low Byte/High Byte	2	32	
0x11	Mz_min(signed int)	Read	Low Byte/High Byte	2	34	
0x12	Gyro_Cal(unsigned int)	Read	Low Byte/High Byte	2	36	