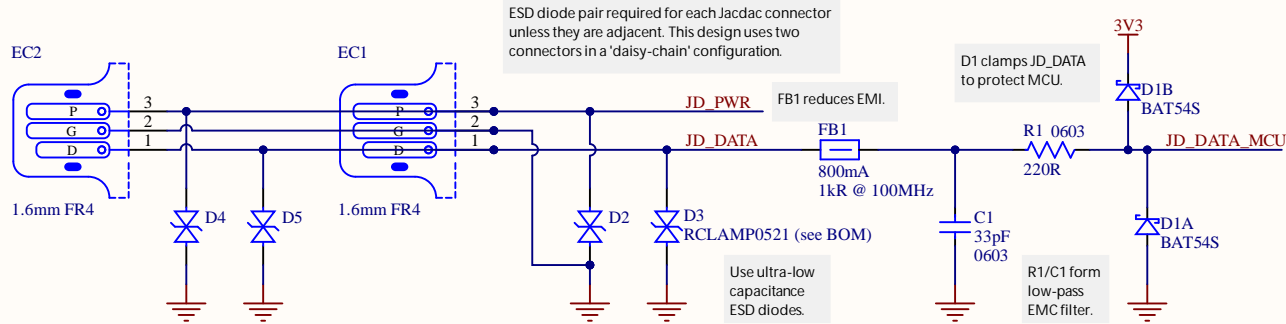
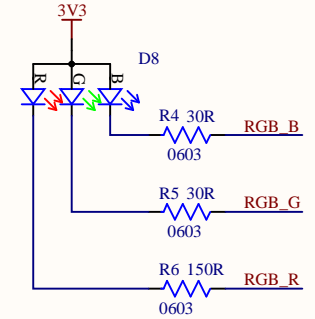


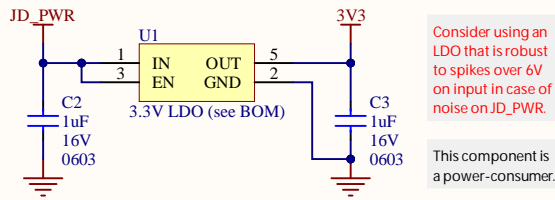
Jacdac connector



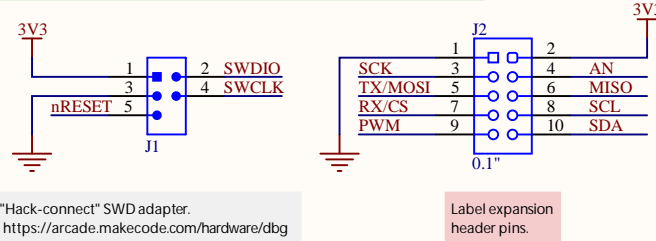
RGB LED



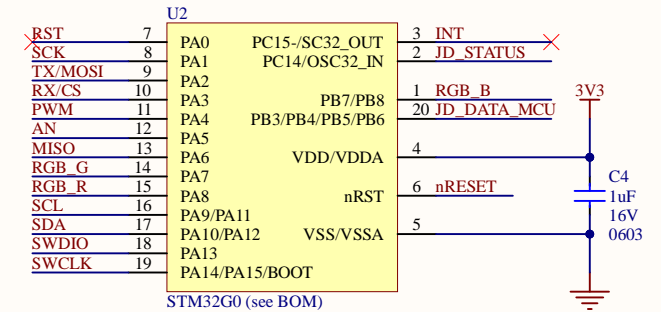
3V3 regulator



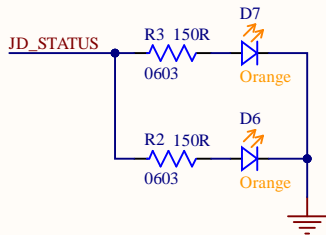
Programming/debug/expansion



MCU

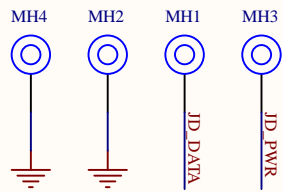


Status LED



Jacdac modules require a status LED per port (can be shared if ports are adjacent). Can be monochrome or multicolor depending on GPIO availability.

Mounting holes



Mounting holes are electrically connected to the Jacdac bus nets so they can be used as an alternative to the PCB edge connector. Please use the following reference designators and net mapping:

MH1 & MH2: GND
MH3: JD_DATA
MH4: JD_PWR

This design uses PTH mounting holes with finished diameter of 3.1mm, annular copper ring of 4.4mm diameter & copper/component keepout of 7.0mm. The mounting holes must be on 10mm pitch. Mounting holes should have appropriate silkscreen marker, and MH1 should have a pin 1 marker in copper on the top side only.

Microsoft

PROJECT DESCRIPTION
G0-based single RGB LED/dev board

SHEET DESCRIPTION
Complete design

PROJECT FILENAME JacdacDevRgbEc30 117.PrjPCB

PROJECT CODENAME JacdacDevRgbEc30

LAST MODIFIED 13/06/2022

PAGE 1 OF 1

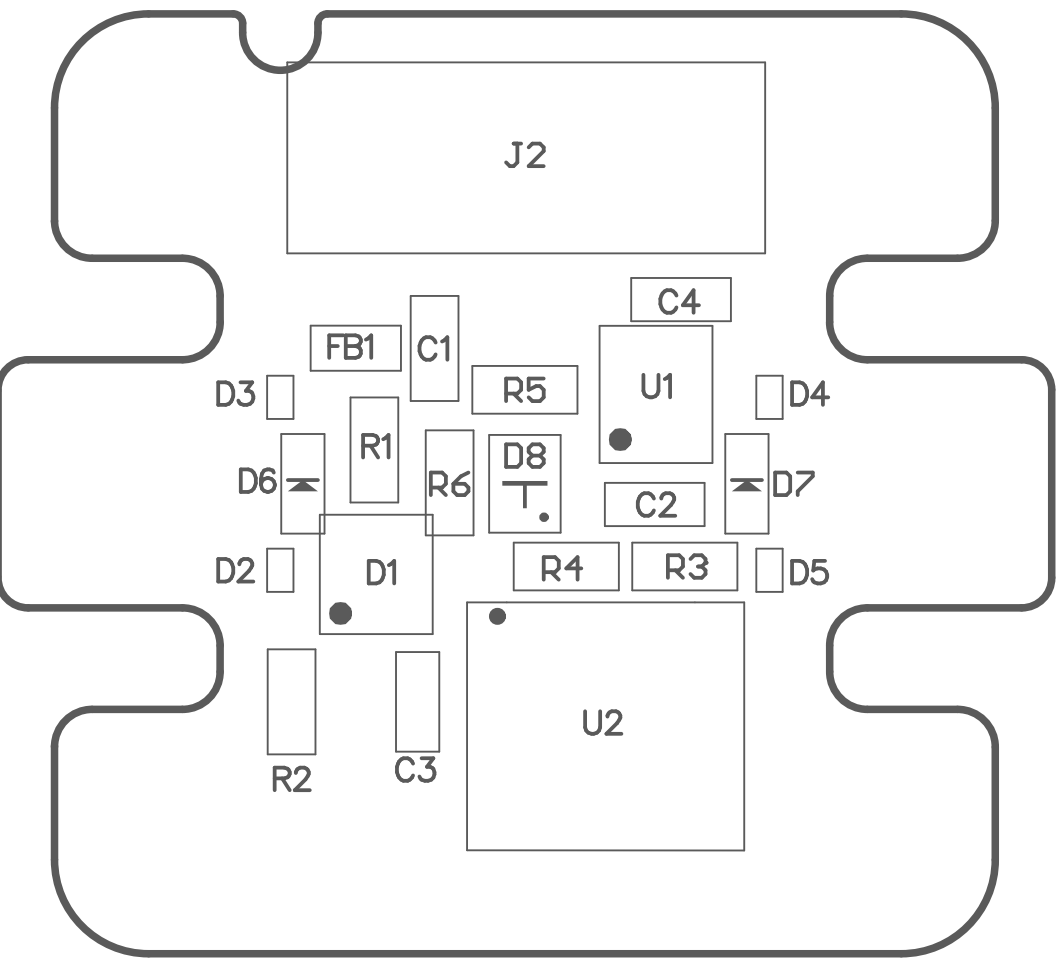
DRAWN BY S. Hodges

REVISION 1.0

PCB ID 117-1.0

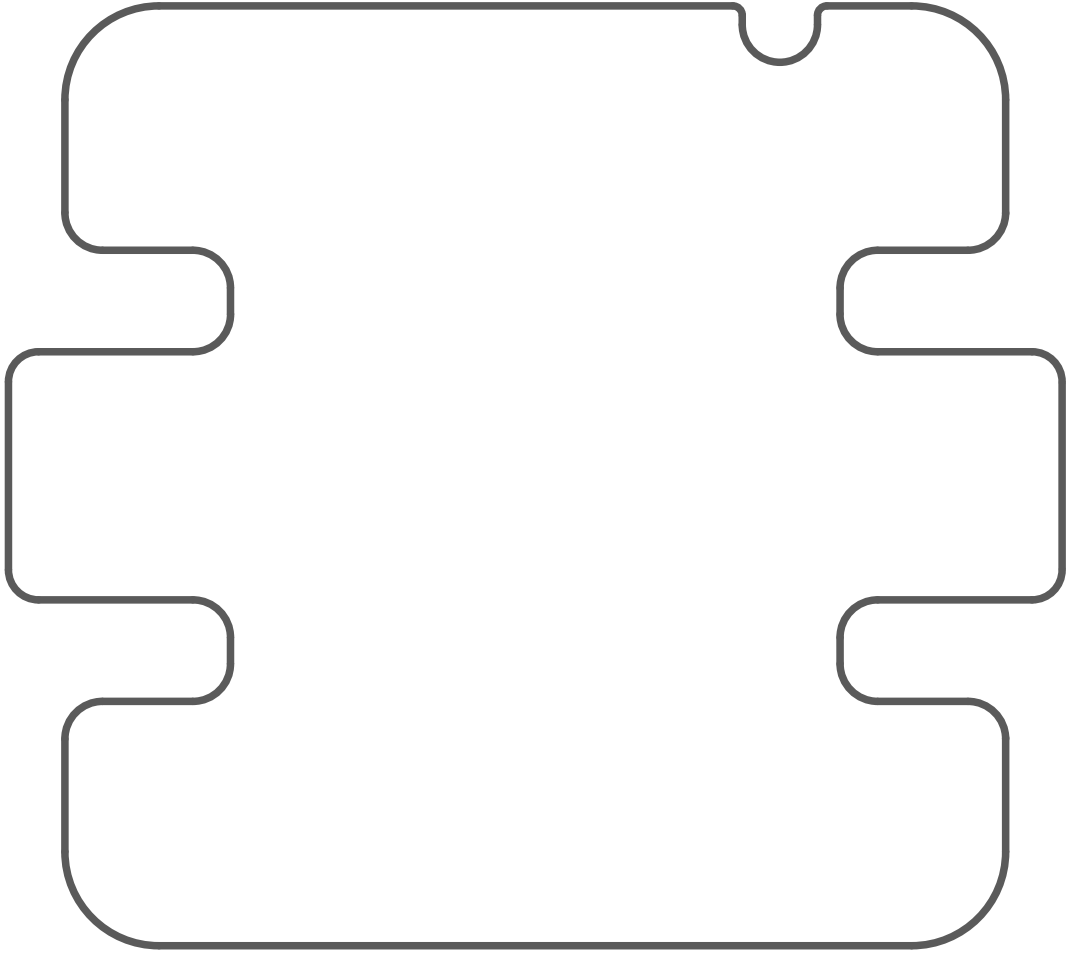
SHEET FILENAME JacdacDevRgbEc30 117.SchDoc

LICENCE Attribution 4.0 International (CC BY 4.0)



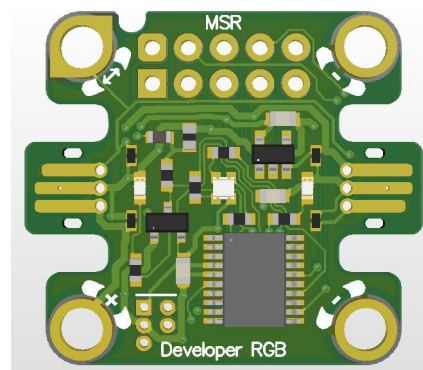
Top Assy

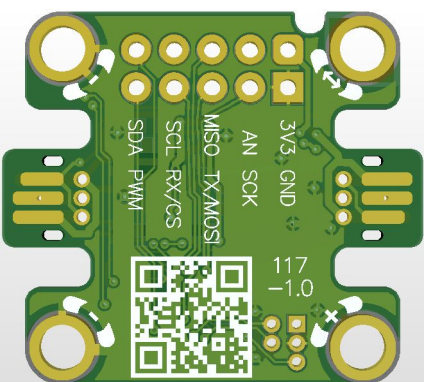
Board Outline

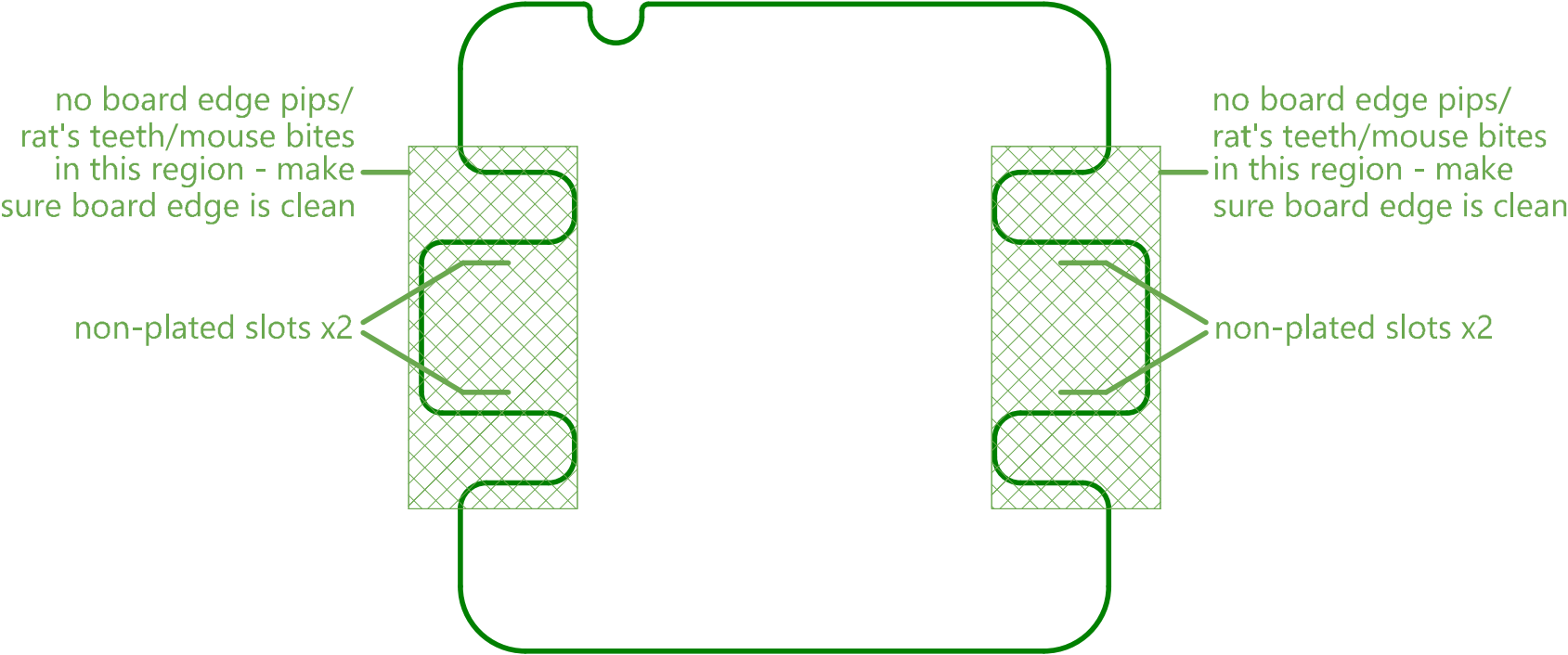


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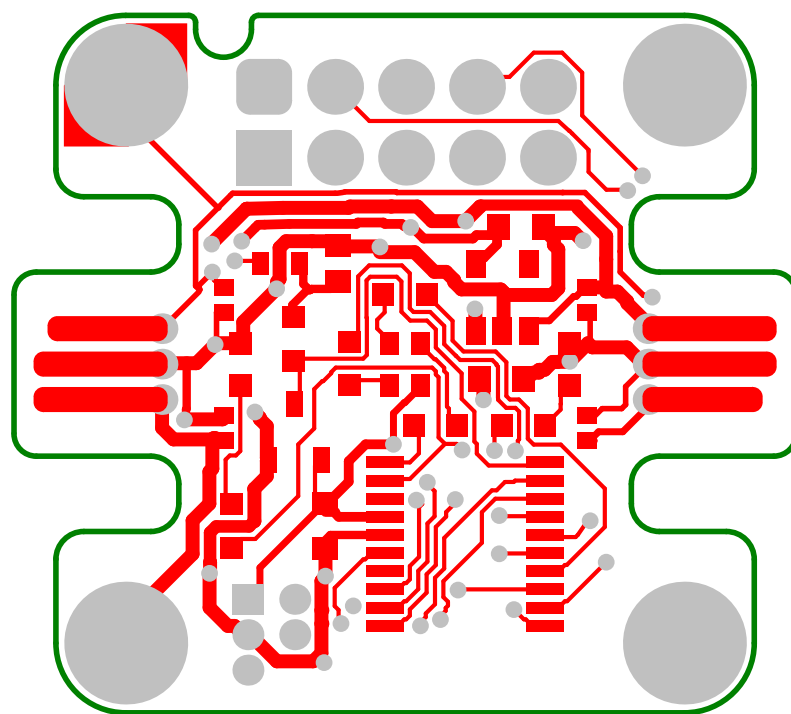
Board Outline





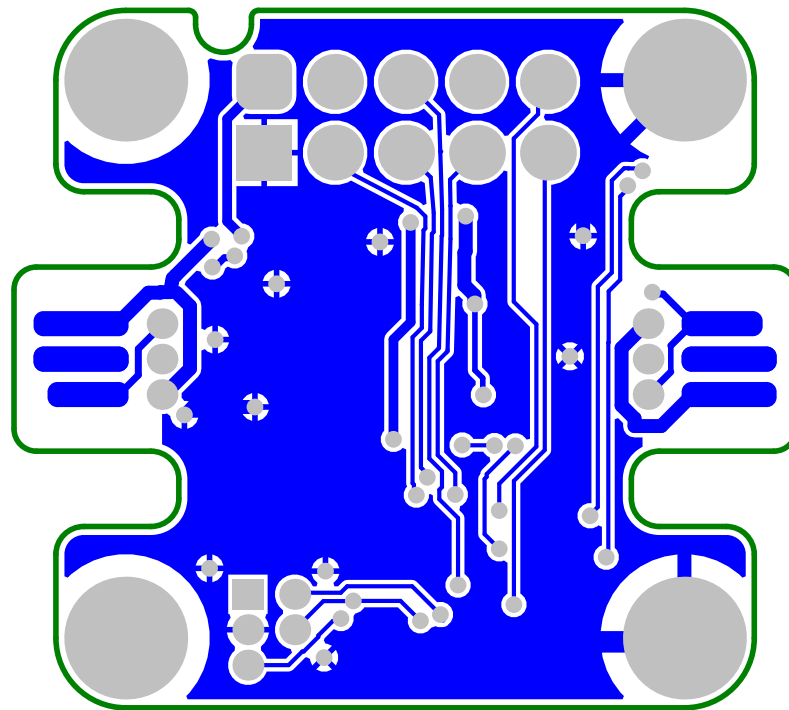


Fabrication Notes
Board Outline



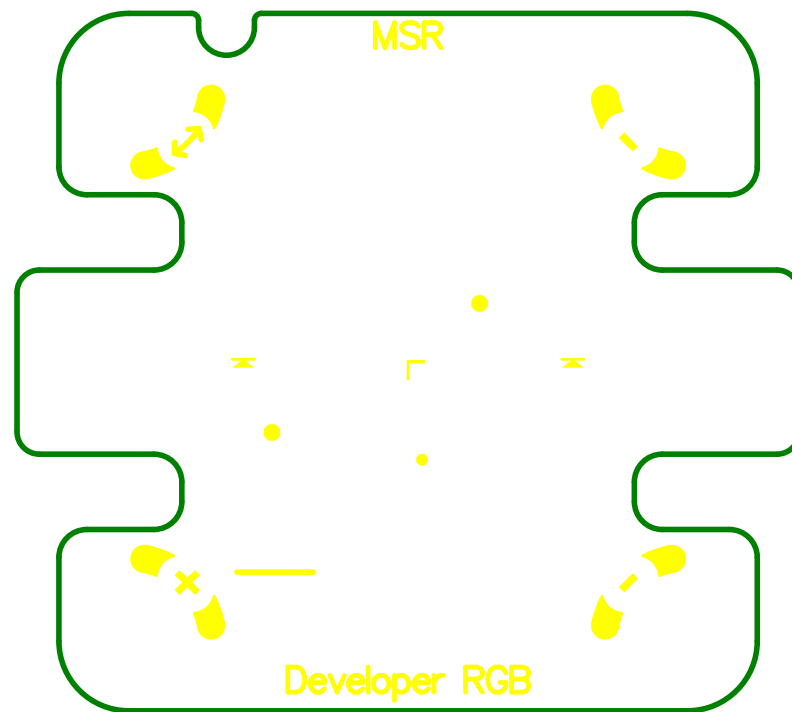
Top Layer

Board Outline

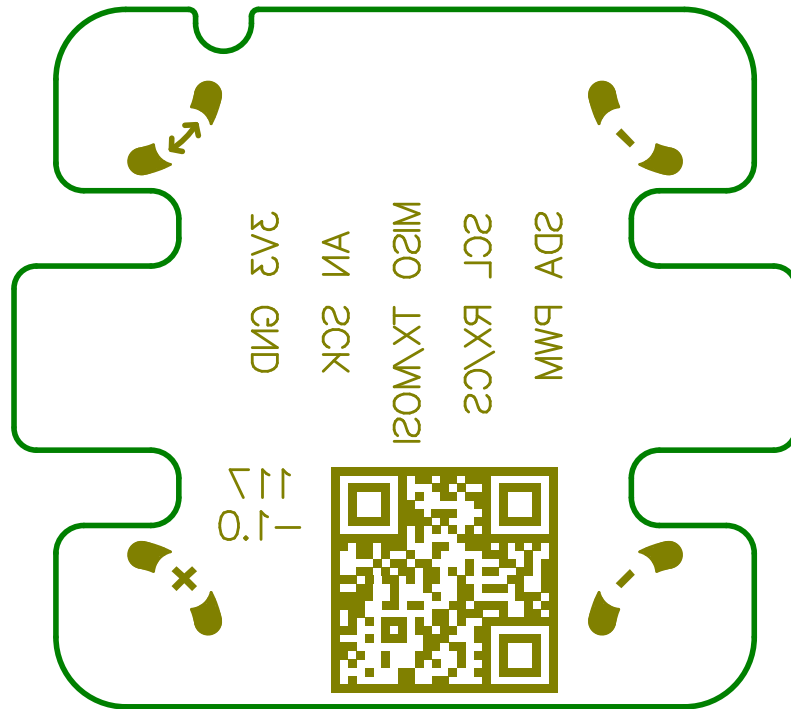


Bottom Layer

Board Outline

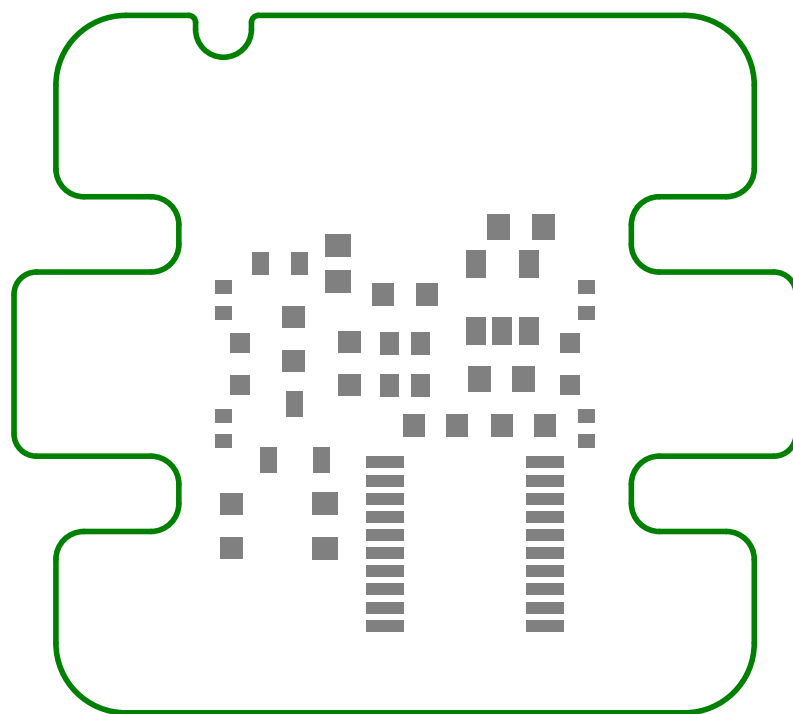


Board Outline



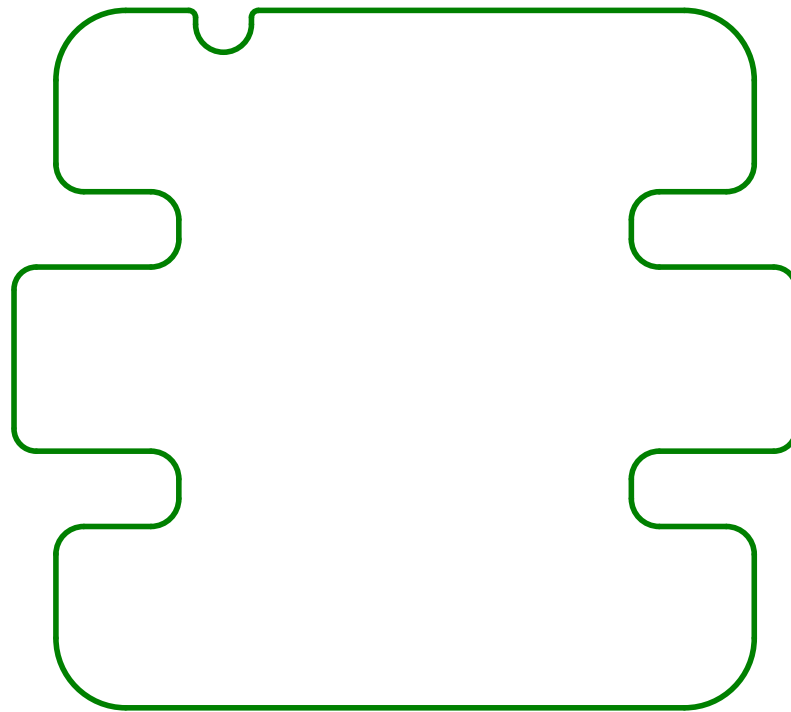
Bottom Overlay

Board Outline



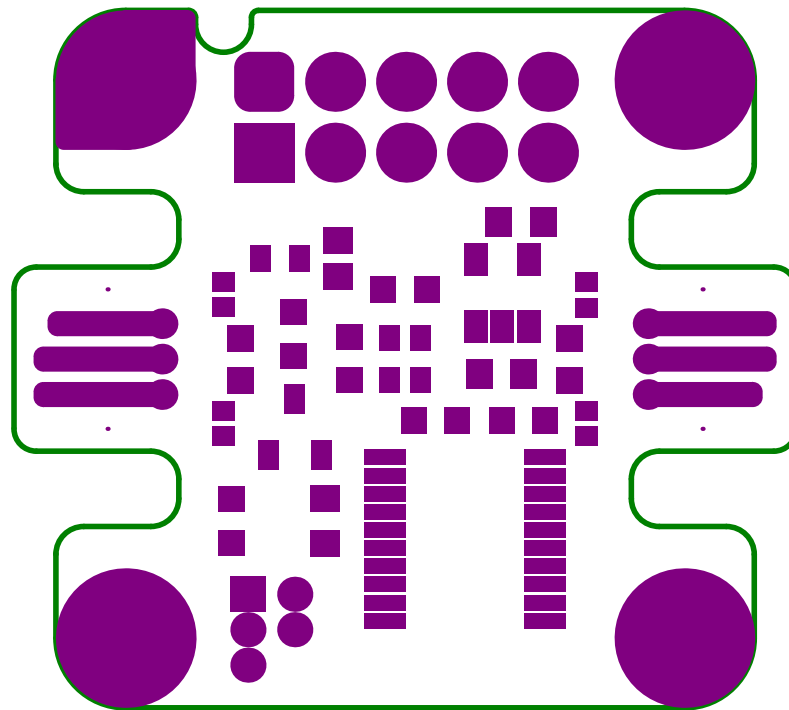
Top Paste

Board Outline



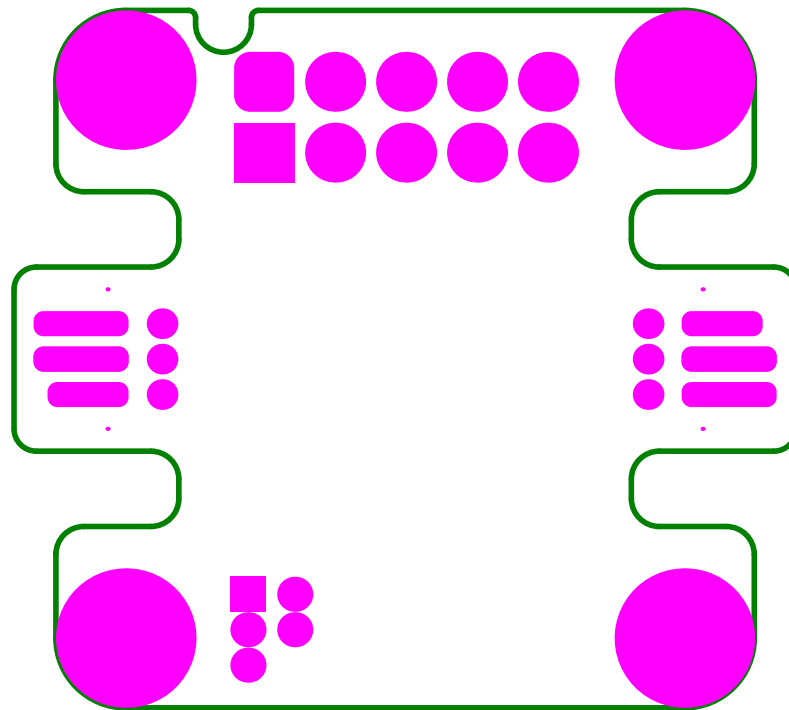
Bottom Paste

Board Outline



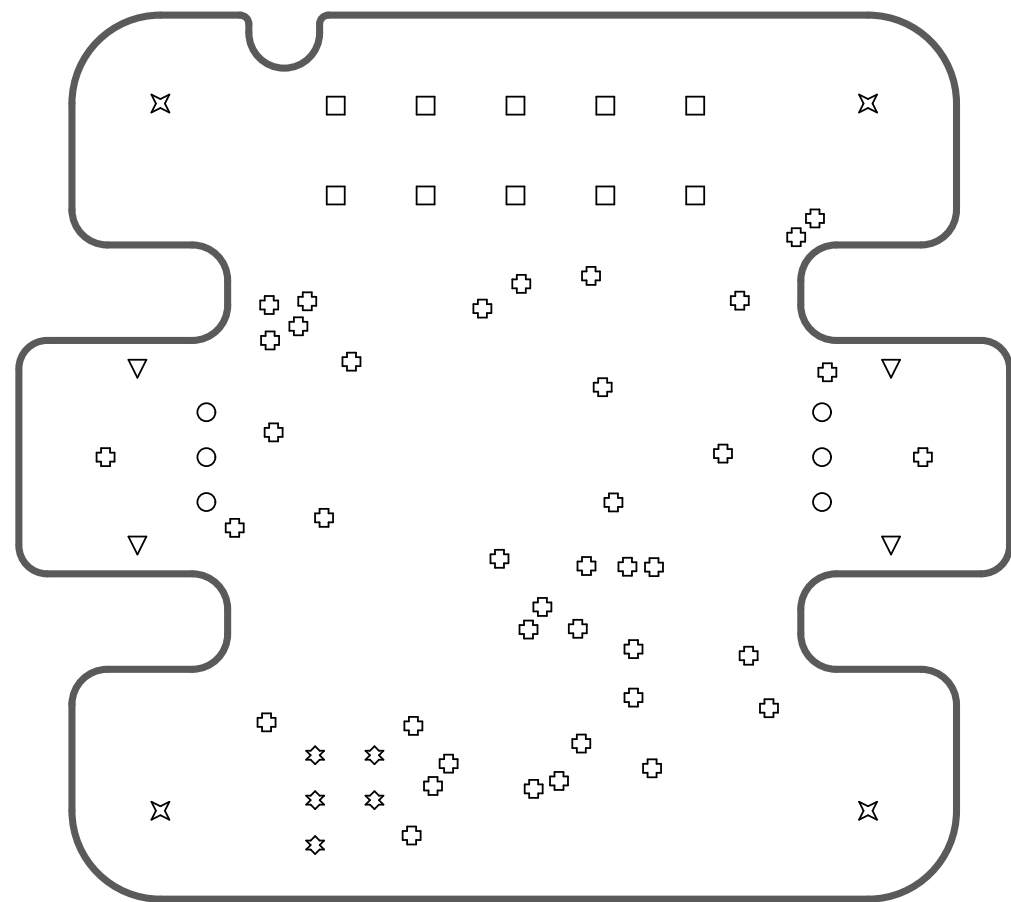
Top Solder (resist)

Board Outline



Board Outline

Bottom Solder (resist)



Board Outline

Drill Drawing