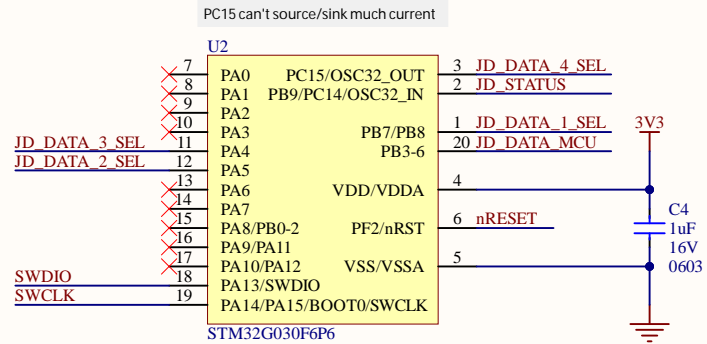
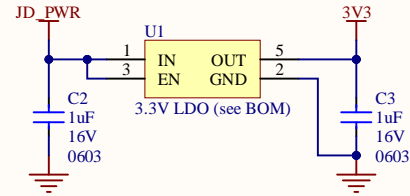


MCU

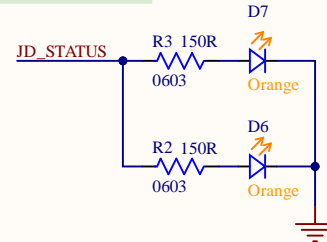
This component is a power-consumer.



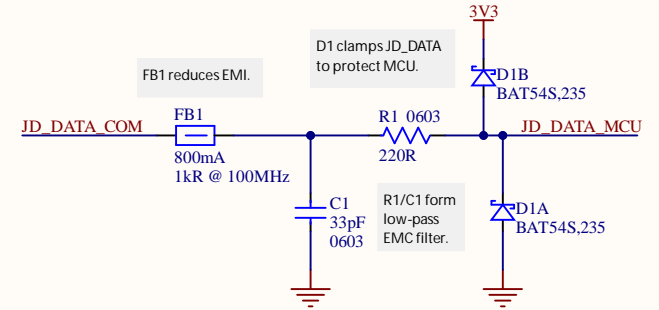
3V3 regulator



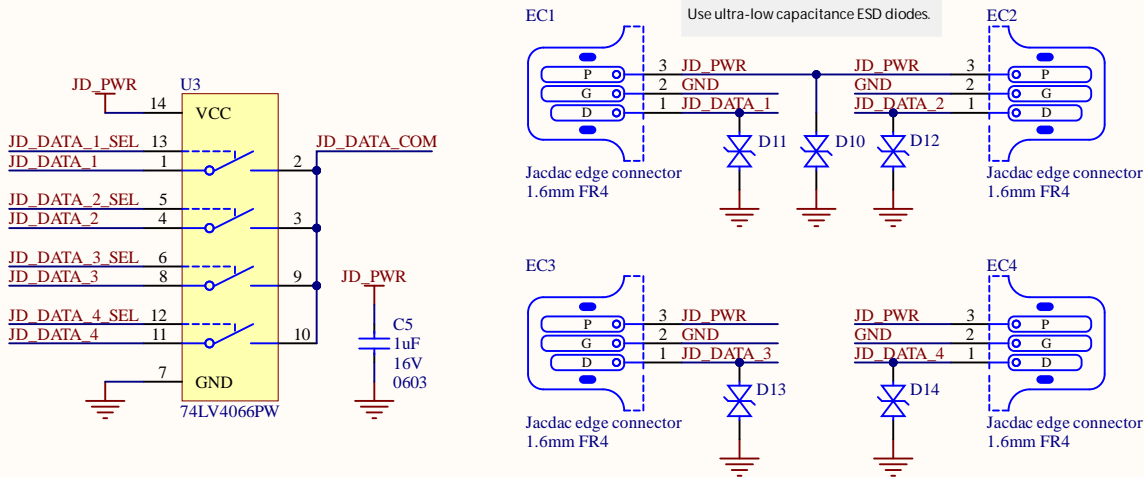
Status LED



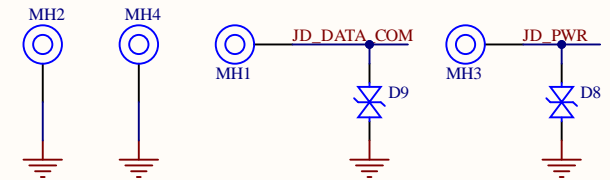
Jacdac data interface



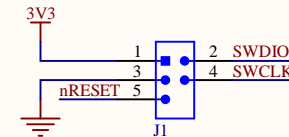
Jacdac connectors



Mounting holes



Programming/debug



Silkscreen & layout notes

Block name

Design notes

When this PDF is viewed with Adobe Reader, clicking on components shows part numbers and other details.

This information is provided "as-is". You bear the risk of using it. Some information relates to pre-released specification which may change without notice. Microsoft makes no warranties, express or implied, with respect to the information provided here.

Microsoft

PROJECT DESCRIPTION
Jacdac 'smart' 4-port hub that detects topology

SHEET DESCRIPTION
Complete design

PROJECT FILENAME JacdacTopologyDetectorEc30 126.PrjPCB

PROJECT CODENAME JacdacTopologyHubEc30

LAST MODIFIED 07/08/2022

PAGE 1 OF 1

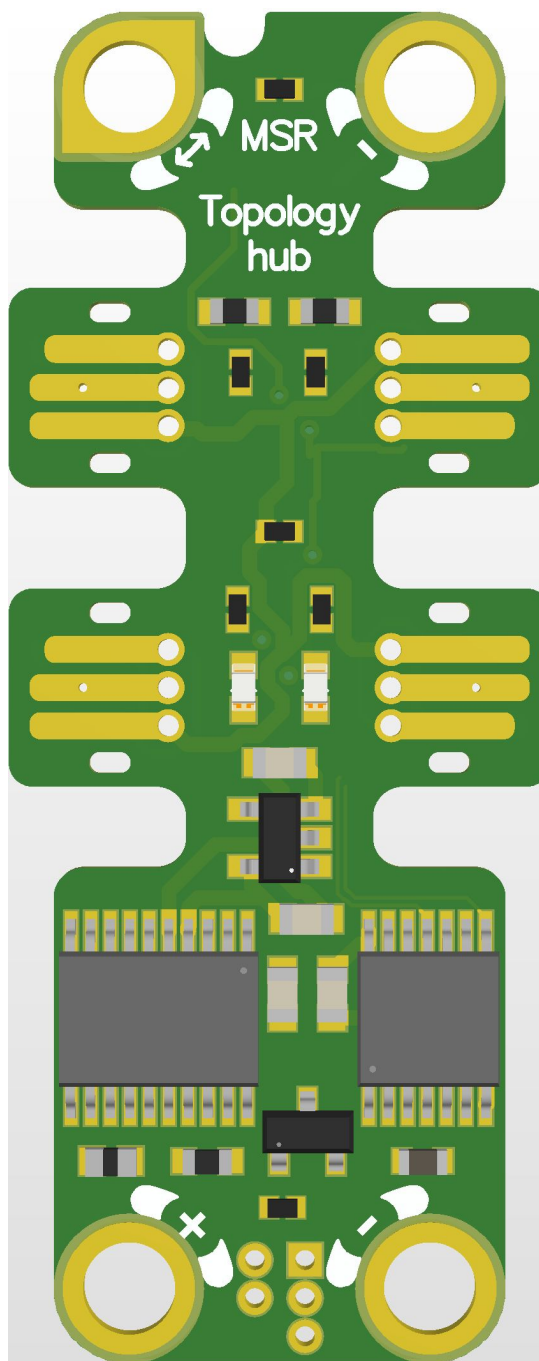
DRAWN BY S. Hodges

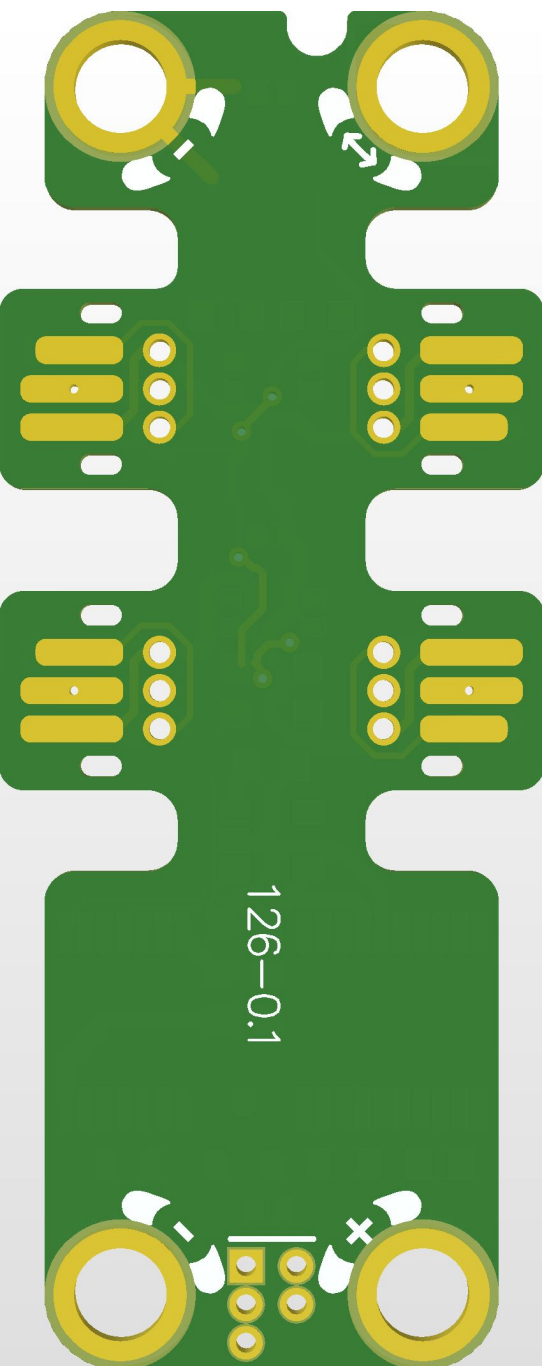
SHEET FILENAME JacdacTopologyHubEc30 126.SchDoc

LICENCE Attribution 4.0 International (CC BY 4.0)

REVISION 0.1

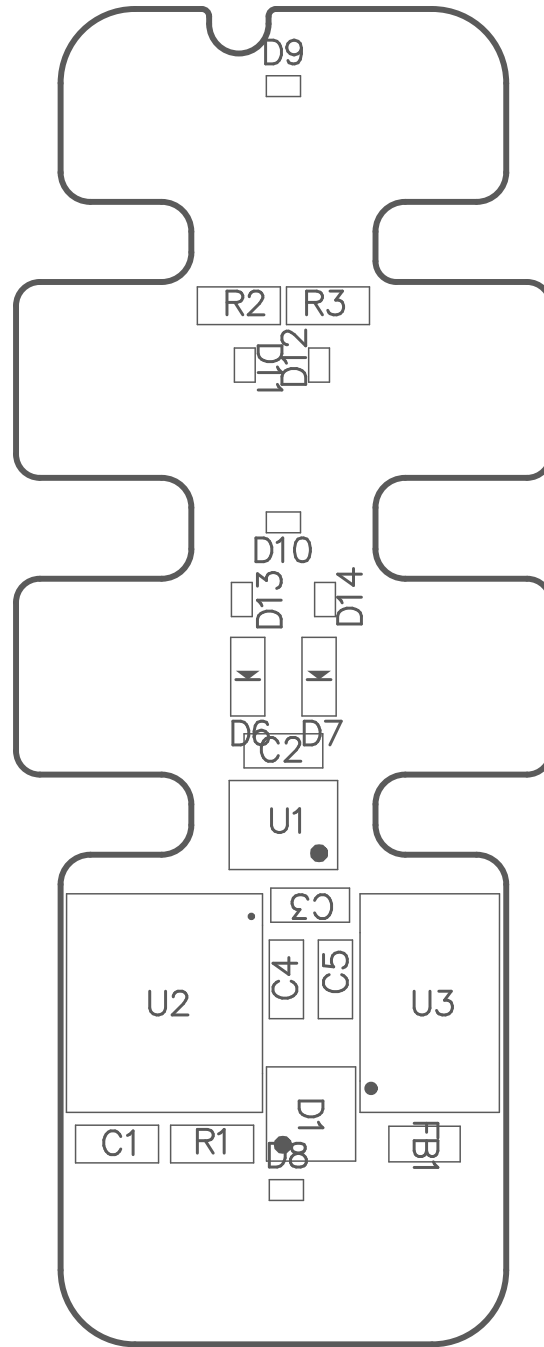
PCB ID 126-0.1





Top Assy

Board Outline



Board Outline

Bottom Assy

