



Current mirror circuit based on 2xPNPs is used to control the FET (which is arranged so that the body diode is usually forward biased). R2 and R3 need to be large to reduce quiescent current consumption although smaller works better. C1 is used to limit the spike of reverse current as the FET switches off.

Key: **TO-DO**

Silkscreen
notes

Design
notes

PROJECT FILENAME IdealDiode 03-1.0.PrjPCB		PROJECT CODENAME MS-IdealDiode		Microsoft		PROJECT DESCRIPTION Ideal diode		SHEET DESCRIPTION complete design	
SHEET FILENAME IdealDiode 03-1.0 complete.SchDoc		LICENCE Attribution 4.0 International (CC BY 4.0)		LAST MODIFIED 28/08/2019		PAGE 1 OF 1		DRAWN BY Steve Hodges	
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