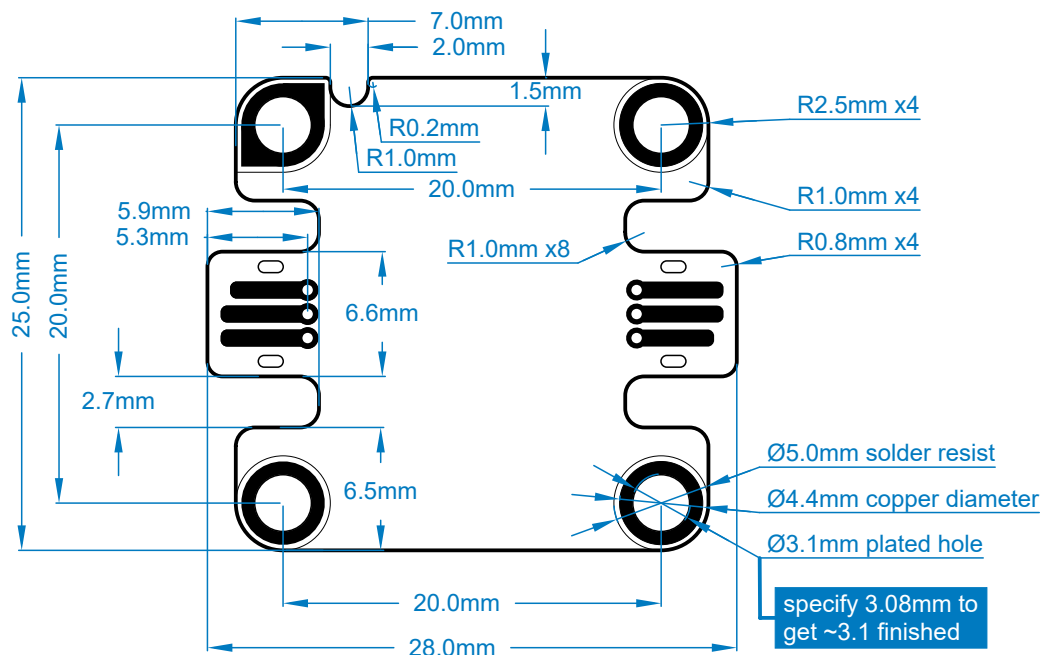
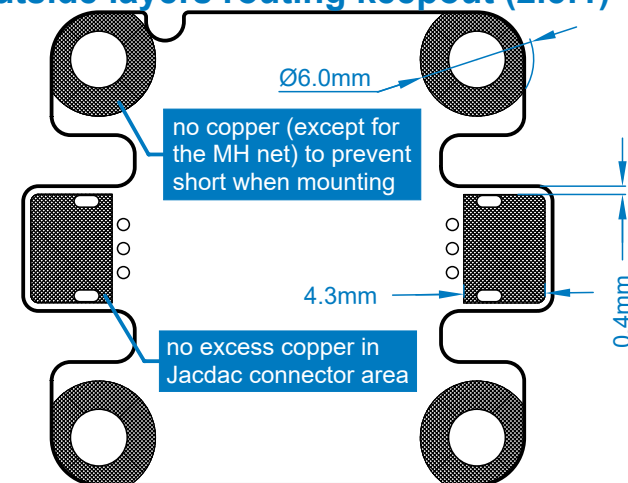


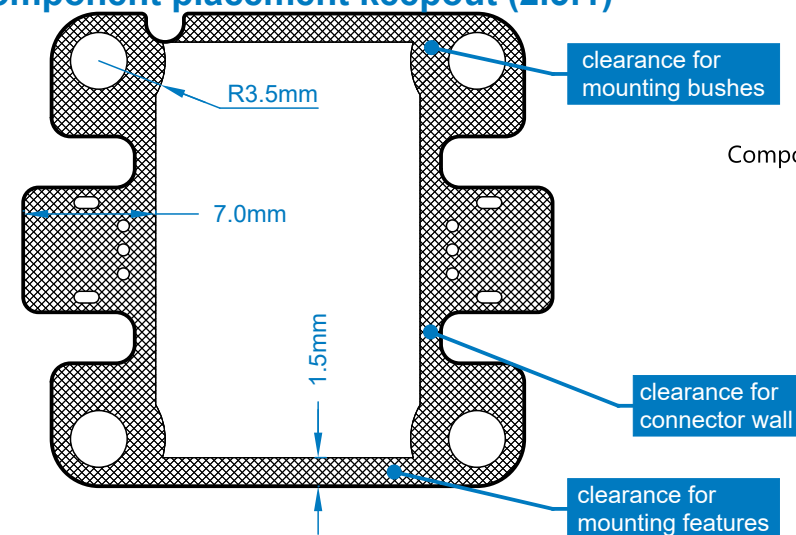
Dimensions (2.5:1)



Outside layers routing keepout (2.5:1)



Component placement keepout (2.5:1)



Jacdac "EC" template: 20x20mm MH, 1+1 conns

PROJECT JacdacEc30Template20x20

DRAWING FILENAME JacdacEc30Template20x20 80.PcbDwf

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REVISION 1.0

LAST MODIFIED 05/06/2022

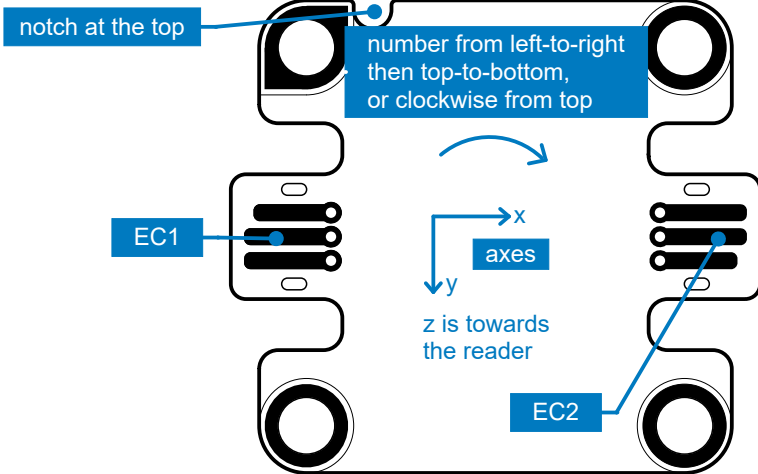
DRAWN BY S. Hodges

ID 80-1.0

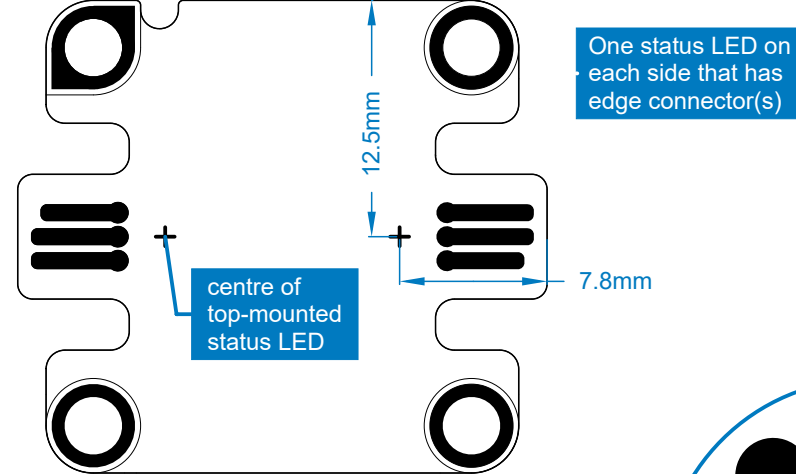
PAGE 1 OF 2

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On-module numbering convention

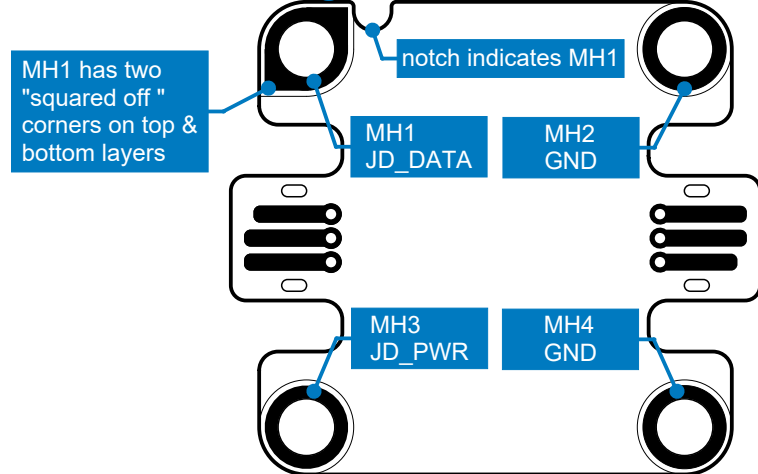


Status LED location

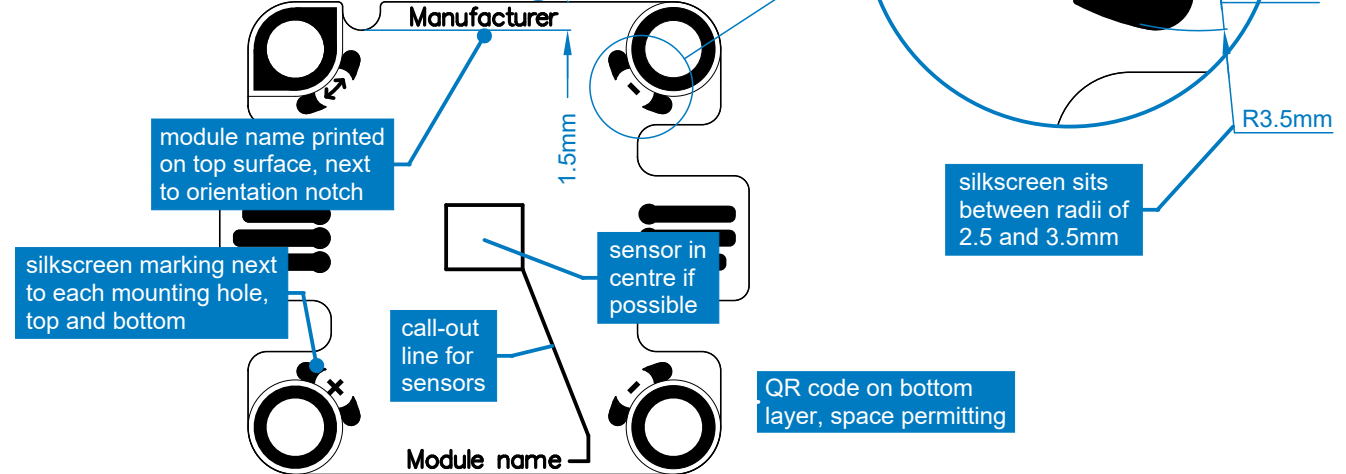


Longer edge connector fingers on top side of PCB

Mounting hole electrical connections



Silkscreen marking



Jacdac "EC" template: 20x20mm MH, 1+1 conns

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PROJECT JacdacEc30Template20x20

DRAWING FILENAME JacdacEc30Template20x20 80.PcbDwf

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