

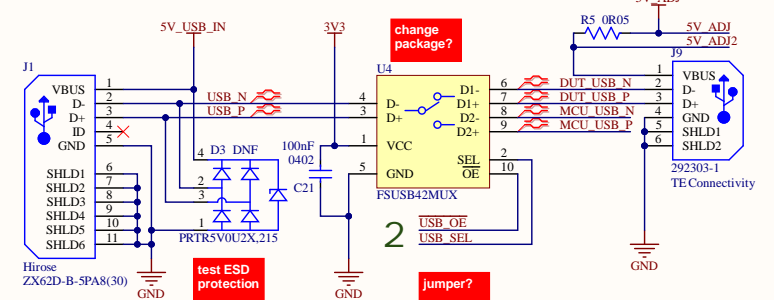
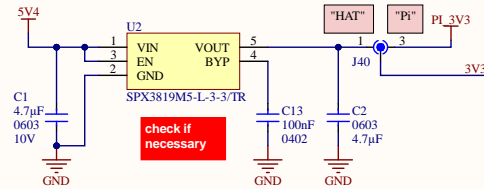
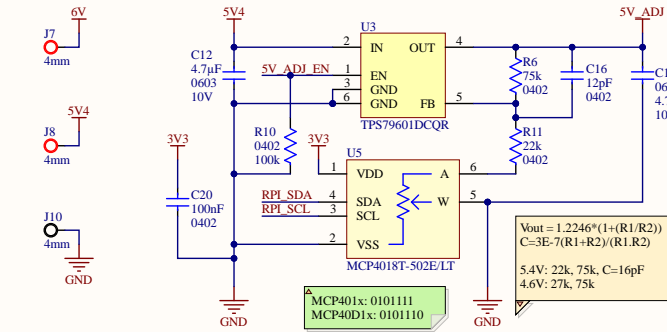
## Power in

## Generate ~4.8-5.2V for testing USB and JACDAC

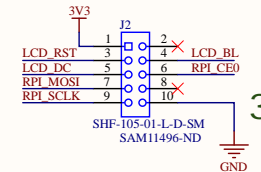
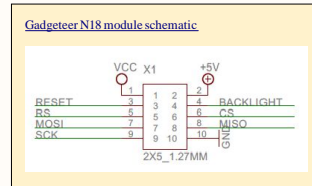
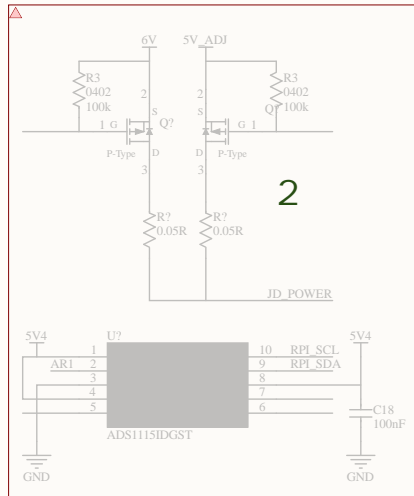
## Generate 3.3V for local use

## USB data pass thru and power provision

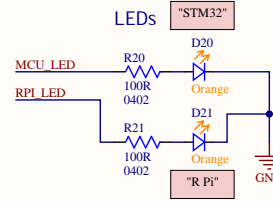
A



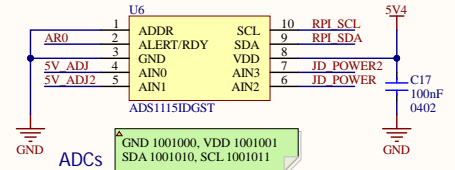
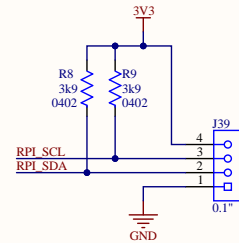
B



C

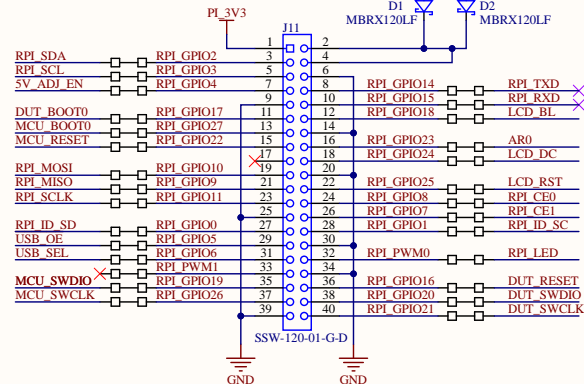
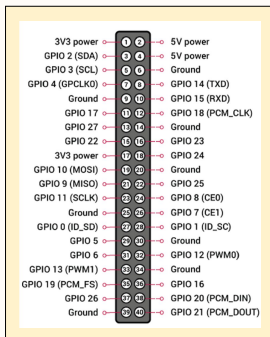


## I2C pull-ups & expansion

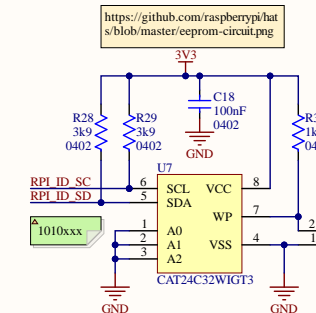


D

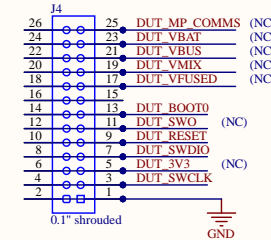
## R Pi header connections



## R Pi HAT identifier EEPROM



## DUT programming, debug & test connector footprints



Connector footprints which support a manufacturing jig and also allow a header to be used to support ad-hoc and low volume testing. Place footprint so that hole centres are exactly 0.1" from board edge.

Key:

TO-DO

Silkscreen notes.

Design notes.

A

B

C

D

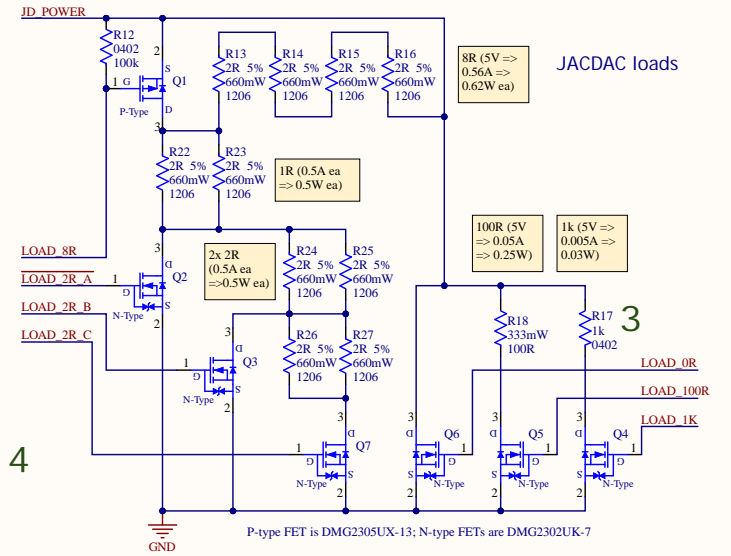
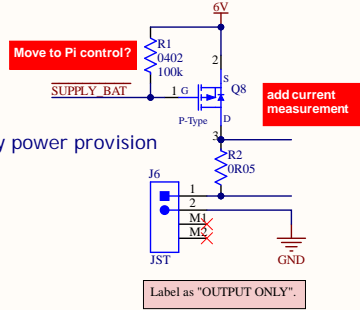
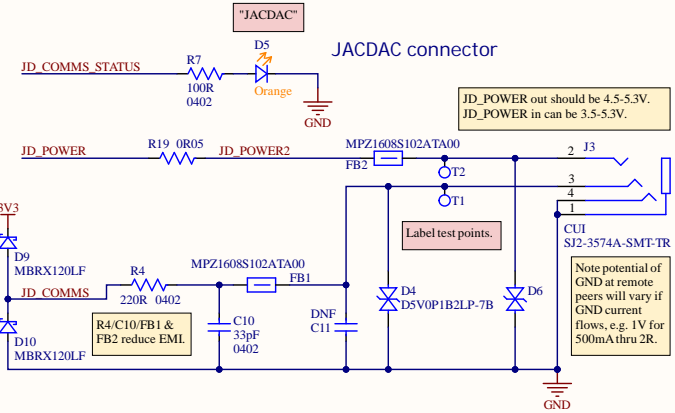
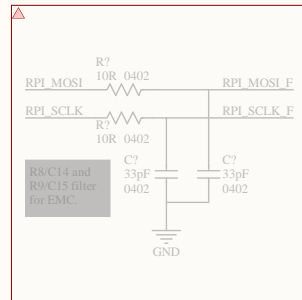
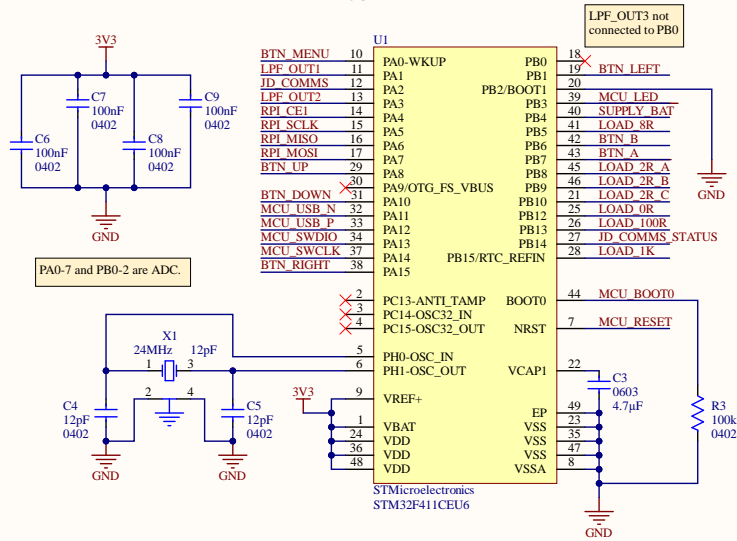
A

B

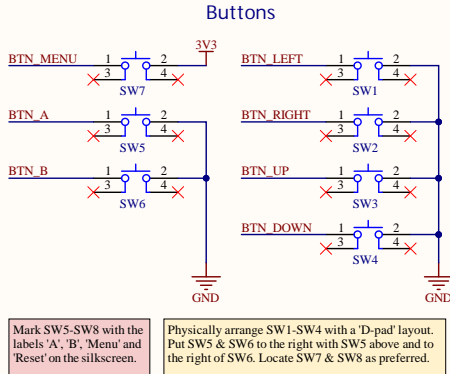
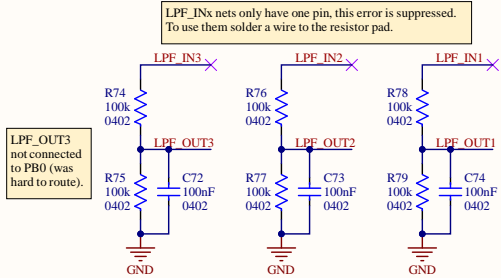
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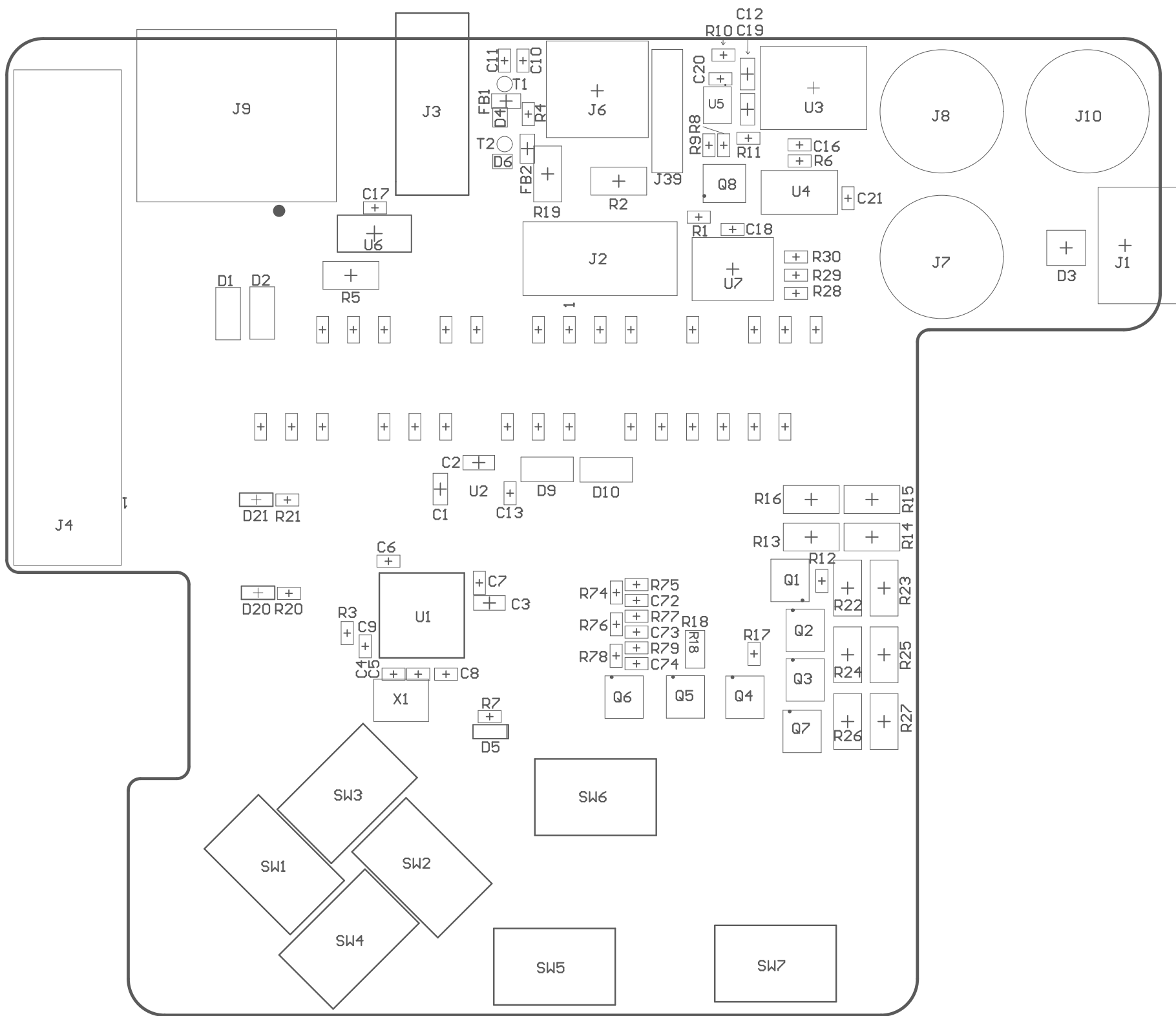
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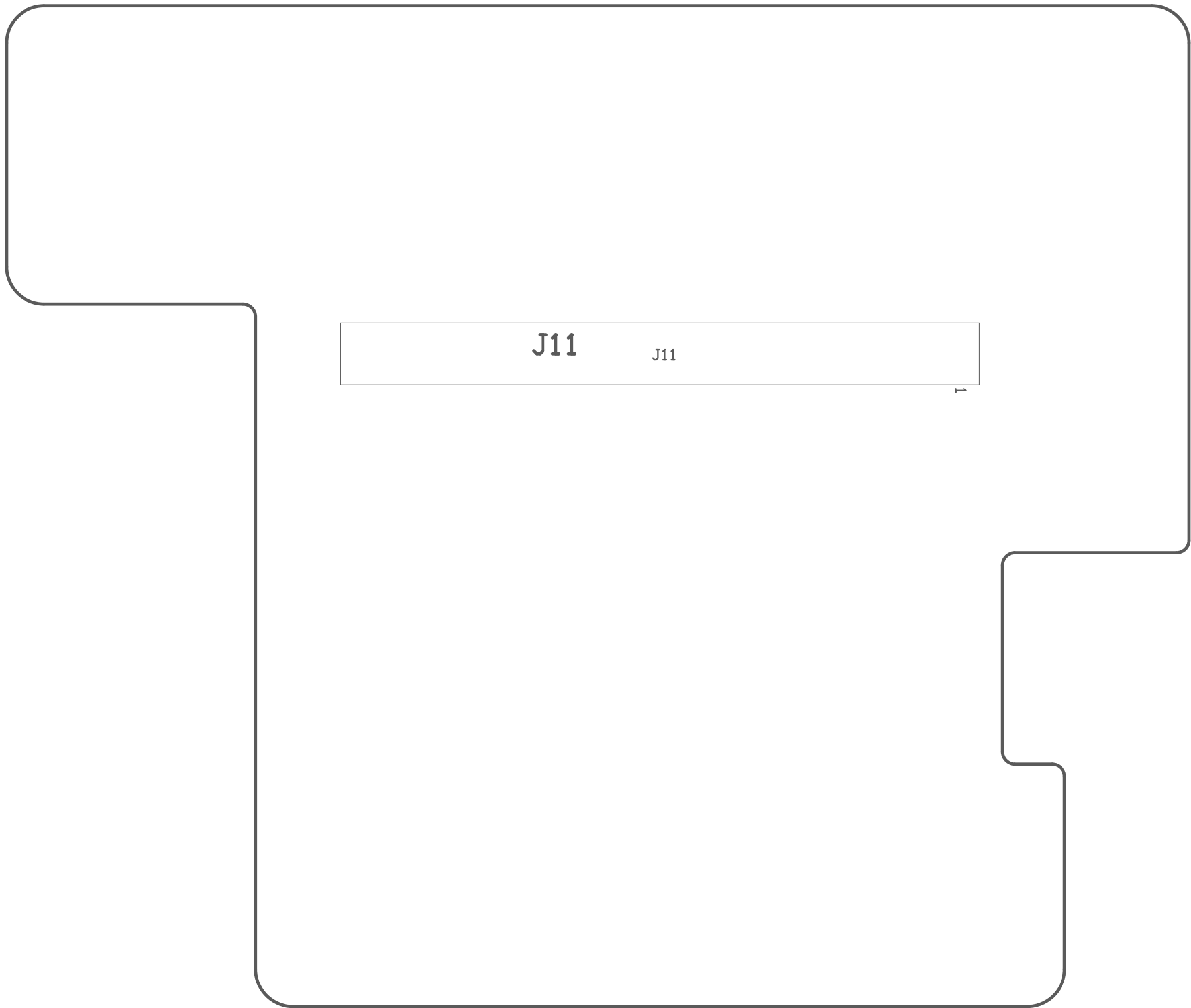
## MCU



## Low-pass filter/dividers for MCU analogue inputs







**J11**

J11

1