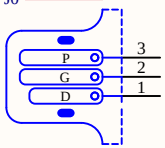
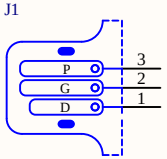
Jacdac connectors with different tongue widths

J0 Standard



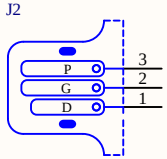
3 pin JACDAC edge connector

J1



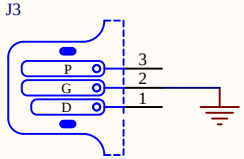
3 pin JACDAC edge connector

J2



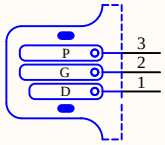
3 pin JACDAC edge connector

J3



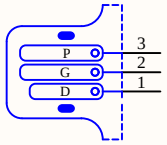
3 pin JACDAC edge connector

J4



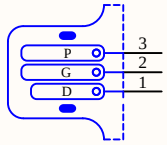
3 pin JACDAC edge connector

J5



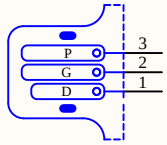
3 pin JACDAC edge connector

J6



3 pin JACDAC edge connector

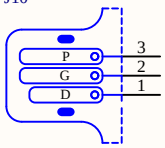
J7



3 pin JACDAC edge connector

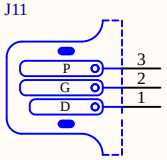
Jacdac connectors with different slot depths

J10 Standard



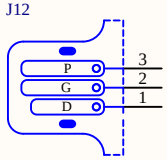
3 pin JACDAC edge connector

J11



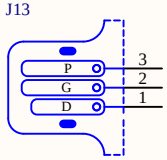
3 pin JACDAC edge connector

J12



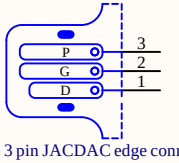
3 pin JACDAC edge connector

J13



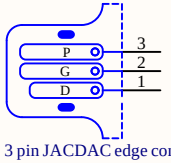
3 pin JACDAC edge connector

J14



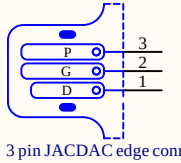
3 pin JACDAC edge connector

J15



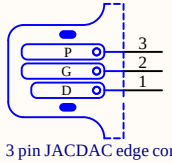
3 pin JACDAC edge connector

J16



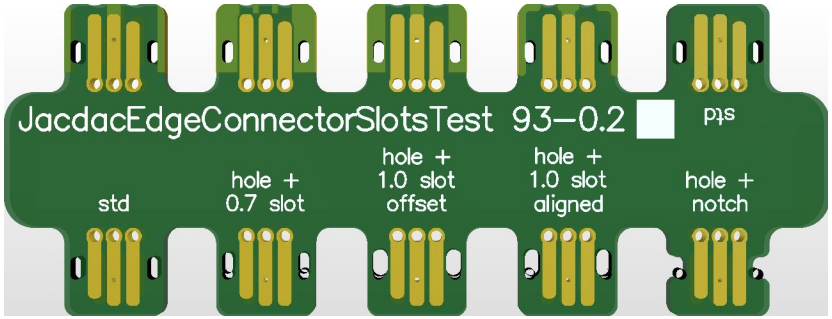
3 pin JACDAC edge connector

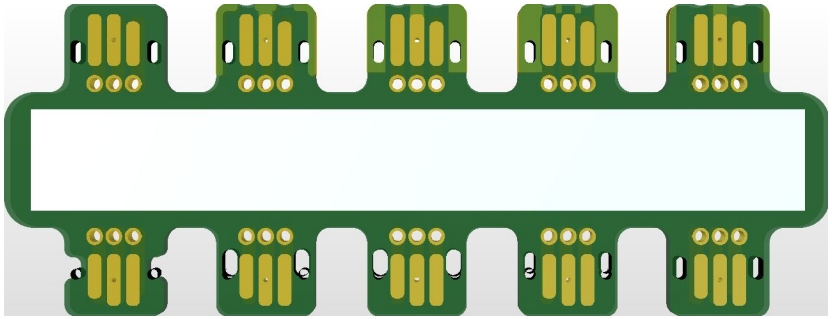
J17



3 pin JACDAC edge connector

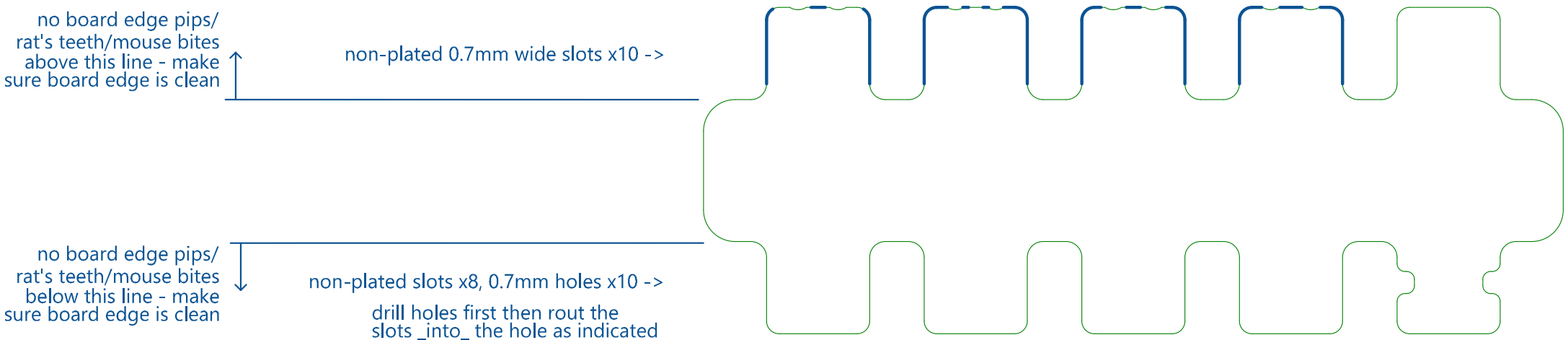
PROJECT FILENAME JacdacEdgeConnectorSlotsTest 93.PrjPCB	PROJECT CODENAME JacdacEdgeConnectorSlotsTest	Microsoft	PROJECT DESCRIPTION Jacdac PCB edge connector slot config test		SHEET DESCRIPTION Complete design
SHEET FILENAME JacdacEdgeConnectorSlotsTest 93.SchDoc	LICENCE Attribution 4.0 International (CC BY 4.0)	LAST MODIFIED 08/03/2022	PAGE 1 OF 1	DRAWN BY S. Hodges	REVISION 0.2 PCB ID 93-0.2


JacdacEdgeConnectorSlotsTest 93–0.2
std
hole +
0.7 slot
hole +
1.0 slot
offset
hole +
1.0 slot
aligned
hole +
notch
std



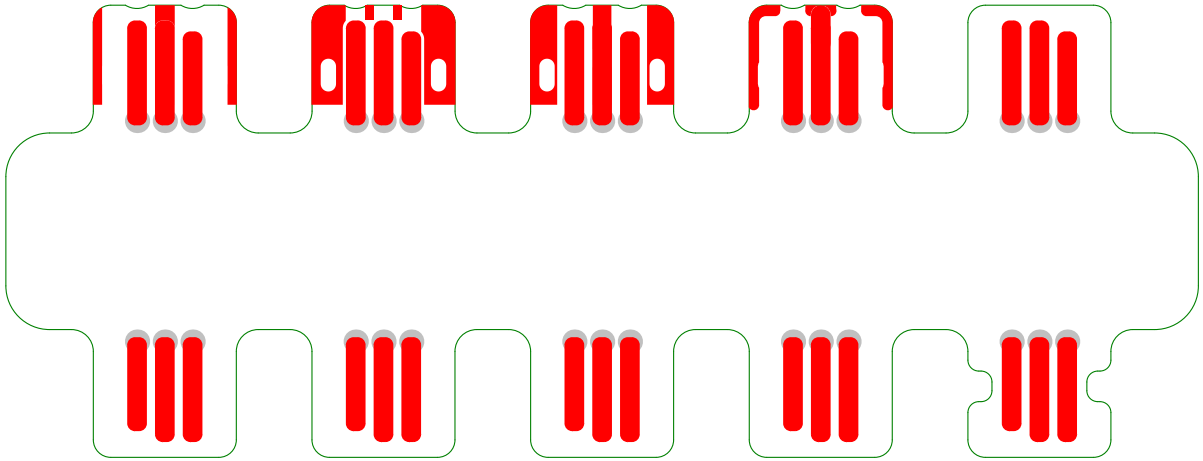
GM3 - Fabrication Notes
GM11 - Board Outline

much of this side of the PCB is edge-plated. the 0.2mm tracks/arcs below indicate where plating is required. in addition, GM10 contains a router path that defines the entire region that is plated. the board outline (GM11) follows a similar path but also defines incursions where plating is not required.



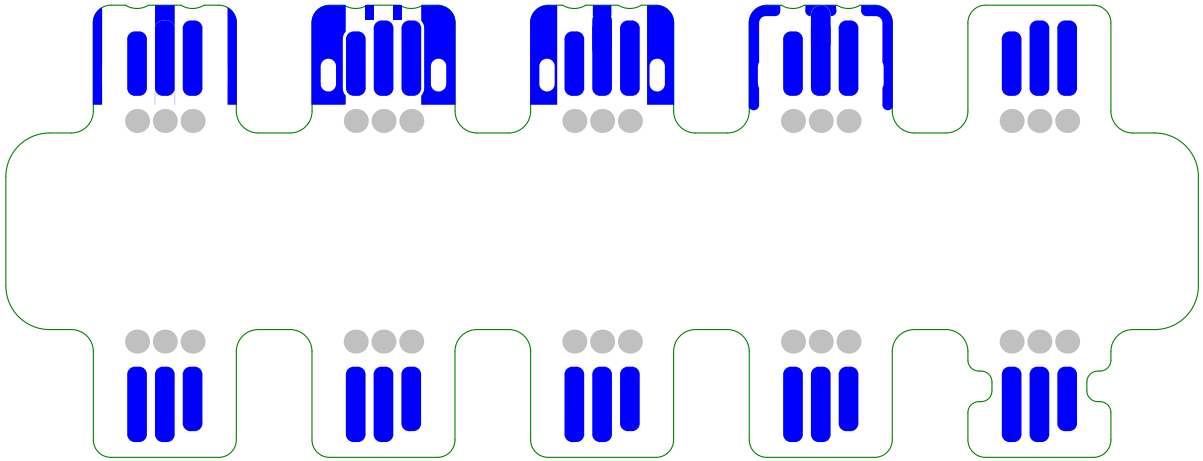
GTL - Top Layer

GM11 - Board Outline



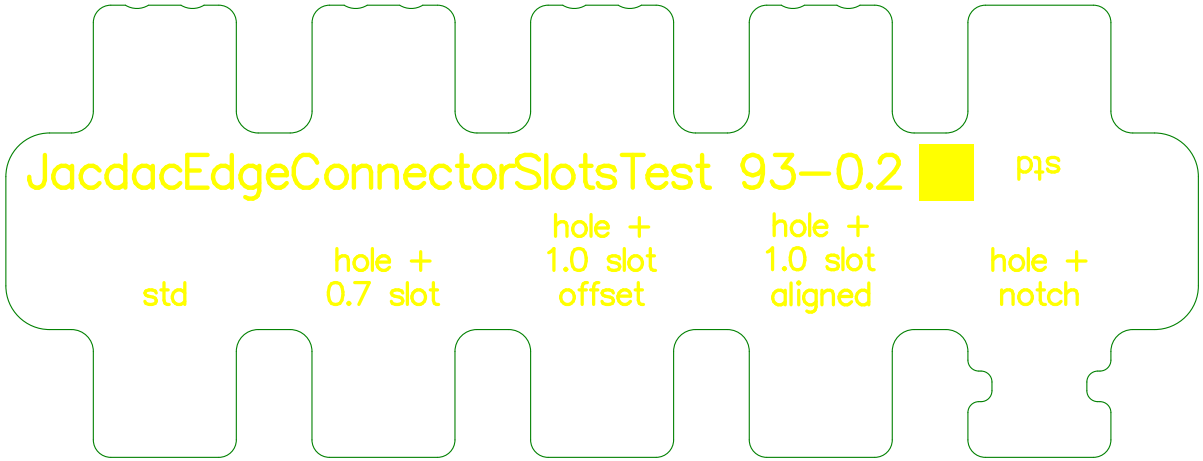
GBL - Bottom Layer

GM11 - Board Outline

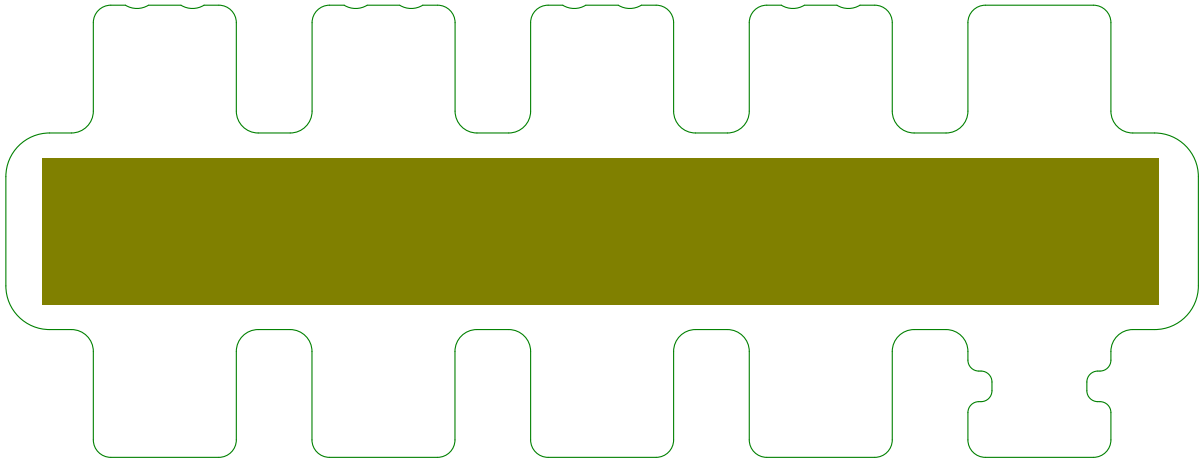


GTO - Top Overlay

GM11 - Board Outline

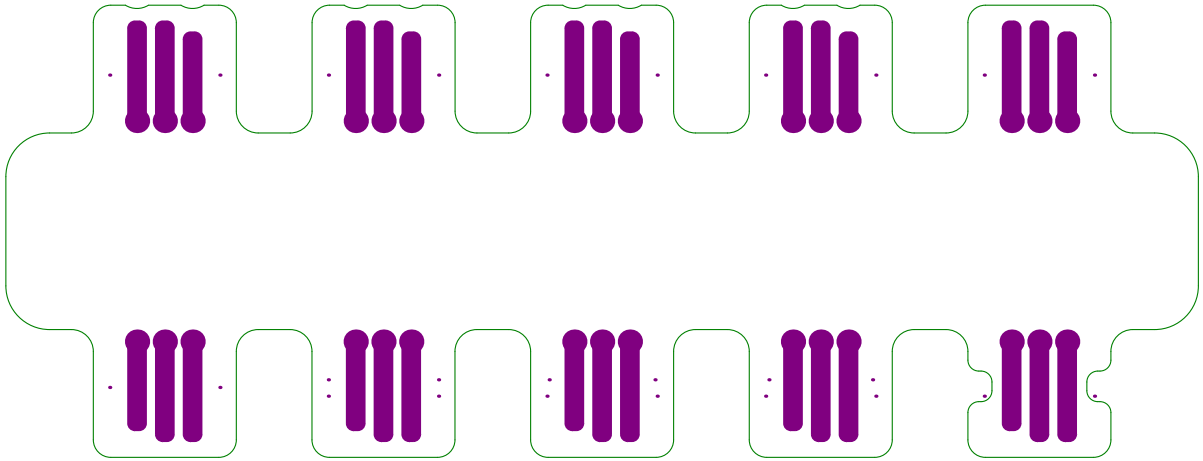


GM11 - Board Outline GBO - Bottom Overlay



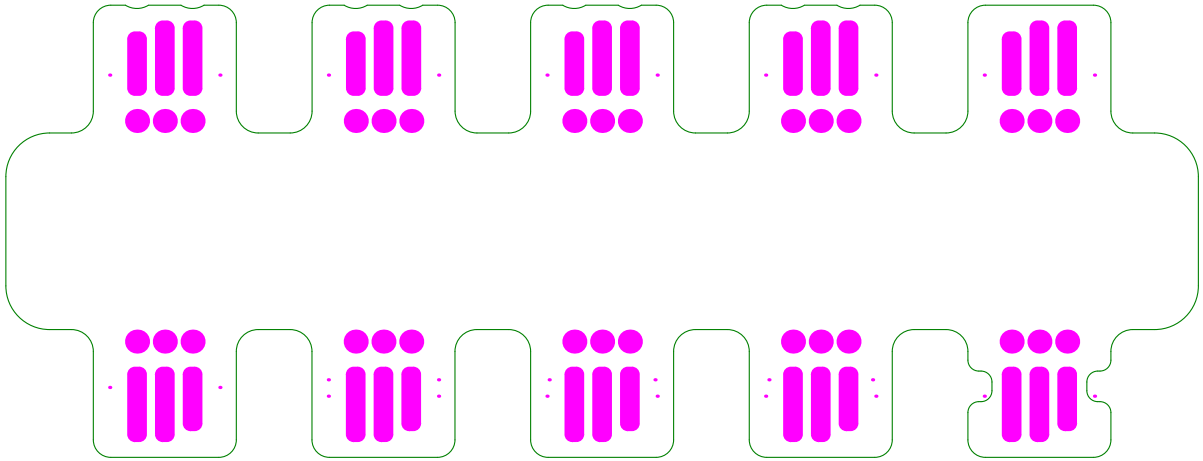
GTS - Top Solder (resist)

GM11 - Board Outline

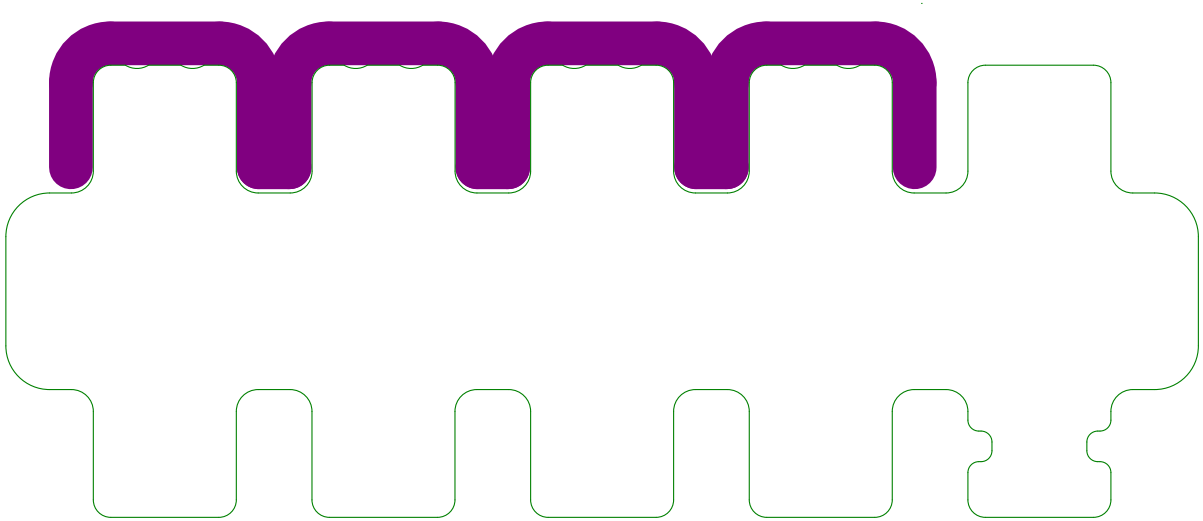


GM11 - Board Outline

GBS - Bottom Solder (resist)



GM11 - Board Outline
GM10 - Board Cutout



GM11 - Board Outline