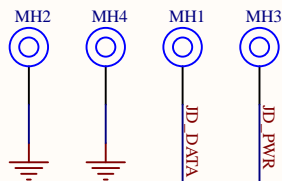


Status LEDs



Place a status LED adjacent to each edge connector. Where several edge connectors are next to each other they can share the same status LED. This design has two LEDs because connectors are at opposite ends of the PCB.

Mounting holes



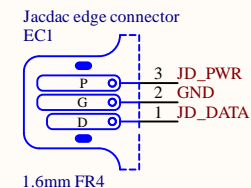
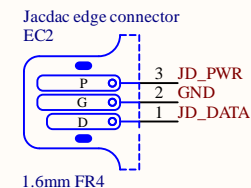
Mounting holes are electrically connected to the Jacdac bus nets so they can be used as an alternative to the PCB edge connector. Please use the following reference designators and net mapping:

MH1: JD_DATA
MH2 & MH4: GND
MH3: JD_PWR

This design uses PTH mounting holes with finished diameter of 3.1mm, annular copper ring of 4.4mm diameter, resist keepout of 5.0mm & copper/component keepout of 7.0mm. The mounting holes must be on a 10mm pitch.

Mounting holes should have appropriate silkscreen marker on both sides of PCB, and MH1 should have a pin 1 marker in copper on the top side only. There is a notch in the PCB outline adjacent to MH1.

Jacdac connectors



This reference design is a guideline. Please refer to the Jacdac docs online at <https://aka.ms/jacdac> for the definitive and most up-to-date information.

This design uses the 'EC30' board shape.

Silkscreen front should include manufacturer and module name. Silkscreen should also identify the version number and optionally a QR code.

Silkscreen & layout notes

Block name

Design notes

When this PDF is viewed with Adobe Reader, clicking on components shows part numbers and other details.

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Microsoft

PROJECT DESCRIPTION
Jacdac "EC30" template: 20x20mm MH, 1+1 conns

SHEET DESCRIPTION
Complete design

PROJECT FILENAME JacdacEc30Template20x20.PrjPCB

PROJECT CODENAME JacdacEc30Template20x20

SHEET FILENAME JacdacEc30Template20x20.SchDoc

LICENCE Attribution 4.0 International (CC BY 4.0)

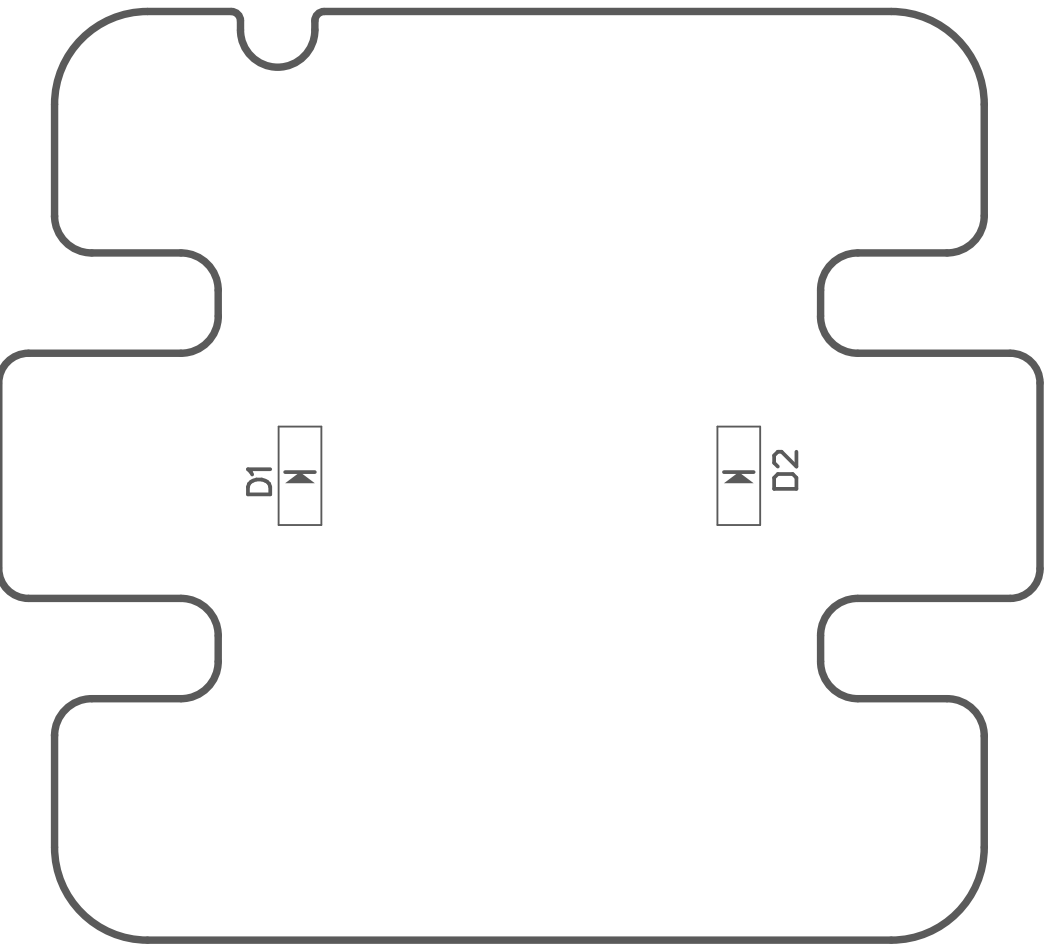
LAST MODIFIED 13/06/2022

PAGE 1 OF 1

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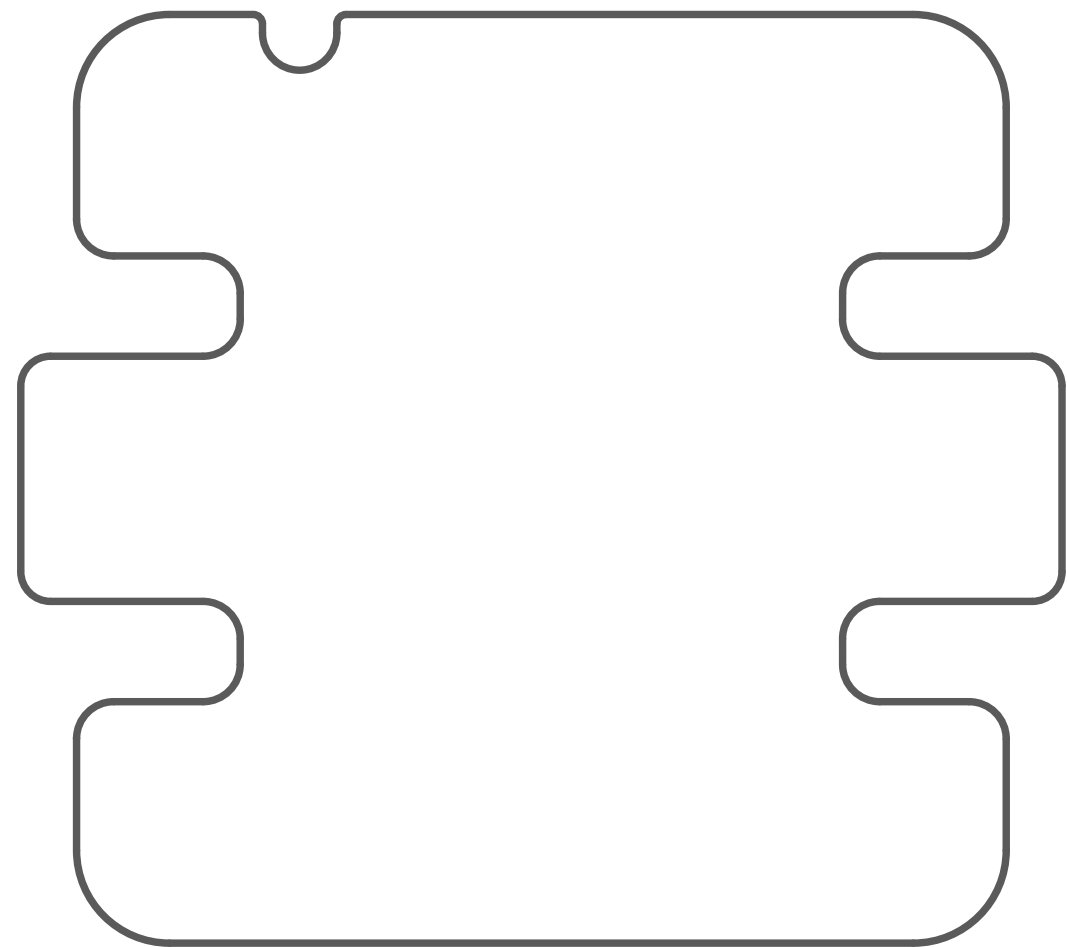
REVISION 1.0

PCB ID 80-1.0



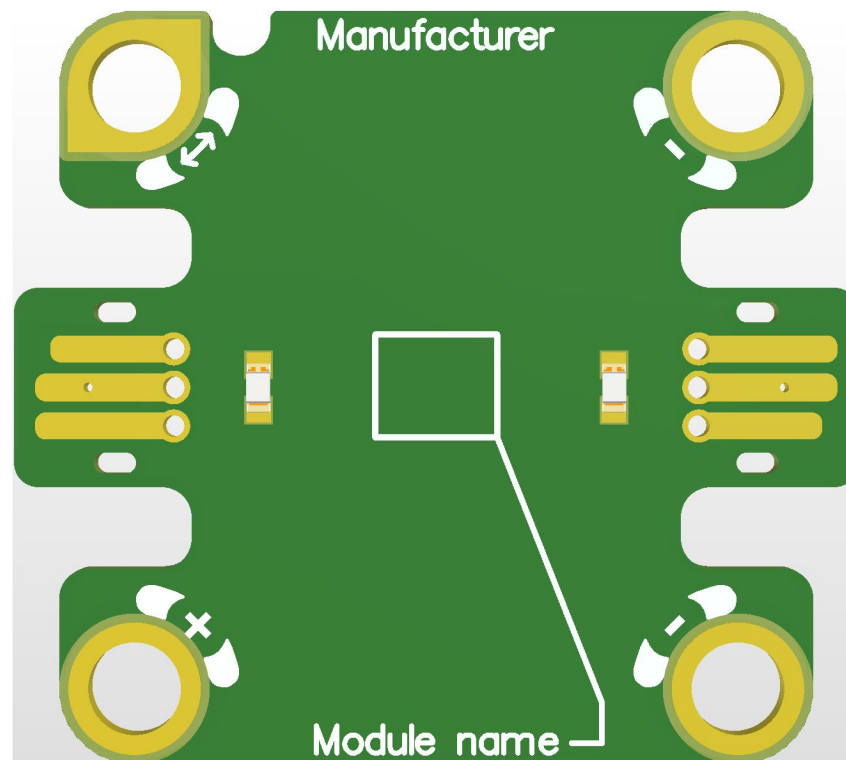
Top Assy

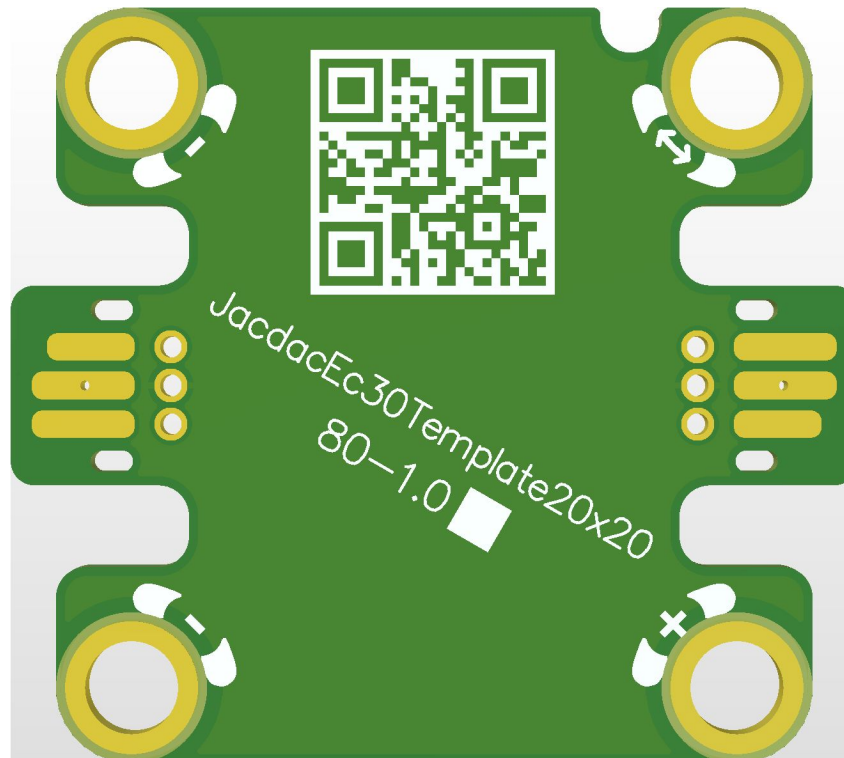
Board Outline

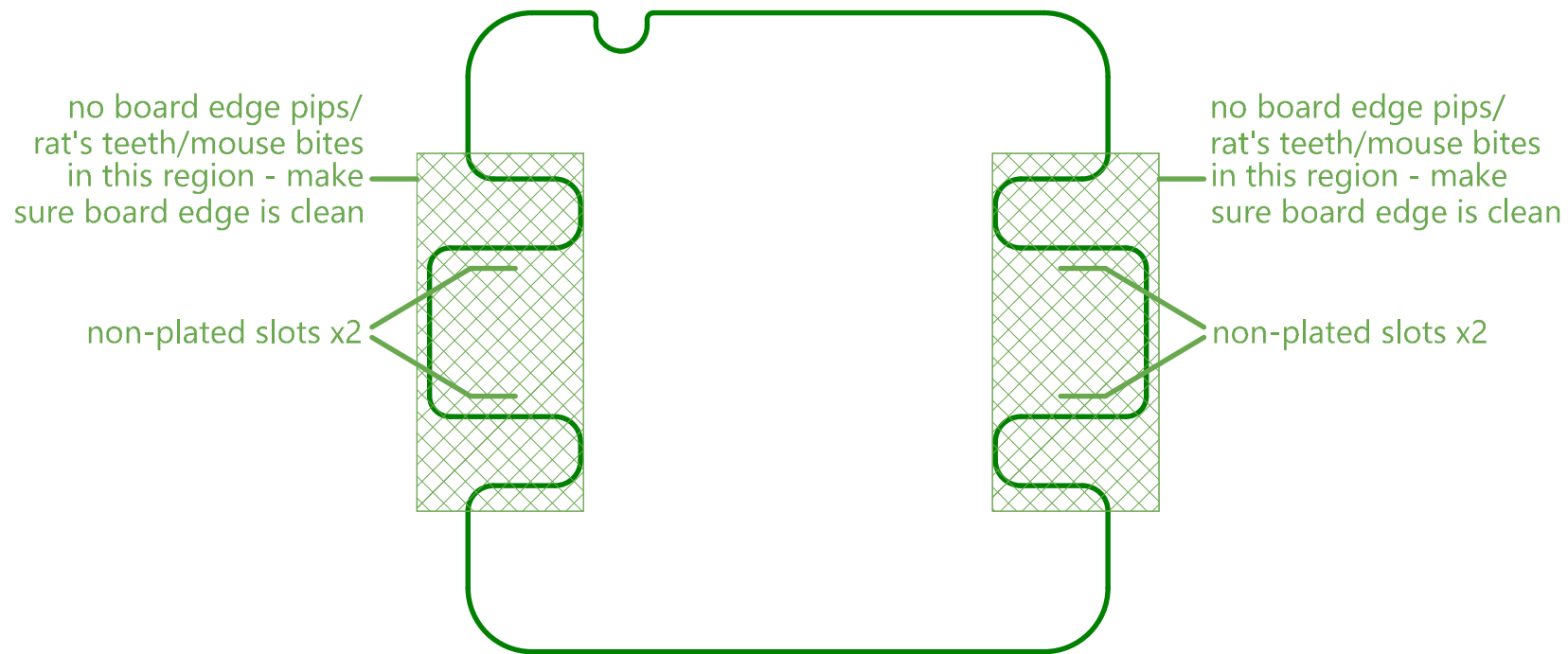


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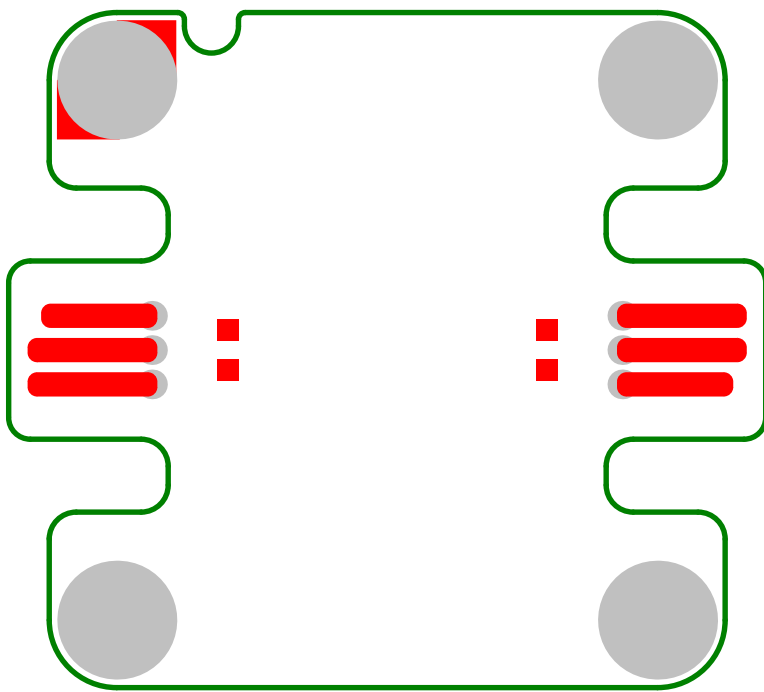
Board Outline





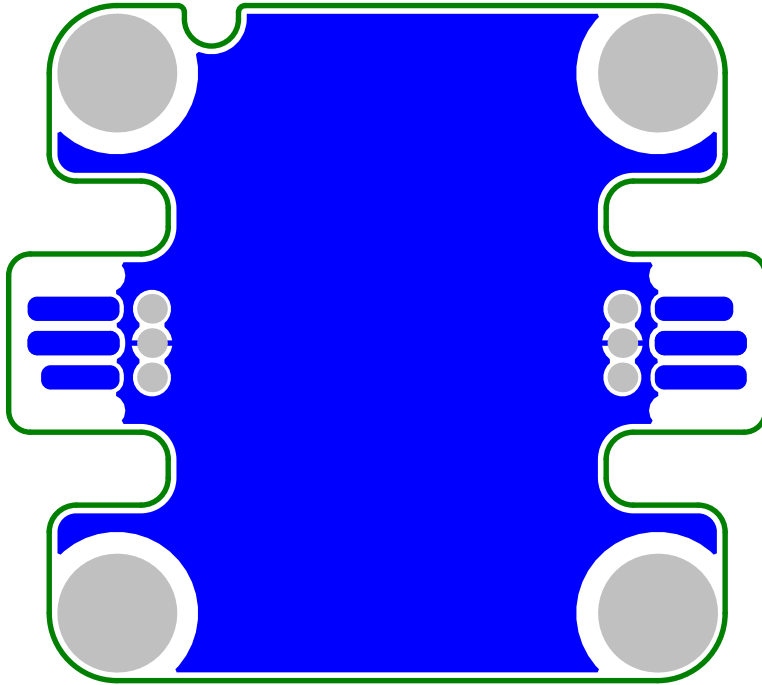


Fabrication Notes
Board Outline



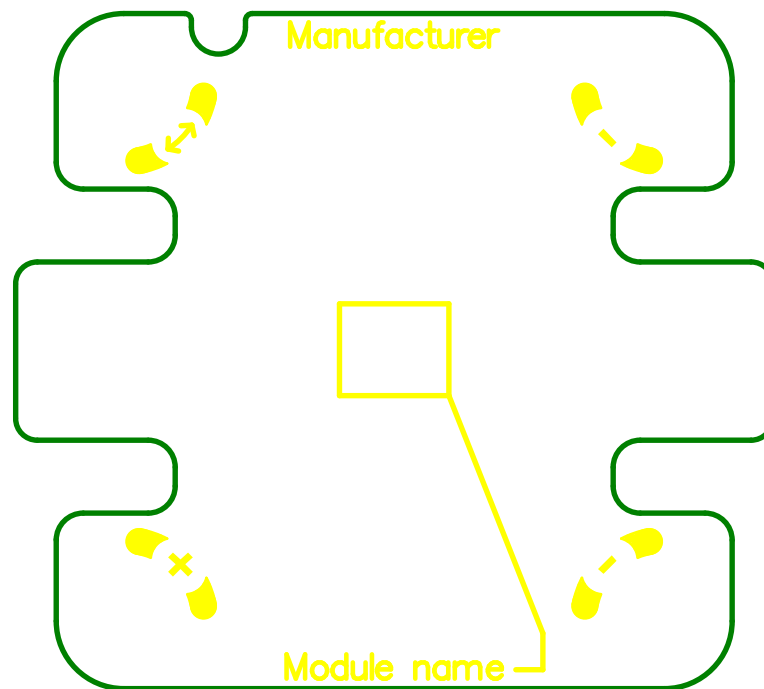
Top Layer

Board Outline

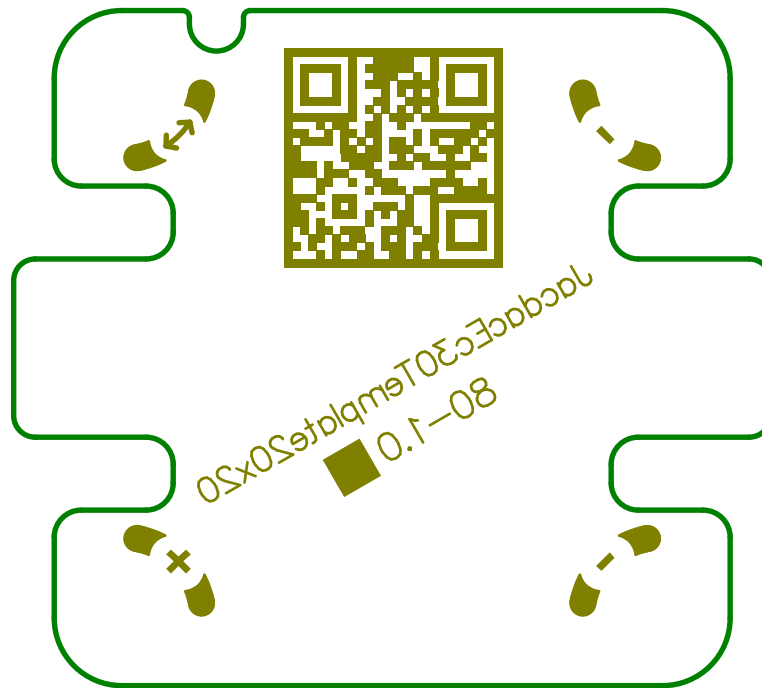


Bottom Layer

Board Outline

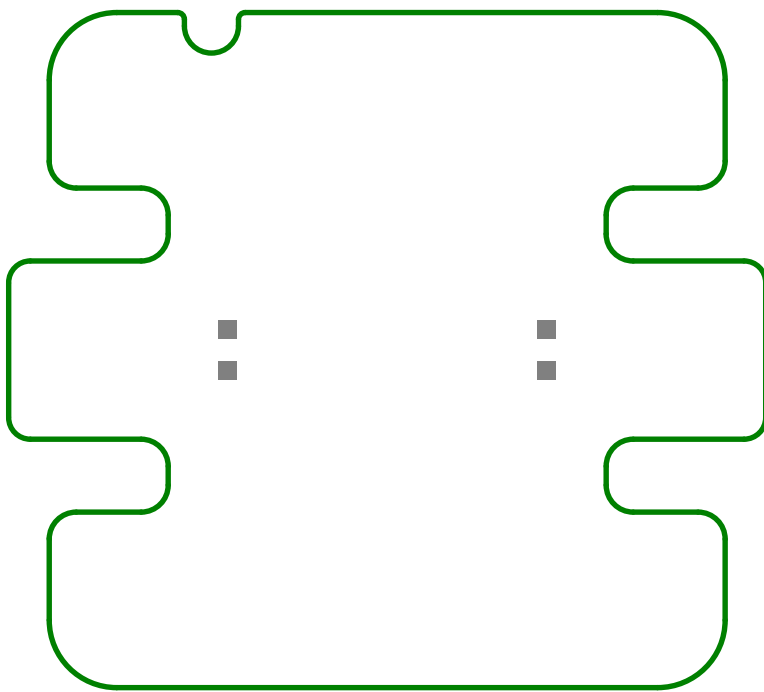


Board Outline



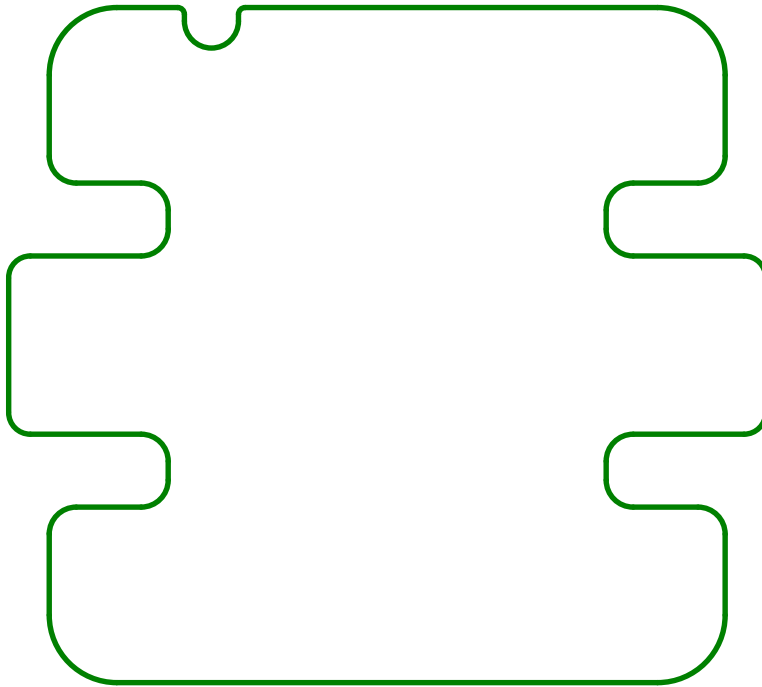
Bottom Overlay

Board Outline



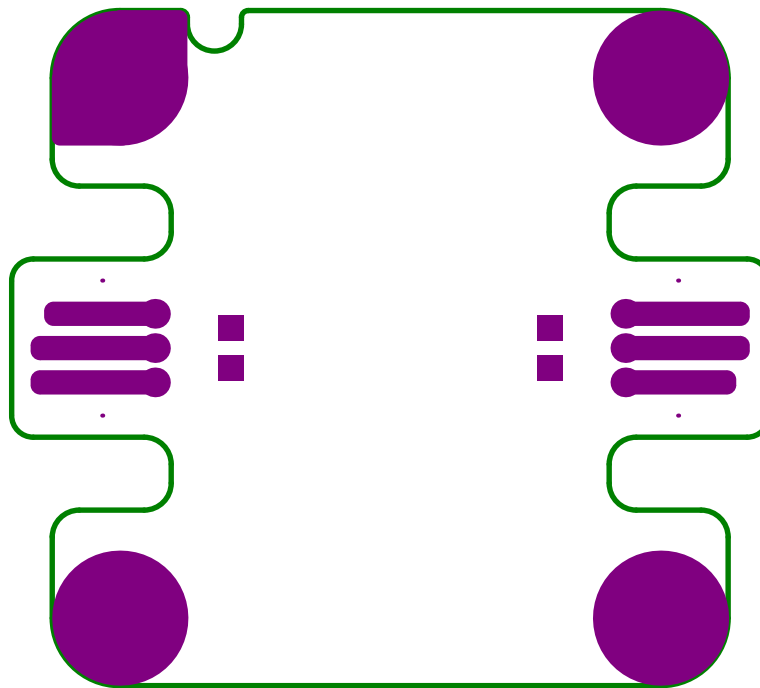
Top Paste

Board Outline



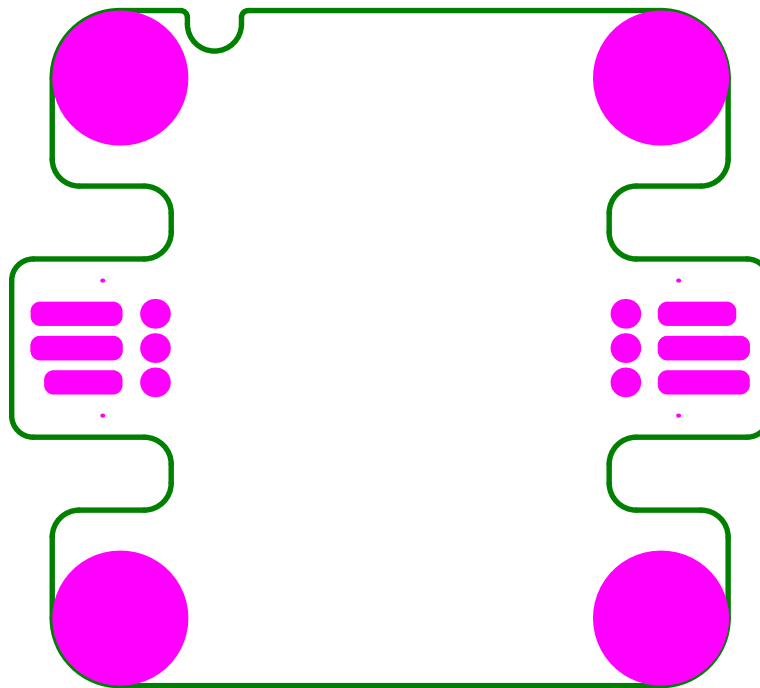
Bottom Paste

Board Outline



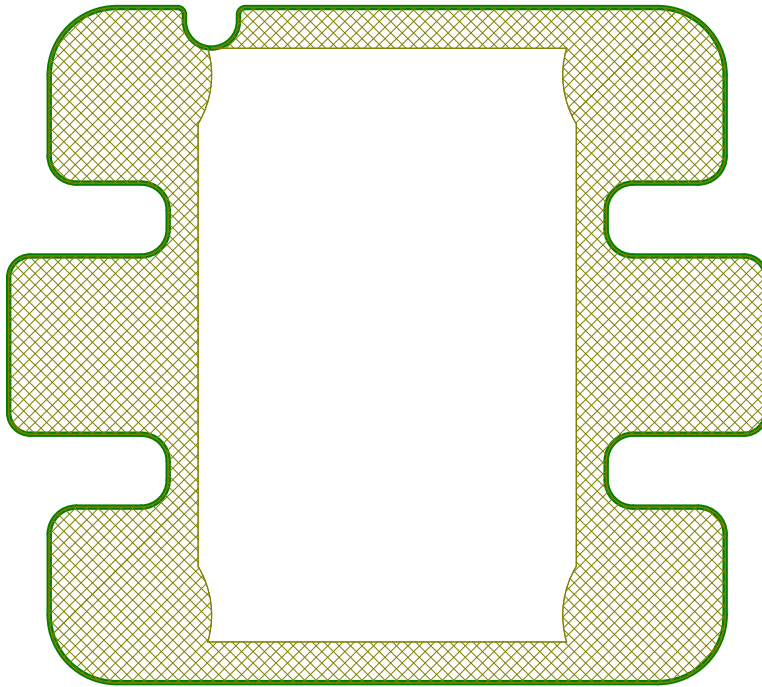
Top Solder (resist)

Board Outline



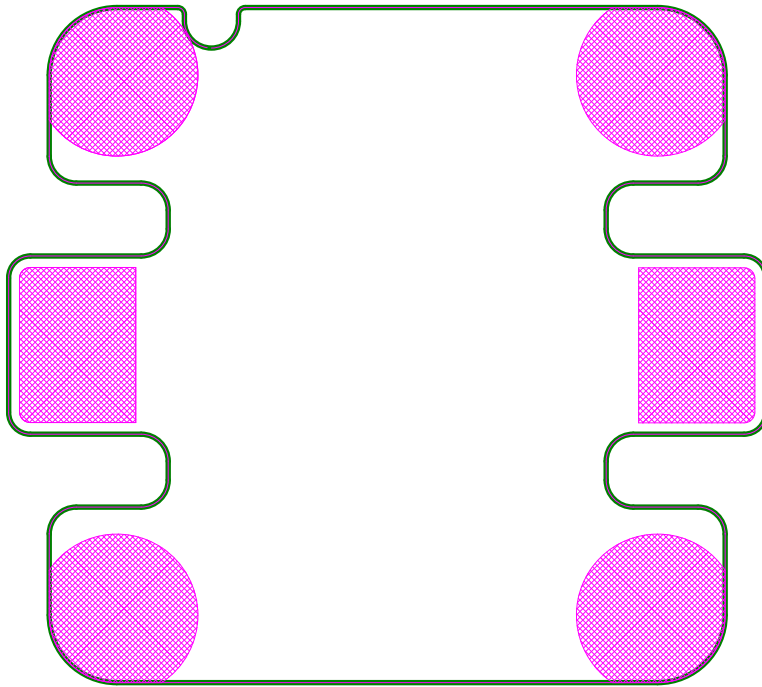
Board Outline

Bottom Solder (resist)



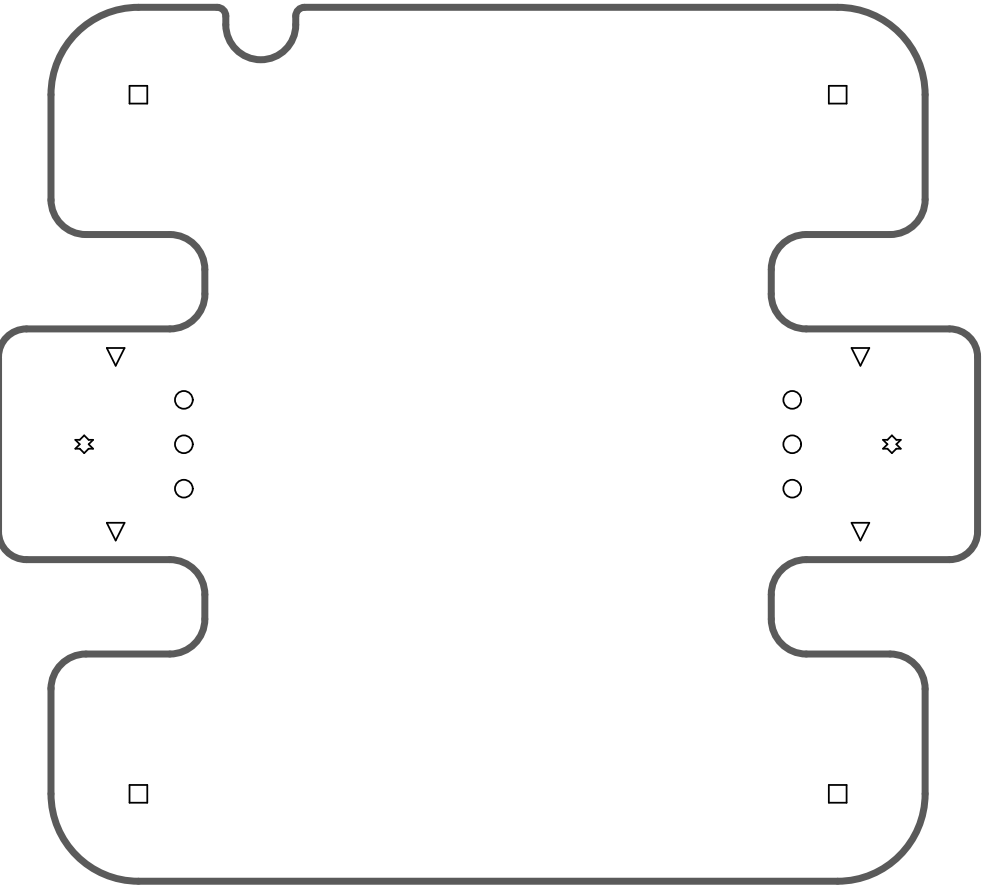
Board Outline

Component Keepout



Board Outline

Routing Keepout



Board Outline

Drill Drawing