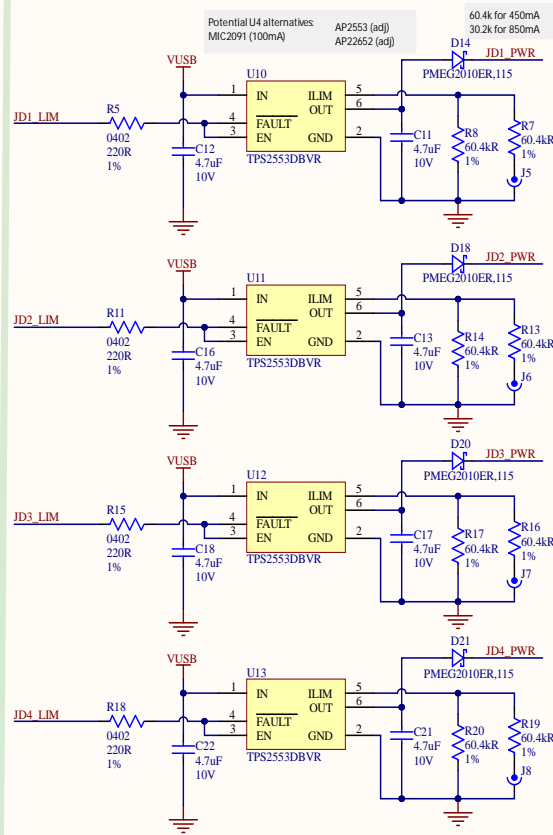
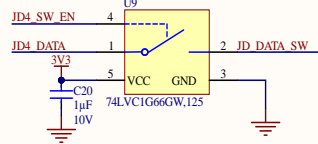
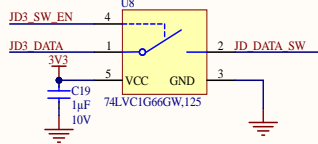
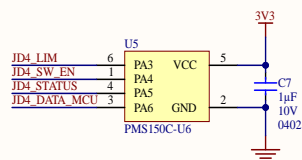
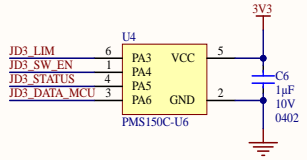
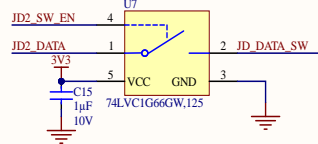
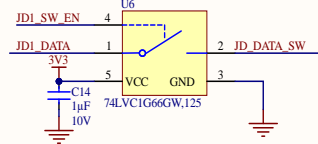
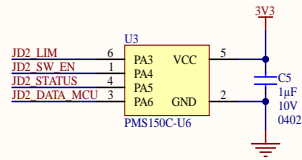
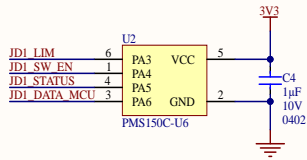
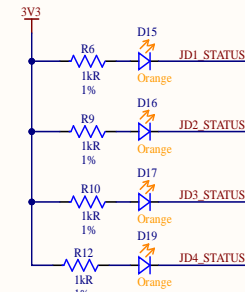


## MCUs & JD SWITCHES

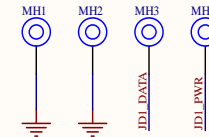
U2, U3, U4 & U5 must be programmed before being soldered down due to restrictive in-circuit programming requirements. JD\_STATUS is PWM-capable GPIO.



## Status LEDs.

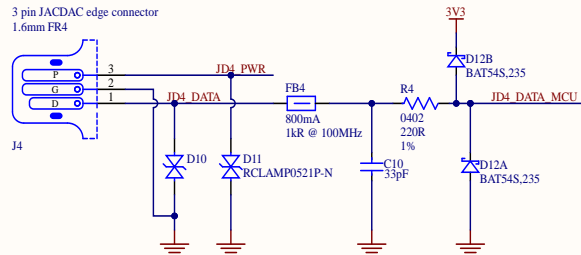
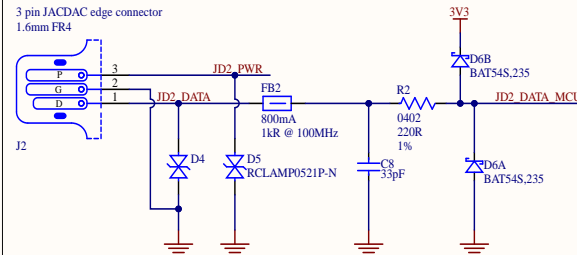
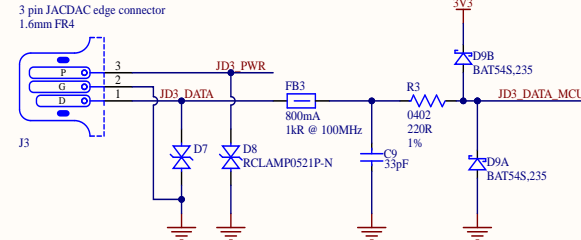
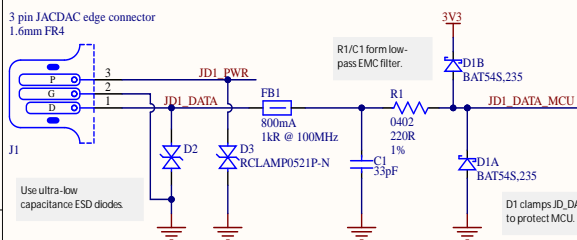


## Mounting holes.

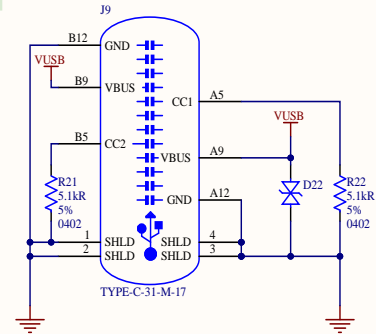


JACDAC mounting holes are grounded plated through hole, finished diameter of 2.2mm and annular ring of 3.2mm diameter.

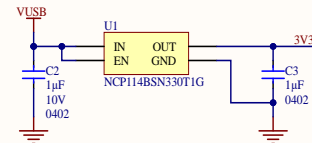
## Jacdac connectors



## Power supply.



## 3V3 Regulator



U1 Alternative part NCP114ASN330T1G

Key: Silkscreen & layout notes  
Block name  
Design notes

## Microsoft

PROJECT DESCRIPTION  
Jacdac smart power provider

SHEET DESCRIPTION  
Complete design