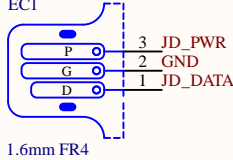
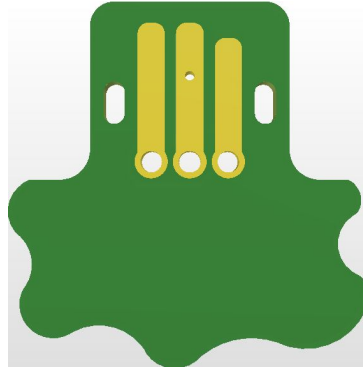
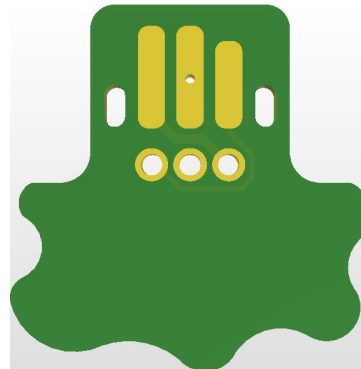
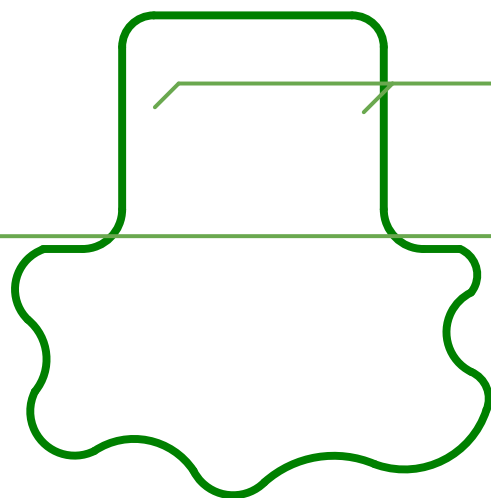


1	2	3	4
A			A
B			B
C			C
D			D
<div><div>Jacdac connector</div><div><div>Jacdac edge connector EC1</div><div></div></div><div><p>This reference design is a guideline. Please refer to the Jacdac docs online at https://aka.ms/jacdac for the definitive and most up-to-date information.</p></div><div><div>Silkscreen & layout notes</div><div>Block name</div><div>Design notes</div><div>When this PDF is viewed with Adobe Reader, clicking on components shows part numbers and other details.</div></div><div>This information is provided "as-is". You bear the risk of using it. Some information relates to pre-released specification which may change without notice. Microsoft makes no warranties, express or implied, with respect to the information provided here.</div></div>			
PROJECT FILENAME	JacdacEdgeConnectorRef 101.PrjPCB	PROJECT CODENAME	JacdacEdgeConnectorRef
SHEET FILENAME	JacdacEdgeConnectorRef 101.SchDoc	LICENCE	Attribution 4.0 International (CC BY 4.0)
Microsoft		PROJECT DESCRIPTION Jacdac PCB edge connector specification	
LAST MODIFIED		14/06/2022	PAGE 1 OF 1
DRAWN BY		S. Hodges	REVISION 1.1
PCB ID		101-1.1	
1	2	3	4



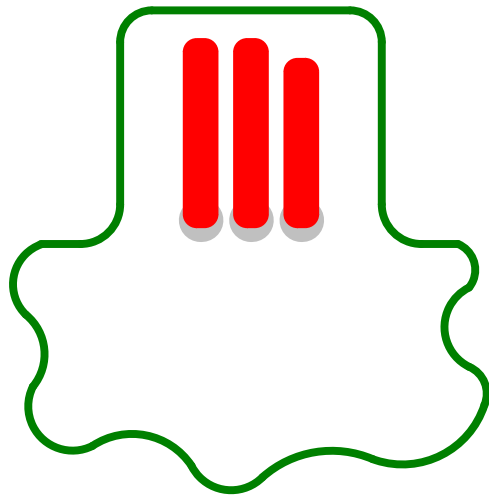




non-plated slots x2

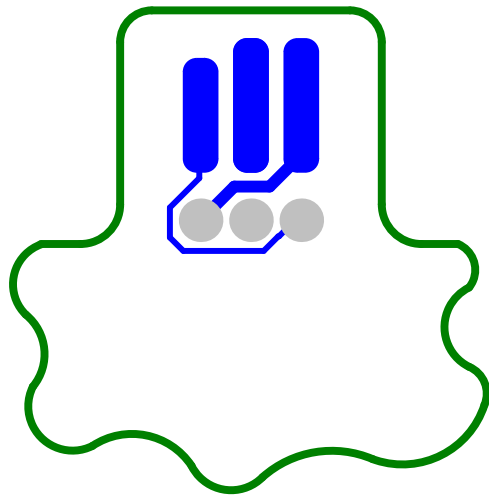
no board edge pips/
rat's teeth/mouse bites
above this line - make
sure board edge is clean

Fabrication Notes
Board Outline



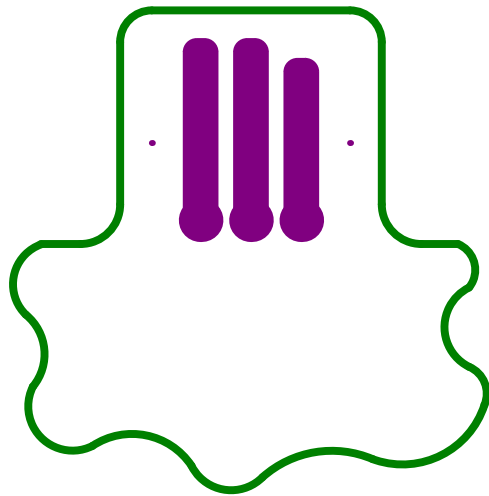
Top Layer

Board Outline



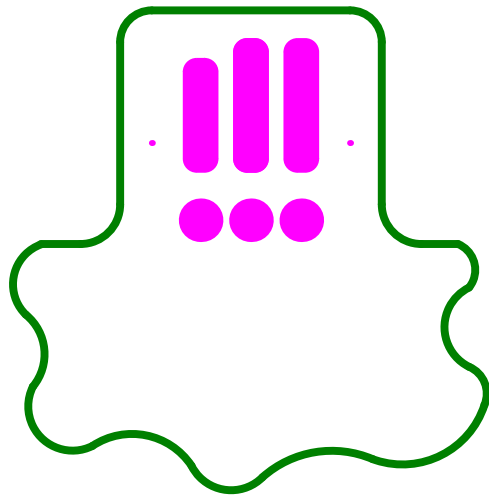
Bottom Layer

Board Outline



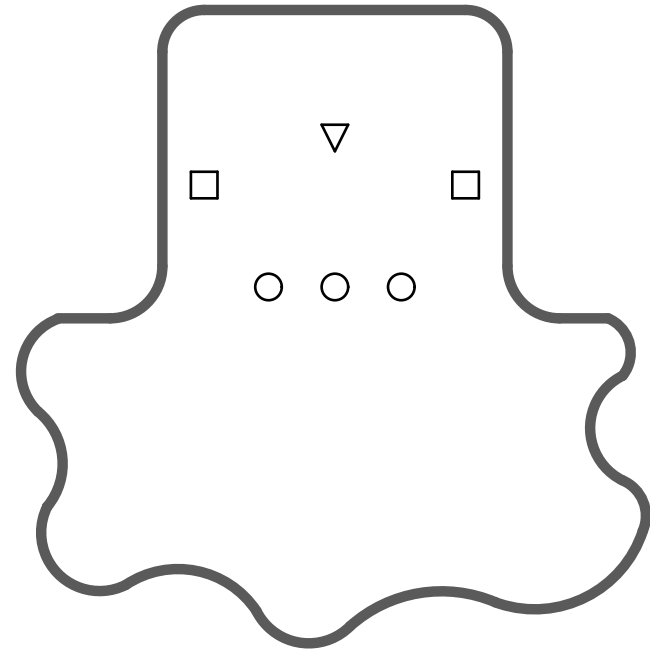
Board Outline

Top Solder (resist)



Board Outline

Bottom Solder (resist)



Board Outline

Drill Drawing