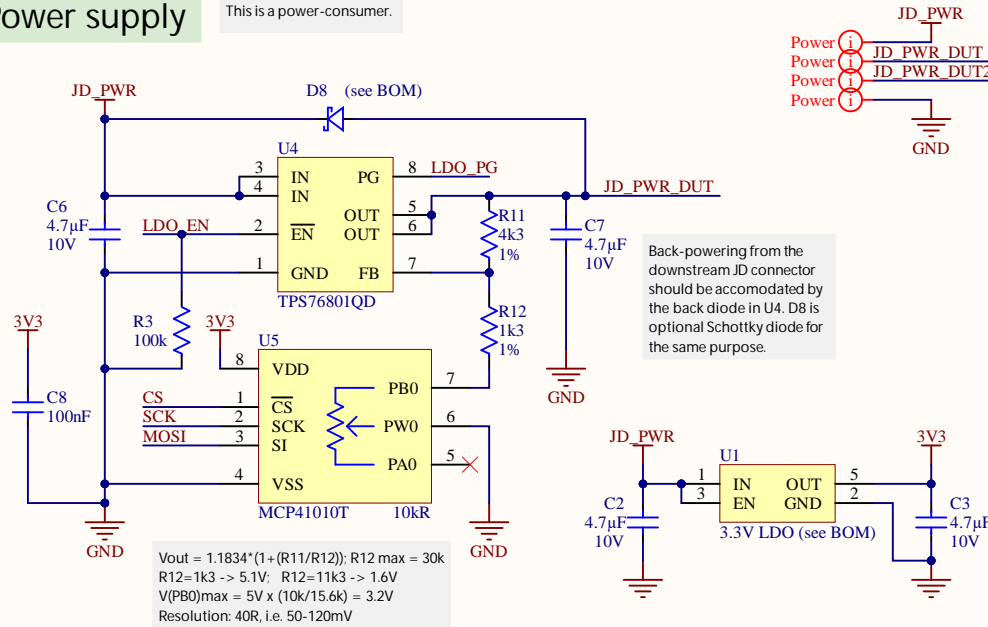
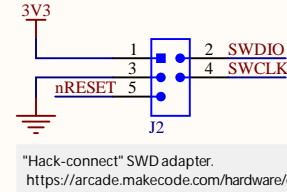


## Power supply

This is a power-consumer.

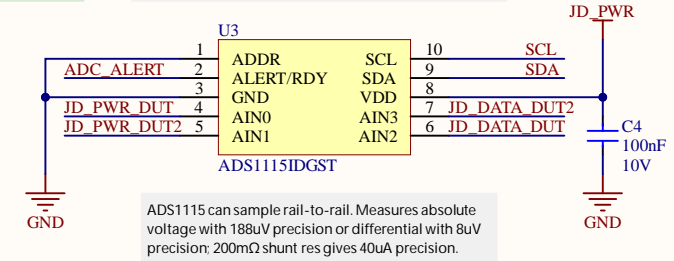


## Program/debug

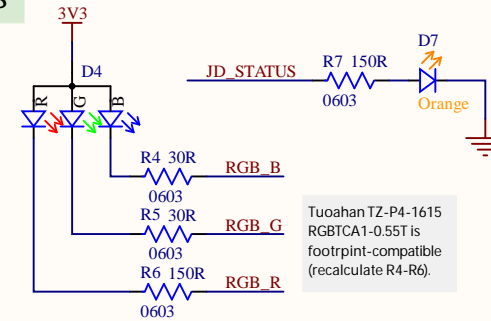


## ADC

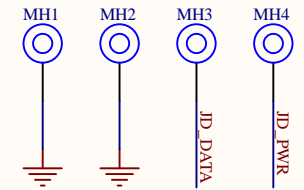
I2C address:  
 GND 1001000; VDD 1001001; SDA 1001010; SCL 1001011



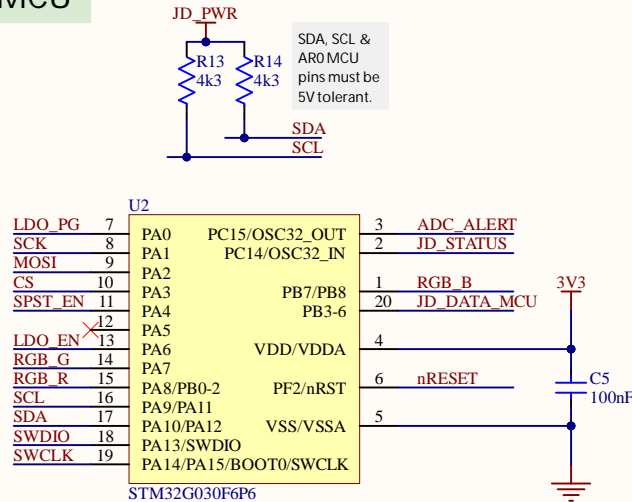
## LEDs



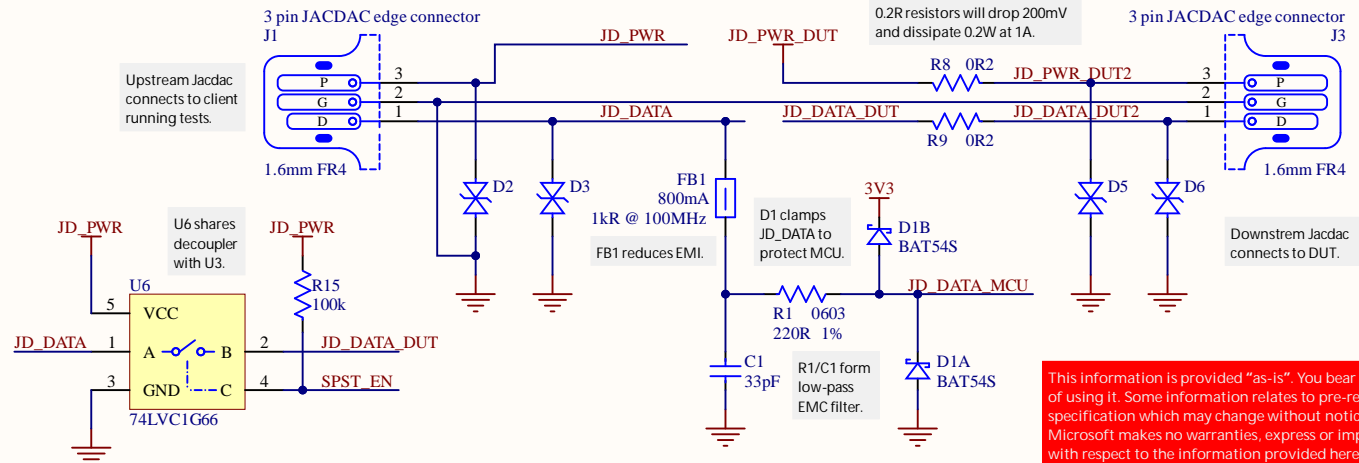
## Mounting holes



## MCU



## Jacdac connector



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## Microsoft

PROJECT DESCRIPTION  
 Jacdac testing module

SHEET DESCRIPTION  
 Complete design

PROJECT FILENAME JacdacModuleTesterEc30 91.PrjPCB

PROJECT CODENAME JacdacModuleTesterEc30

LAST MODIFIED 13/03/2022

PAGE 1 OF 1

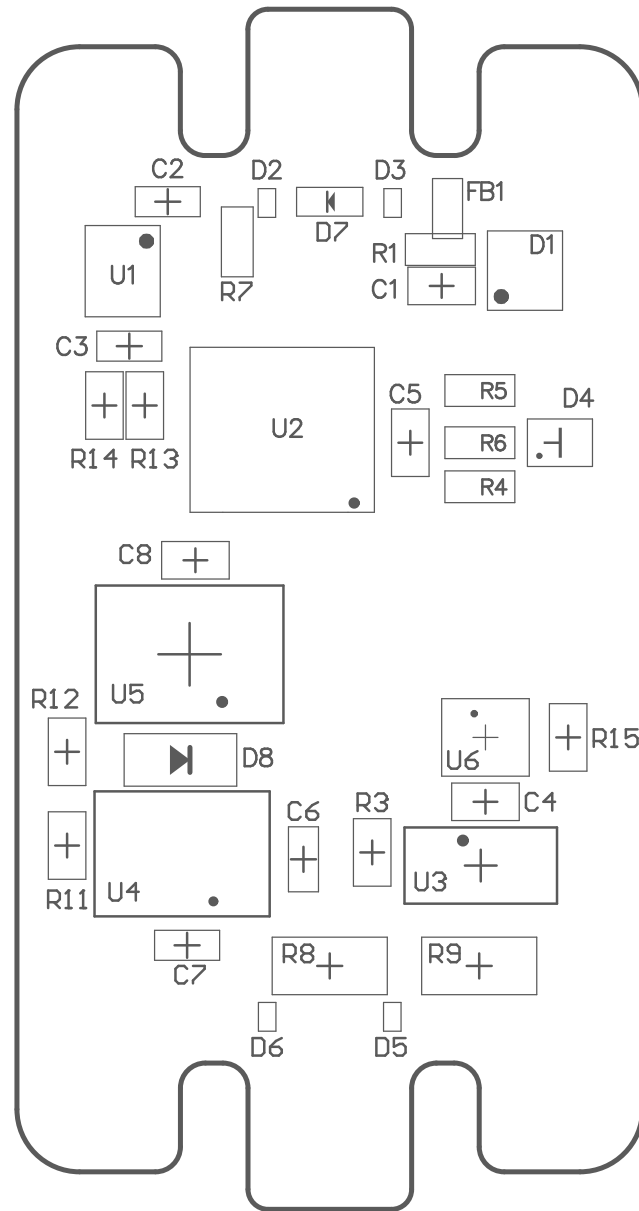
DRAWN BY D. Gakure & S. Hodges

REVISION 0.2

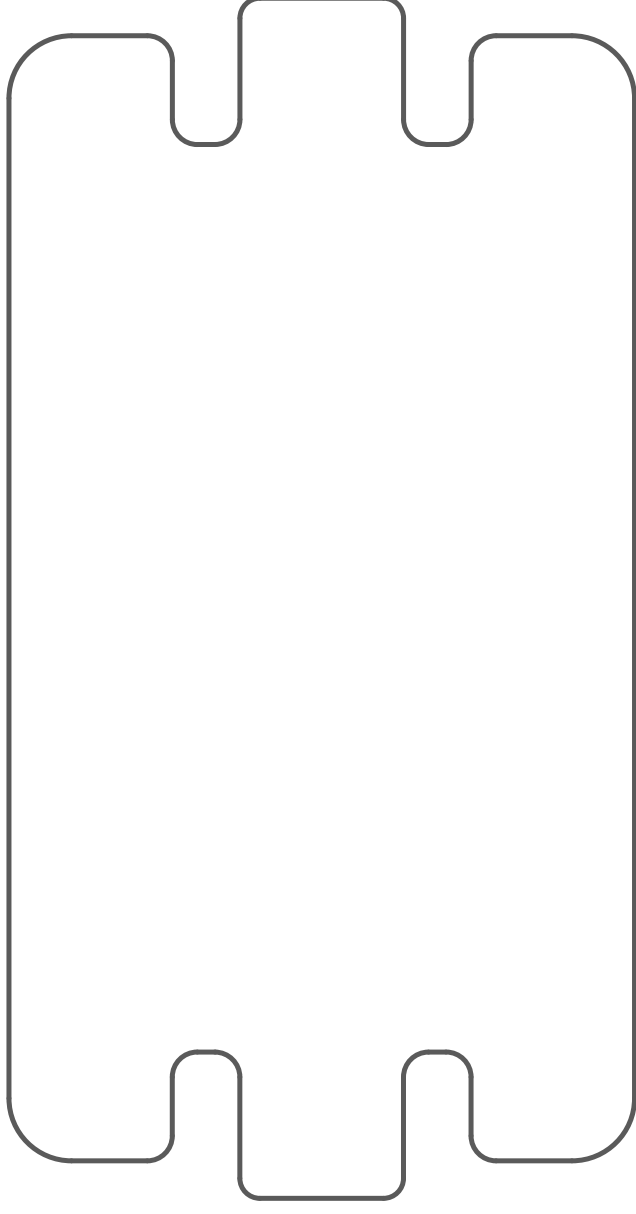
PCB ID 91-02

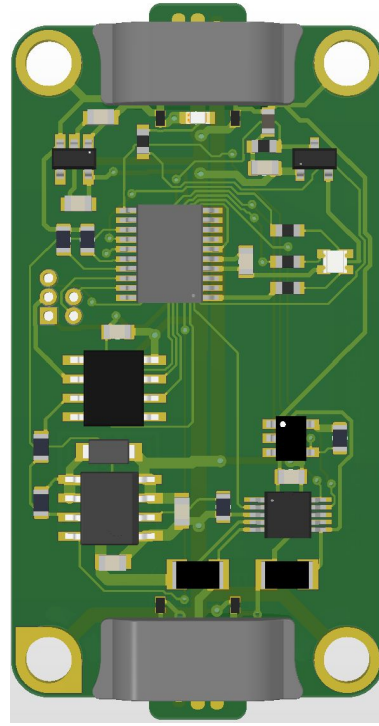
SHEET FILENAME JacdacModuleTesterEc30 91.SchDoc

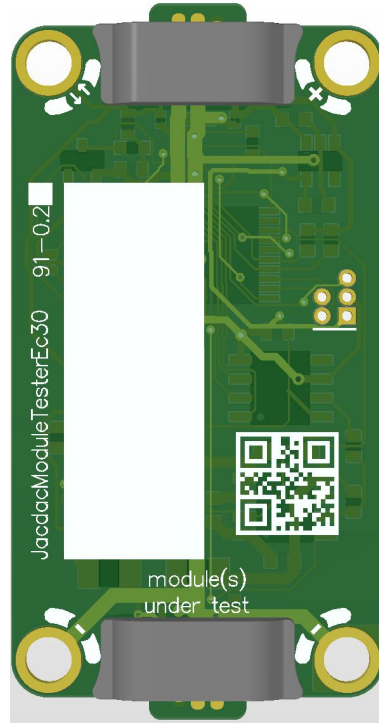
LICENCE Attribution 4.0 International (CC BY 4.0)



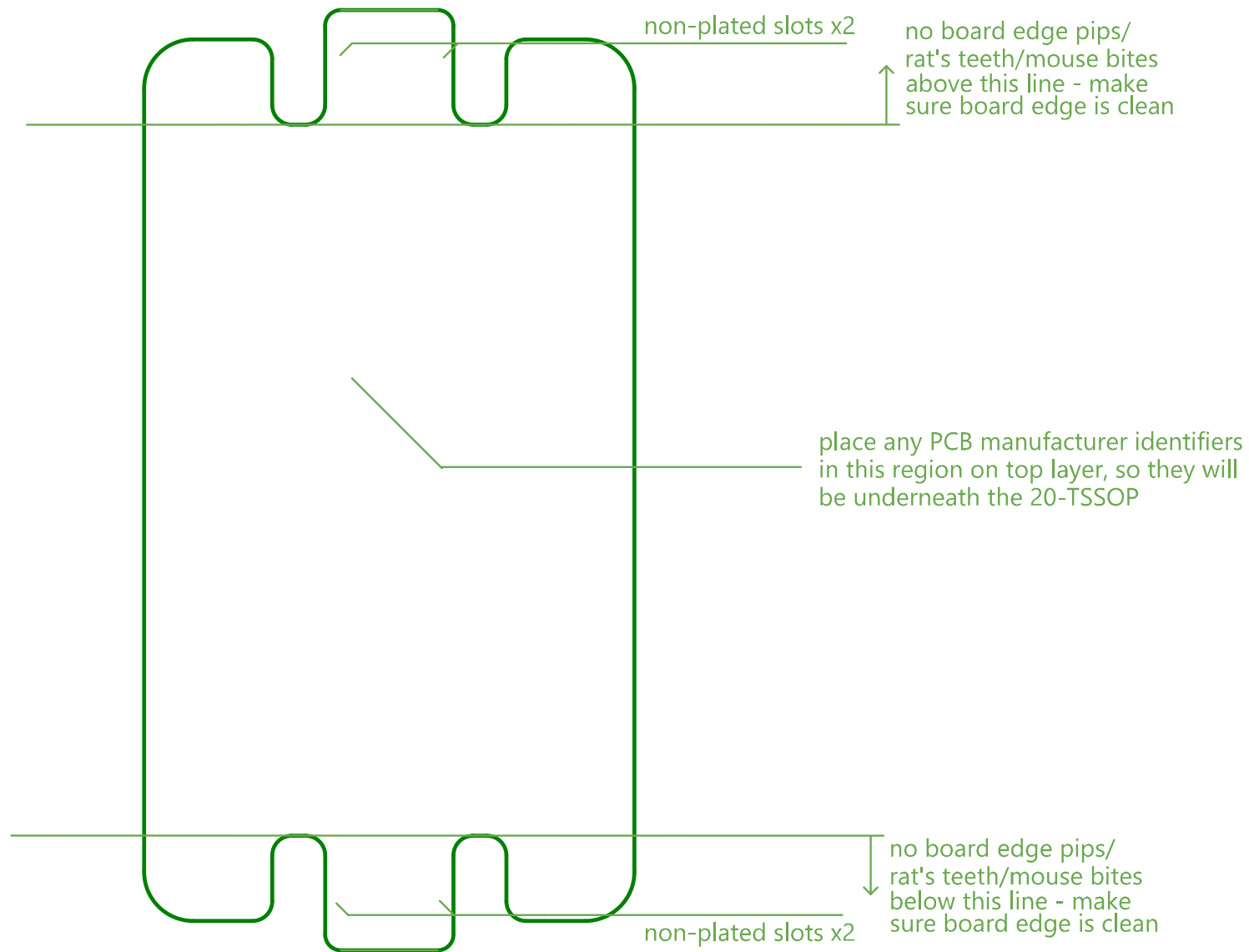
Board Outline  
Bottom Assy





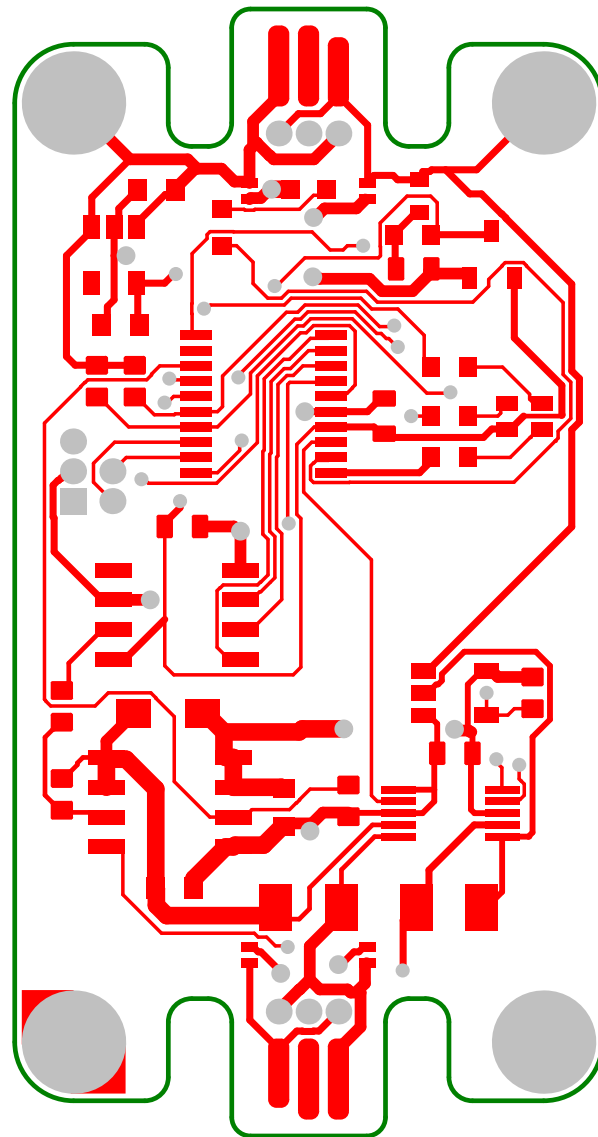


Fabrication Notes  
Board Outline



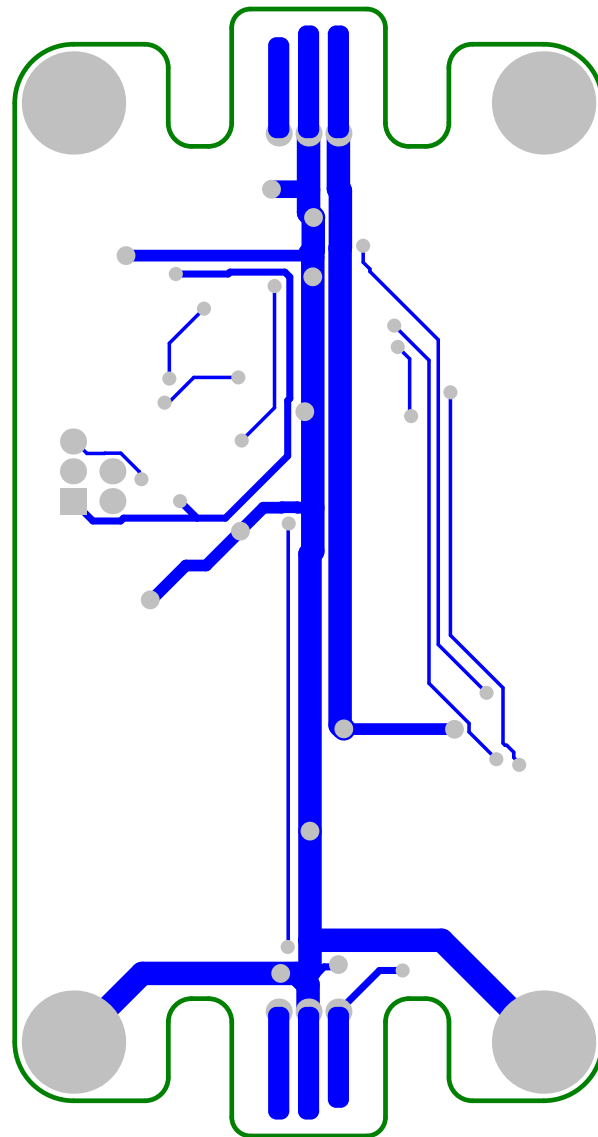
Top Layer

Board Outline



Bottom Layer

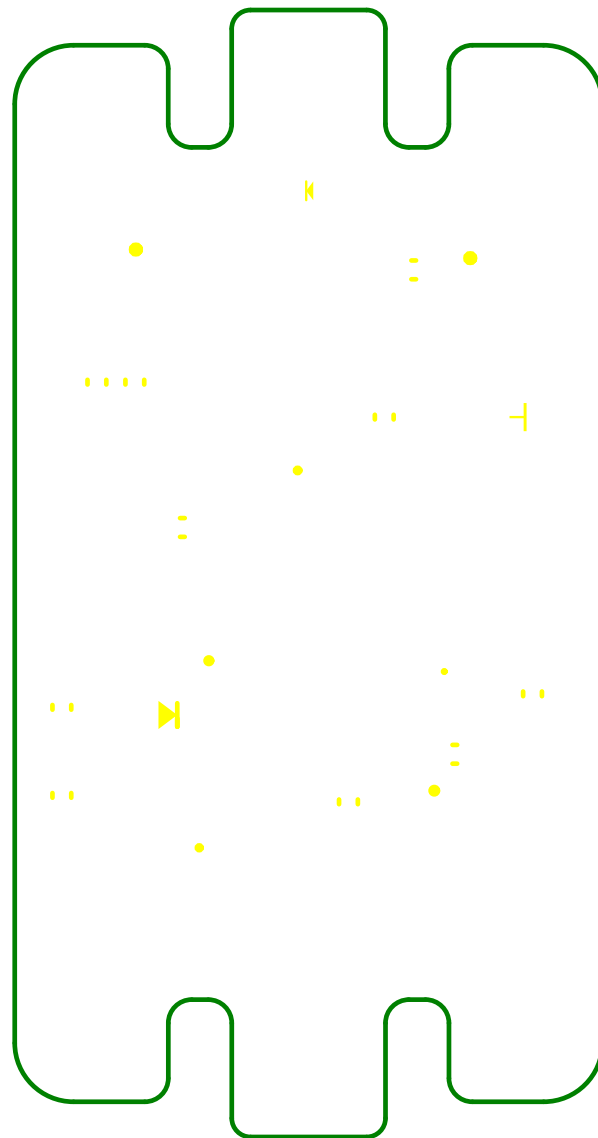
Board Outline





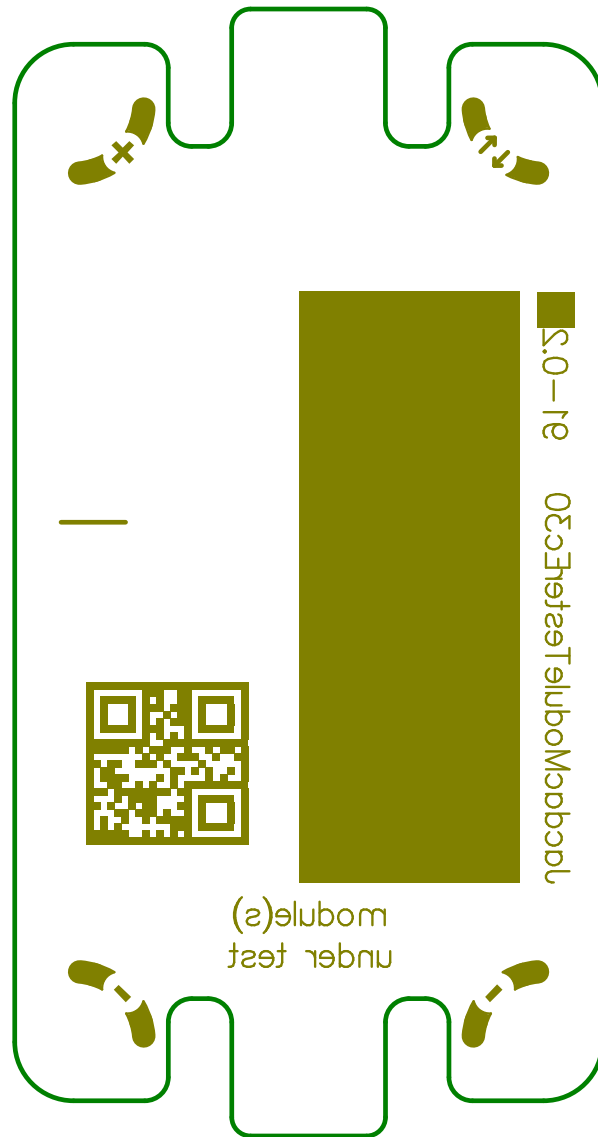
Board Outline

Top Overlay



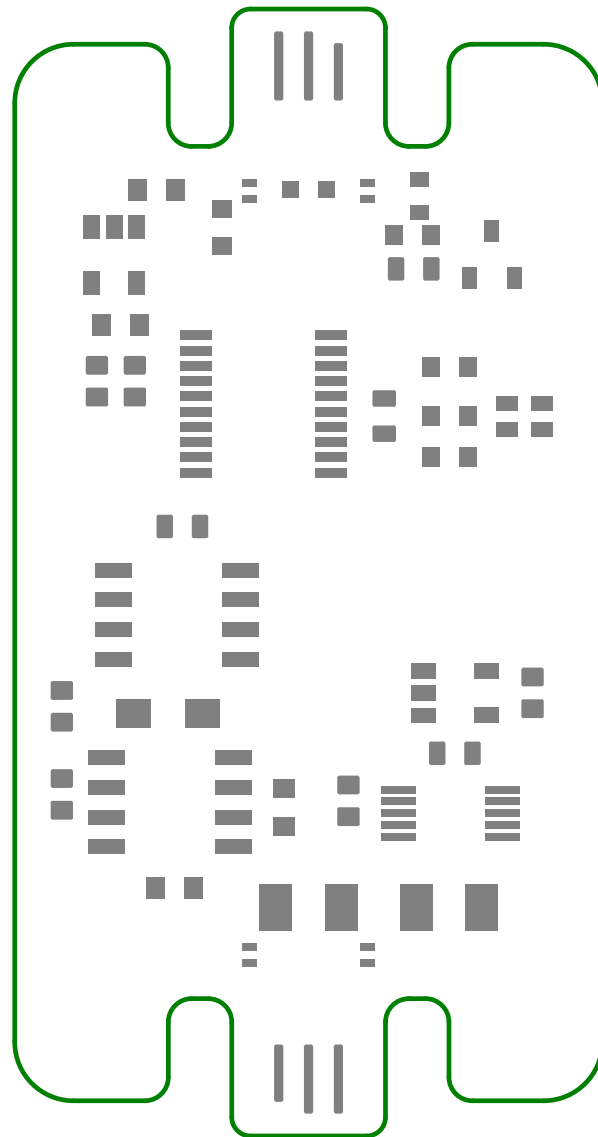
Board Outline

Bottom Overlay



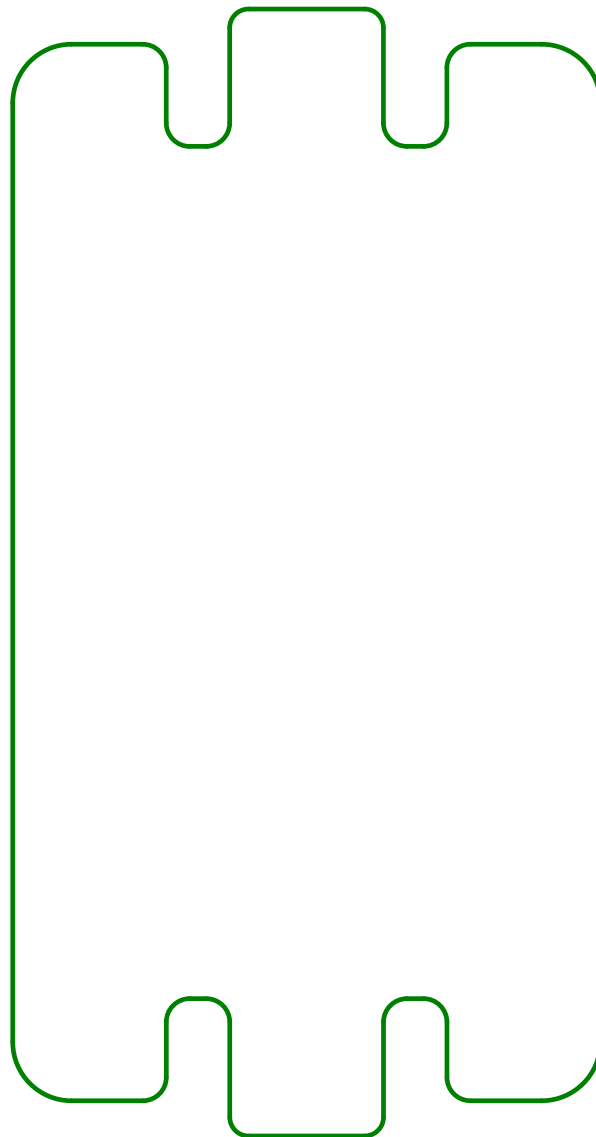
## Board Outline

Top Paste



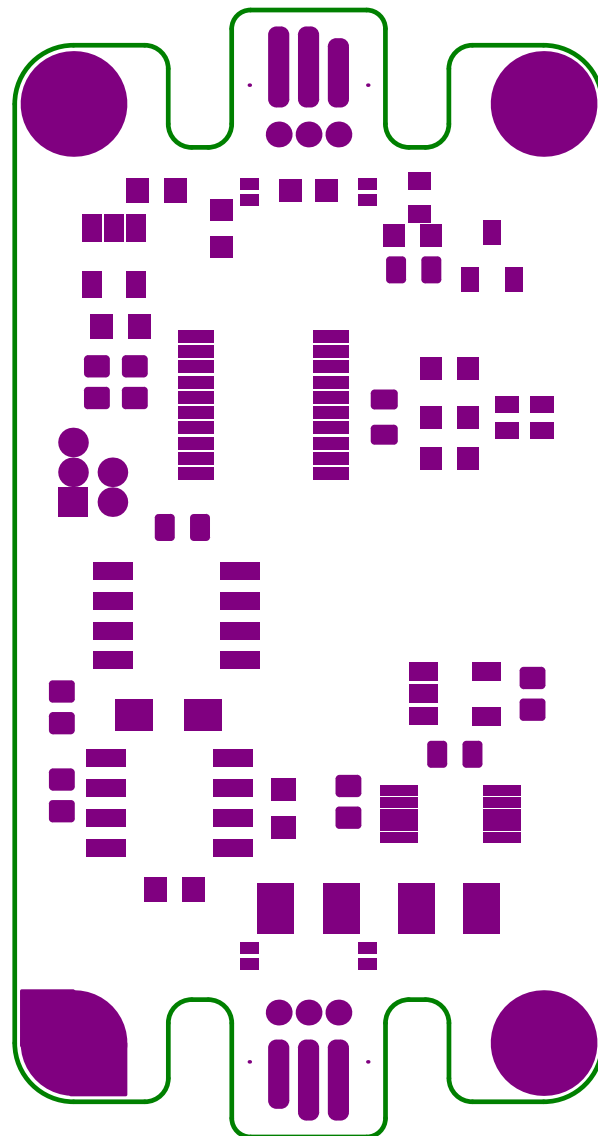
Board Outline

Bottom Paste



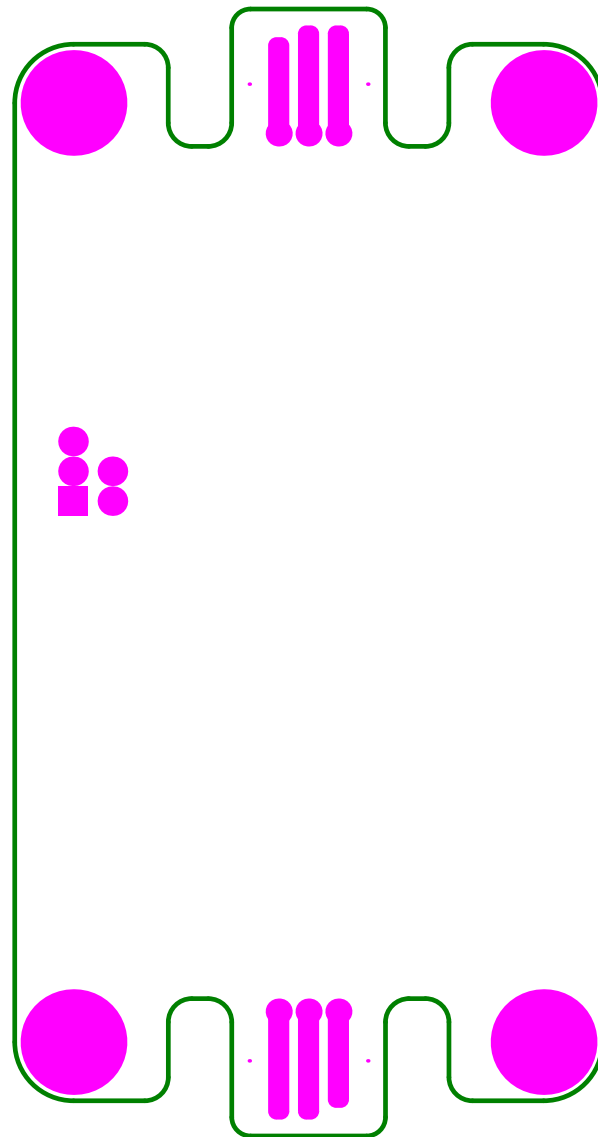
Board Outline

Top Solder (resist)



Board Outline

Bottom Solder (resist)



Board Outline

