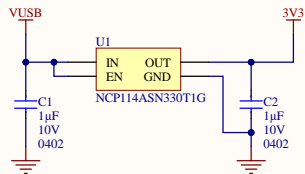
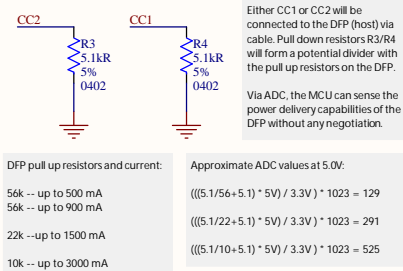
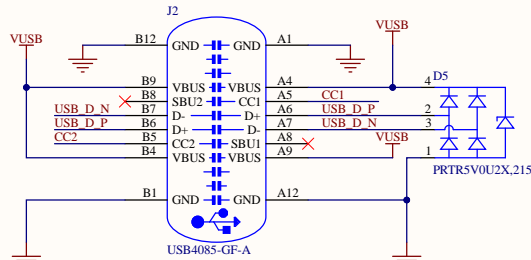


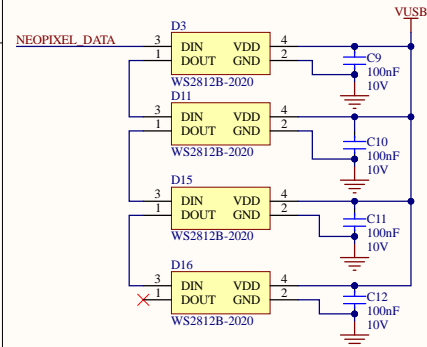
### 3V3 Regulator



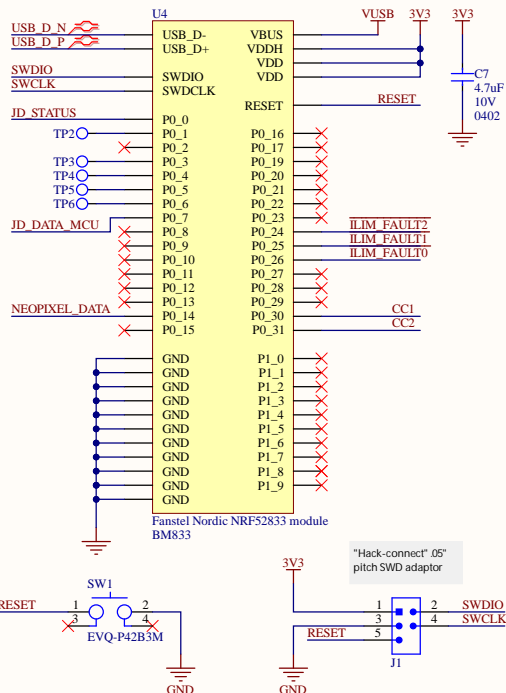
## Power supply



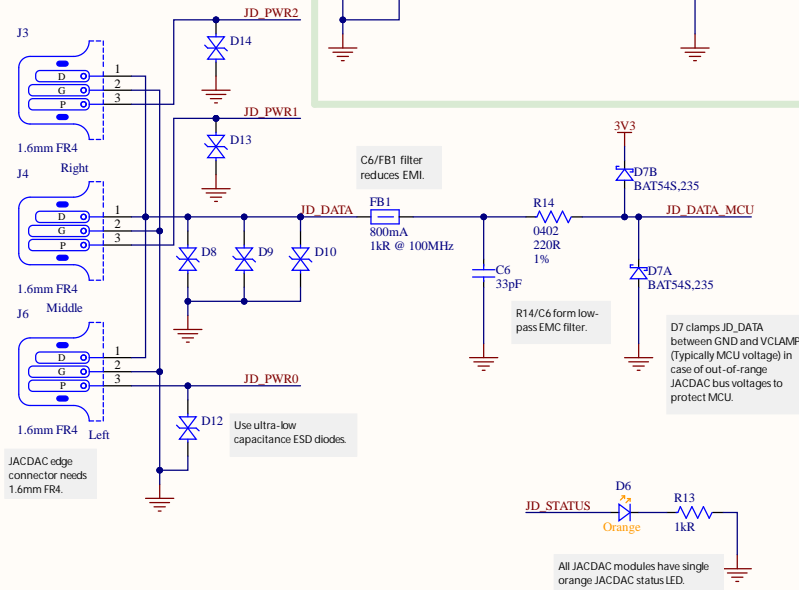
## Configuration indicators



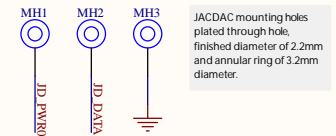
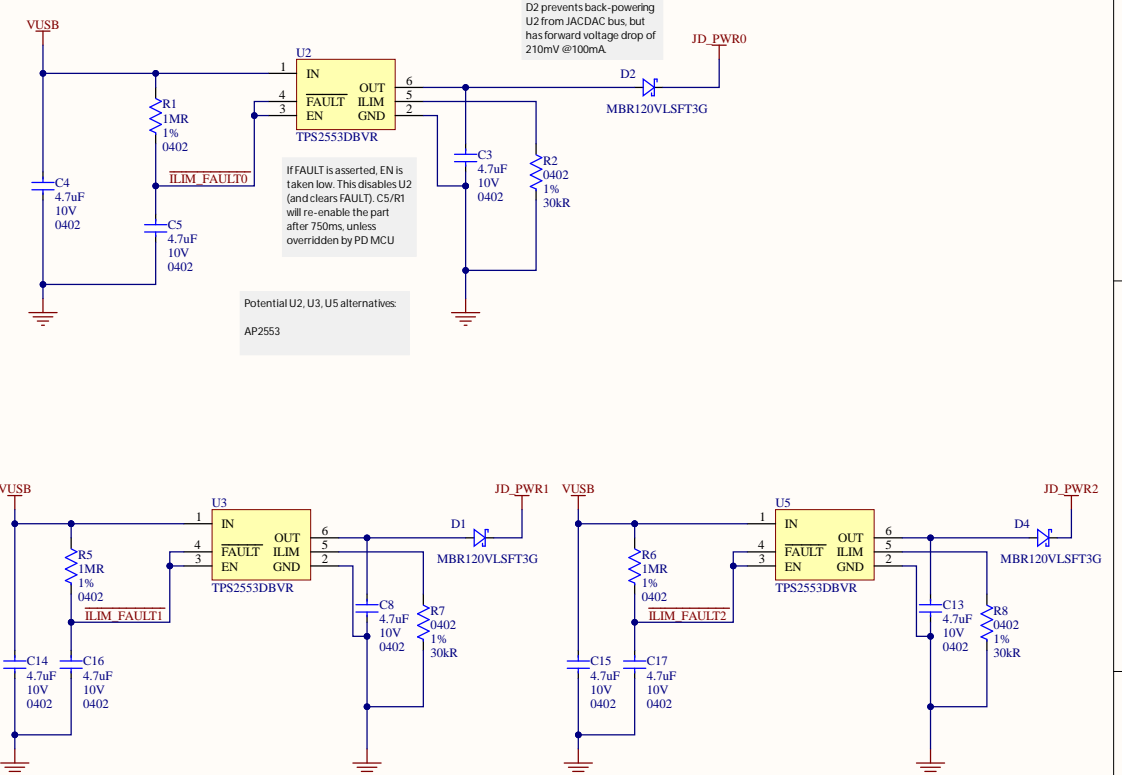
## Jacdac Brain



## JACDAC interface

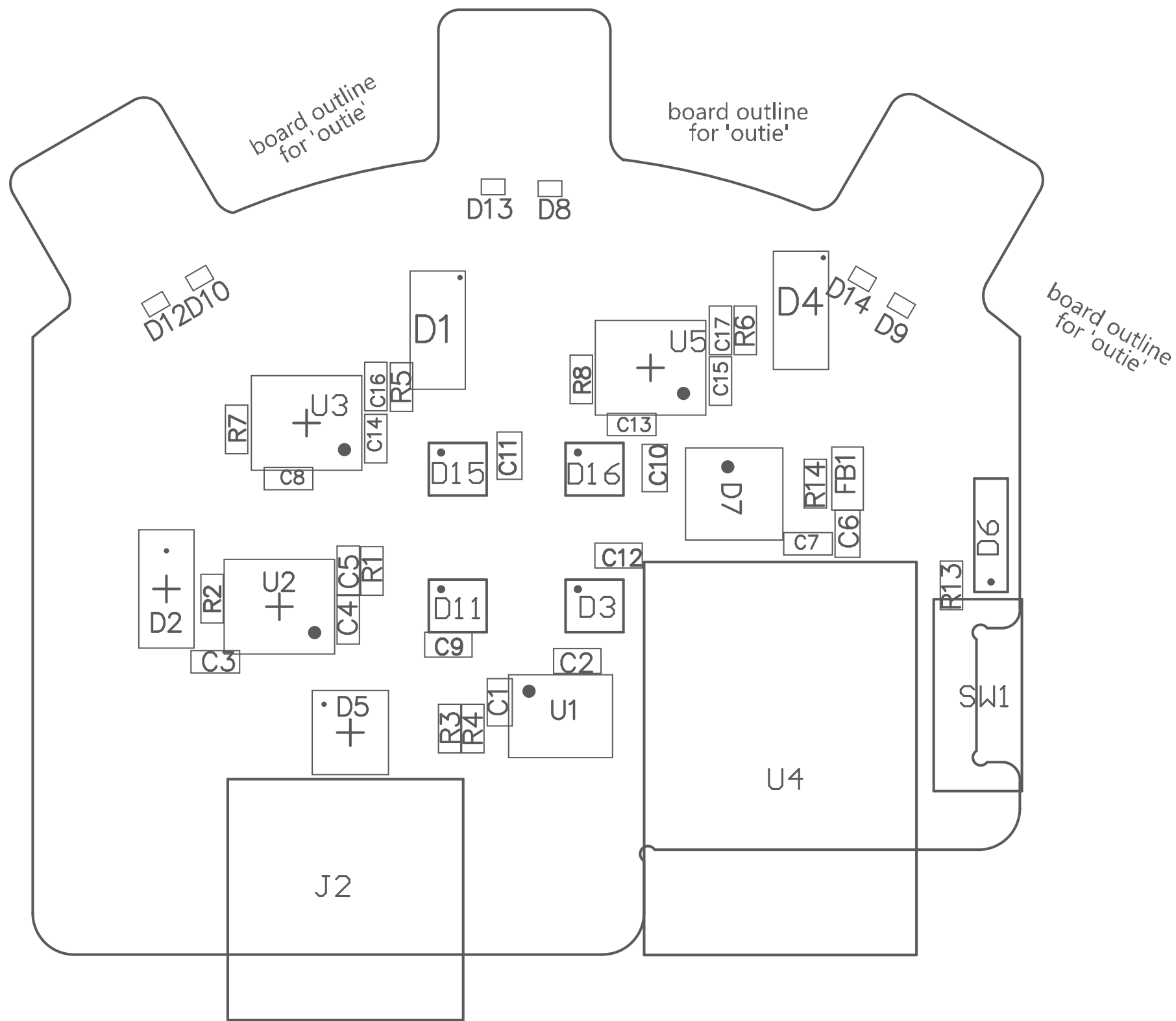


## Current limiters



|                           |  |
|---------------------------|--|
| Silkscreen & layout notes | This information is provided "as-is". You bear the risk of using it. Some information relates to pre-released specification which may change without notice. Microsoft makes no warranties, express or implied, with respect to the information provided here. |
| Block name                |  |
| Design notes              |  |

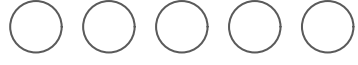
When this PDF is viewed with Adobe Reader, clicking on components shows part numbers and other details.

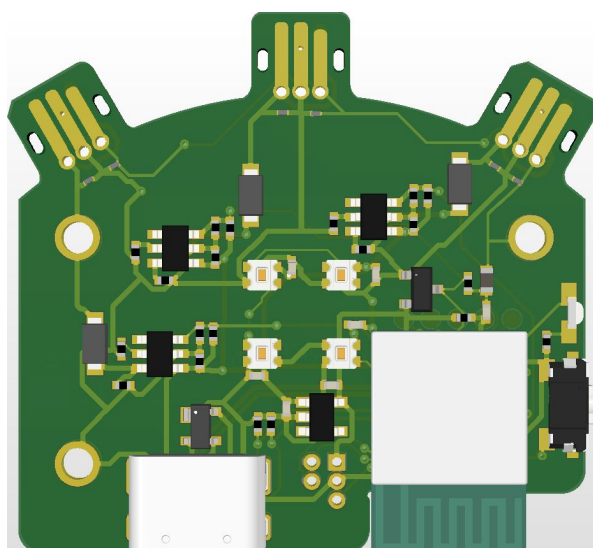


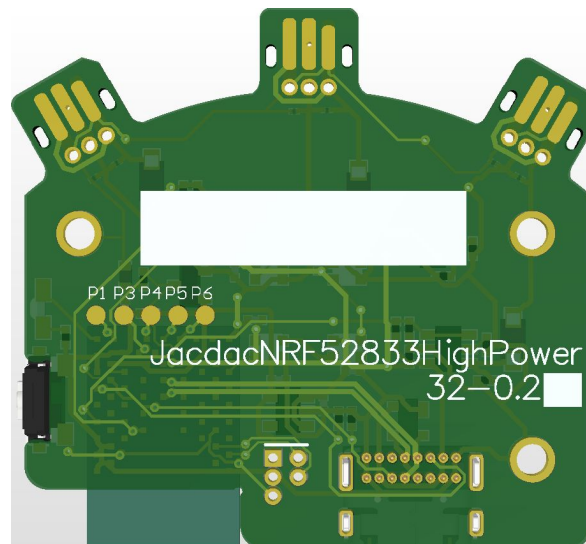
board outline  
for outline

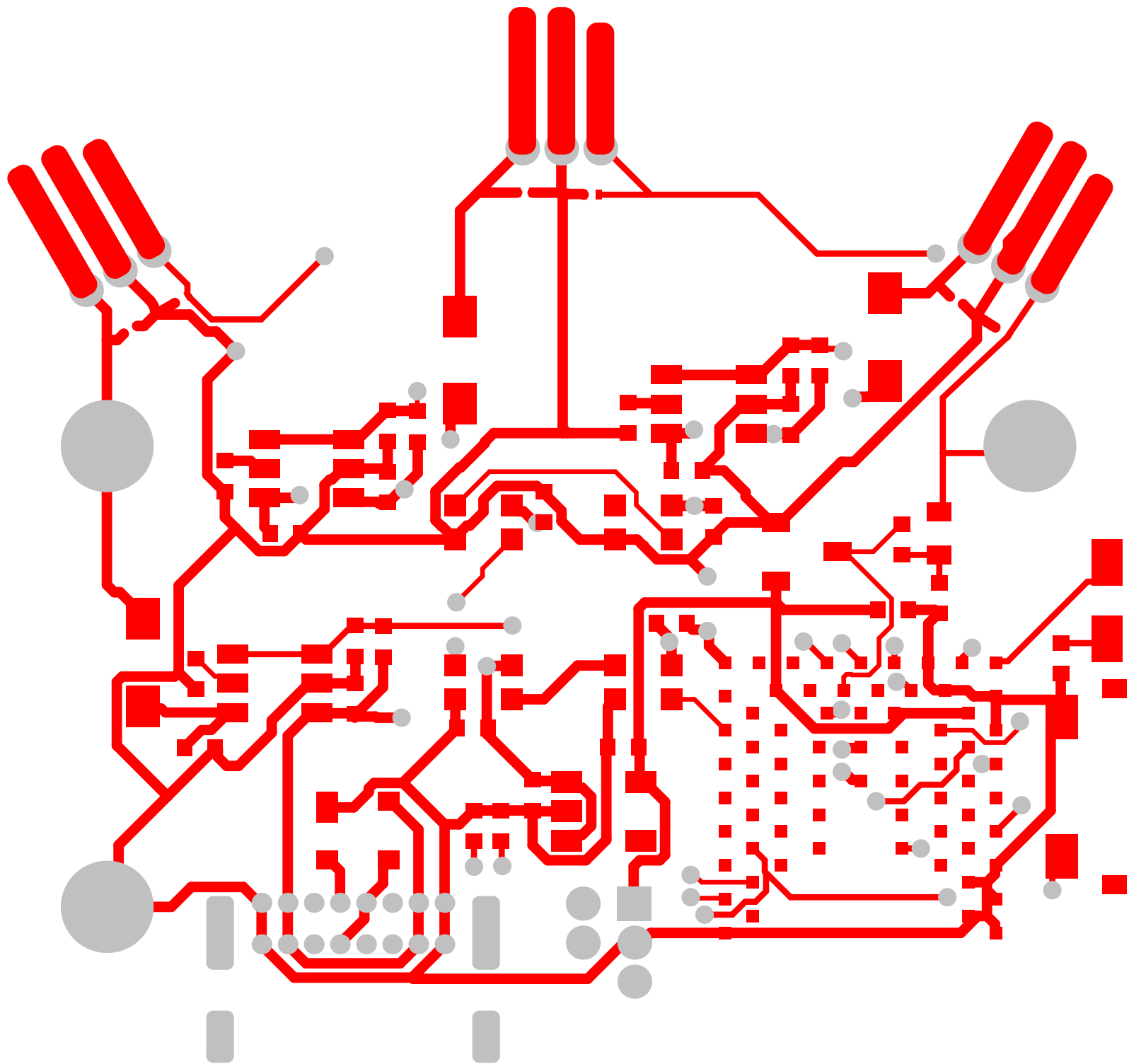
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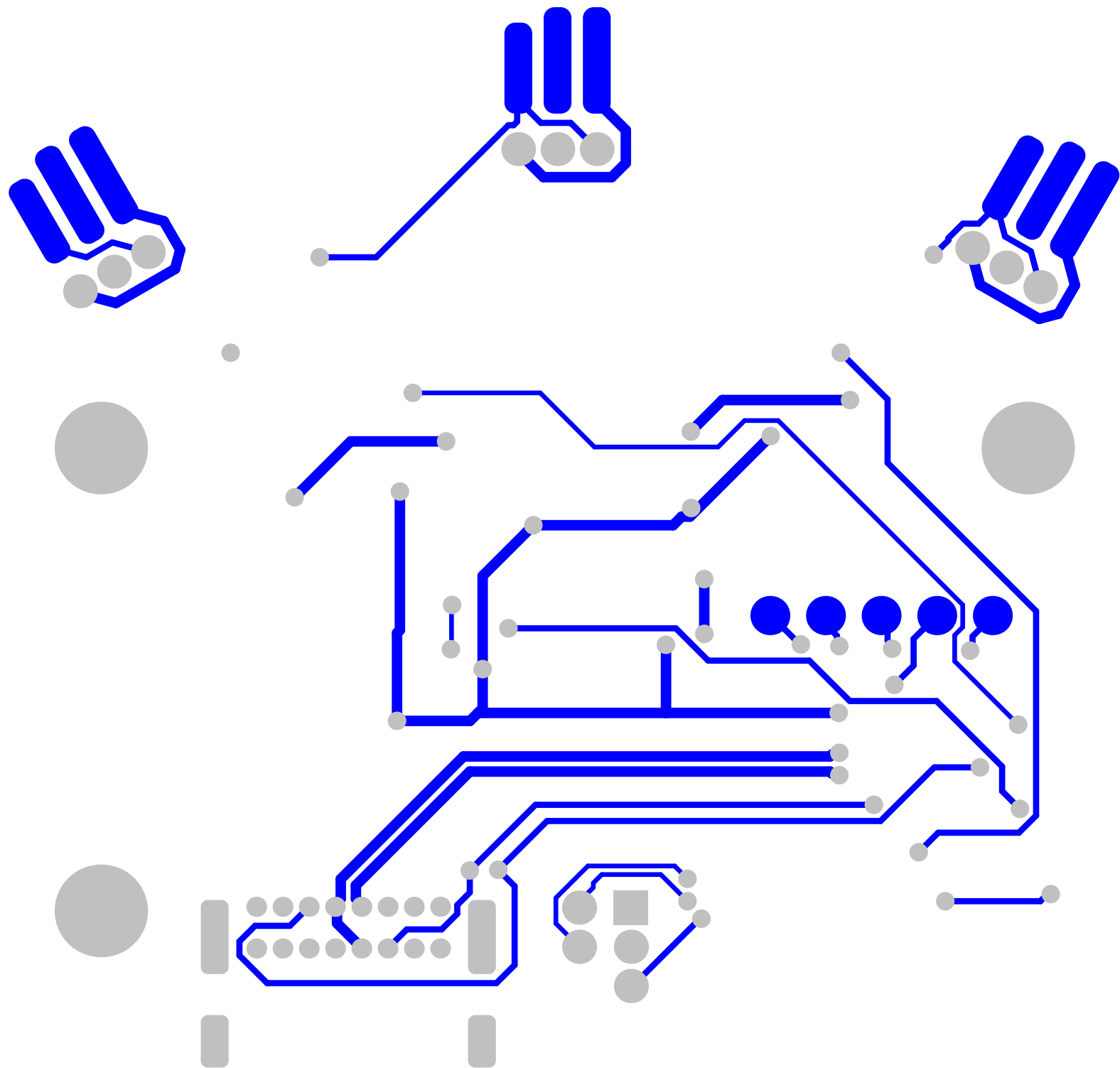
board outline  
for outline















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JaccardNRF53HighPower

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