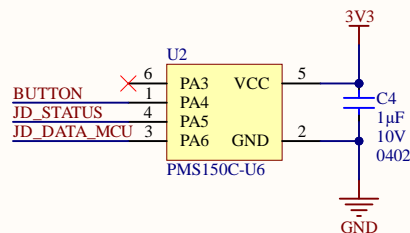
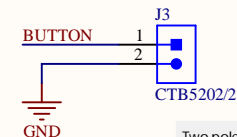


MCU



U2 must be programmed before being soldered down due to restrictive in-circuit programming requirements. JD_STATUS is PWM-capable GPIO.

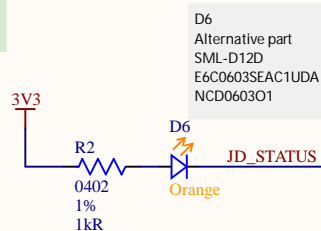
Button



MCU must have internal pull-up on BUTTON GPIO.

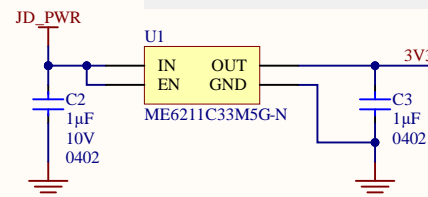
Two pole terminal block

Status LED



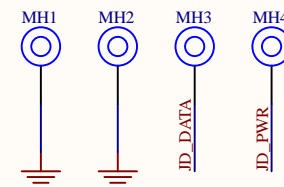
All Jacdac modules have Jacdac status LED. Relatively large R2 value limits current consumption.

3V3 Regulator



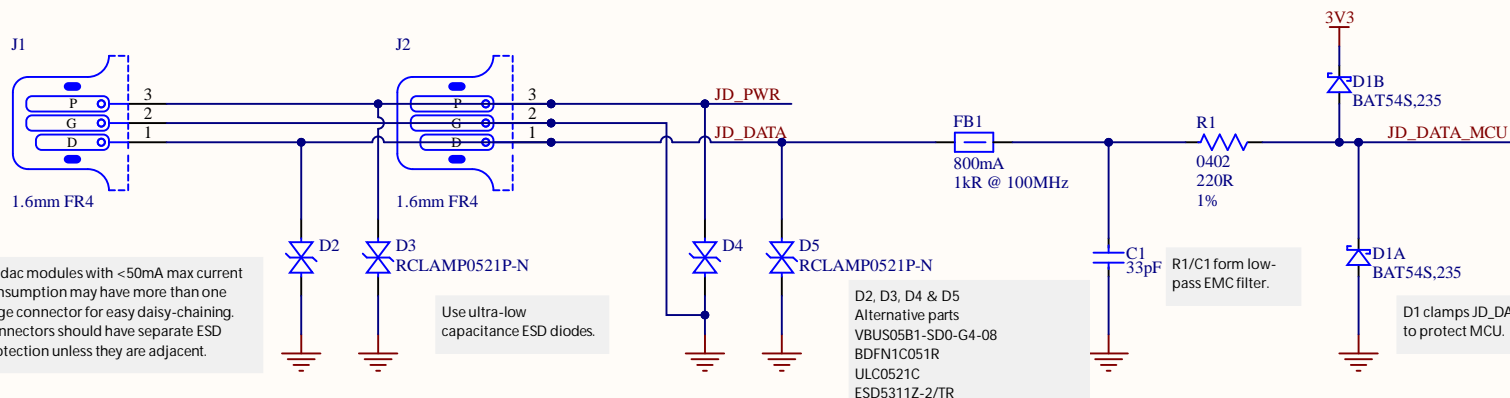
U1 alternative parts:
ME6212C33M5G 6V 260mV @ 200mA Iout 350mA
NCP114ASN330T1G 5.5V 225mV @ 300mA Iout 300mA
NCP114BSN330T1G 5.5V 225mV @ 300mA Iout 300mA

Mounting holes



JACDAC mounting holes are grounded plated through hole, finished diameter of 2.2mm and annular ring of 3.2mm diameter.

Jacdac connector



Jacdac modules with <50mA max current consumption may have more than one edge connector for easy daisy-chaining. Connectors should have separate ESD protection unless they are adjacent.

Use ultra-low capacitance ESD diodes.

D2, D3, D4 & D5
Alternative parts
VBUS05B1-SD0-G4-08
BDFN1C051R
ULC0521C
ESD5311Z-2/TR

R1/C1 form low-pass EMC filter.

D1 clamps JD_DATA to protect MCU.

Key:
Silkscreen & layout notes
Block name
Design notes

Microsoft

PROJECT DESCRIPTION
Jacdac button based on low-cost OTP PADAUK MCU

SHEET DESCRIPTION
Complete design

PROJECT FILENAME JacdacTBlockButton 62.PrjPCB

PROJECT CODENAME JacdacTBlockButton

LAST MODIFIED 03/09/2021

PAGE 1 OF 1

DRAWN BY NT

REVISION 0.1

PCB ID 62-0.1

SHEET FILENAME JacdacTBlockButton 62.SchDoc

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