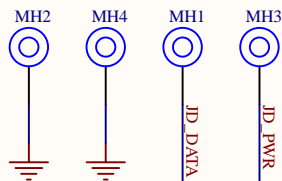


## Status LED



Place a status LED adjacent to each edge connector. Where several edge connectors are next to each other they can share the same status LED. This design has one edge connector and one LED.

## Mounting holes



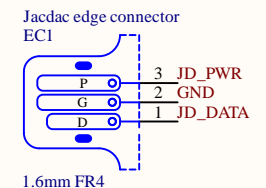
Mounting holes are electrically connected to the Jacdac bus nets so they can be used as an alternative to the PCB edge connector. Please use the following reference designators and net mapping:

MH1: JD\_DATA  
MH2 & MH4: GND  
MH3: JD\_PWR

This design uses PTH mounting holes with finished diameter of 3.1mm, annular copper ring of 4.4mm diameter, resist keepout of 5.0mm & copper/component keepout of 7.0mm. The mounting holes must be on 10mm pitch.

Mounting holes should have appropriate silkscreen marker on both sides of PCB, and MH1 should have a pin 1 marker in copper on the side only.

## Jacdac connector



This reference design is a guideline. Please refer to the Jacdac docs online at <https://aka.ms/jacdac> for the definitive and most up-to-date information.

This design uses an enclosure compatible board shape.

Silkscreen should include text to identify the module type and revision, and optionally a QR code.

Silkscreen & layout notes

Block name

Design notes

When this PDF is viewed with Adobe Reader, clicking on components shows part numbers and other details.

This information is provided "as-is". You bear the risk of using it. Some information relates to pre-released specification which may change without notice. Microsoft makes no warranties, express or implied, with respect to the information provided here.

PROJECT FILENAME JacdacEc30RgbLedRing8 105.PrjPCB

PROJECT CODENAME JacdacEc30RgbLedRing8

SHEET FILENAME JacdacEc30RgbLedRing8 105.SchDoc

LICENCE Attribution 4.0 International (CC BY 4.0)

Microsoft

PROJECT DESCRIPTION  
Jacdac "EC" RGB LED ring of 8

LAST MODIFIED 28/04/2022

PAGE 1 OF 1

DRAWN BY S. Hodges

SHEET DESCRIPTION  
Complete design

REVISION 0.3

PCB ID 105-0.3