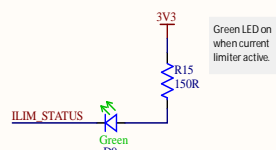
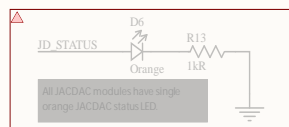
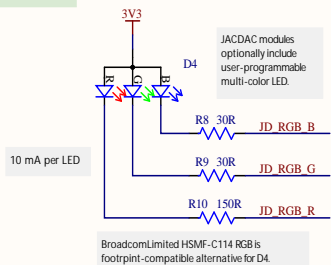
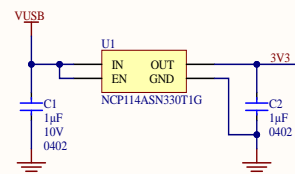


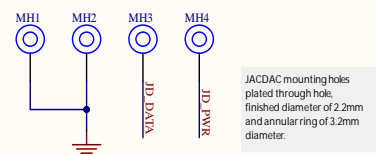
## Status LEDs



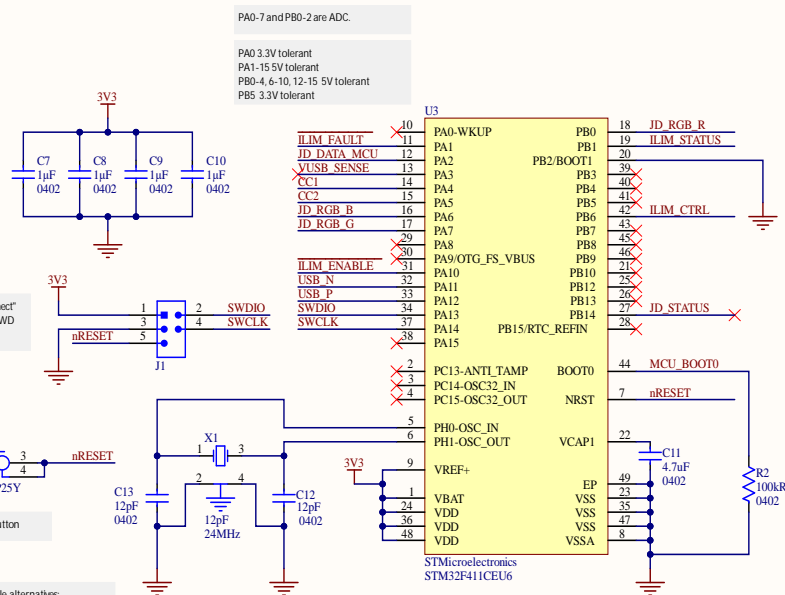
## 3V3 regulator



## Mounting holes

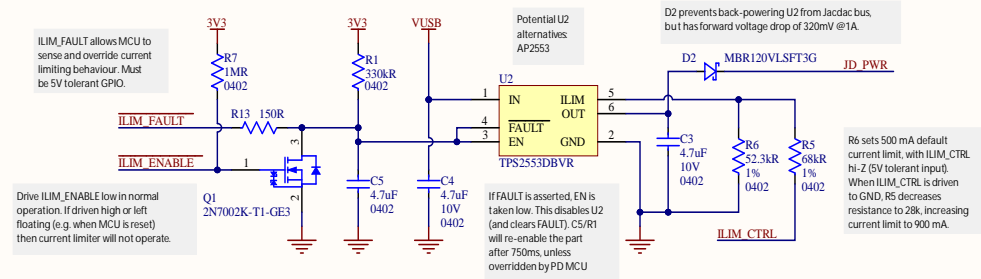


Brain & power delivery MCU

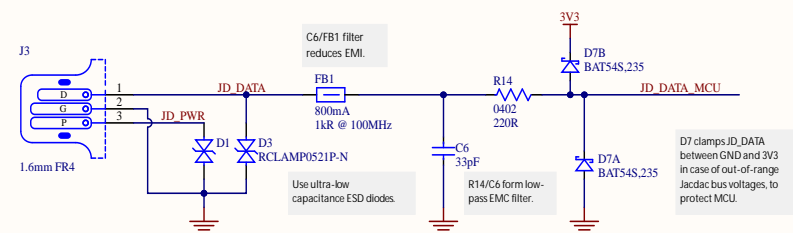


SW1 Footprint compatible alternatives:  
PTS810SJG250SMTRLFS  
UK-B0267-ZT-SP25Y  
TS-1145A-B-A  
K2-1808SN-A4SW-01  
TS-1089S-02526

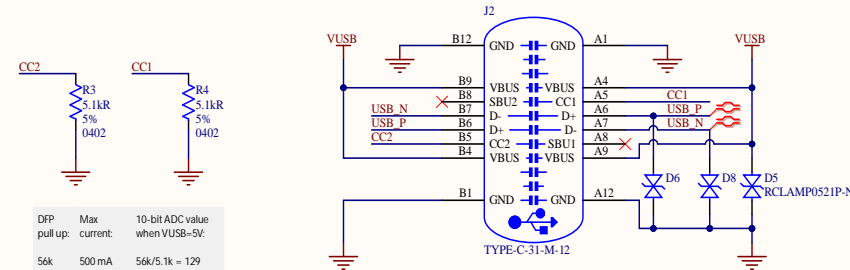
Current limiter



## Jacdac interface

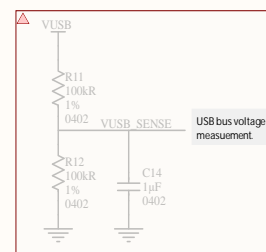


USB interface



Either CC1 or CC2 will be connected to the DFP (host) via cable. Pull down resistors R3/R4 will form a potential divider with the pull up resistors on the DFP.

DFP pull up:	Max current:	10-bit ADC value when VUSB=5V:
56k	500 mA	$56k/5.1k = 129$
22k	1500 mA	$22k/5.1k = 292$
10k	3000 mA	$10k/5.1k = 525$



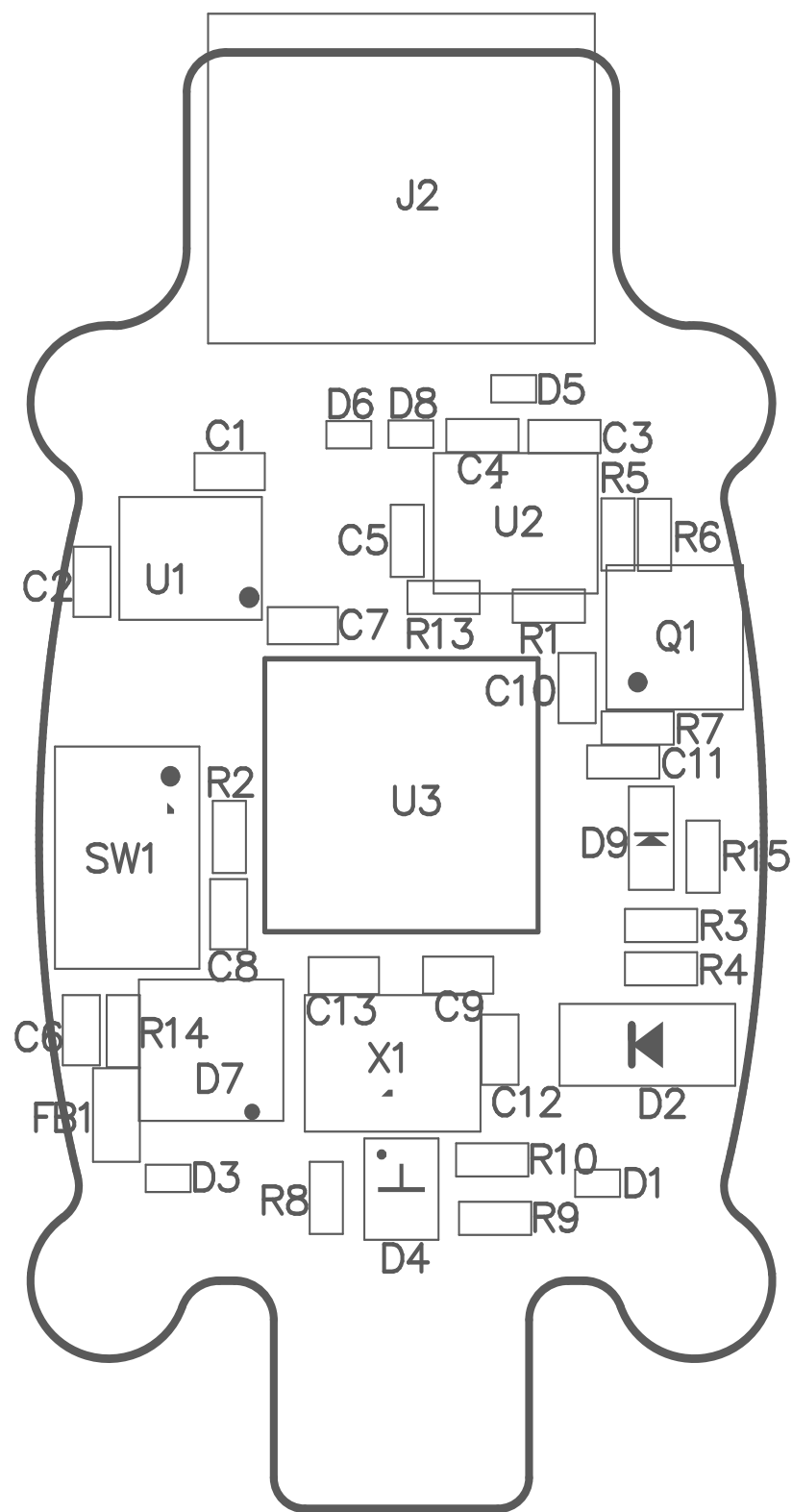
Silkscreen & layout notes

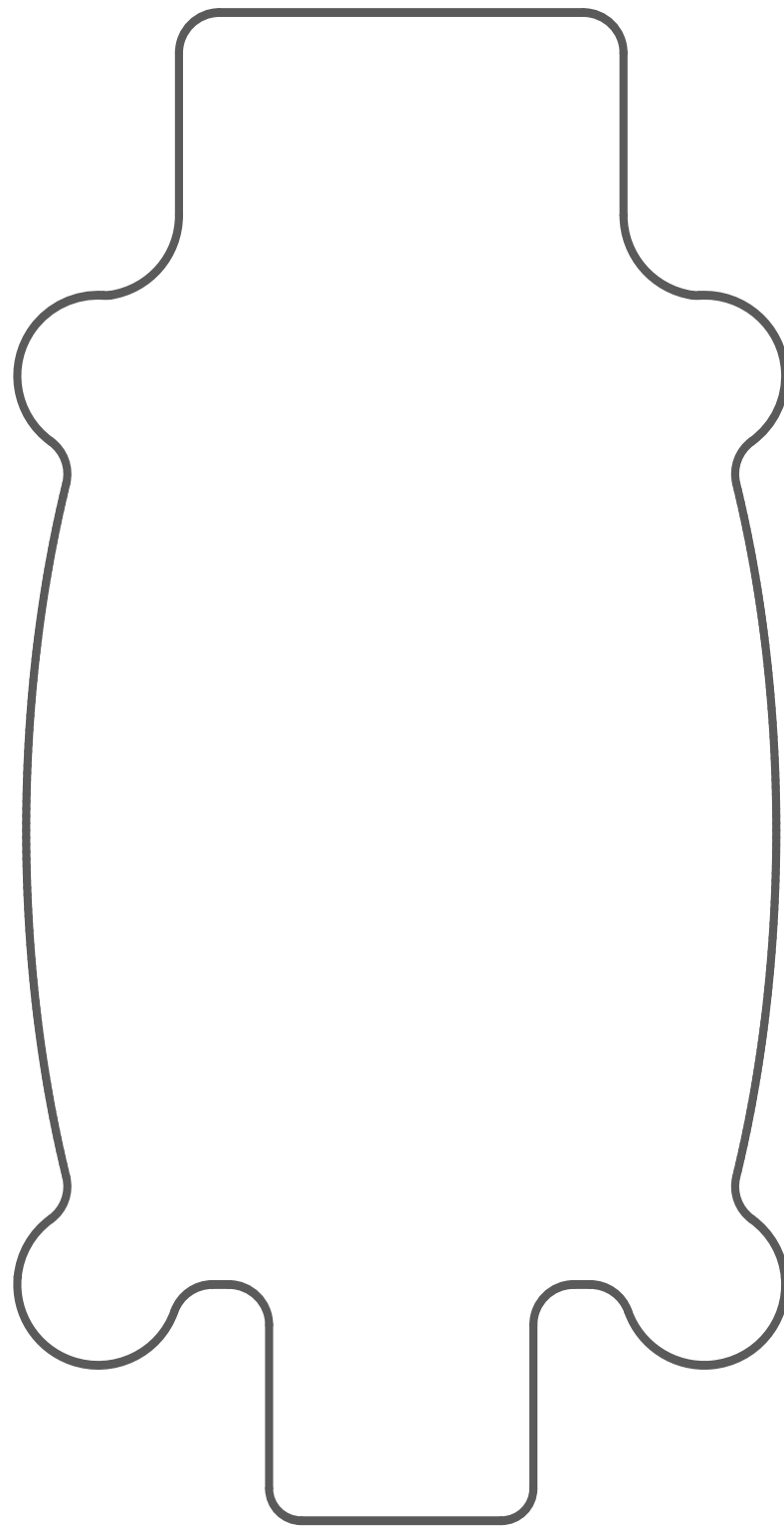
Block name

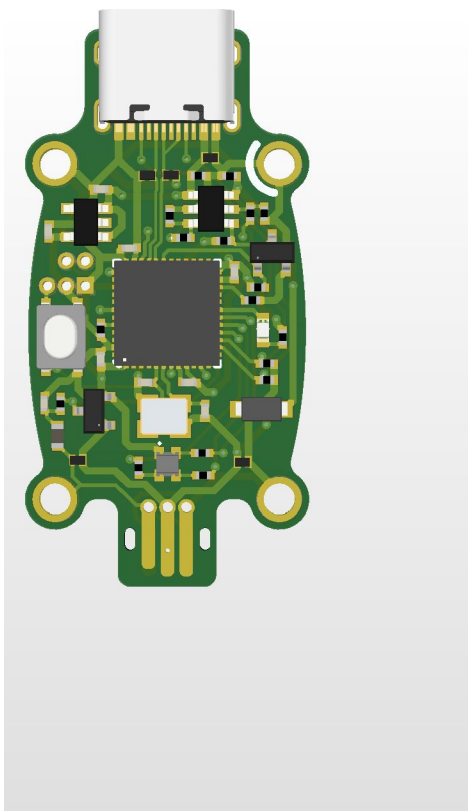
### Design notes

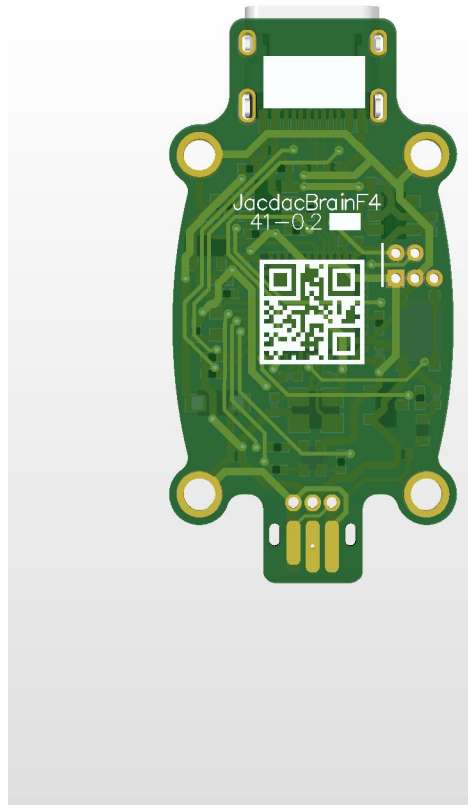
When this PDF is viewed with Adobe Reader, clicking on components shows part numbers and other details.

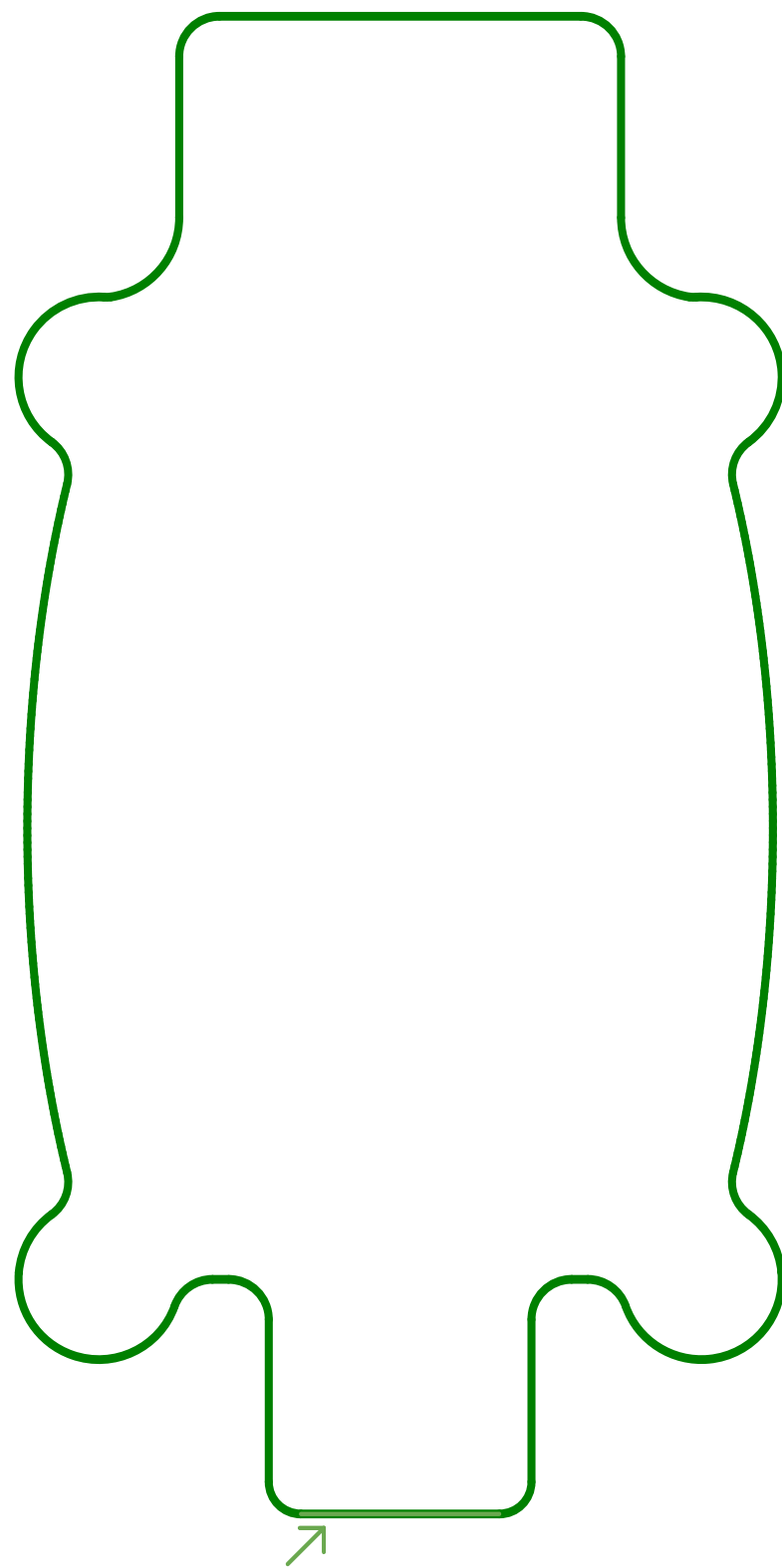
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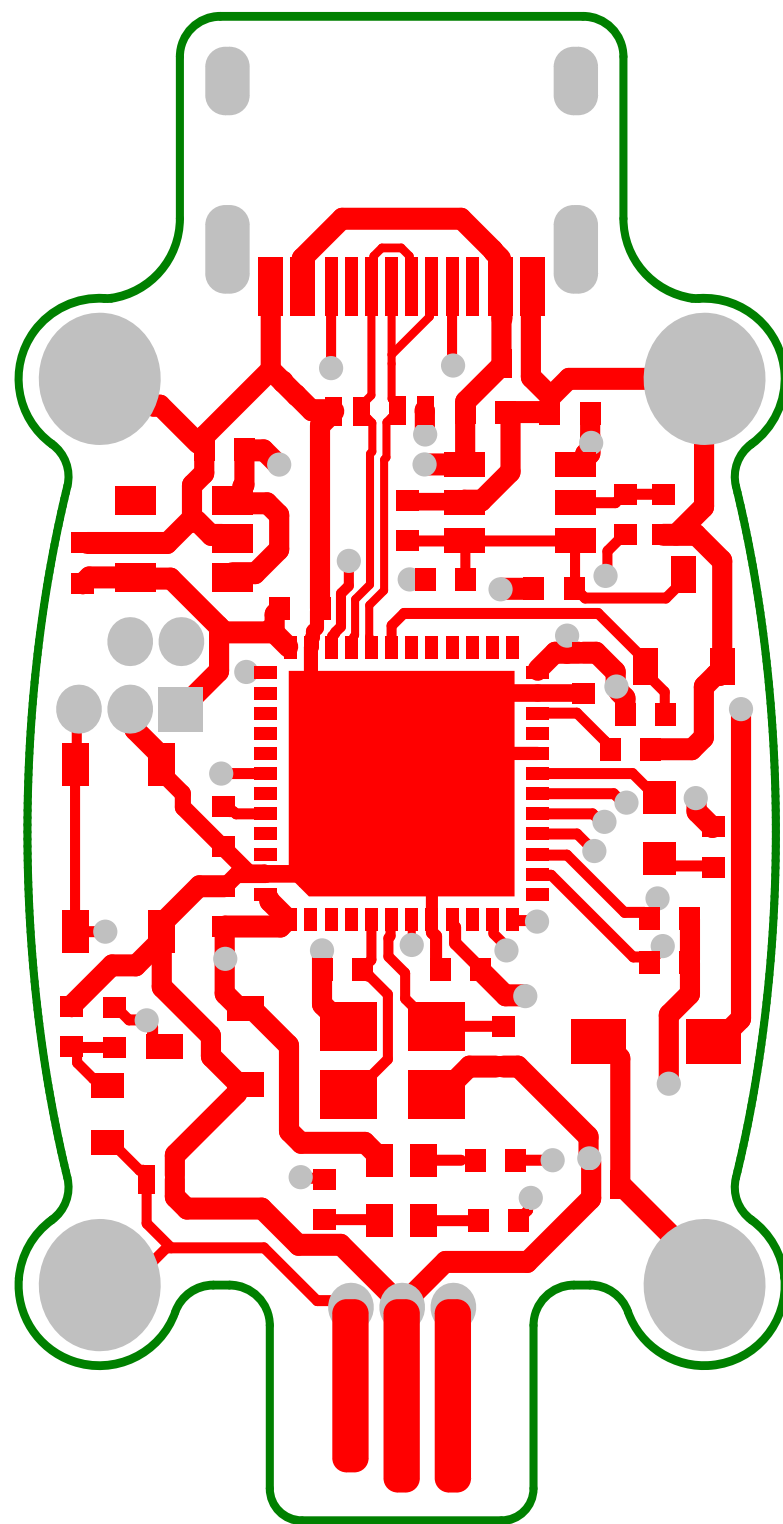


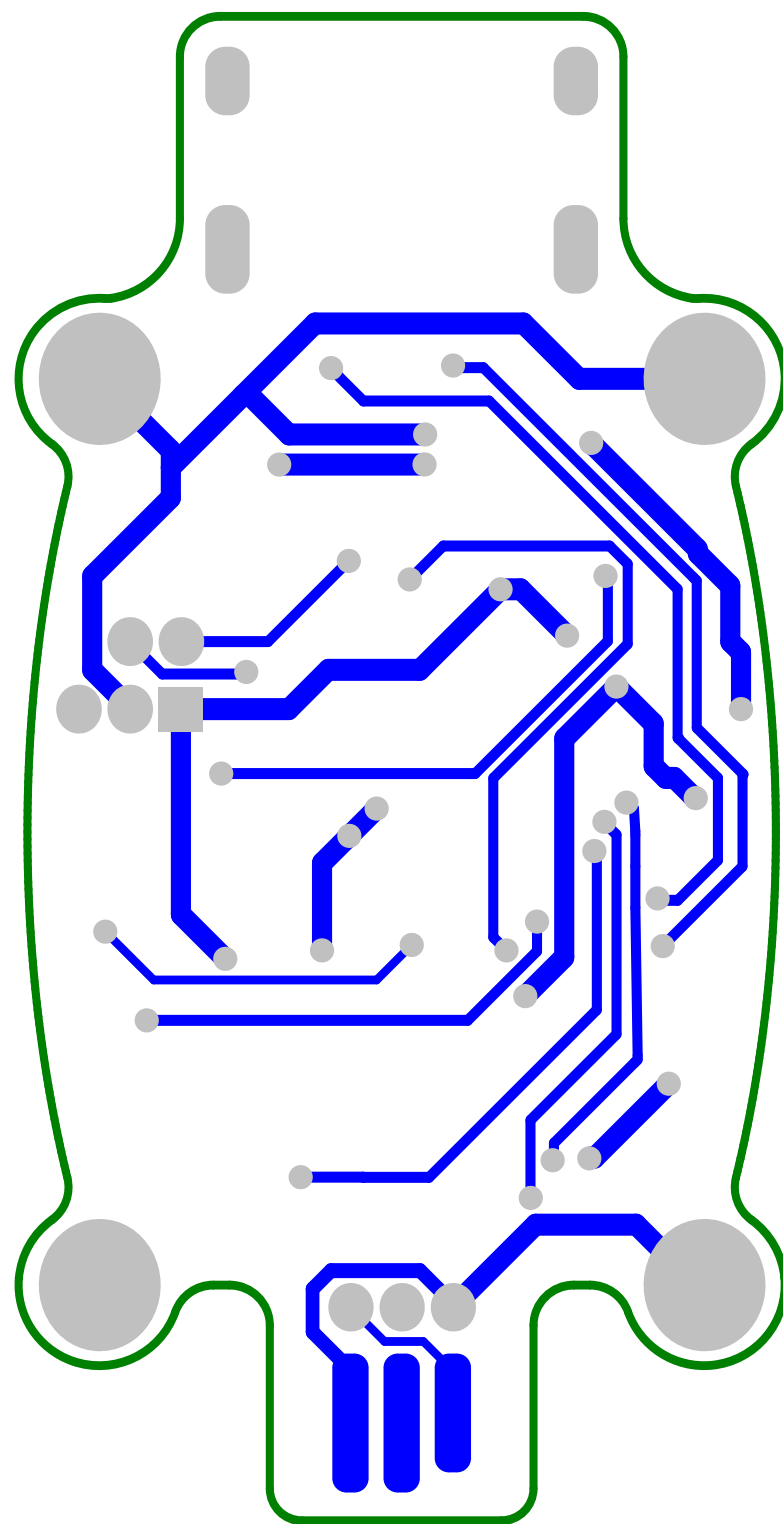




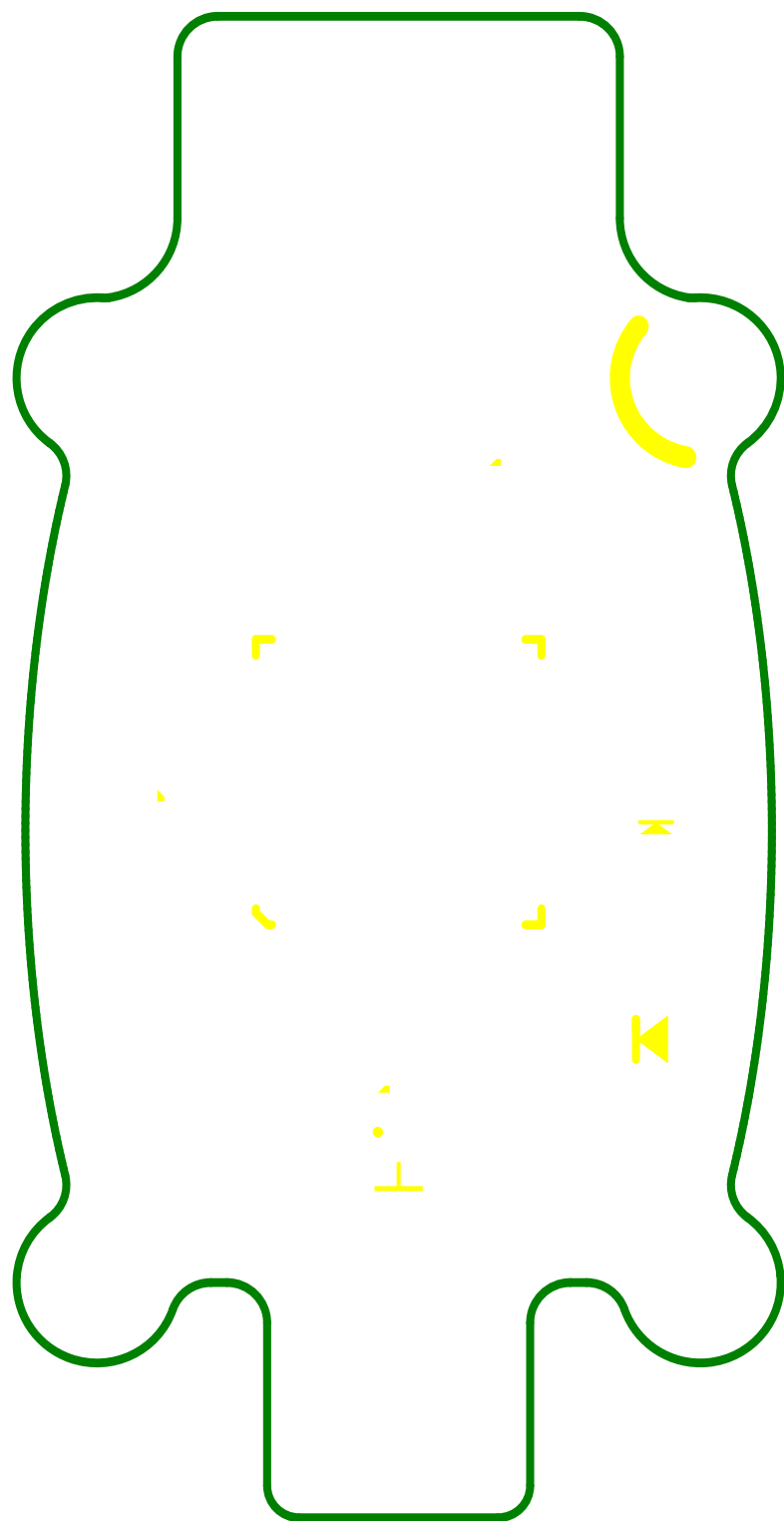














41-0.2  
JacdacBrainF4

