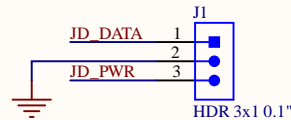
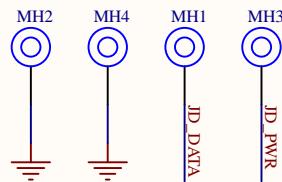


## Test & develeopment header



## Mounting holes



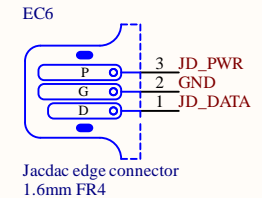
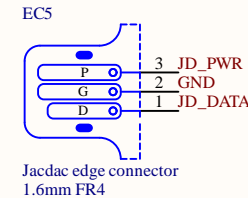
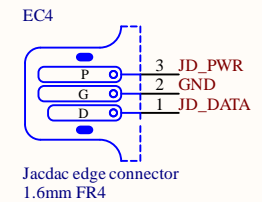
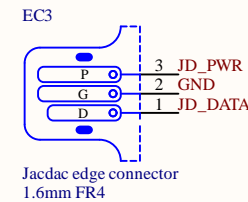
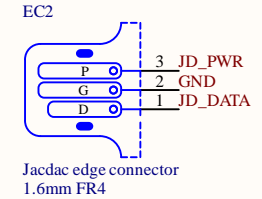
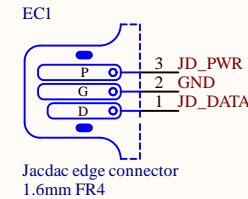
Mounting holes are electrically connected to the Jacdac bus nets so they can be used as an alternative to the PCB edge connector. Please use the following reference designators and net mapping:

MH1: JD\_DATA  
MH2 & MH4: GND  
MH3: JD\_PWR

This design uses PTH mounting holes with finished diameter of 3.1mm, annular copper ring of 4.4mm diameter, resist keepout of 5.0mm & copper/component keepout of 7.0mm. The mounting holes must be on a 10mm pitch.

Mounting holes should have appropriate silkscreen marker on both sides of PCB, and MH1 should have a pin 1 marker in copper on the top side only. There is a notch in the PCB outline adjacent to MH1.

## Jacdac connectors



This reference design is a guideline. Please refer to the Jacdac docs online at <https://aka.ms/jacdac> for the definitive and most up-to-date information.

This design uses an enclosure compatible board shape.

Silkscreen should include text to identify the module type and revision, and optionally a QR code.

Silkscreen & layout notes

Block name

Design notes

When this PDF is viewed with Adobe Reader, clicking on components shows part numbers and other details.

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PROJECT DESCRIPTION  
Jacdac "EC" compatible 6 port hub

SHEET DESCRIPTION  
Complete design

PROJECT FILENAME JacdacEc30Hub10x40 114.PrjPCB

PROJECT CODENAME JacdacEc30Hub10x40

SHEET FILENAME JacdacEc30Hub10x40 114.SchDoc

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DRAWN BY S. Hodges

REVISION 1.0

PCB ID 114-1.0