

1

2

3

4

A

A

B

B

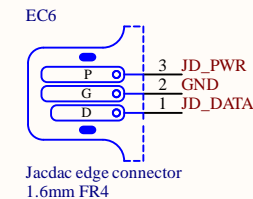
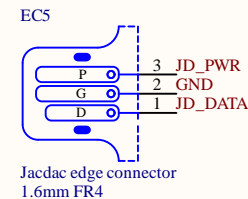
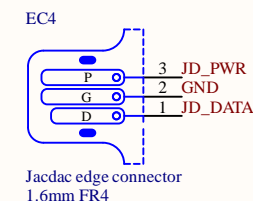
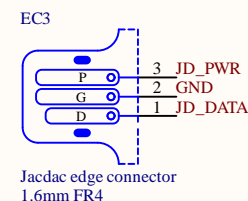
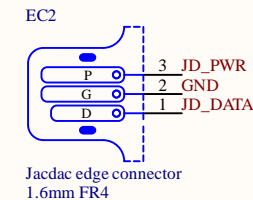
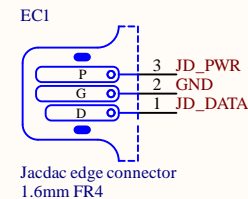
C

C

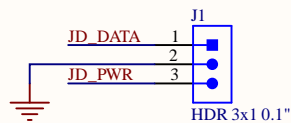
D

D

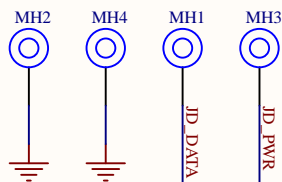
Jacdac connectors



Test & develeopment header



Mounting holes



Mounting holes are electrically connected to the Jacdac bus nets so they can be used as an alternative to the PCB edge connector. Please use the following reference designators and net mapping:

MH1: JD_DATA
MH2 & MH4: GND
MH3: JD_PWR

This design uses PTH mounting holes with finished diameter of 3.1mm, annular copper ring of 4.4mm diameter, resist keepout of 5.0mm & copper/component keepout of 7.0mm. The mounting holes must be on a 10mm pitch.

Mounting holes should have appropriate silkscreen marker on both sides of PCB, and MH1 should have a pin 1 marker in copper on the top side only. There is a notch in the PCB outline adjacent to MH1.

This reference design is a guideline. Please refer to the Jacdac docs online at <https://aka.ms/jacdac> for the definitive and most up-to-date information.

This design uses an enclosure compatible board shape.

Silkscreen should include text to identify the module type and revision, and optionally a QR code.

Silkscreen & layout notes

Block name

Design notes

When this PDF is viewed with Adobe Reader, clicking on components shows part numbers and other details.

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PROJECT FILENAME JacdacEc30Hub10x40 114.PrjPCB

PROJECT CODENAME JacdacEc30Hub10x40

SHEET FILENAME JacdacEc30Hub10x40 114.SchDoc

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Microsoft

PROJECT DESCRIPTION
Jacdac "EC" compatible 6 port hub

SHEET DESCRIPTION
Complete design

LAST MODIFIED 13/06/2022

PAGE 1 OF 1

DRAWN BY S. Hodges

REVISION 1.0

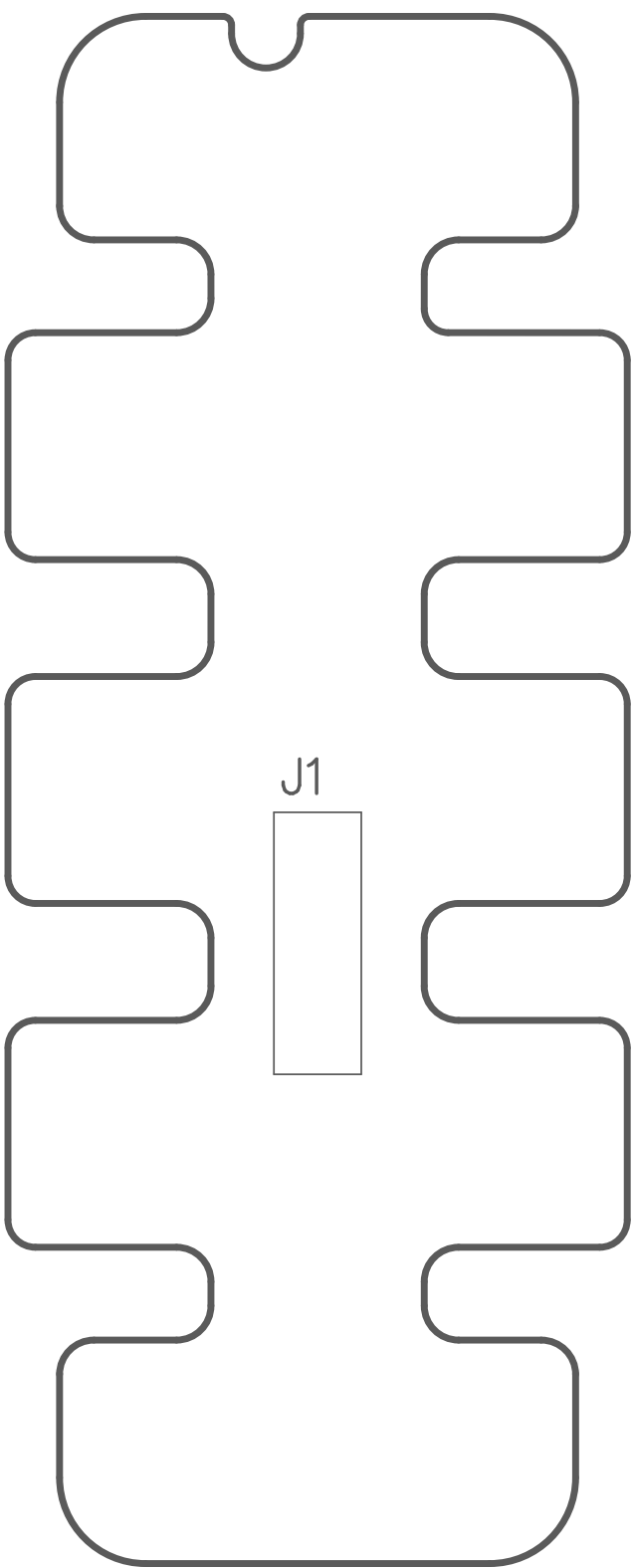
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1

2

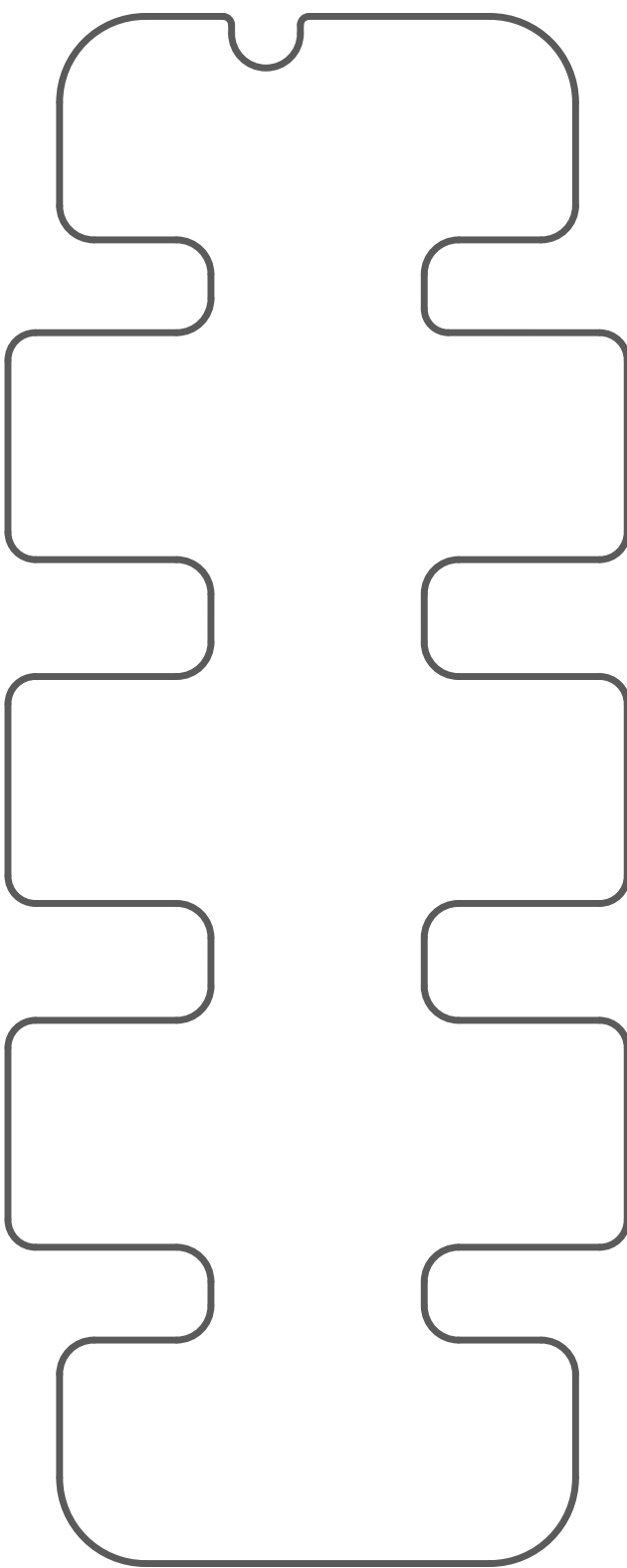
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4



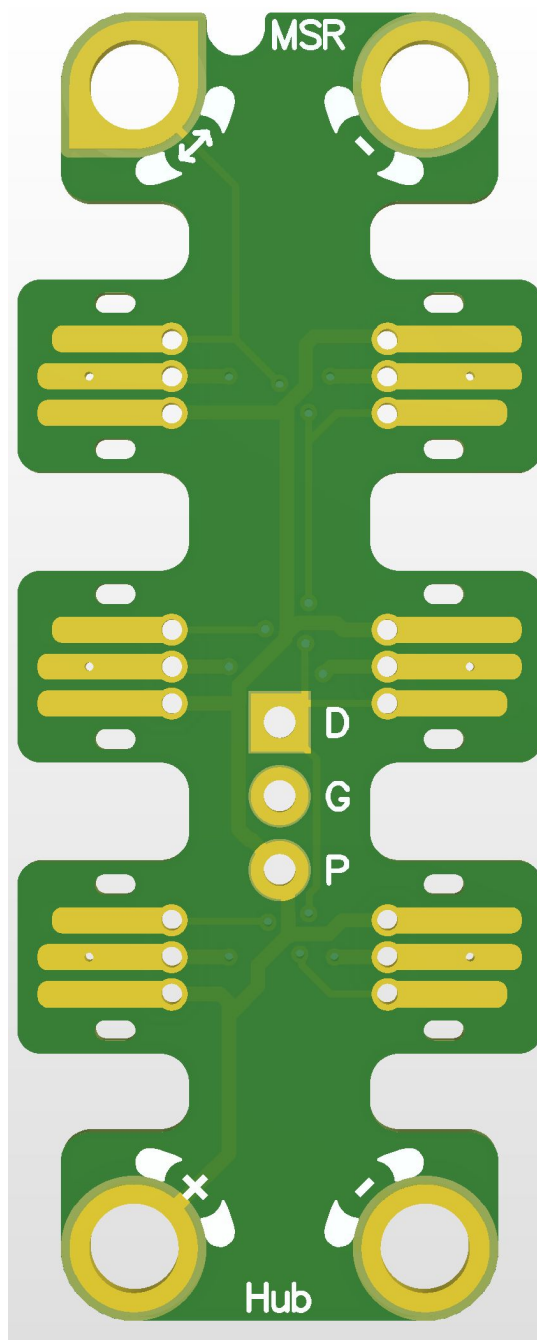
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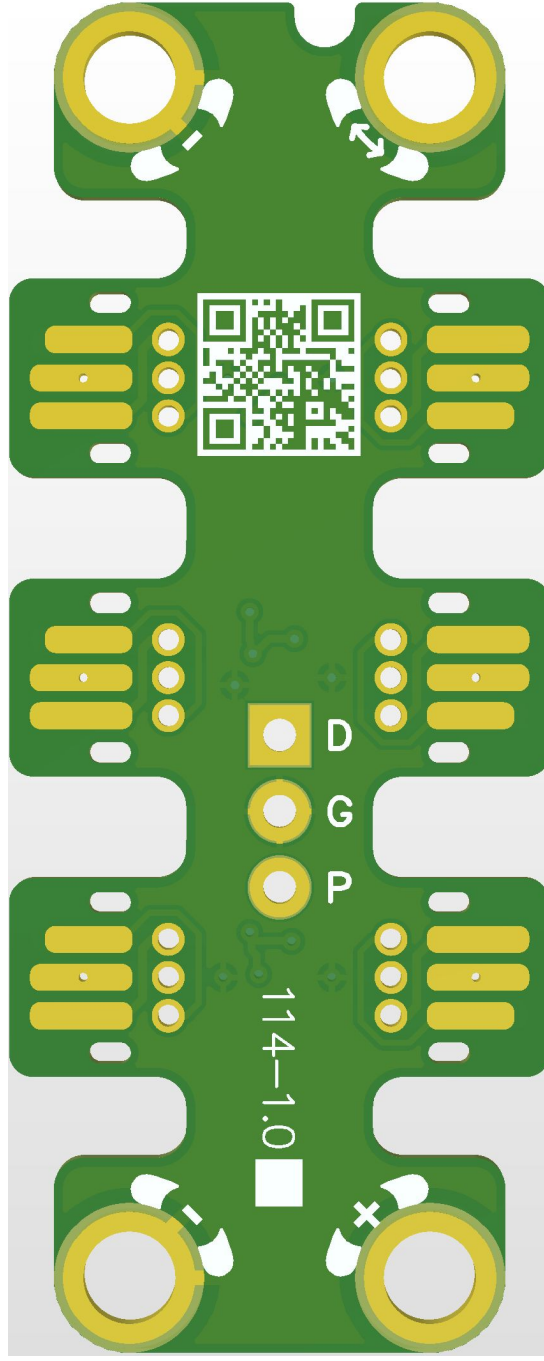
Board Outline



Bottom Assy

Board Outline





Fabrication Notes Board Outline

no board edge pips/
rat's teeth/mouse bites
in this region - make
sure board edge is clean

no board edge pips/
rat's teeth/mouse bites
in this region - make
sure board edge is clean

non-plated slots x2

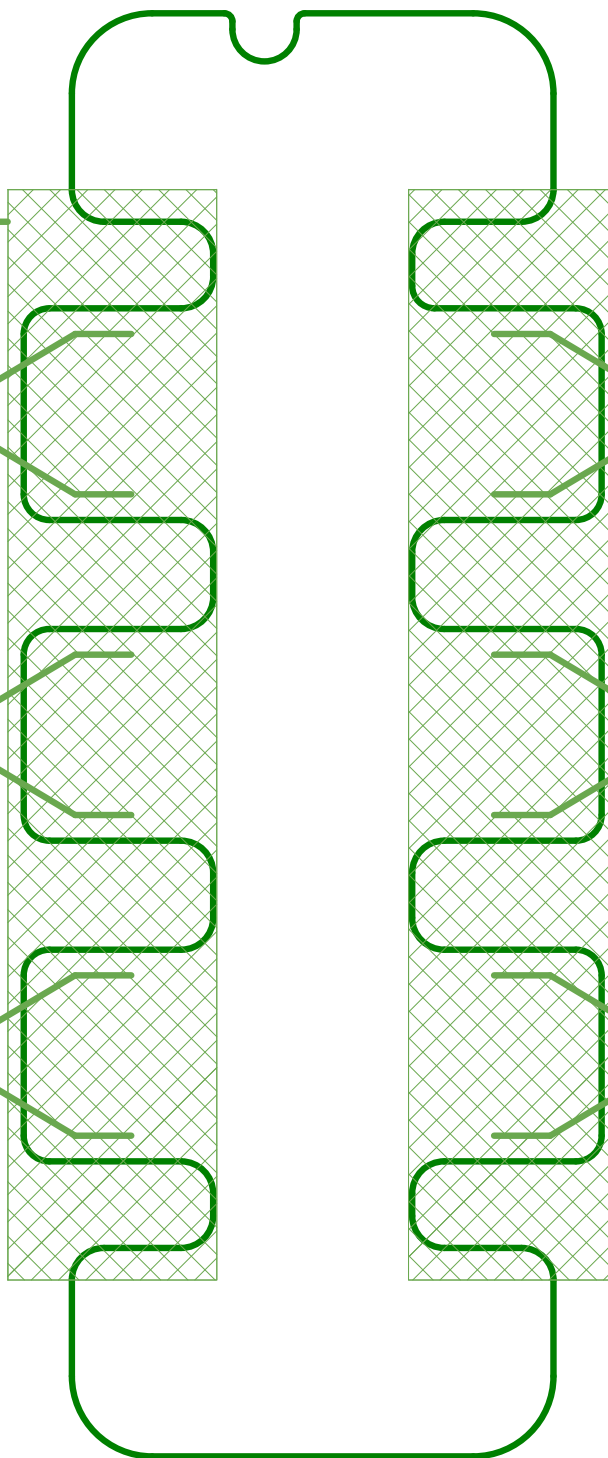
non-plated slots x2

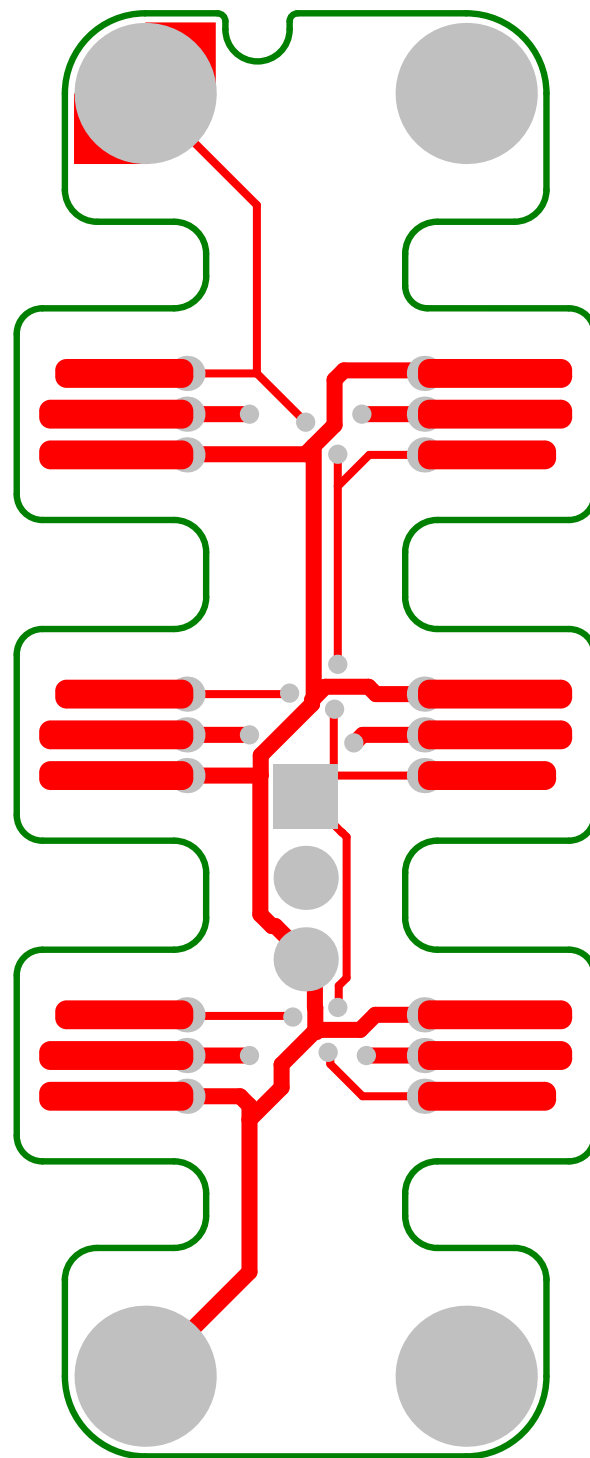
non-plated slots x2

non-plated slots x2

non-plated slots x2

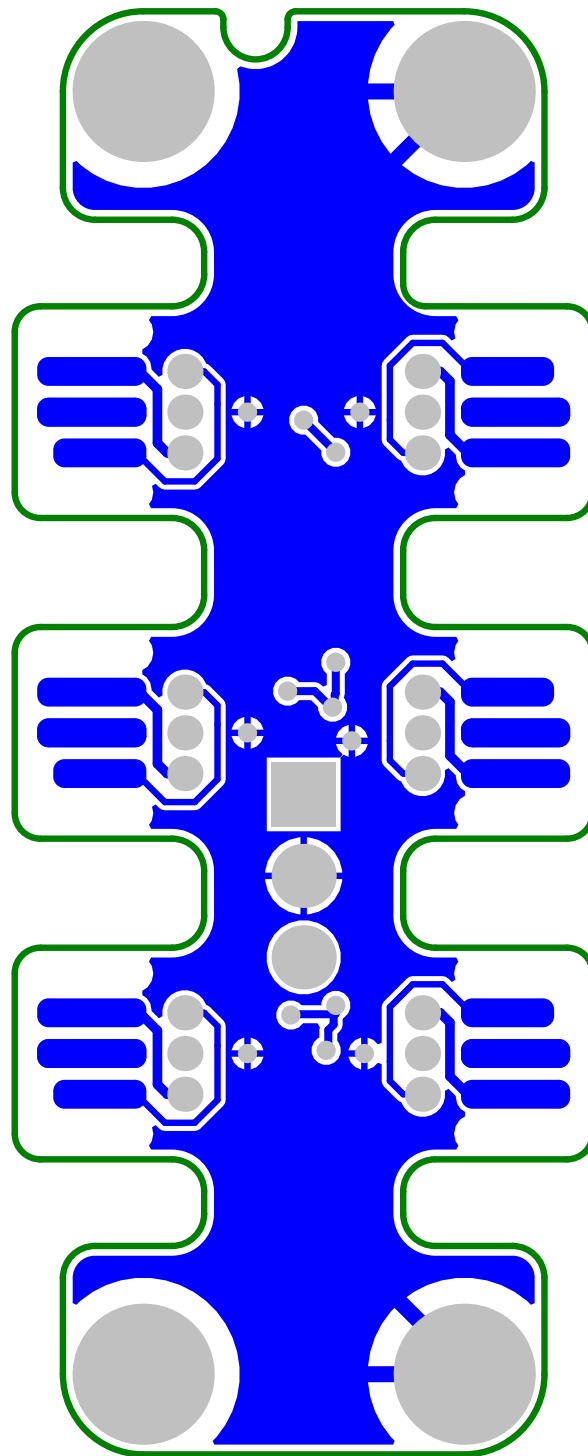
non-plated slots x2





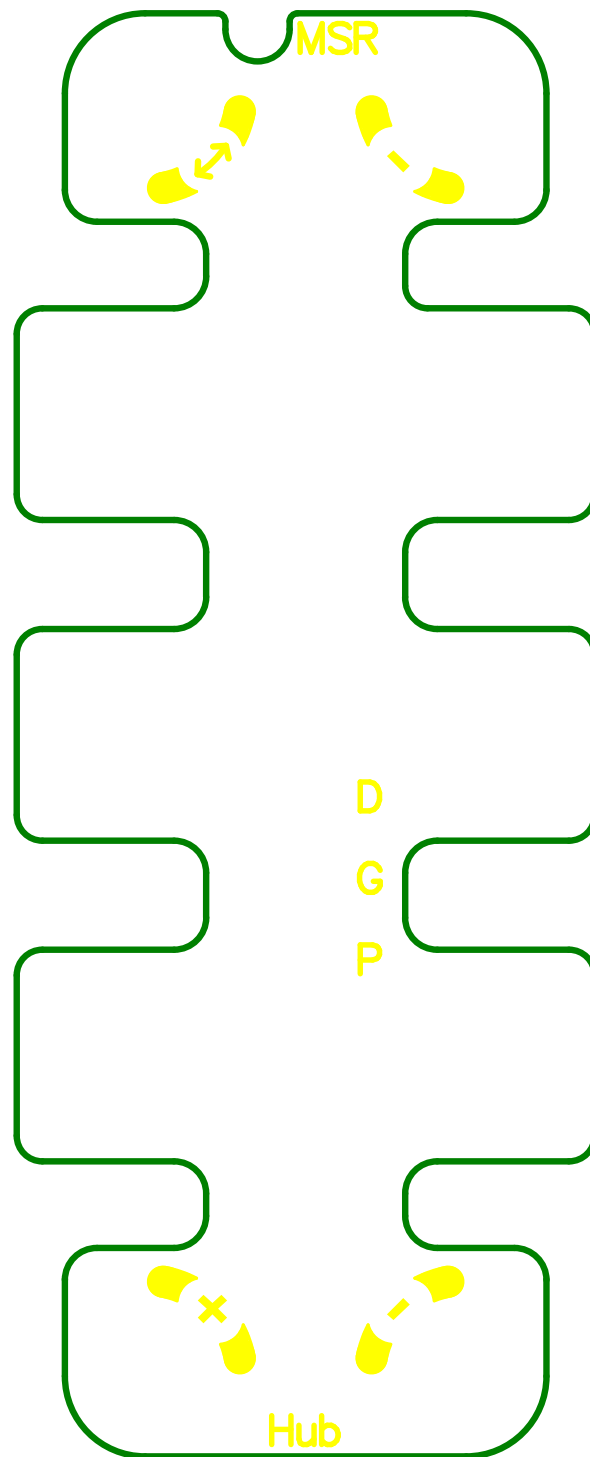
Top Layer

Board Outline

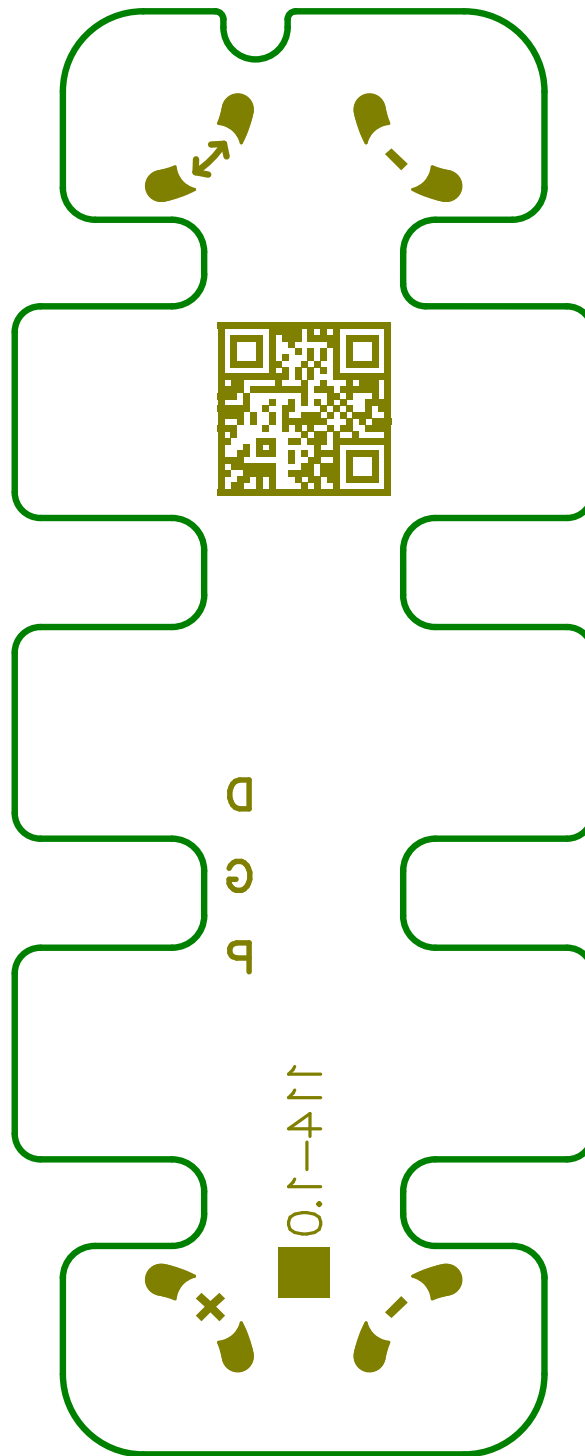


Bottom Layer

Board Outline

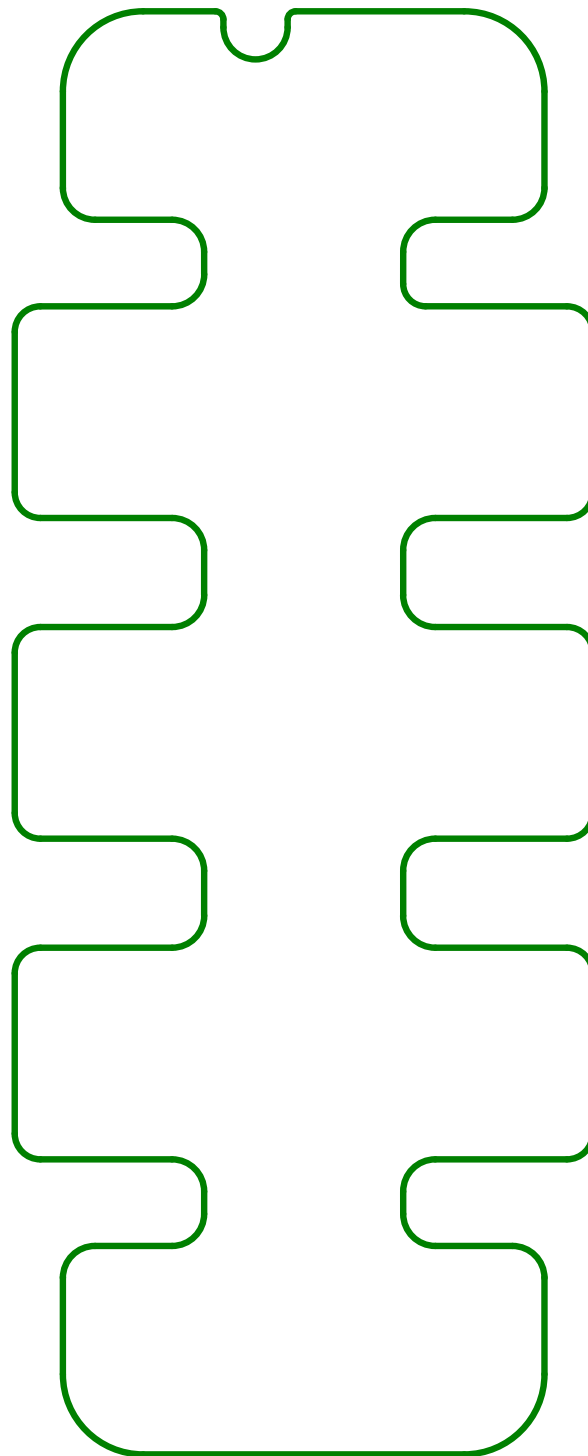


Board Outline



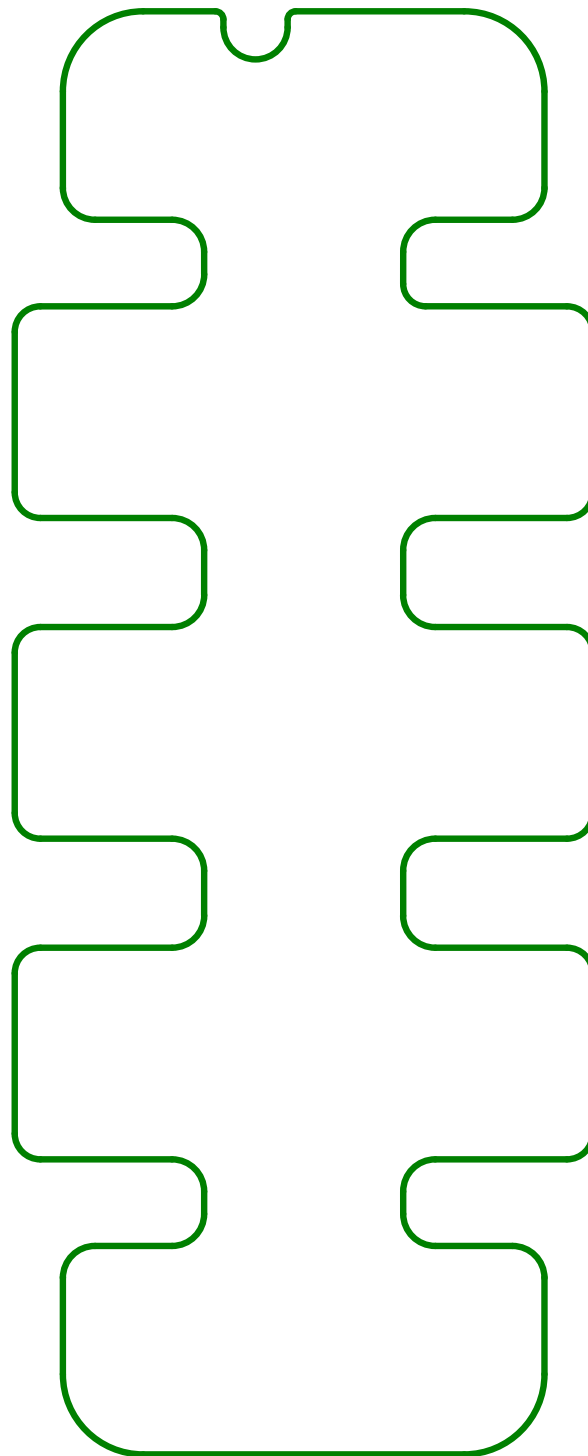
Bottom Overlay

Board Outline



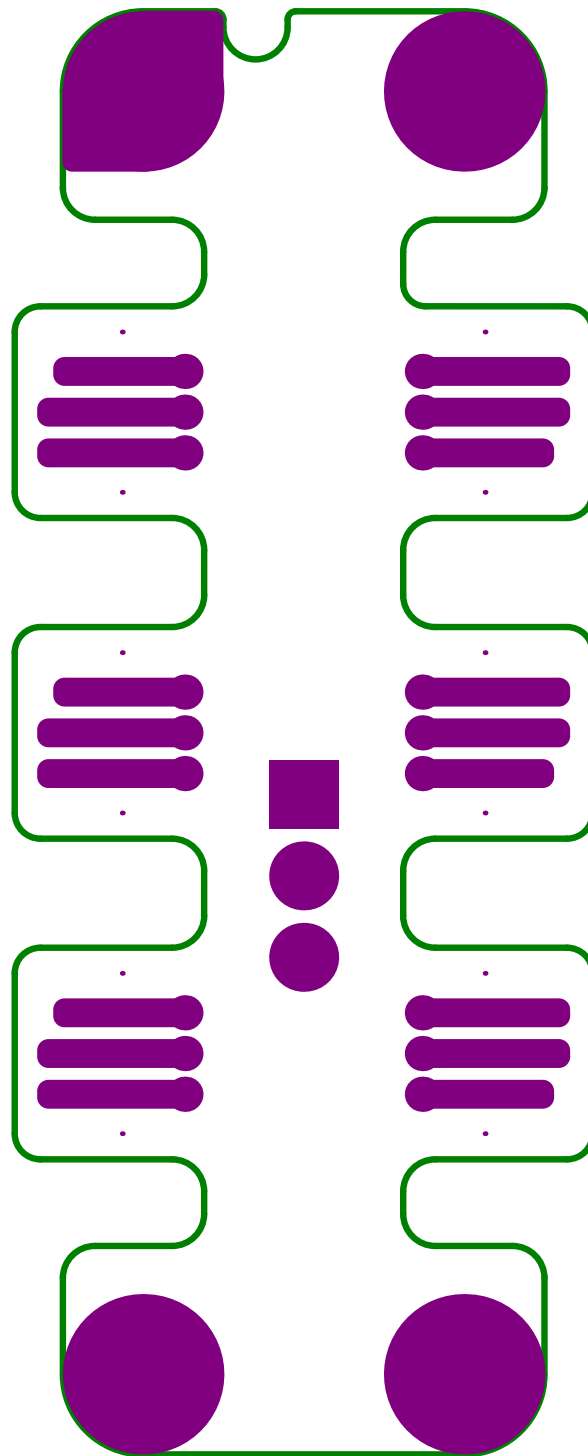
Top Paste

Board Outline



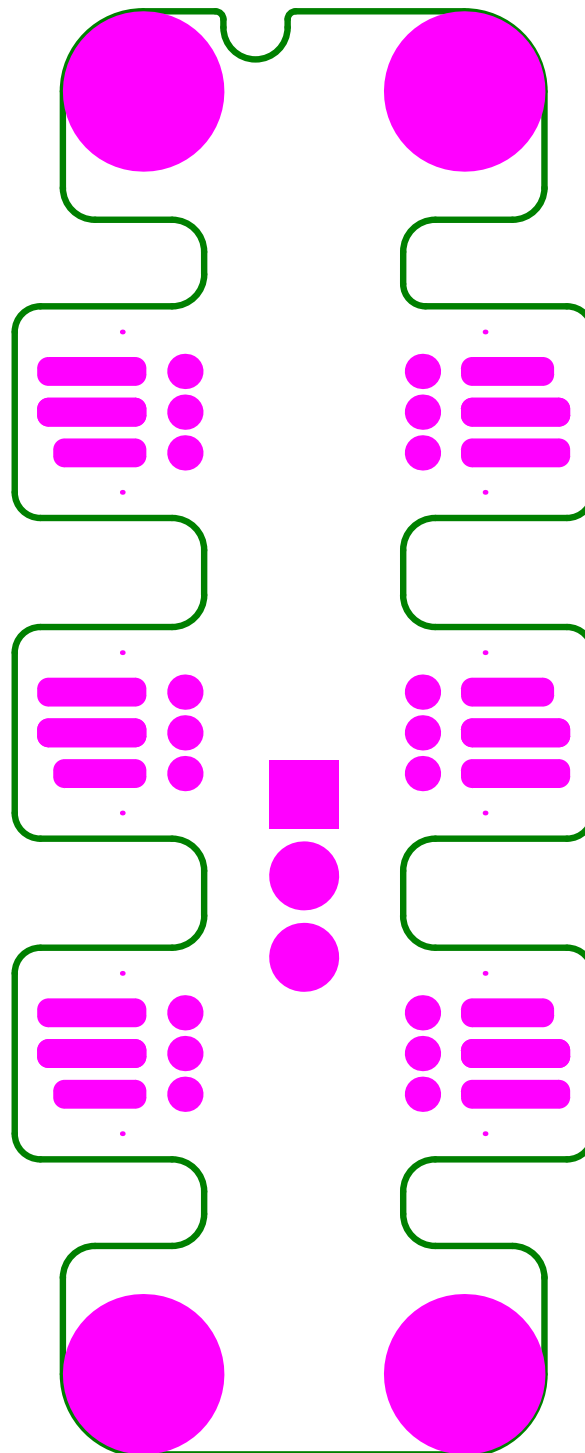
Bottom Paste

Board Outline



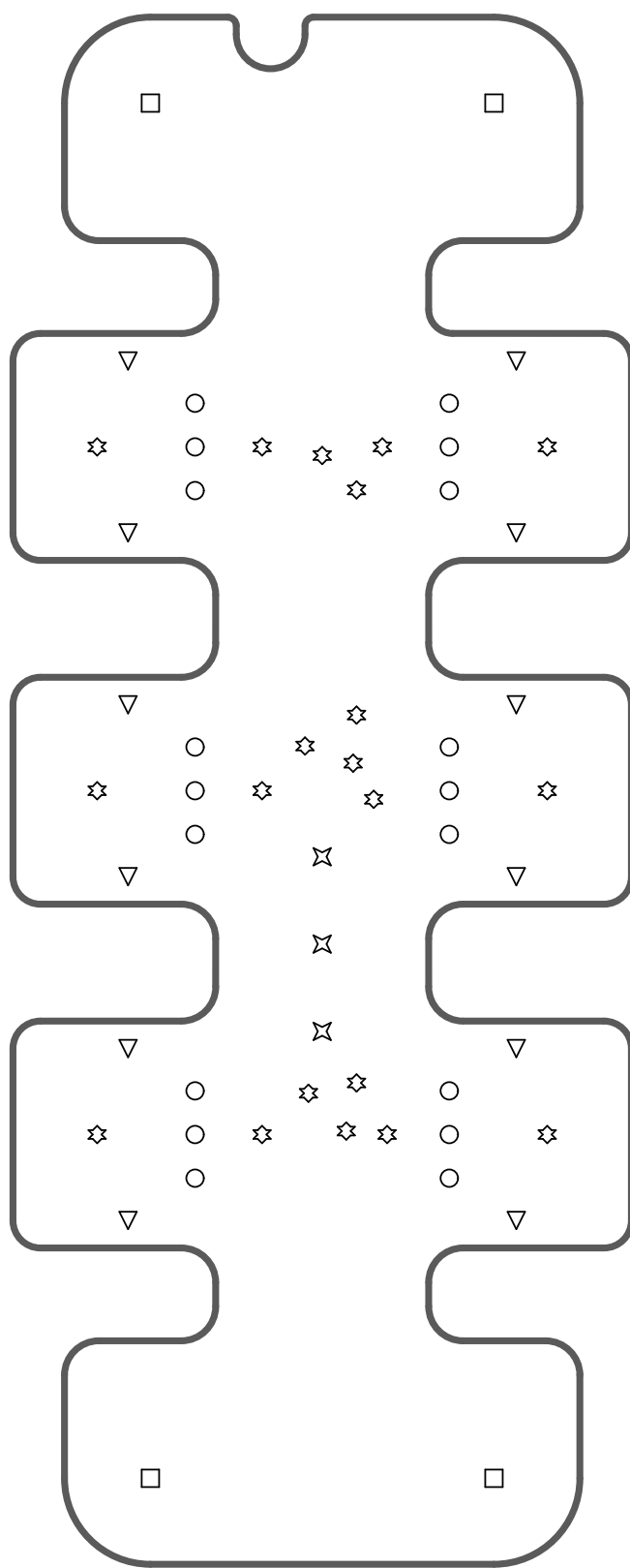
Board Outline

Top Solder (resist)



Board Outline

Bottom Solder (resist)



Board Outline

Drill Drawing