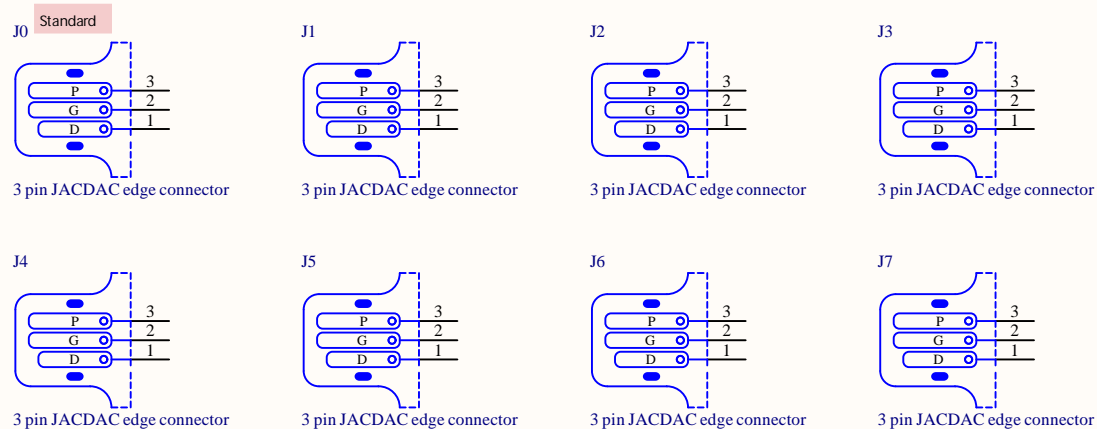
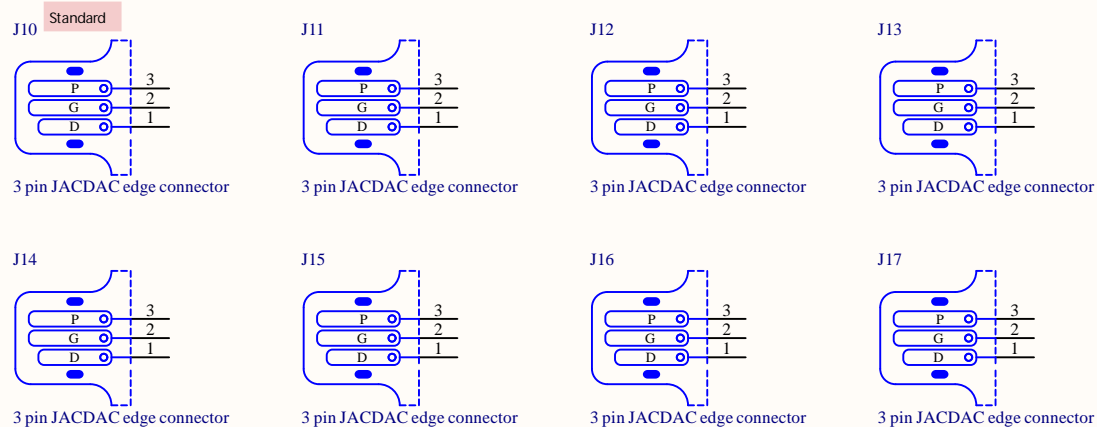


## Jacdac connectors with different tongue widths



## Jacdac connectors with different slot depths



This information is provided "as-is". You bear the risk of using it. Some information relates to pre-released specification which may change without notice. Microsoft makes no warranties, express or implied, with respect to the information provided here.

Microsoft

PROJECT DESCRIPTION  
Jacdac PCB edge connector dimension test

SHEET DESCRIPTION  
Complete design

PROJECT FILENAME JacdacEdgeConnectorDimsTest 92.PrjPCB

PROJECT CODENAME JacdacEdgeConnectorDimsTest

LAST MODIFIED 06/03/2022

PAGE 1 OF 1

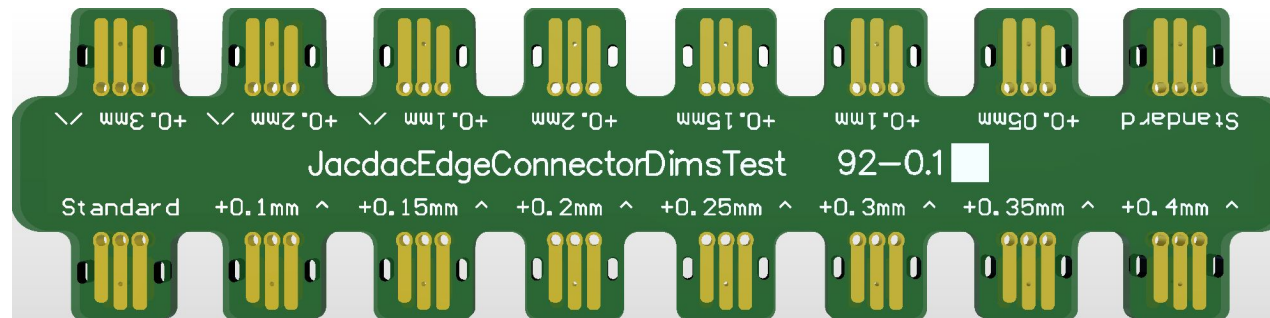
DRAWN BY S. Hodges

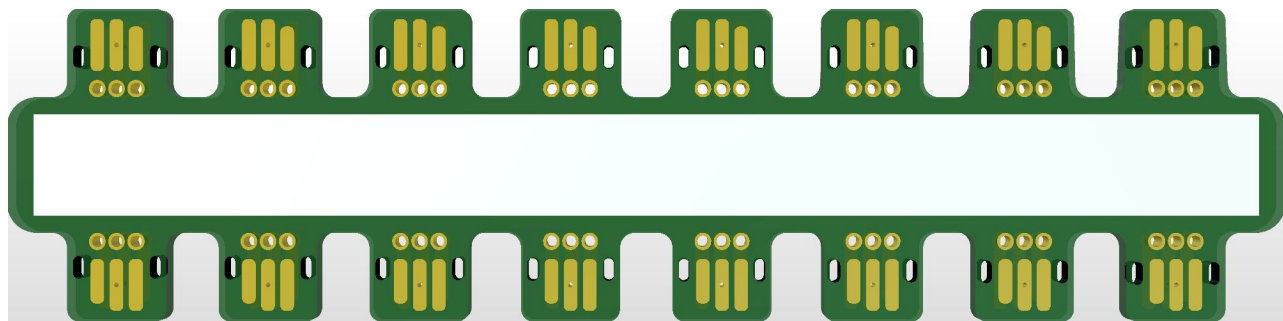
SHEET FILENAME JacdacEdgeConnectorDimsTest 92.SchDoc

LICENCE Attribution 4.0 International (CC BY 4.0)

REVISION 0.1

PCB ID 92-0.1





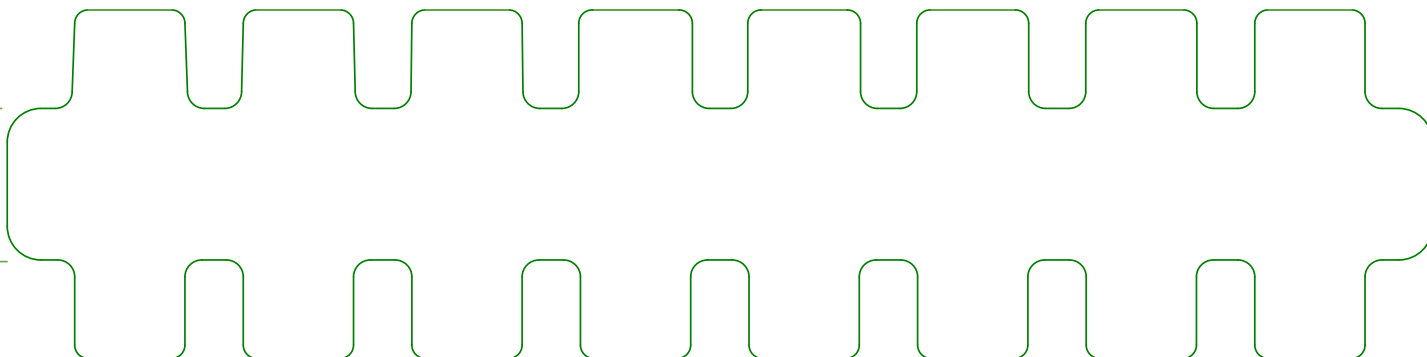
Fabrication Notes  
Board Outline

no board edge pips/  
rat's teeth/mouse bites  
above this line - make  
sure board edge is clean

non-plated 0.7mm wide slots x16 ->

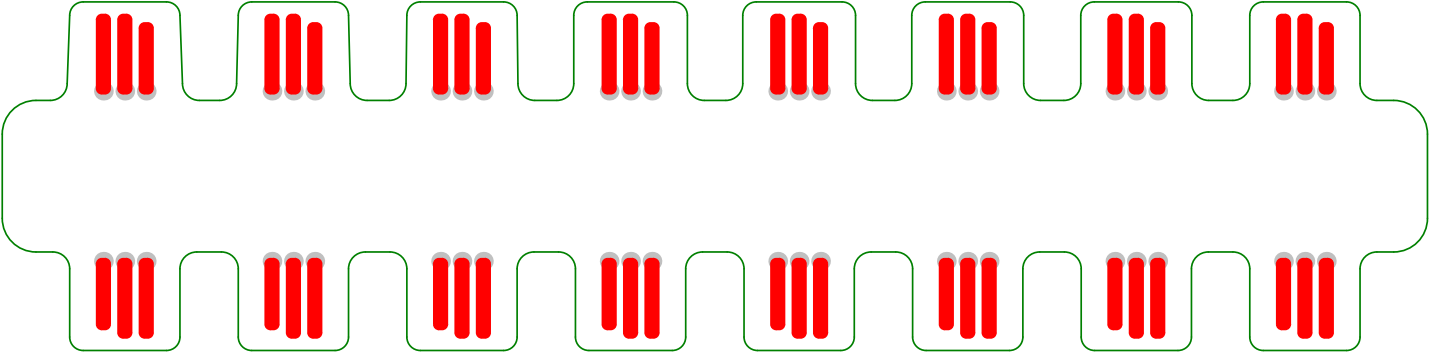
no board edge pips/  
rat's teeth/mouse bites  
below this line - make  
sure board edge is clean

non-plated 0.7mm wide slots x16 ->



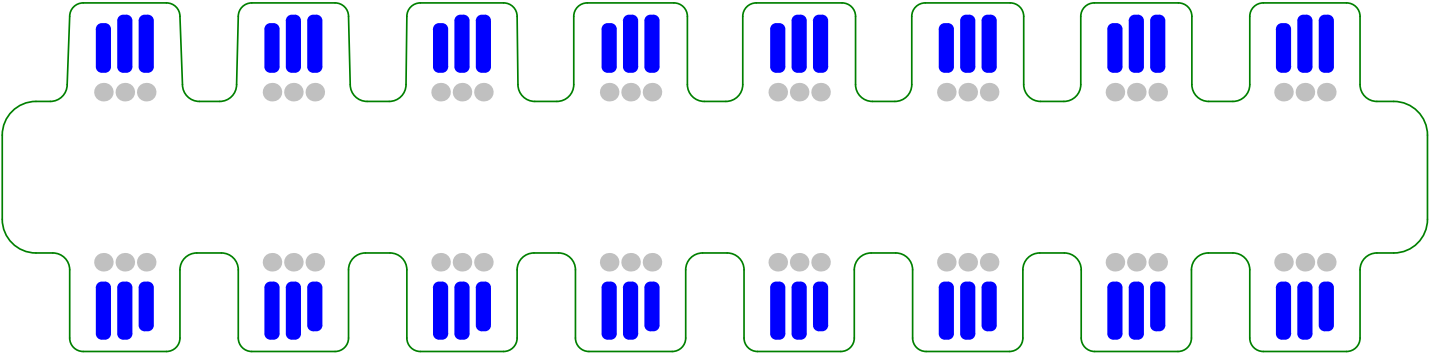
Top Layer

Board Outline



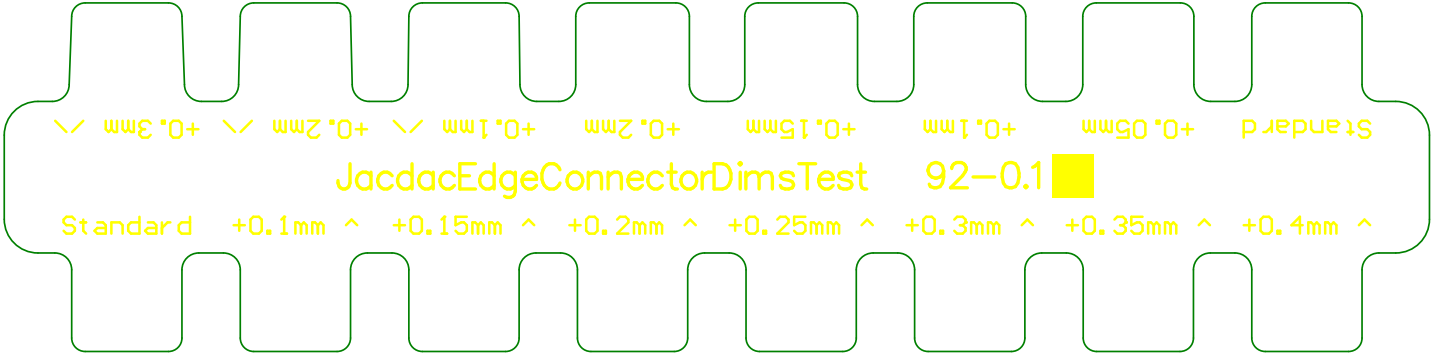
Bottom Layer

Board Outline



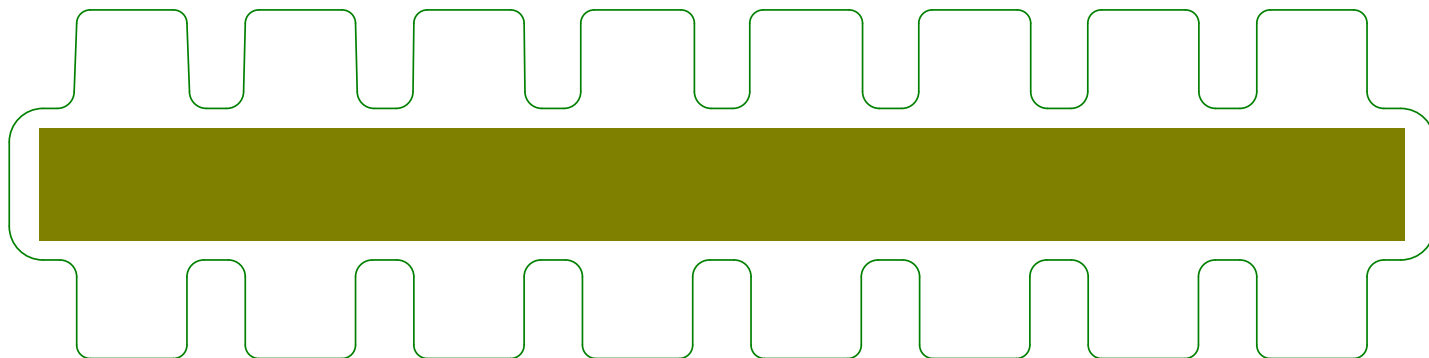
Board Outline

Top Overlay



Board Outline

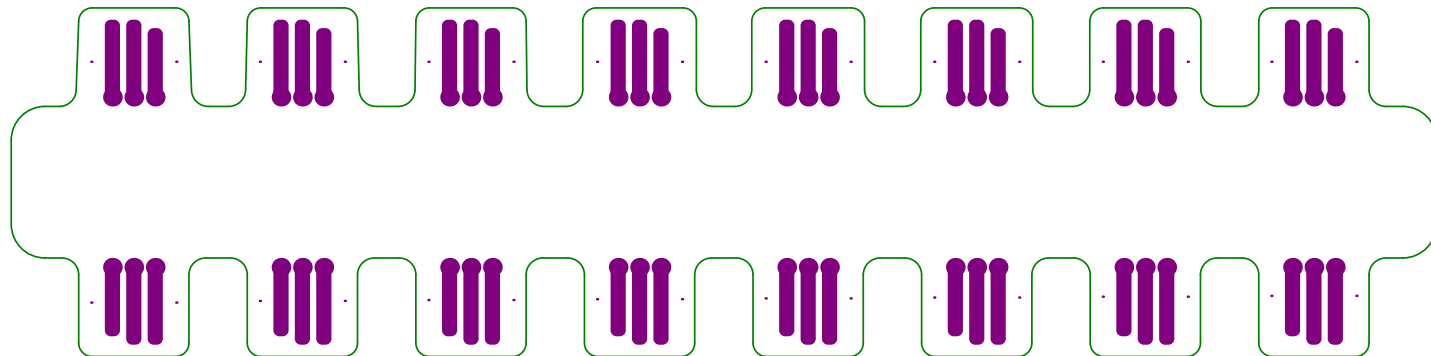
Bottom Overlay





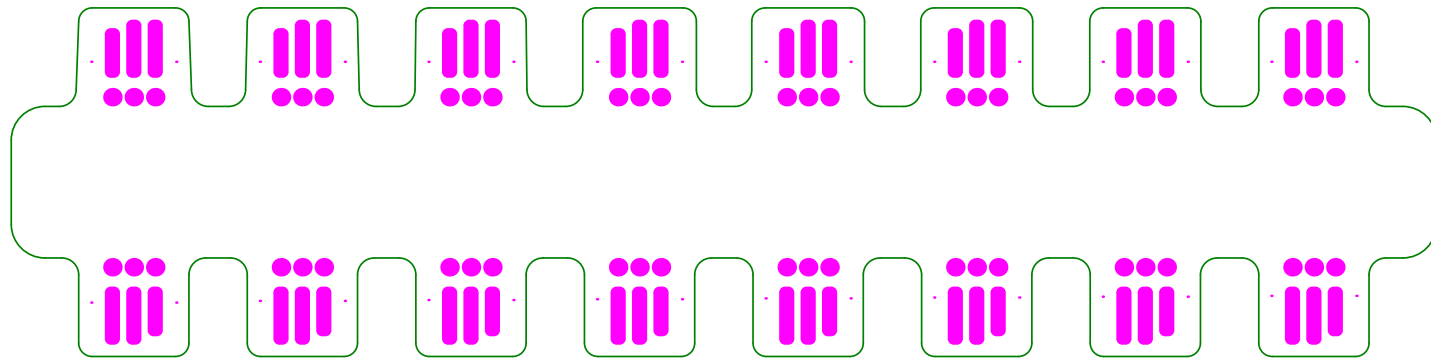
Board Outline

Top Solder (resist)



Board Outline

Bottom Solder (resist)



Board Outline

