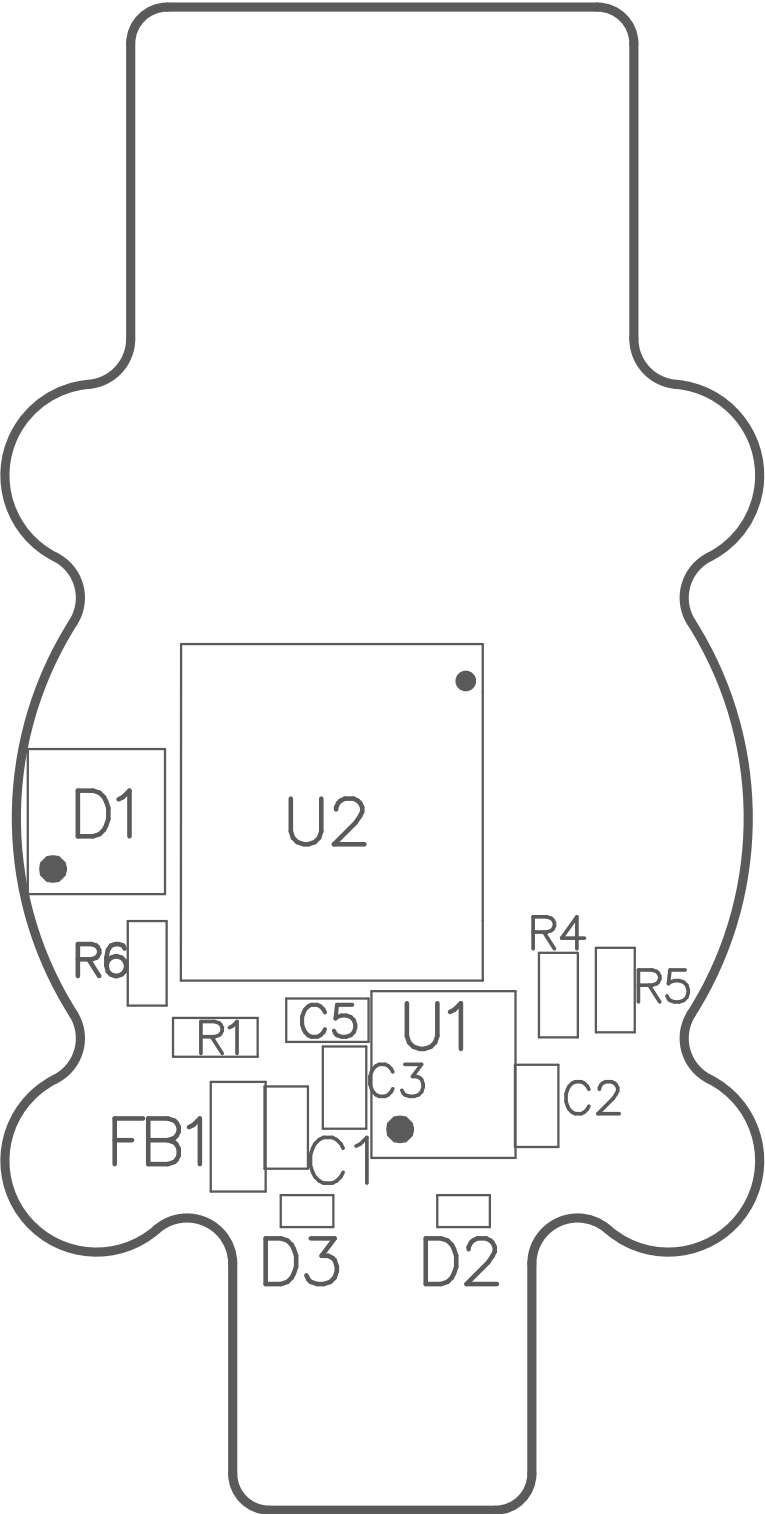


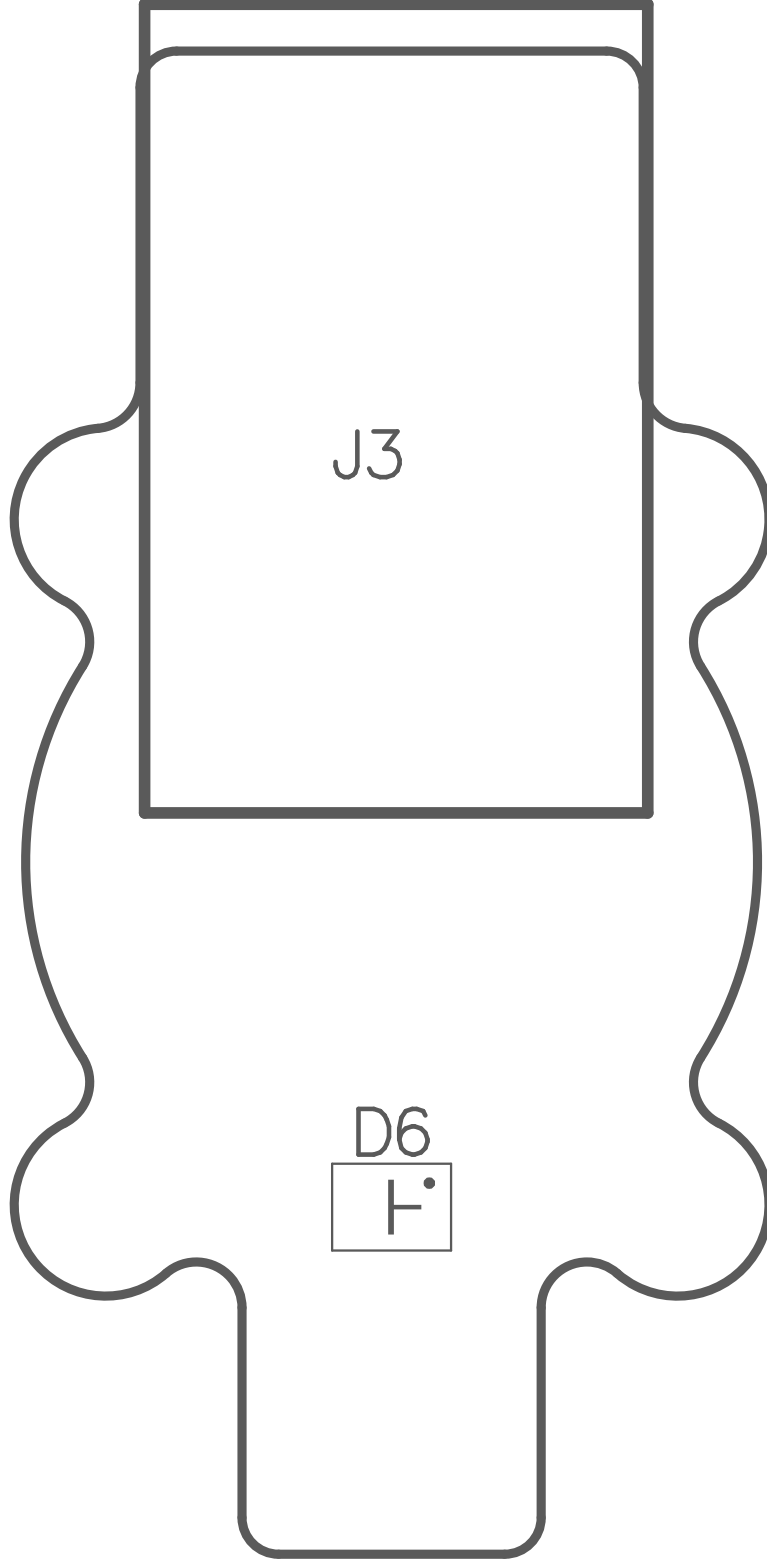
Board Outline

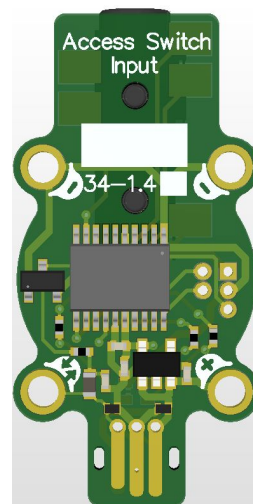
Top Assy

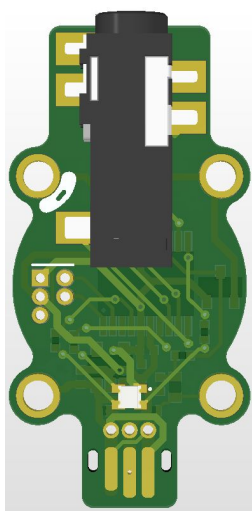


Board Outline

Bottom Assy







Fabrication Notes Board Outline



place any PCB manufacturer identifiers
in this region on top layer,
so they will be underneath the TSSOP

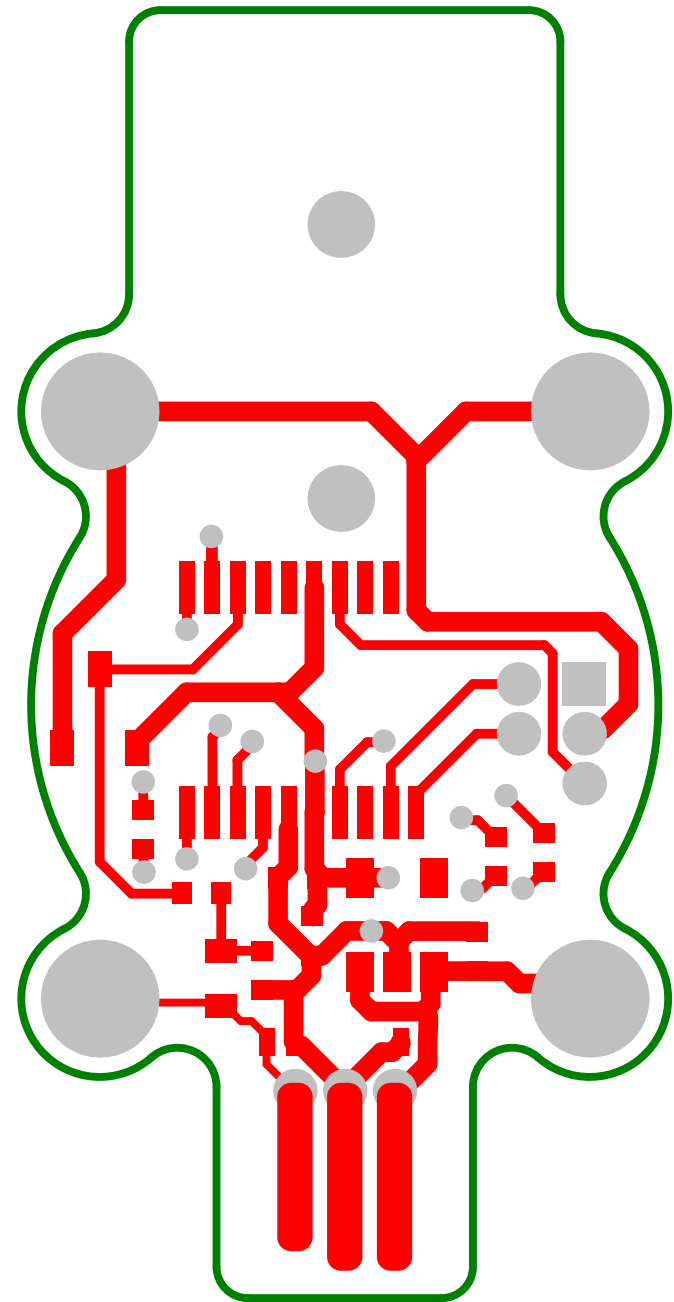
no board edge pips/
rat's teeth/mouse bites
below this line - make
sure board edge is clean

non-plated slots x2



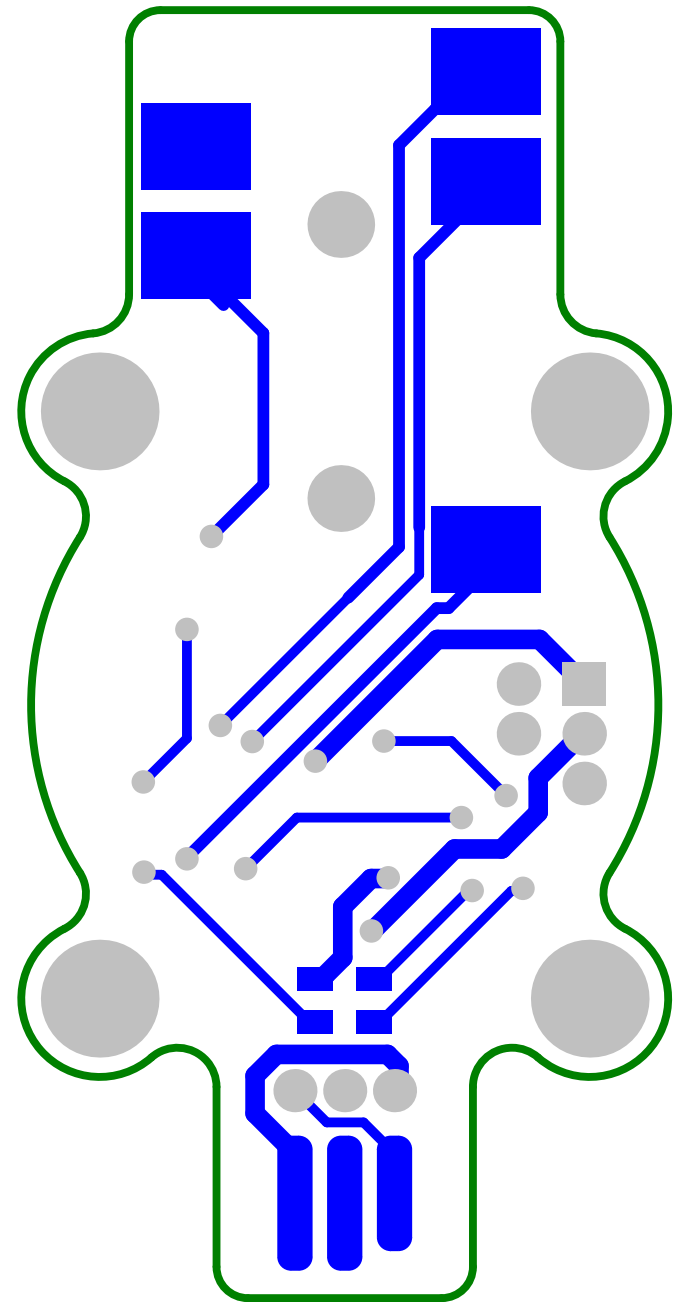
Top Layer

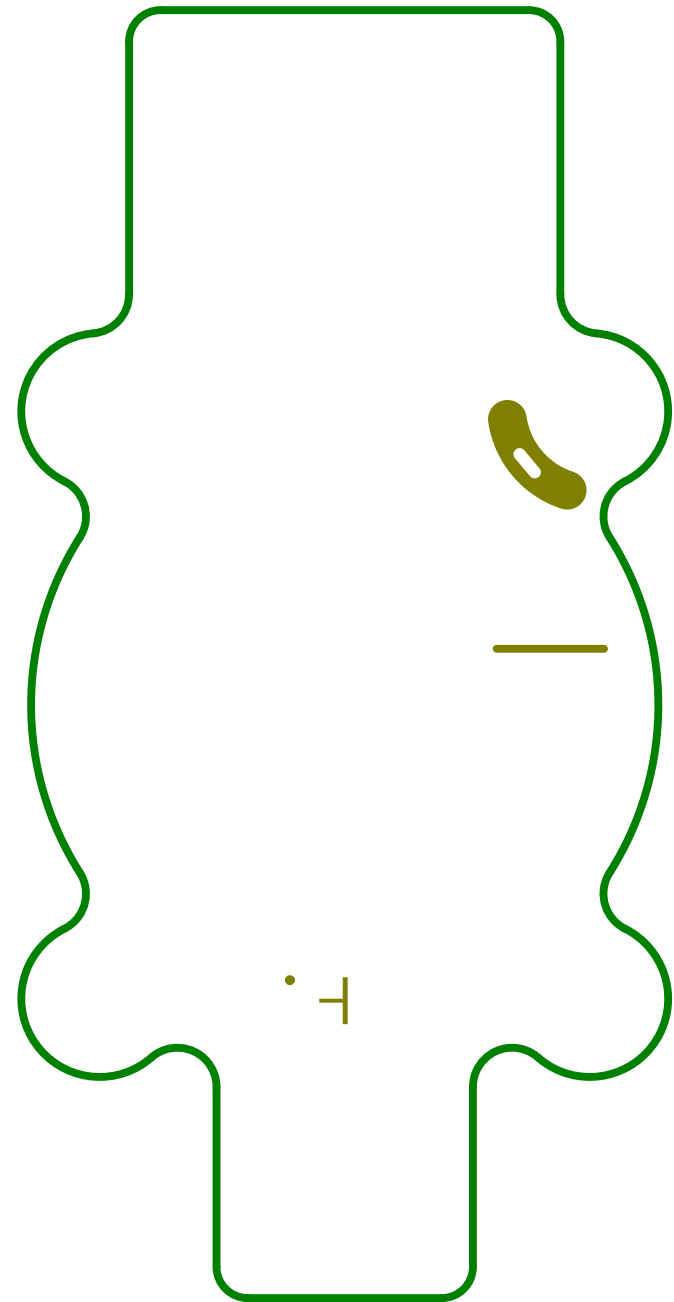
Board Outline



Bottom Layer

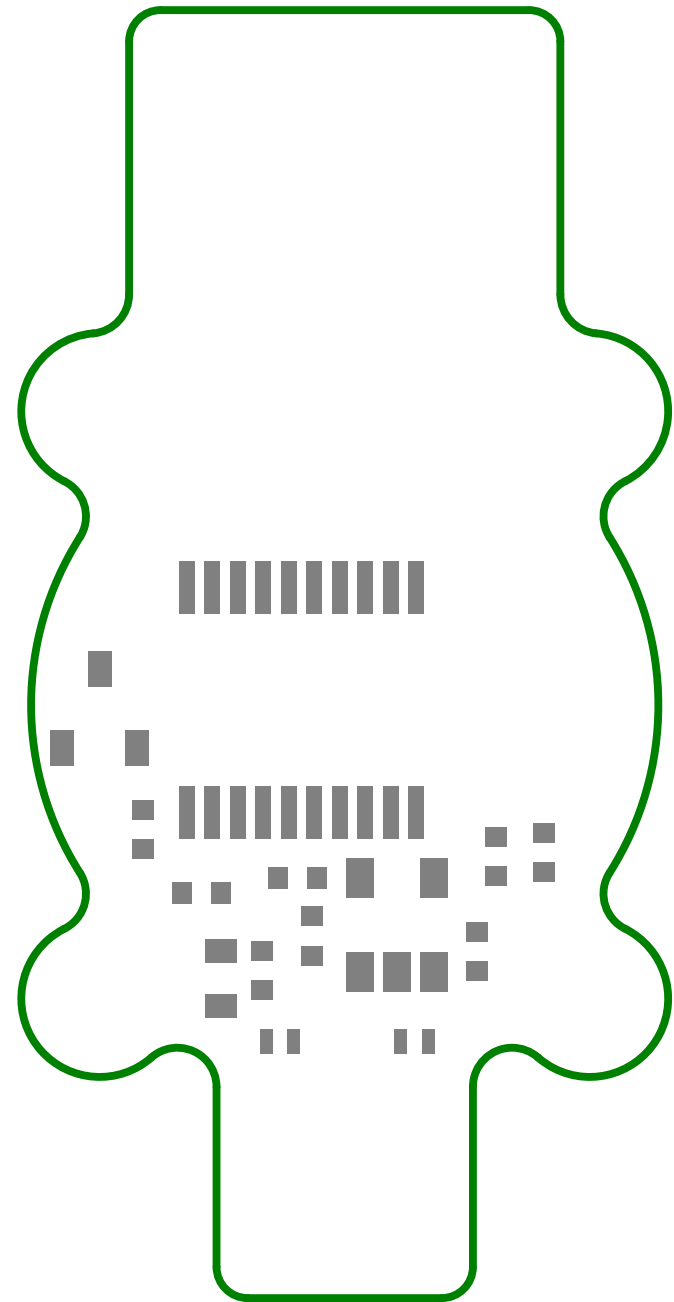
Board Outline





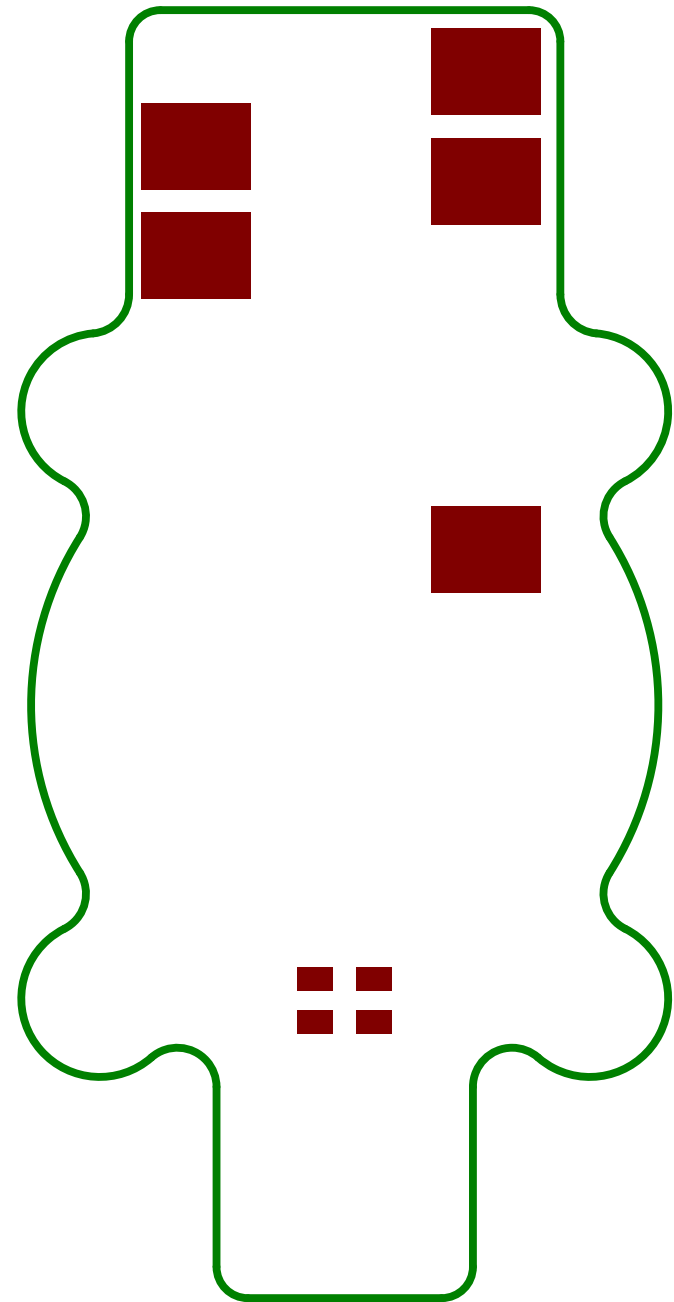
Board Outline

Bottom Overlay



Board Outline

Top Paste

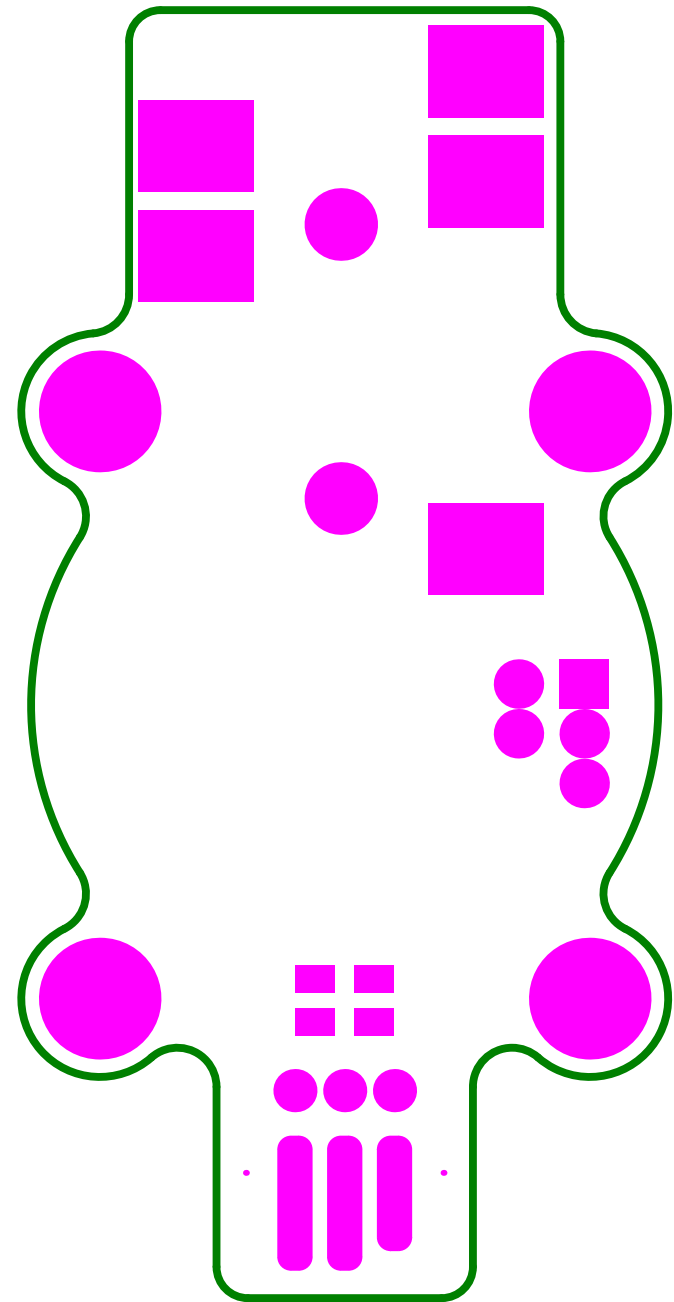


Board Outline

Bottom Paste

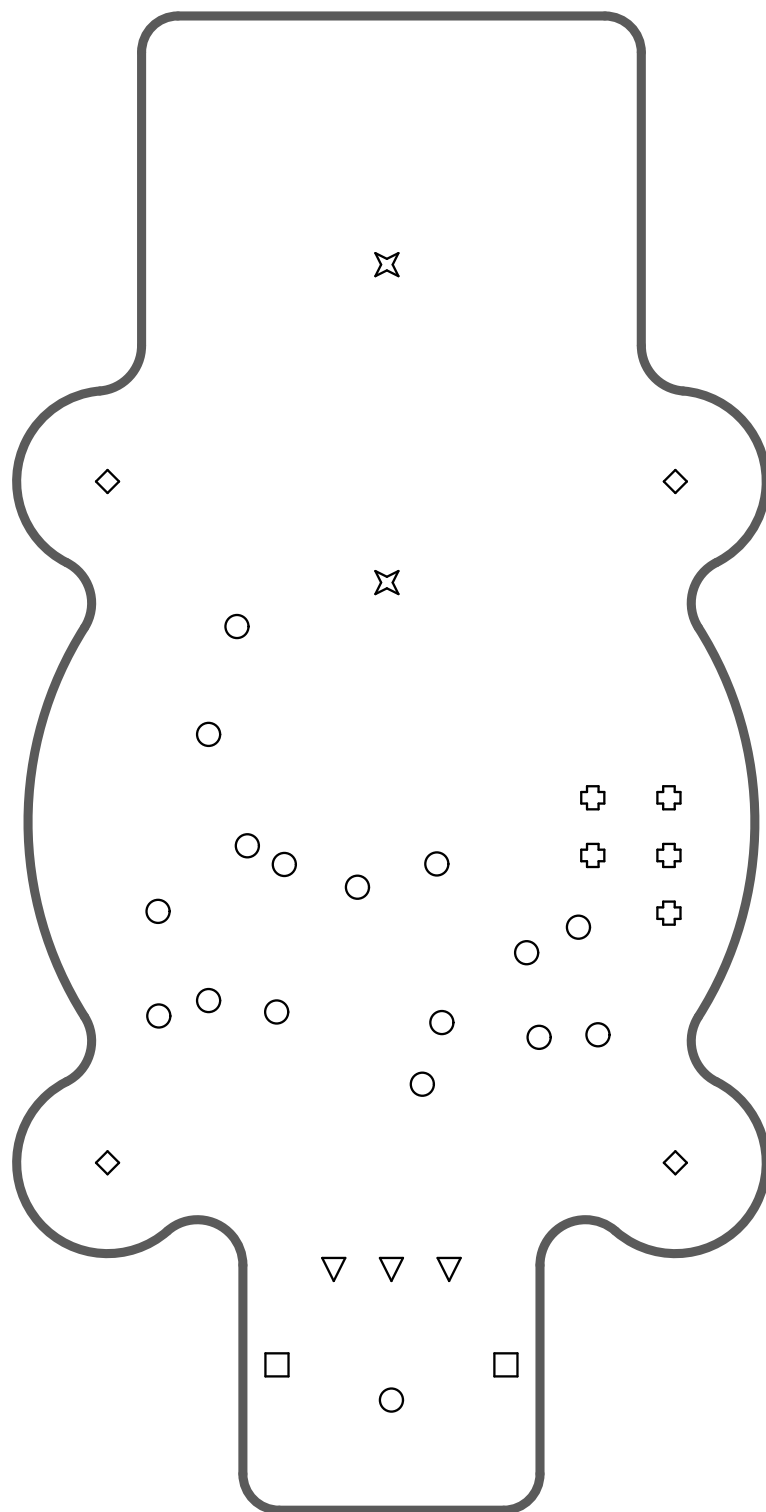
Board Outline

Top Solder (resist)



Board Outline

Bottom Solder (resist)



Board Outline