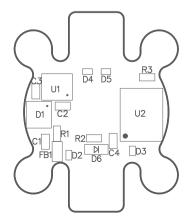
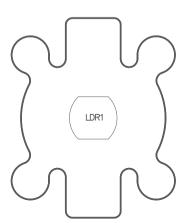
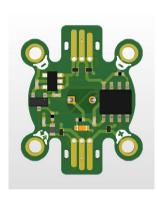


Board Outline Top Assy



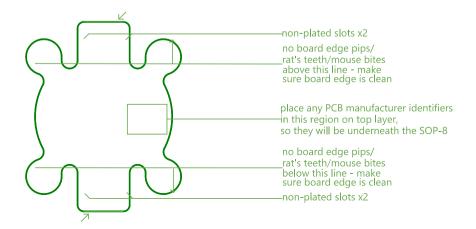
Board Outline Bottom Assy



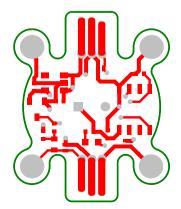




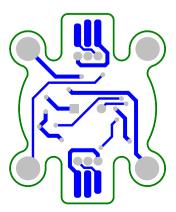
## Fabrication Notes Board Outline



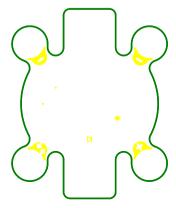
Top Layer



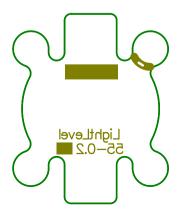
**Bottom Layer** 



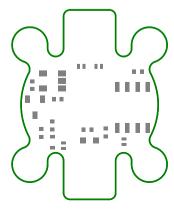
Top Overlay



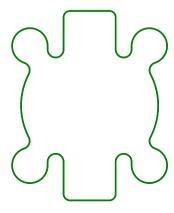
Bottom Overlay



Top Paste



**Bottom Paste** 



Top Solder (resist)

