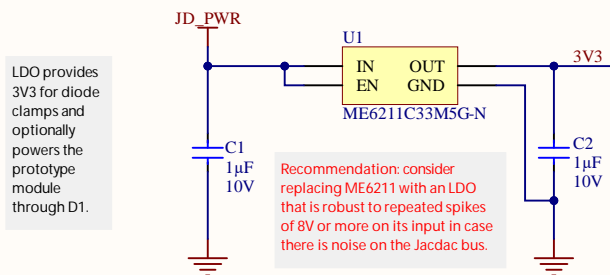


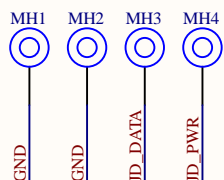
3V3 Regulator

This component is a power-consumer.



U1 alternative parts:
ME6212C33M5G 6V 260mV @ 200mA Iout 350mA
NCP114ASN330T1G 5.5V 225mV @ 300mA Iout 300mA
NCP114BSN330T1G 5.5V 225mV @ 300mA Iout 300mA
...and many other LDOs.

Mounting holes

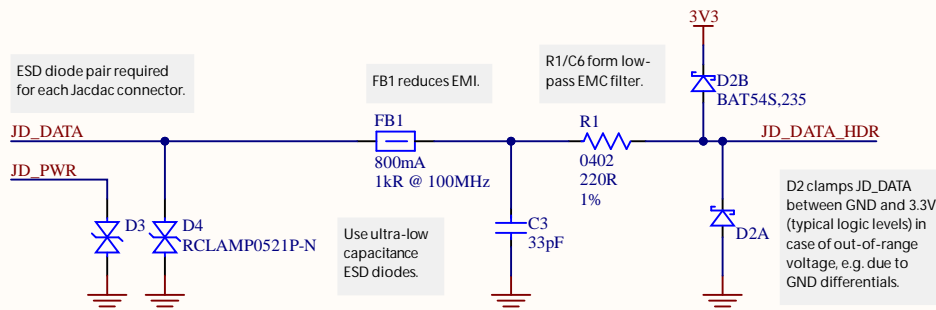
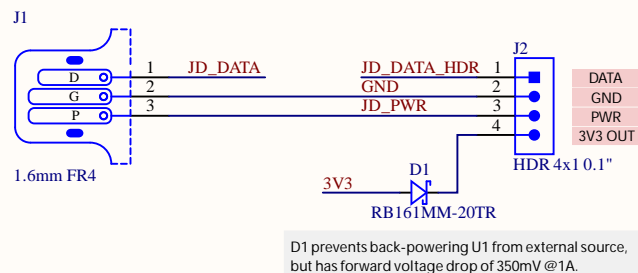


Mounting holes are electrically connected to the Jacdac bus nets so they can be used as an alternative to the PCB edge connector. Please use the following reference designators and net mapping:

MH1 & MH2: GND
MH3: JD_DATA
MH4: JD_PWR

This design uses Jacdac 'small' mounting holes: PTH with finished diameter of 2.1mm, annular copper ring of 3.0mm diameter & copper/component keepout of 5.0mm. The mounting holes must be on 2.5mm pitch. Mounting holes should have appropriate silkscreen marker, and MH1 should have a pin 1 marker on the top side.

Jacdac interface



This reference design is a guideline. Please refer to the Jacdac docs online at <https://aka.ms/jacdac> for the definitive and most up-to-date information.

Silkscreen should include text to identify the module type and revision, and optionally a QR code.

This design uses a 'cute' board shape.

Silkscreen & layout notes

Block name

Design notes

When this PDF is viewed with Adobe Reader, clicking on components shows part numbers and other details.

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Microsoft

PROJECT DESCRIPTION
Jacdac bus breakout for module prototyping

SHEET DESCRIPTION
Complete design

PROJECT FILENAME JacdacPinHeaders 45.PrjPCB

PROJECT CODENAME JacdacPinHeader

SHEET FILENAME JacdacPinHeaders 45.SchDoc

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DRAWN BY GD, JD, SH

REVISION 0.3 PCB ID 45-0.3