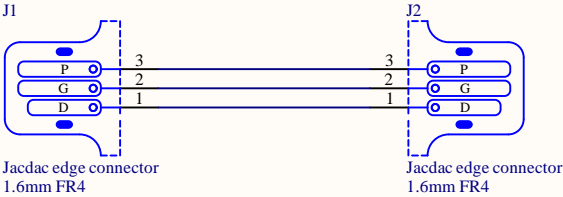
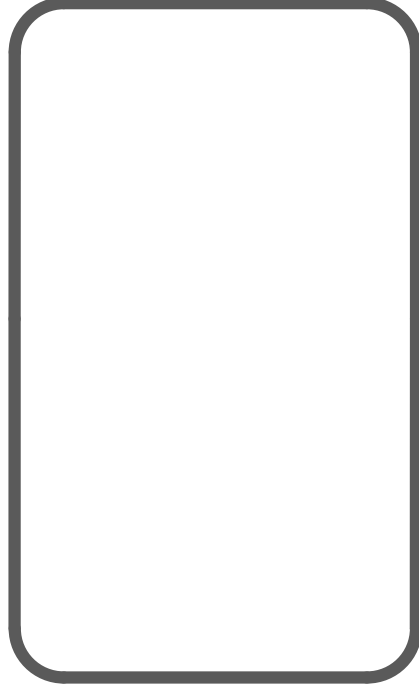
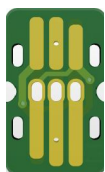


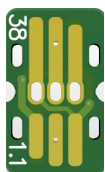
1	2	3	4
A			A
B			B
C			C
D			D
<div>Jacdac connector</div> <div></div> <div><div>Key:</div><div>TO-DO</div><div>Silkscreen &amp; layout notes</div><div>Block name</div><div>Design notes</div></div>			
PROJECT FILENAME JacdacCableExtender 38.PrjPCB	PROJECT CODENAME JacdacCableExtender	MicrosoftPROJECT DESCRIPTIONDouble-ended Jacdac cable extender	
SHEET FILENAME JacdacCableExtender 38.SchDoc	LICENCE Attribution 4.0 International (CC BY 4.0)	LAST MODIFIED 21/12/2021PAGE 1 OF 1DRAWN BY S.Hodges	SHEET DESCRIPTIONComplete designREVISION 1.1PCB ID 38-1.1
1	2	3	4

Board Outline  
Top Assy



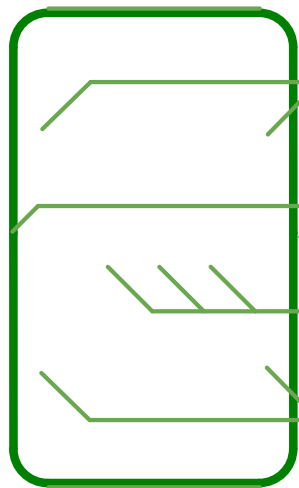
Board Outline  
Bottom Assy





do not add any PCB  
manufacturer identifiers  
no board edge pips/  
rat's teeth/mouse bites

## Fabrication Notes Board Outline

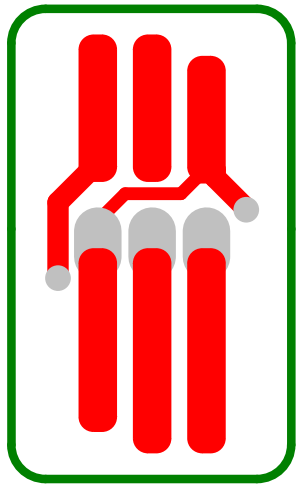


non-plated slots x2

non-plated drill holes that cross the  
board outline will end up as half-holes

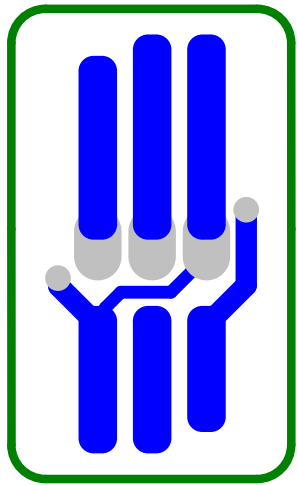
plated slots x3

non-plated slots x2



Top Layer

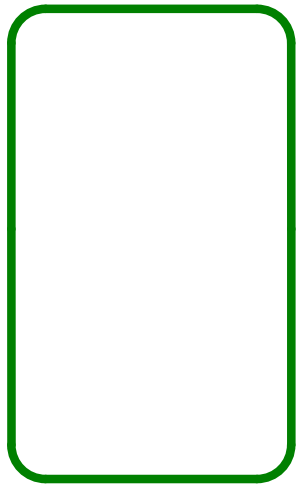
Board Outline



Bottom Layer

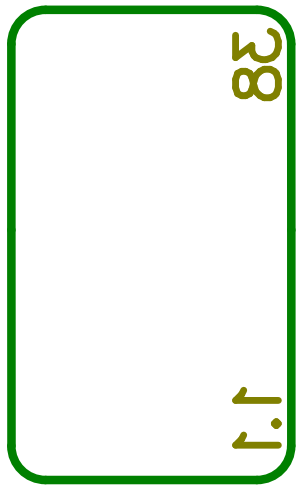
Board Outline





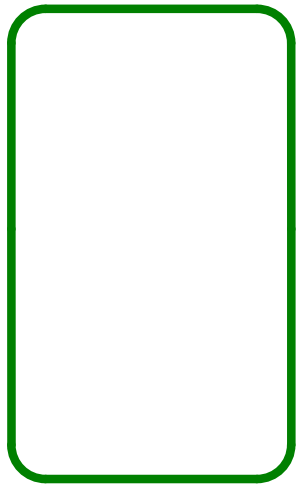
Board Outline

Top Overlay



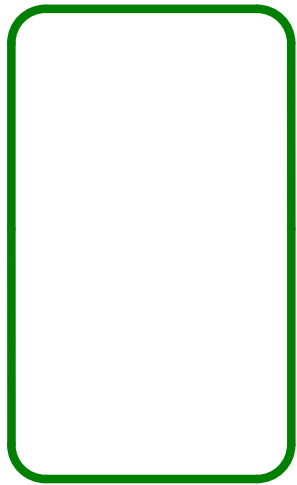
Board Outline

Bottom Overlay



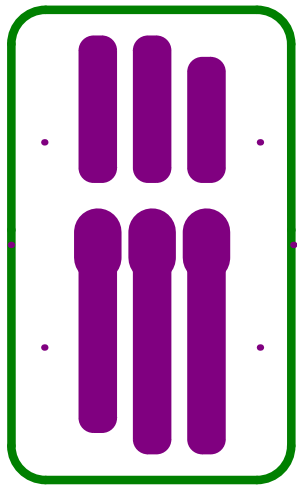
Board Outline

Top Paste



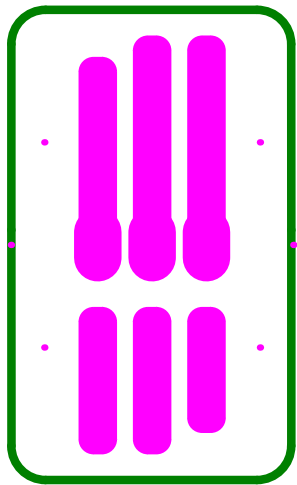
Board Outline

Bottom Paste



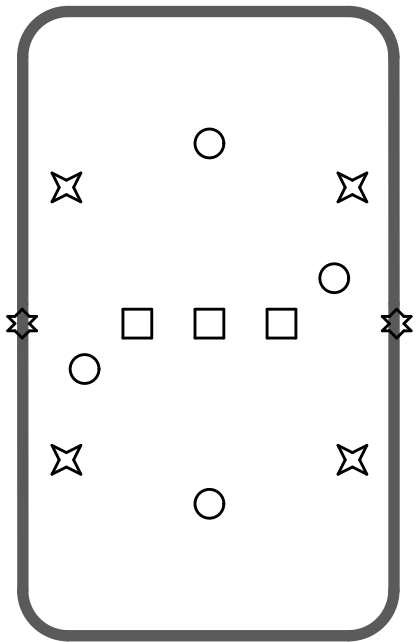
Board Outline

Top Solder (resist)



Board Outline

Bottom Solder (resist)



Board Outline