# 112-1 Electrical Engineering Fundamentals I

Quiz 6

Keys

# 1. 30% Obtain $\mathbf{i}_1$ and $\mathbf{i}_2$ for t > 0 in the circuit of Figure 1.

#### At t < 0, 4u(t) = 0, L shorted

$$i_1(0^-) = i_2(0^-) = 0$$
 (A)

At 
$$t > 0$$
,  $4u(t) = 4$  (A)

Nodal analysis:  

$$4 = \frac{1}{2} \times 1 \times \frac{di_1}{dt} + i_1 + i_2$$

$$\Rightarrow \frac{di_1}{dt} + 2i_1 + 2i_2 = 8$$

$$\Rightarrow \frac{d^2i_1}{dt^2} + 2\frac{di_1}{dt} + 2\frac{di_2}{dt} = 0$$

$$i_2 = \frac{v_{L1} - v_{L2}}{3}$$

$$\Rightarrow 3i_2 = \frac{di_1}{dt} - \frac{di_2}{dt}$$

$$\Rightarrow \frac{di_2}{dt} = \frac{di_1}{dt} - 3i_2$$

$$= \frac{di_1}{dt} - 3\left(4 - \frac{1}{2}\frac{di_1}{dt} - i_1\right)$$

$$\frac{d^2i_1}{dt^2} + 2\frac{di_1}{dt} + 2\frac{di_2}{dt} = 0$$

$$\Rightarrow \frac{d^2i_1}{dt^2} + 2\frac{di_1}{dt} + 2\left(\frac{di_1}{dt} - 12 + \frac{3}{2}\frac{di_1}{dt} + 3i_1\right) = 0$$

$$\Rightarrow \frac{d^2i_1}{dt^2} + 7\frac{di_1}{dt} + 6i_1 = 24$$

$$\Rightarrow s^2 + 7s + 6 = 0$$

$$\Rightarrow s = -1 - 6$$

$$i_{1}(0) = i_{1}(0^{+}) = i_{1}(0^{-}) = 0 \text{ (A)}$$

$$i_{2}(0) = i_{2}(0^{+}) = i_{2}(0^{-}) = 0 \text{ (A)}$$

$$\frac{di_{1}}{dt} + 2i_{1} + 2i_{2} = 8 \Rightarrow \frac{di_{1}}{dt} = 8 - 2i_{1} + 2i_{2}$$

$$\Rightarrow \frac{di_{1}(0)}{dt} = 8 - 2i_{1}(0) + 2i_{2}(0) = 8 \text{ (A/s)}$$

$$i_{1}(\infty) = 4(A) \Rightarrow I_{1SS} = 4(A)$$

$$i_{1}(\infty) = 4(A) \Rightarrow I_{1SS} = 4(A)$$

$$i_{1}(t) = I_{1SS} + Ae^{S_{1}t} + Ae^{S_{2}t} = 4 + Ae^{-t} + Be^{-6t}$$

$$\frac{di_{1}}{dt} = -Ae^{-t} - 6Be^{-6t}$$

$$i_{1}(0) \to 4 + A + B = 0 \Rightarrow A + B = -4$$

$$\frac{di_{1}(0)}{dt} \to -A - 6B = 8$$

$$i_{1}(t) = 4 - 3.2e^{-t} - 0.8e^{-6t} (A)$$

$$\frac{di_1}{dt} + 2i_1 + 2i_2 = 8 \Rightarrow i_2 = 4 - i_1 - 0.5 \frac{di_1}{dt}$$
$$i_2(t) = 4 - (4 - 3.2e^{-t} - 0.8e^{-6t}) - 0.5(3.2e^{-t} + 4.8e^{-6t})$$

Figure 1

$$i_2(t) = 1.6e^{-t} - 1.6e^{-6t}$$
  
= 1.6(  $e^{-t} - e^{-6t}$ )(A)

## 2. 25% Find $\alpha$ and $\omega_0$ and calculate V(t) for t > 0 in the circuit of Fig. 2.

### At t < 0, sw closed, L shorted & C opened

$$v_C(0^-) = 24 \times \frac{60 / (15 + 25)}{12 + 60 / (15 + 25)} = 24 \times \frac{24}{12 + 24} = 16 (V)$$
  
 $i_L(0^-) = 0 (A)$ 

#### For t > 0, sw opened, Series RLC circuit

$$R_{eq} = 6 + 60 / (15 + 25) = 6 + 24 = 30 \Omega$$

$$\alpha = \frac{R}{2L} = \frac{30}{2 \times 3} = 5$$

$$\omega_0 = \frac{1}{\sqrt{3 \times \frac{1}{27}}} = 3 \qquad \alpha > \omega_0$$

$$\Rightarrow \text{ system is overdamped}$$

$$s_{1,2} = -\alpha \pm \sqrt{\alpha^2 - \omega_0^2} = -5 \pm \sqrt{5^2 - 3^2} = -9, -1$$

$$i(t) = A_1 e^{-9t} + A_2 e^{-t}$$
$$\frac{di}{dt} = -9A_1 e^{-9t} - A_2 e^{-t}$$

$$i(0) = A_1 + A_2 = 0 \cdots (1)$$

$$\frac{di(0)}{dt} = -9A_1 - A_2 = \frac{-16}{3} \cdots (2)$$

$$\Rightarrow \begin{cases} A_1 = \frac{2}{3} \\ A_2 = \frac{-2}{3} \end{cases}$$

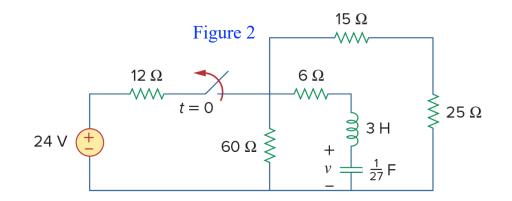
$$i(t) = A_1 e^{-9t} + A_2 e^{-t}$$
$$= \frac{2}{3} e^{-9t} - \frac{2}{3} e^{-t}$$

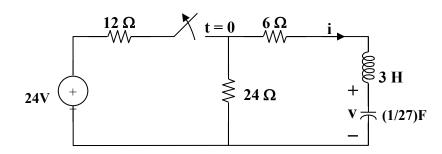
$$v(t) = -v_L(t) - i(t) \times 30$$

$$= -L \frac{di(t)}{dt} - 30i(t)$$

$$= -3\left(-6e^{-9t} + \frac{2}{3}e^{-t}\right) - 20e^{-9t} + 20e^{-t}$$

$$= -2e^{-9t} + 18e^{-t}$$





$$v_C(0^+) = v_C(0^-) = 16 (V)$$

$$i(0^+) = i_L(0^+) = i_L(0^-) = 0 (A)$$

$$v_L(0^+) = -i(0^+) \times (6 + 24) - v_C(0^+)$$

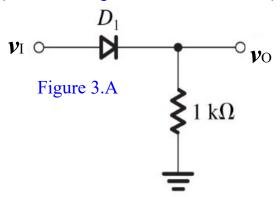
$$= -0 \times (6 + 24) = -16(V)$$

$$\frac{di(0^+)}{dt} = \frac{1}{L}v_L(0^+) = \frac{-16}{3} (V)$$



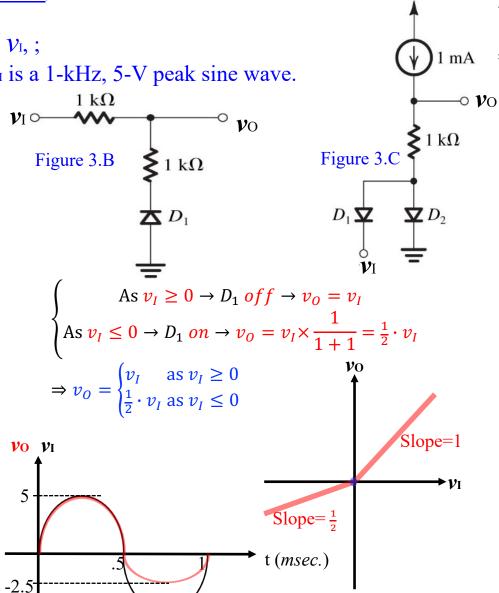
- (A) Derive  $v_0$  in terms of  $v_1$ ;
- (B) Sketch transfer characteristics  $v_0$  vs  $v_1$ ,;
- (C) Draw output waveforms  $v_0(t)$ , as  $v_1$  is a 1-kHz, 5-V peak sine wave.

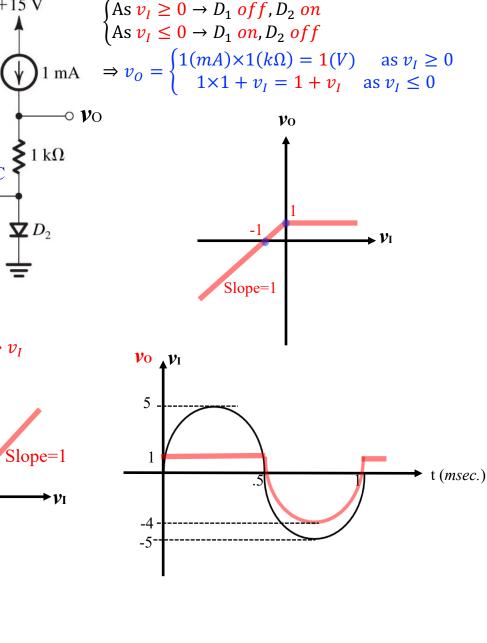
-5



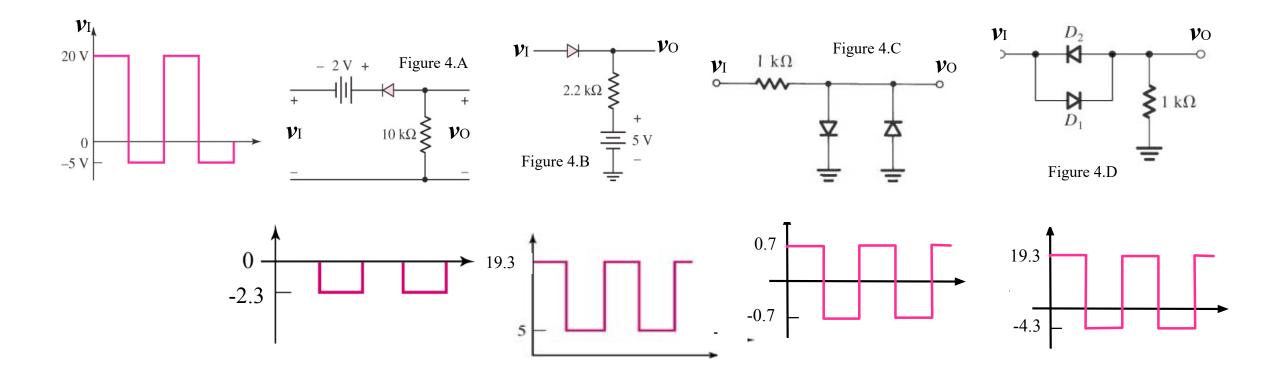
$$\begin{cases} As \ v_I \ge 0 \to D_1 \ on \to v_0 = v_I \\ As \ v_I \le 0 \to D_1 \ off \to v_0 = 0 \end{cases}$$

$$\Rightarrow v_0 = \begin{cases} v_I \ as \ v_I \ge 0 \\ 0 \ as \ v_I \le 0 \end{cases}$$
Slope=1





4. 20% Assume the diodes have a constant voltage drop  $V_D=0.7$  (V) while conducting (Constant voltage drop model). For Figure 4.A~D, with the shown input  $V_I$ , sketch the output waveform  $V_0$ .



# **5. 20%** In Figure 5, assuming that the diodes are ideal, find V and I for the circuit Figure 5.A and 5.B.

(a) Assume 
$$\begin{cases} D_1 \\ D_2 \end{cases}$$
 are both on

$$\Rightarrow$$
 V<sub>A</sub> = 0 and V = V<sub>A</sub> = 0

$$\Rightarrow I_{12k} = \frac{3 - V_A}{12k} = \frac{3 - 0}{12k} = 0.25 \text{ (mA)}$$

$$\Rightarrow I_{D2} = I_{6k} = \frac{V - (-3)}{6k} = \frac{0 + 3}{6k} = 0.5 \text{ (mA)}$$

$$\Rightarrow$$
 I = I<sub>12k</sub>- I<sub>6k</sub> = 0.25 - 0.5 = -0.25(mA)

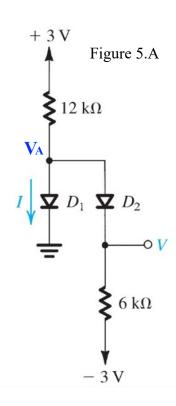
- ⇒ Impossible
- $\Rightarrow$  assumption on D<sub>1</sub> is incorrect

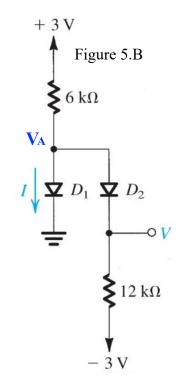
$$\Rightarrow \begin{cases} D_1 \text{ is off} \\ D_2 \text{ is on} \end{cases} \Rightarrow I = 0$$

$$\Rightarrow I_{12k} = I_{D2} = I_{6k} = \frac{3 - (-3)}{12k + 6k} = \frac{1}{3} (\text{mA}) = 0.3333 (\text{mA})$$
  
$$\Rightarrow V = I_{6k} \times 6(k) + (-3) = \frac{1}{3} (\text{m}) \times 6(k) - 3 = -1(V)$$

$$\Rightarrow$$
 V = I<sub>6k</sub>×6(k) + (-3) =  $\frac{1}{3}$ (m)×6(k) - 3 = -1(V)

$$\Rightarrow \begin{cases} I = 0 \\ V = -1(V) \end{cases}$$





(b) Assume 
$$\begin{cases} D_1 \\ D_2 \end{cases}$$
 are both on  $\Rightarrow V_A = 0$  and  $V = V_A = 0$   $\Rightarrow I_{6k} = \frac{3 - V_A}{6k} = \frac{3 - 0}{6k} = 0.5 \text{(mA)}$   $\Rightarrow I_{D2} = I_{12k} = \frac{V - (-3)}{12k} = \frac{0 + 3}{12k} = 0.25 \text{(mA)}$  assumption is correct!  $\Rightarrow I = I_{6k} - I_{12k} = 0.5 - 0.25 = 0.25 \text{(mA)}$