Introduction

* Brief motivation in context of CLIC
* CTF3 intro
* System layout/overview

Hardware

* Phase Monitors
  + Brief design of cavities and electronics.
  + Diode issues
  + Calibration
  + Resolution
  + Position dependence
  + Bandwidth
* FONT5a Board
  + Brief design
  + Firmware overview
  + PFF algorithm and optimal gain
* Amplifier
  + Brief design
  + Output vs. input – mean and shape
* Kickers
  + Brief design

Optics

* Chicane
  + Integration of kickers
  + Optics constraints
  + Optics plots
* Phase Propagation
  + R56 equations, requirements
  + Initial conditions
  + R56 scan
  + T566
  + Final conditions

System Setup and Verification

* Correction Range and Shape
  + Phase vs. kicker voltage
  + Shape of kick along the pulse
* Orbit Closure
  + Plot
* Correction Timing
  + Latency
  + K1 fine timing
  + K2 with respect to K1 timing
* Correction Gain
  + Gain scan

Results

* Mean phase jitter
  + Best result (same as short paper but different plots maybe)
* Pulse shape
  + Best result
* Long-term
  + Jitter e.g. across an hour
  + Correlation and gain stability

Conclusions