Dwight Look College of
ENGINEERING
TEXAS A&M UNIVERSITY

Project name: WBG Devices-Based Matrix Converter

Team members: Jack Alagood, Kyle Bedrich, Ian Farrar

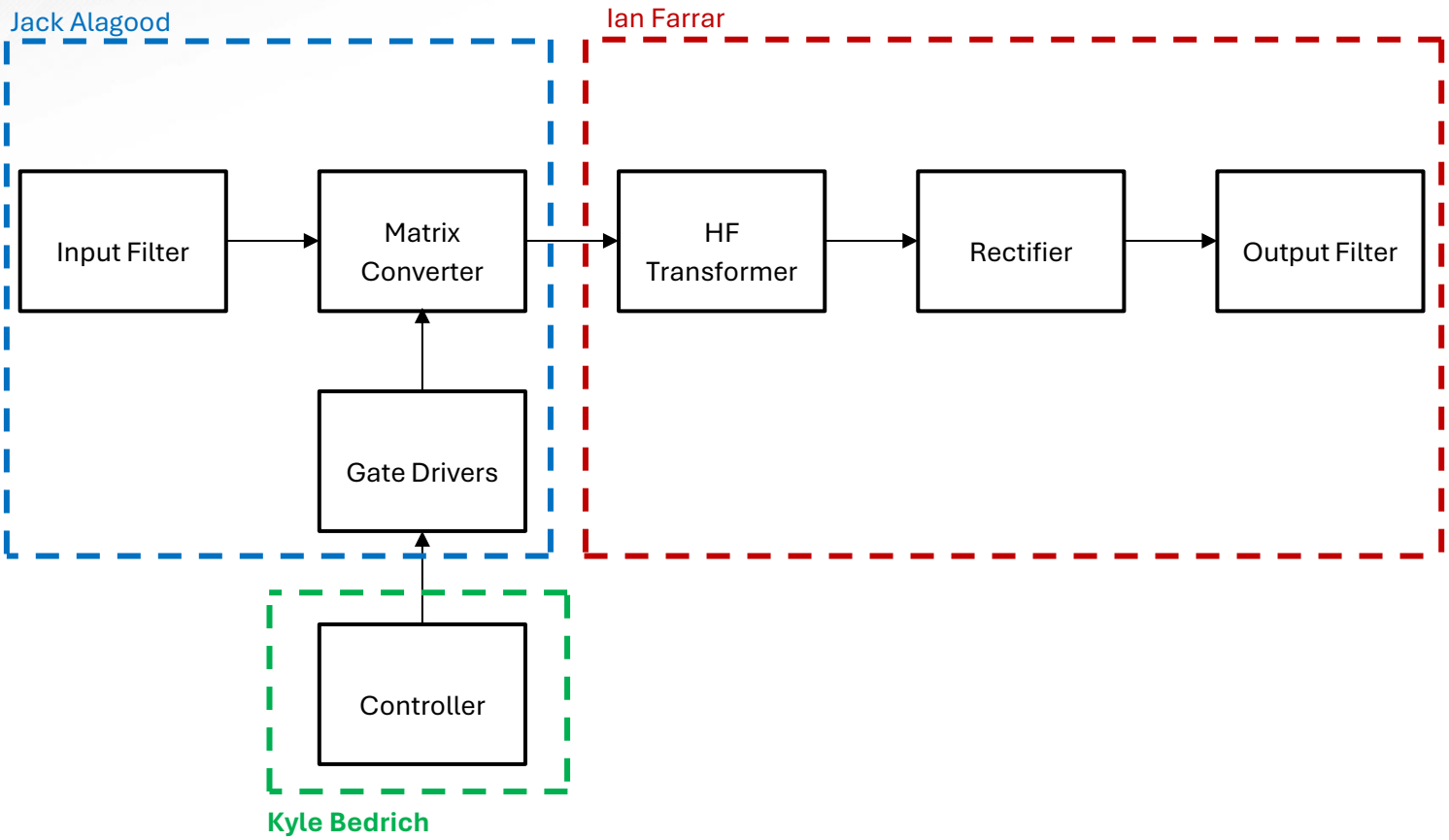
Problem Statement

- The rise of energy-intensive computing (AI model training, cloud computing, data centers, etc.) creates a need to optimize power delivery to these loads
- Though many solutions have been presented, there remains room for improvement in efficiency and cost

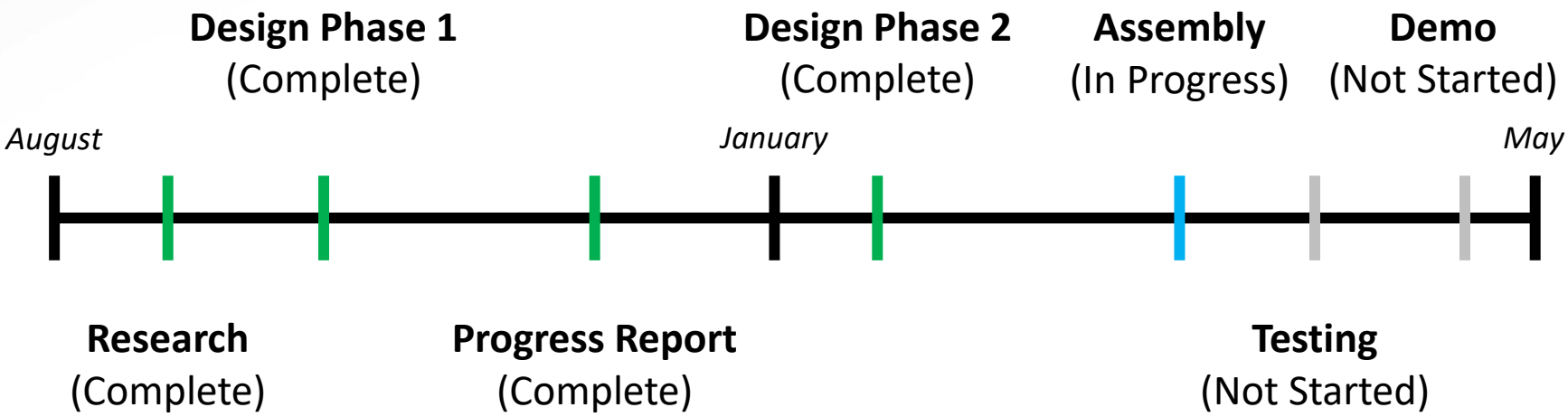
Proposed Solution

- GaN technology promises greater power density than SiC
- Matrix converters offer bi-directional power flow, adjustable input power factor, and greater power density due to less storage elements

System Diagram



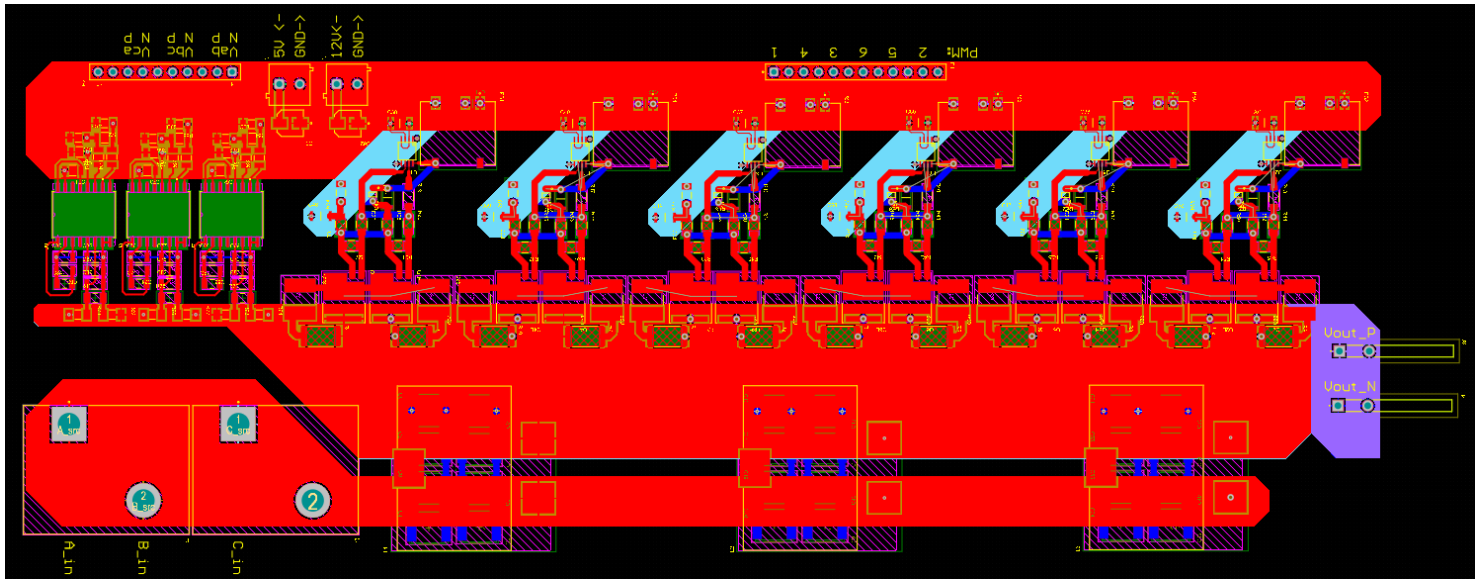
Project Timeline



Subsystem 1 (Primary Side)

Jack Alagood

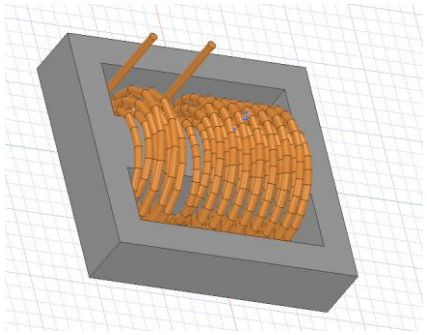
Accomplishments since last update	**Ongoing progress/problems and plans until the next presentation**
• Finalized PCB (traces, pours, labels, etc.)	• Assemble board • Test incrementally; record measurements



Subsystem 2 (Secondary Side)

Ian Farrar

Accomplishments since last update	**Ongoing progress/problems and plans until the next presentation**
• Bought Core and Litz Wire • Finished Running Simulations	• Core bought based off datasheets instead of simulation results as planned due to time, so finish analysis of simulation results

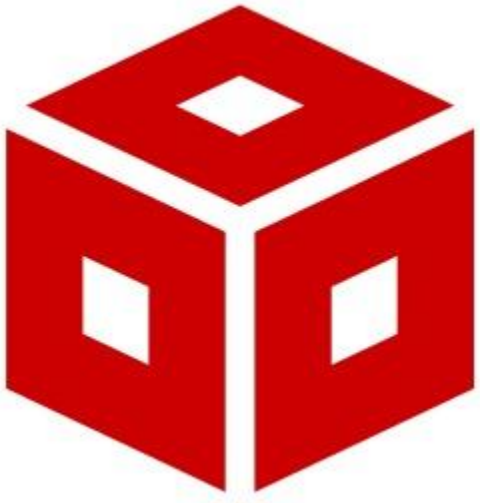


Multilayered transformer model

Subsystem 3 (Controls)

Kyle Bedrich

Accomplishments since last update	**Ongoing progress/problems and plans until the next presentation**
• Control system validation complete using built-in PLL in software, use of custom PLL with PSIM blocks does not work properly.	• Moving to programming the control system in C instead of using PSIM blocks for better customizability.



Parts Ordering Status

- Parts ordered
- Board ready to be soldered

Execution/Validation Plans

ECEN 404	Owner(s)	1/12/2025	1/19/2025	1/26/2025	2/2/2025	2/9/2025	2/16/2025	2/23/2025	3/2/2025
Schematic 3-phase Extension	Jack								
PCB 3-phase Extension	Jack								
Transformer Design	Ian								
DSP Debugging	Kyle								
DSP Testing	Kyle								
Simulations	Each								
		3/9/2025	3/16/2025	3/23/2025	3/30/2025	4/6/2025	4/13/2025	4/20/2025	4/27/2025
Transformer Testing	Ian								
Board Assembly	Jack								
Board Testing	Group								
Final Presentation	Group								
Final Demo	Group								
Final Report	Group								

Task	Deadline	Status
Schematic 3-phase Extension	1/31/2025	Complete
PCB 3-phase Extension	2/21/2025	Complete
Transformer Design	2/28/2025	Complete
Transformer Testing	3/28/2025	In Progress
DSP Debugging	2/14/2025	Complete
DSP Testing	2/28/2025	Complete
Simulations	3/7/2025	Complete
Board Assembly	3/28/2025	In Progress
Board Testing	4/11/2025	Not Started
Final Presentation	4/16/2025	Not Started
Final Demo	4/26/2025	Not Started
Final Report	4/28/2025	Not Started

Legend
Complete
In Progress
Overdue
Not Started

Thank You