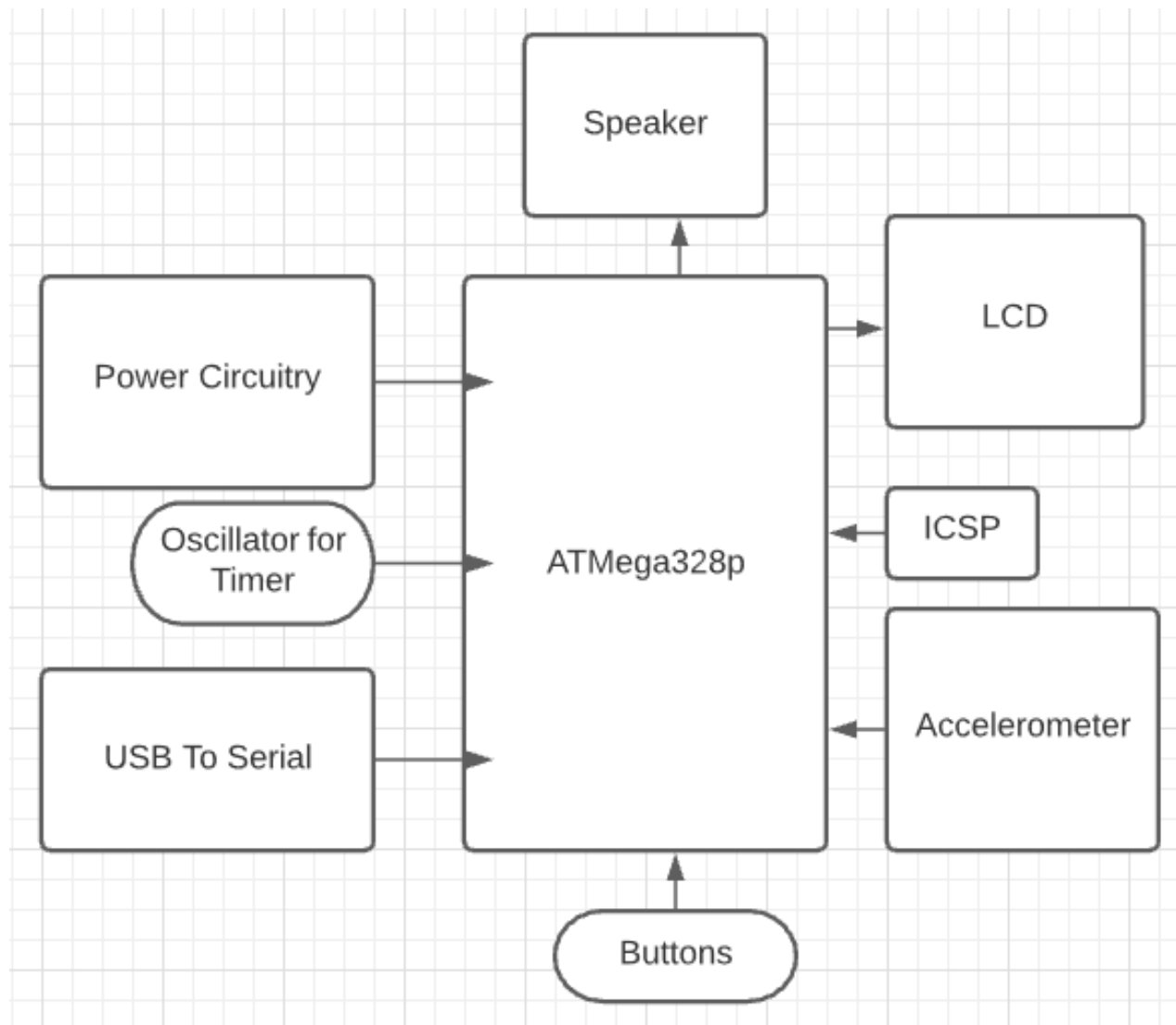


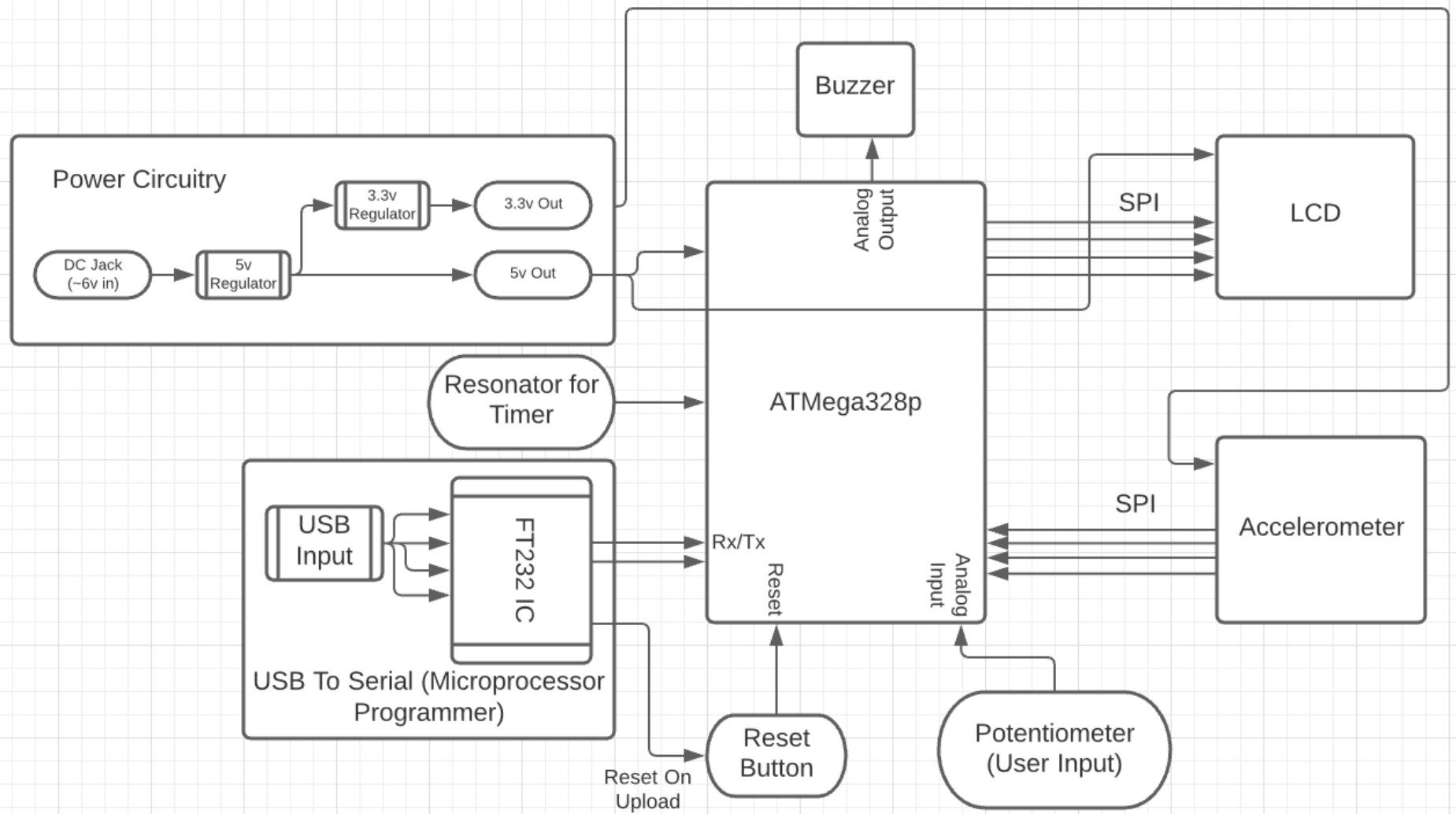
System Design: Functional Decomposition

Team 03 – Jack Chen, Devon Mickels, Hannah Mortimer, Nicole Fellows

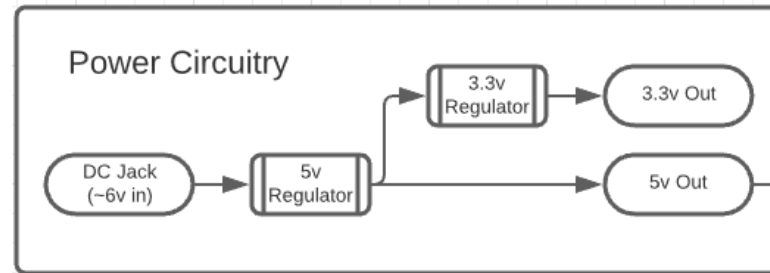
High Level Block Diagram



Low Level Block Diagram

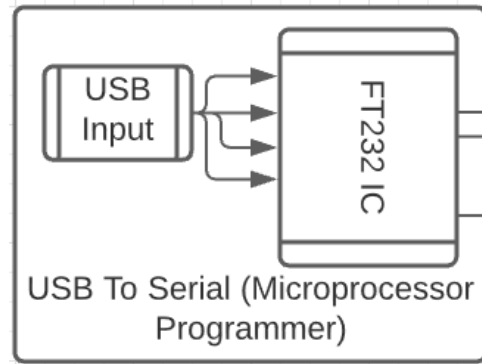


Power Circuitry



Module	Power Circuitry
Inputs	6V
Outputs	5V and 3.3V
Functionality	Converts 6v power from battery pack into a 5v and 3.3v line to power different components in the system.

USB To Serial



Module	Power Circuitry
Inputs	USB B Cable
Outputs	TX, RX and DTR signal.
Functionality	Used to convert an incoming USB signal to a usable serial communication protocol for use in programming the ATMEGA328p