Ruchira Tawade

Software Engineer

Portland, OR - Email me on Indeed: indeed.com/r/Ruchira-Tawade/bcbbd7e7522acd93

Graduate student in Electrical and Computer Engineering(Digital IC Design and Verification), actively seeking
Intern/ Co-op

opportunities in Digital Logic Design, Comp Arch, ASIC /Verification, Validation starting from June '17.

• Related Coursework by June 2017:

Digital Integrated Circuit Design I, ASIC Modeling and Synthesis, Microprocessor System Design, Pre-Silicon Functional Validation,

Computer Architecture, System On Chip with FPGA.

Willing to relocate: Anywhere

Sponsorship required to work in the US

WORK EXPERIENCE

Software Engineer

Rolta India LTD - Mumbai, Maharashtra - December 2015 to August 2016

- Peer review and Verification of software modules, also written shell scripts for automation and scheduling tasks.
- Documented Test plans, test cases, also executed regression & negative testing, bug tracking matrix for software modules.

System Engineer

Tata Consultancy Services - Mumbai, Maharashtra - April 2013 to December 2015

Involved in Software Development Life Cycle (Waterfall & Agile methodology) throughout the Requirement gathering,

Analysis, Development and Maintenance phase.

- Documented Low Level Design as well as High Level Design of Functional requirements, User queries & Change Requests while providing customer support(UK United Biscuits, Canada TD bank, ING bank Singapore)
- Implemented various modules (Front end- asp.net with C# & Back end SQL/oracle database) in the project.
- Production support for DatawareHousing projects.
- Conducted training sessions to train juniors for the same technology.
- Safety Champion of Women safety initiative in the Organization (TCS) for 2015.

EDUCATION

Master of Science in Electrical Engineering

Portland State University September 2016

Bachelor of Engineering in Electronics and Telecomm

Mumbai University - Mumbai, Maharashtra July 2009 to June 2012

SKILLS

C# (2 years), SQL (2 years), .NET (Less than 1 year), ALLEGRO MENTOR (Less than 1 year), ASSEMBLY (Less than 1 year)

ADDITIONAL INFORMATION

TECHNICAL SKILLS:

• Simulation Tools - Cadence (Layout, Virtuoso, Spectre), Synopsys (HSPICE, Waveviewer), Mentor Graphics (Calibre,

Modelsim), Visual studio 2008, Sql server [...] ETL tools Business Intelligence development Studio, IBM Datastage, TFS for code Version control.

• Programming Languages - Verilog, UVM System Verilog, C, C++, C#, Shell Scripting, TCL, SQL, PL/SQL, Pearl,

Assembly Language, HTML5.