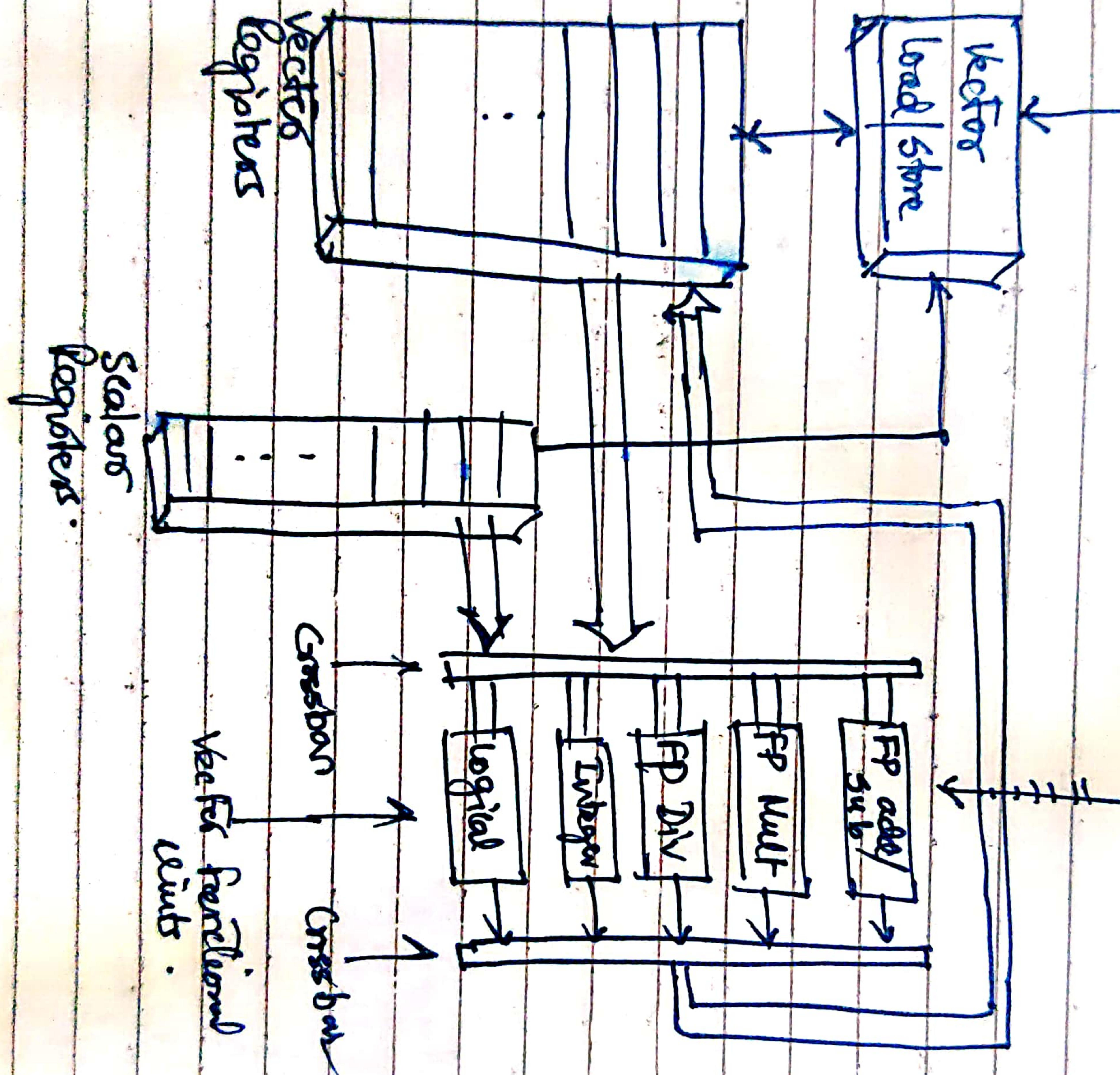


(Rect)



Basic Structure of a vector register architecture

Vektor-Vektor Multiplikation

v_j Register

v_k Register

v_i Register

v_j Register. v_k Register

v_i Register

Faktions Unit.

Vektor-Skalas Multiplikation

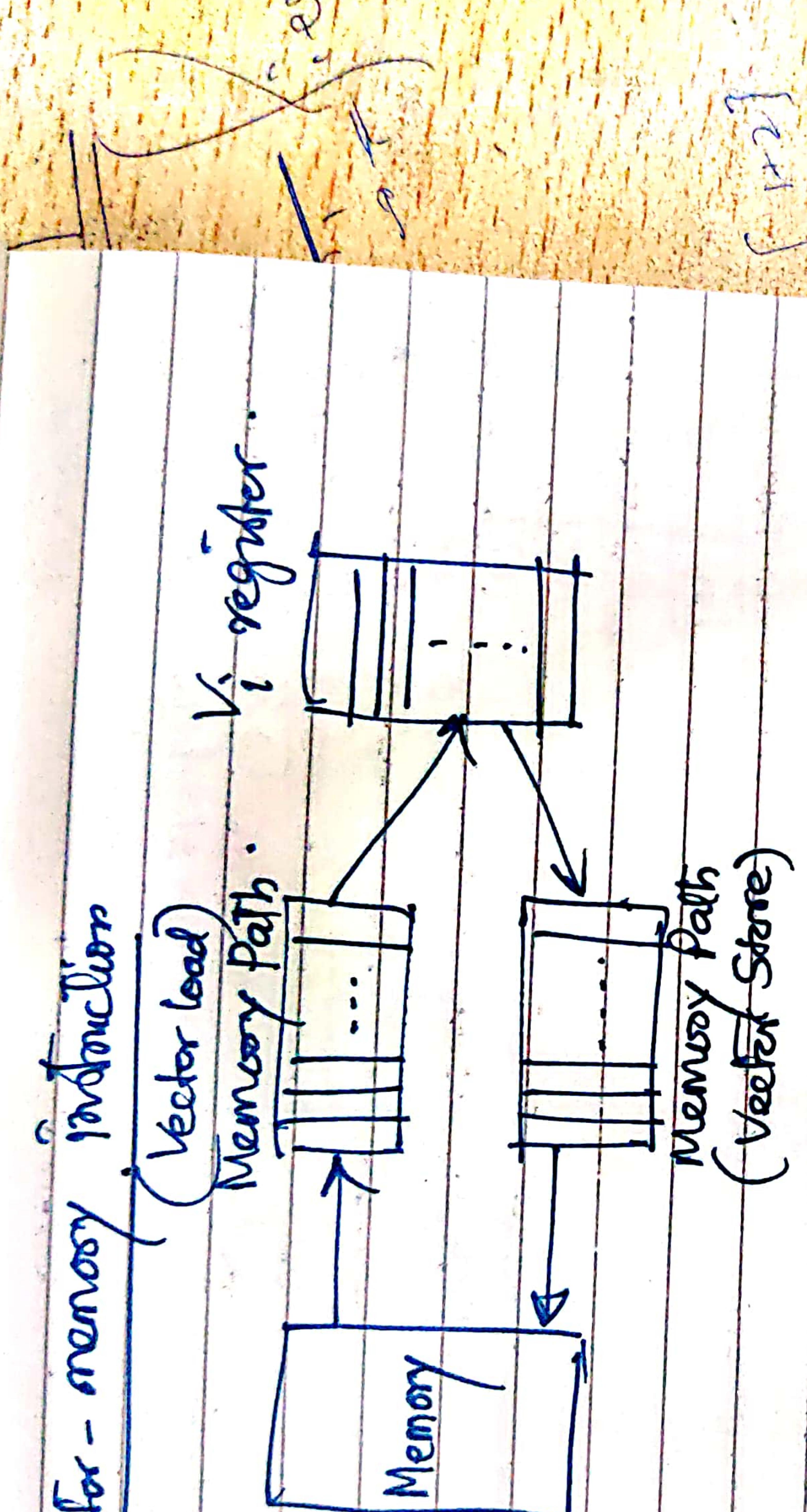
v_k Register

v_i Register

s_j Register

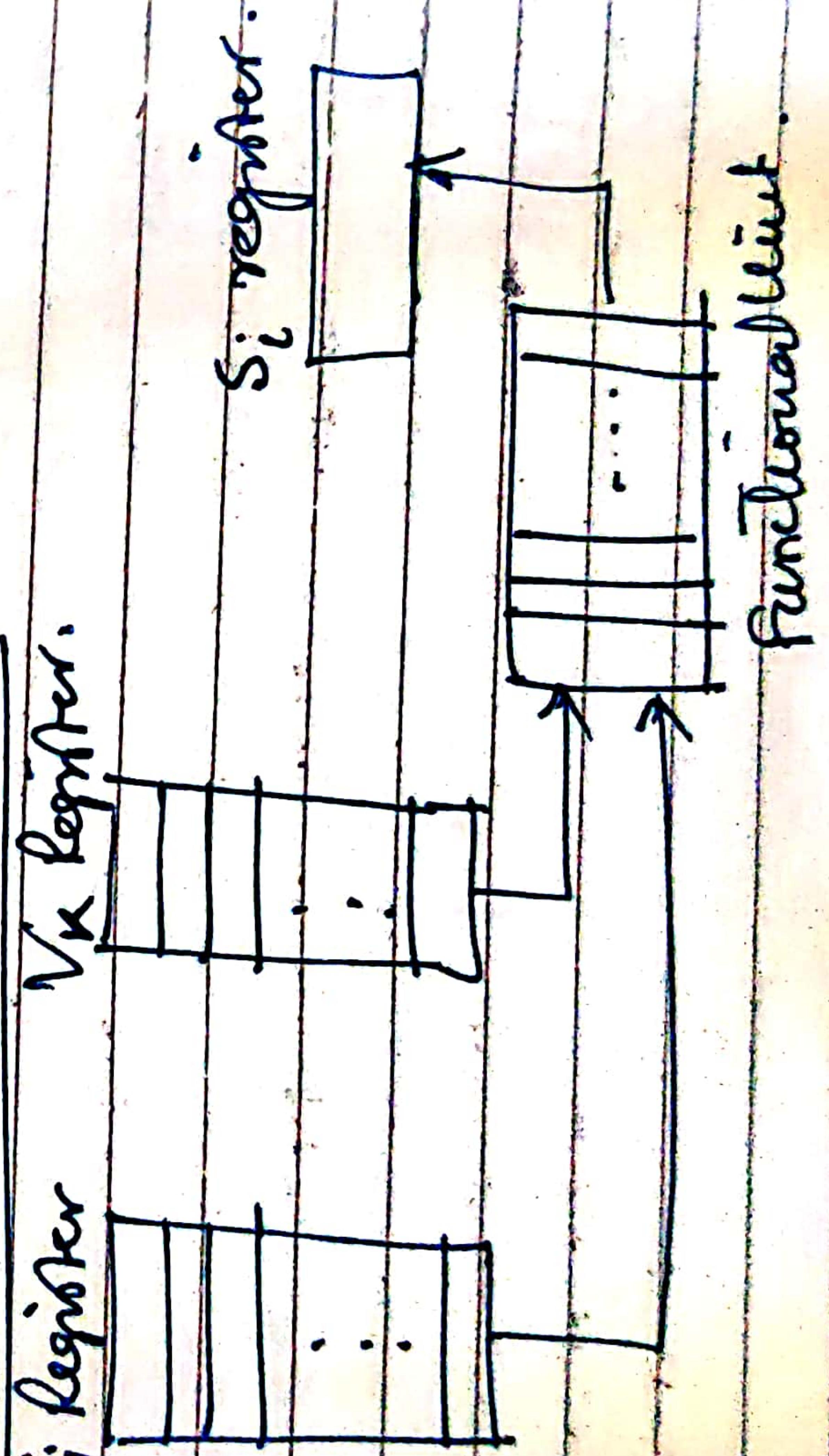
funktional reicht.

Vest - monogram
Handwriting
Computerized



Vectoren
Produktionsmodelle
im Monolithen

Loghter
W
Si Regnher



frühjahr - **Frühjahr** - **Frühjahr**

(16)

100	100	100	100	100
4	4	4	4	4
100	100	100	100	100
200	200	200	200	200
300	300	300	300	300
500	500	500	500	500
100	100	100	100	100
103	103	103	103	103
102	102	102	102	102
101	101	101	101	101
104	104	104	104	104
105	105	105	105	105
106	106	106	106	106
107	107	107	107	107
108	108	108	108	108
400	400	400	400	400
Measure	Adder	Subtractor	Multiplexer	Register

VL Registers VL Logicals

Softw. Functionality

100	100	100	100	100
4	4	4	4	4
100	100	100	100	100
200	200	200	200	200
300	300	300	300	300
400	400	400	400	400
500	500	500	500	500
600	600	600	600	600
700	700	700	700	700
800	800	800	800	800
900	900	900	900	900
1000	1000	1000	1000	1000
103	103	103	103	103
102	102	102	102	102
101	101	101	101	101
104	104	104	104	104
105	105	105	105	105
106	106	106	106	106
107	107	107	107	107
108	108	108	108	108
200 #	200 #	200 #	200 #	200 #
350	350	350	350	350
Measure	Adder	Subtractor	Multiplexer	Register

VL Registers VL Logicals

Softw. Functionality

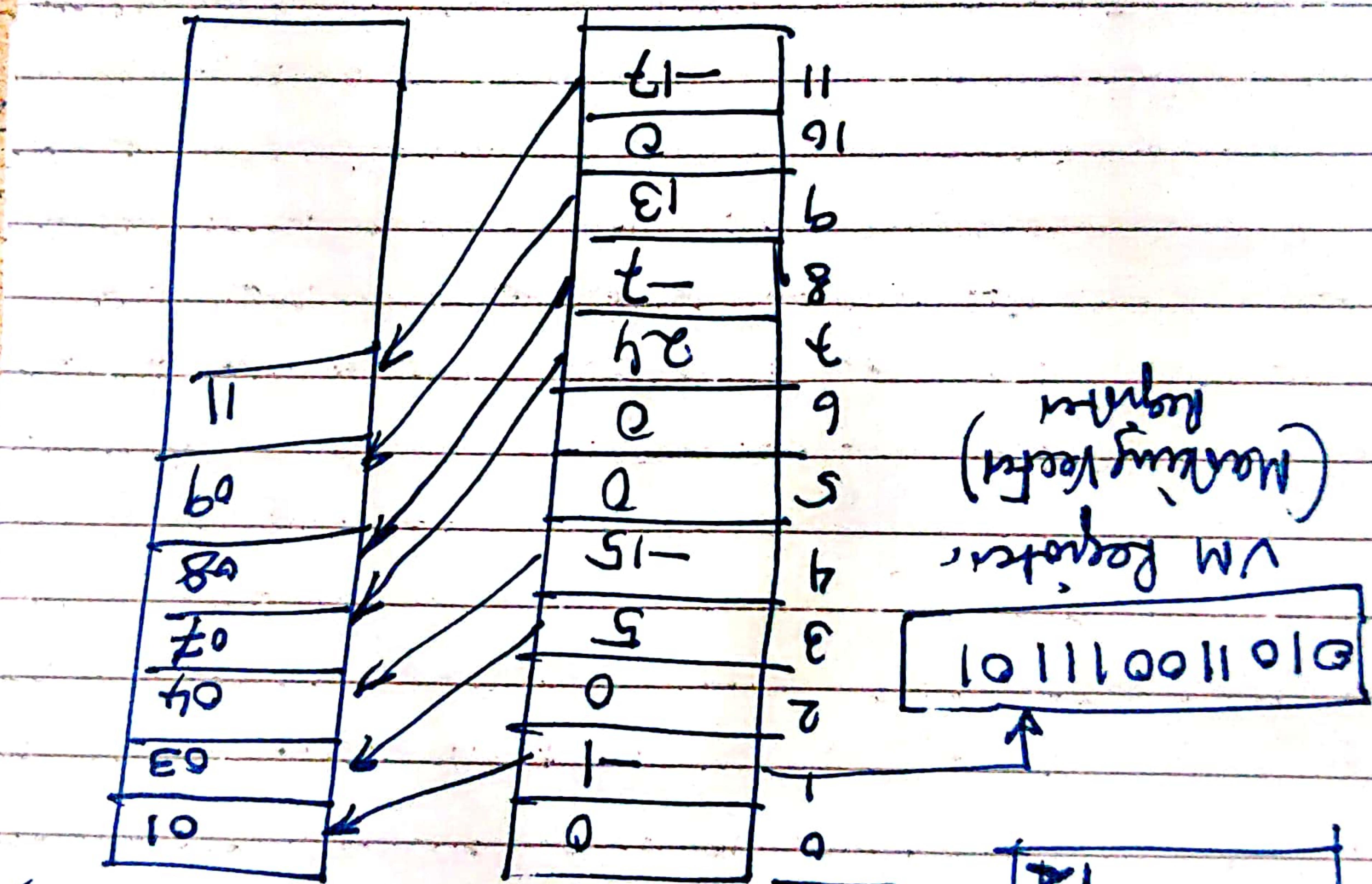
• Encourages over-work to stagnate ← in N

poem Smog Report to Mother 6-7A

canz-won orgasmic
canz coroner 0

earoz-mon o 4 WA
earoz corporis o 4 WA

Montgomery - now go over ref p211 & 9A



(from) Raboy H

• ~~Chor - Учи до окошку~~
Почти сумні
малося аж

131
maggie 78

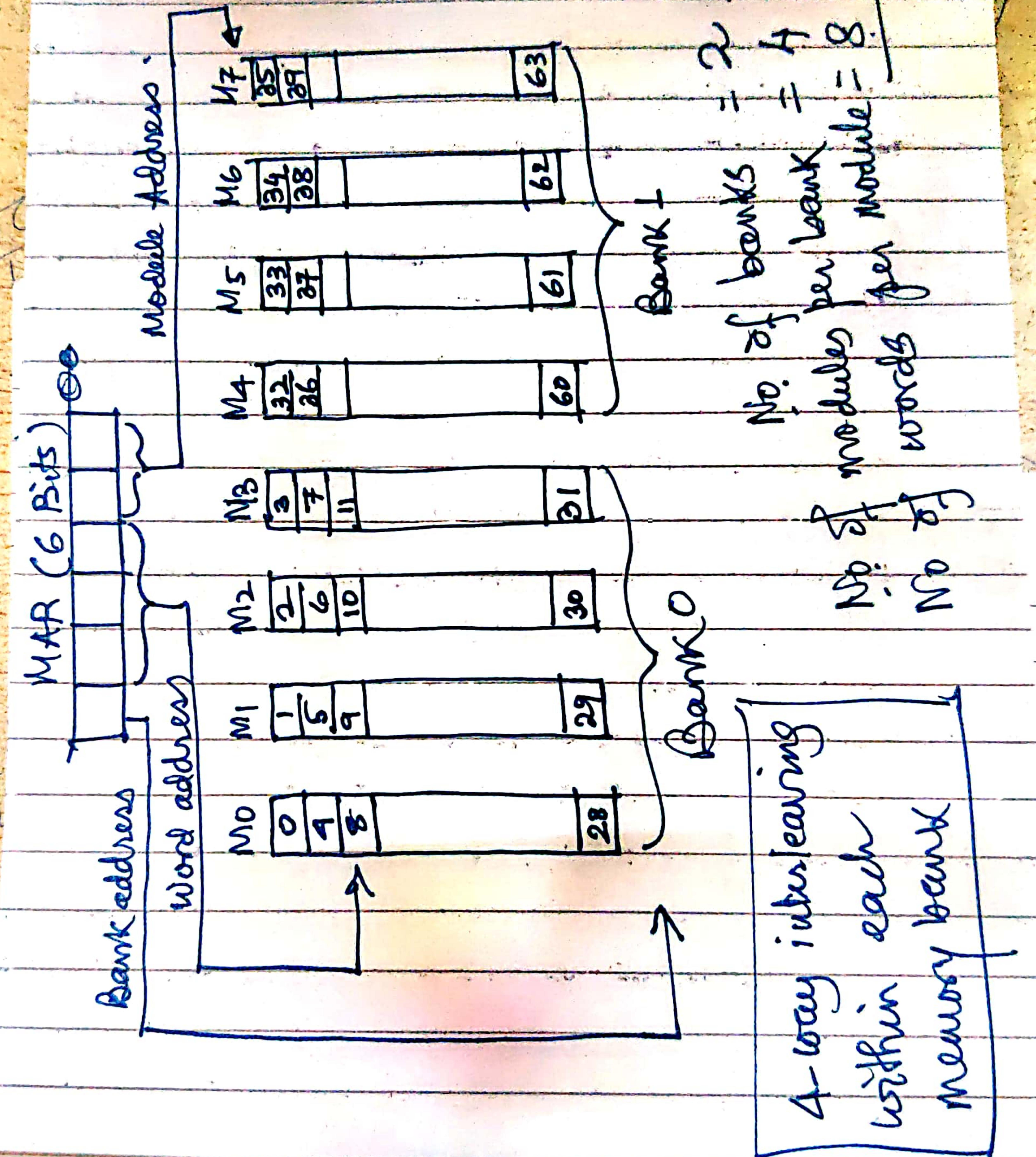
অসম প্রকাশনা বিল্ডার্স

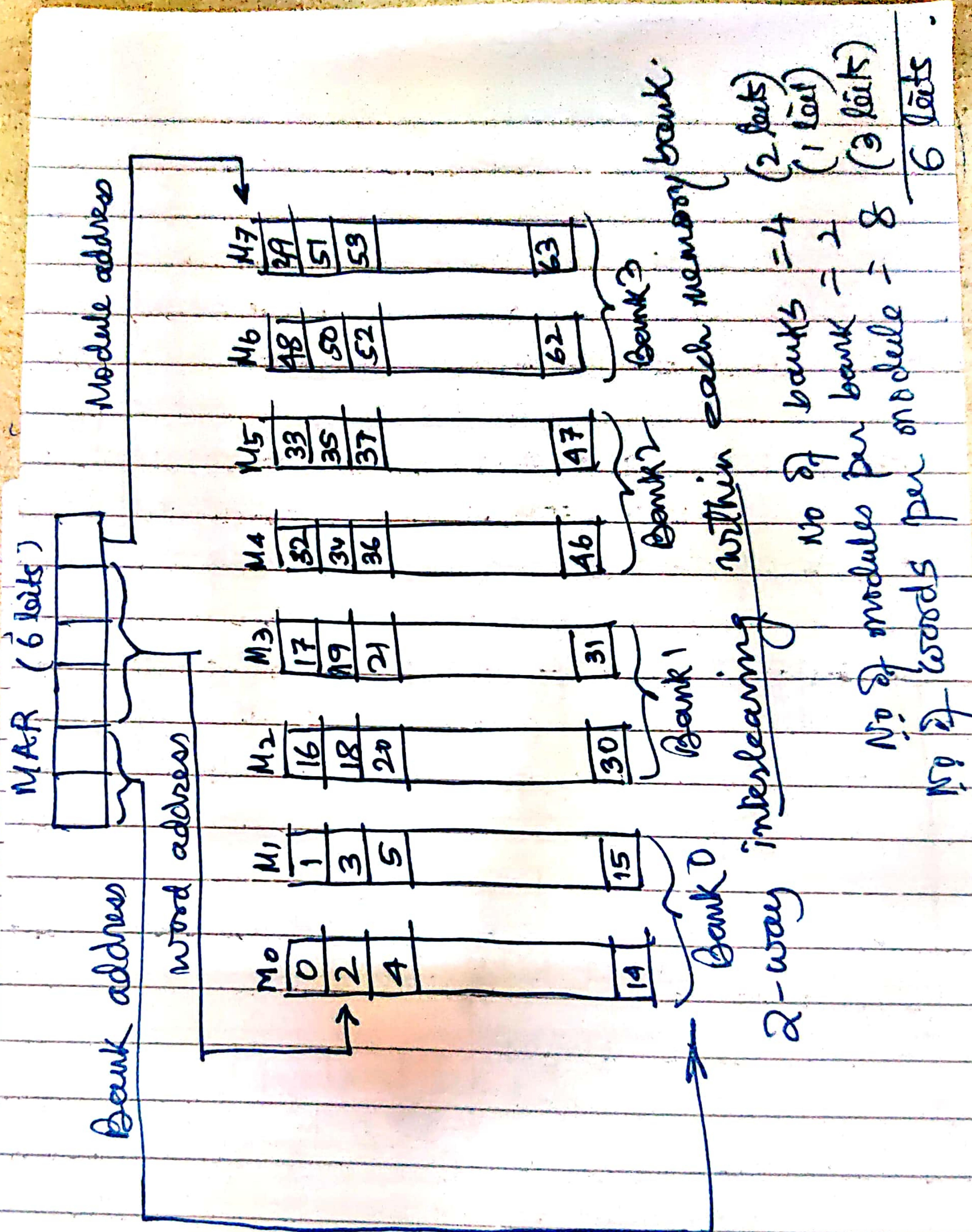
column major order: 147 858 369.

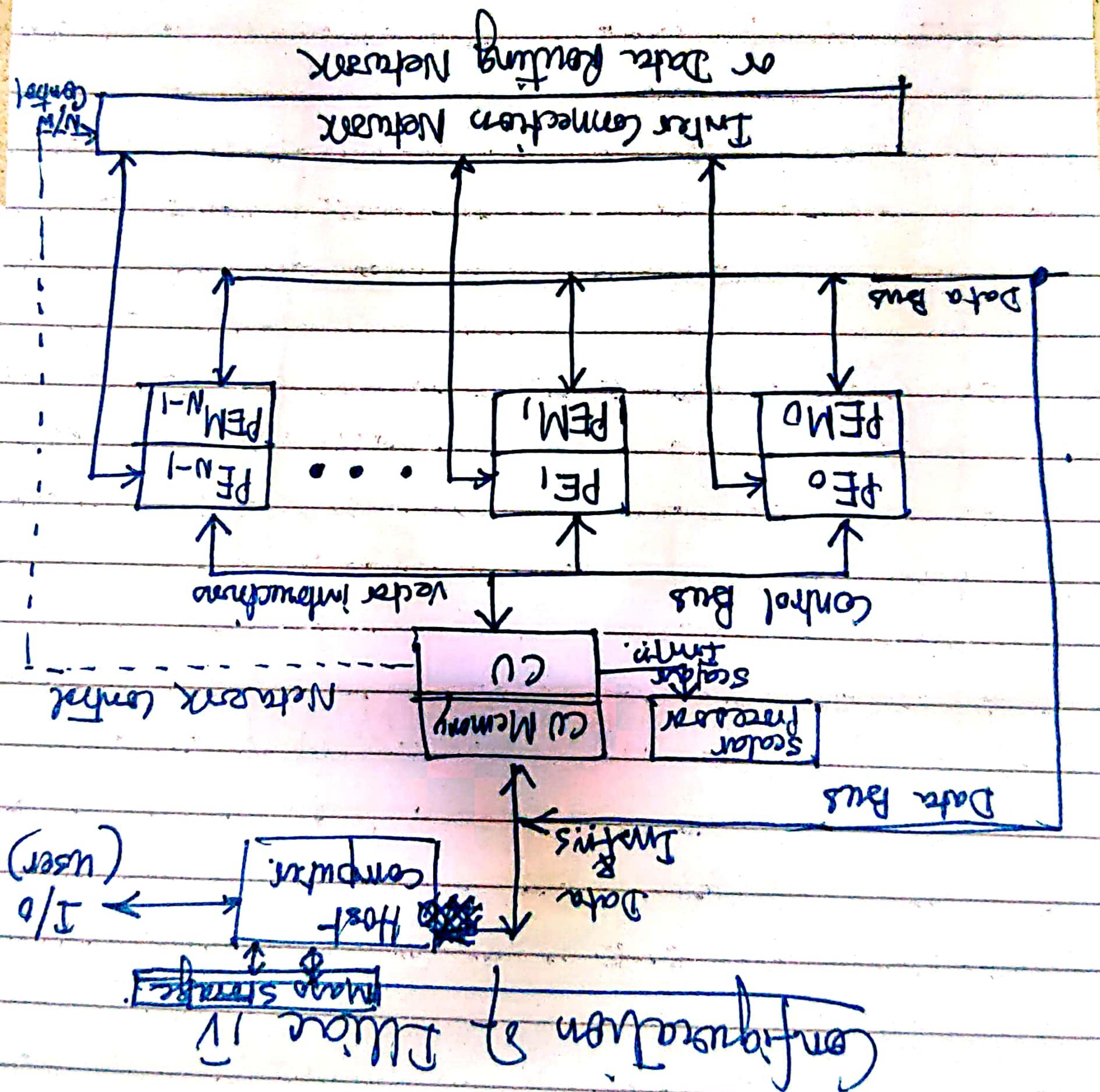
Row major order: 123 456 789

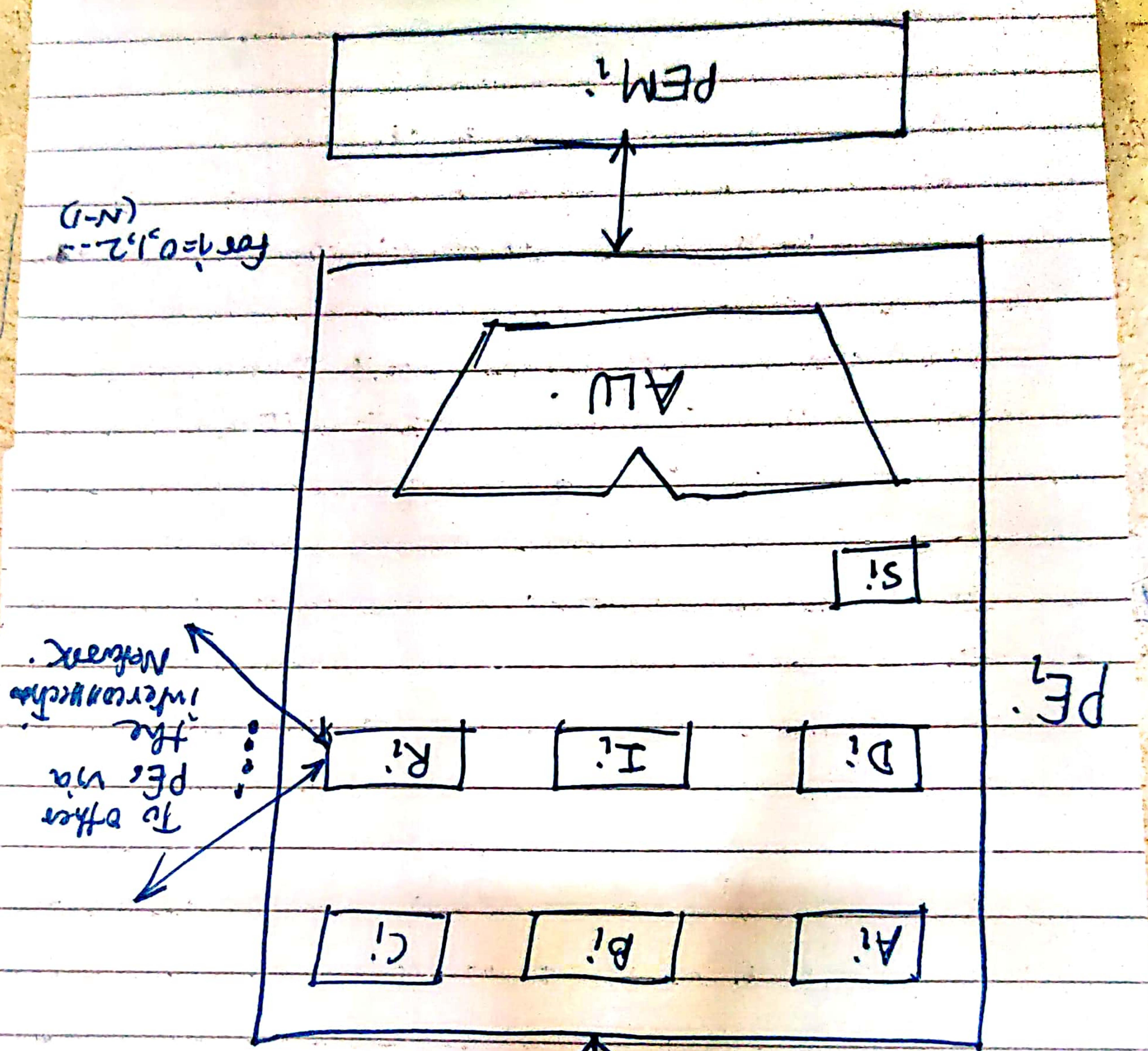
6	8	4
9	5	1
2	3	-

Lecter Share

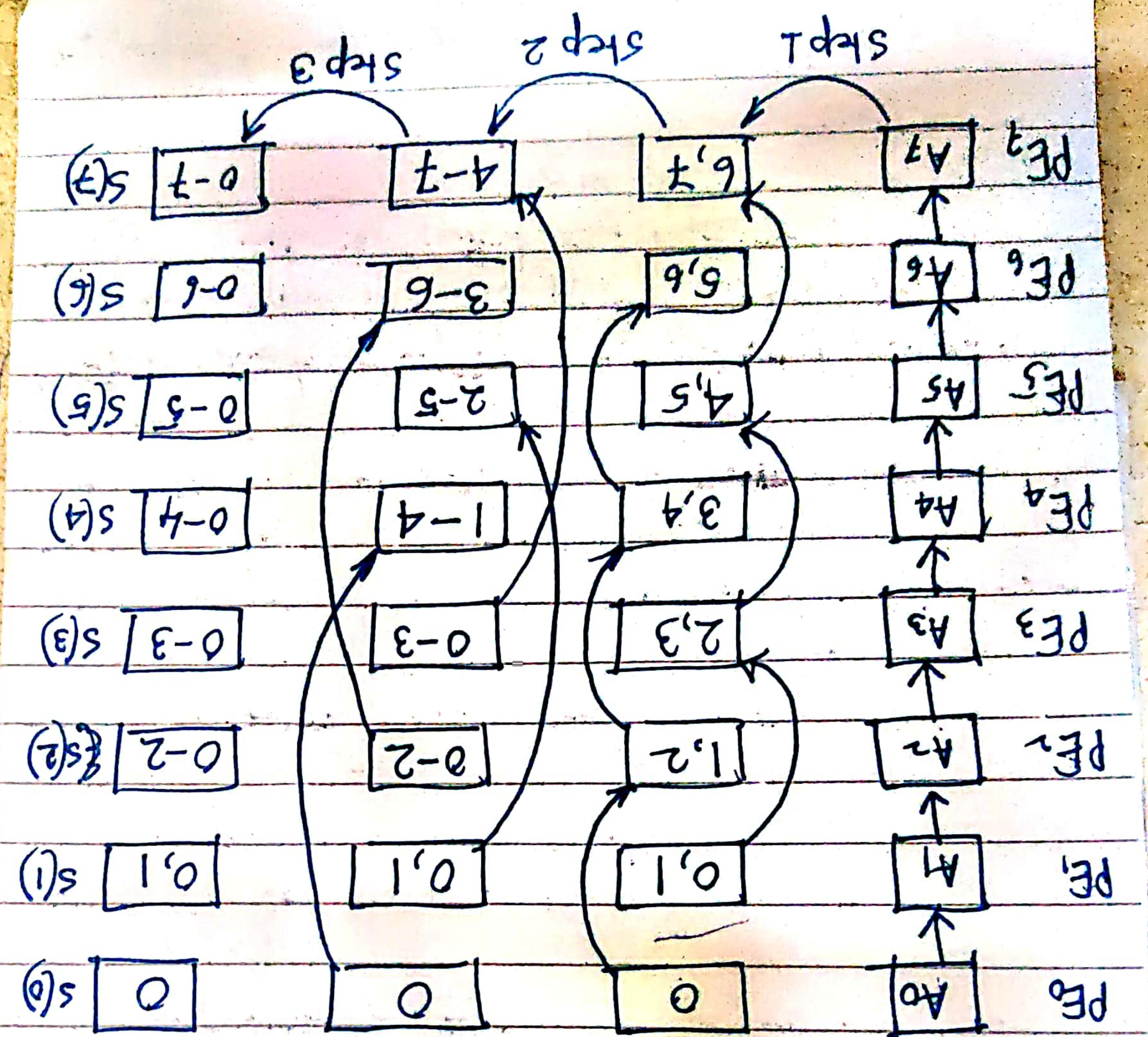








Components of a floating-point element



In an SIMD Machine

$$\text{Calculation of } S(k) = \sum_{i=0}^{k-1} A_i, \quad k=0, 1, \dots, 7$$

Lecturer Shinde / Shekhar Muniy / vedra chowdhury / 2018[5(e)]
Sims : What's this in ILLIAC-IV
Data mainly logical & Sims to compare
Many memory mechanisms in Sims many part
2018 [4(e)] New - New field. New processor in up or
Borrowed from