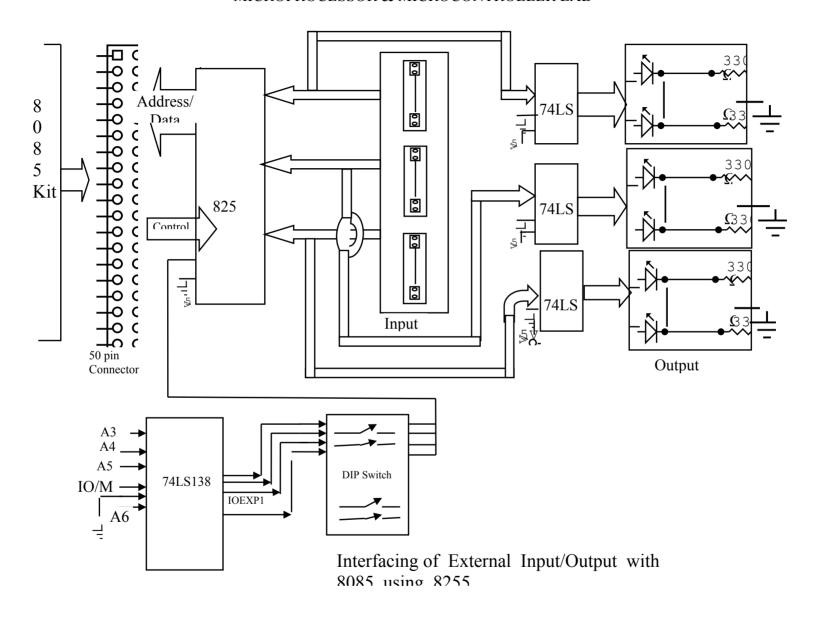
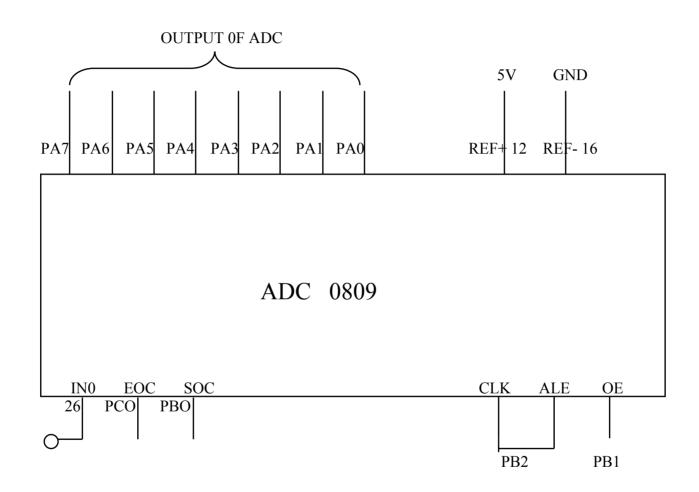
# MICROPROCESSOR & MICROCONTROLLER LAB





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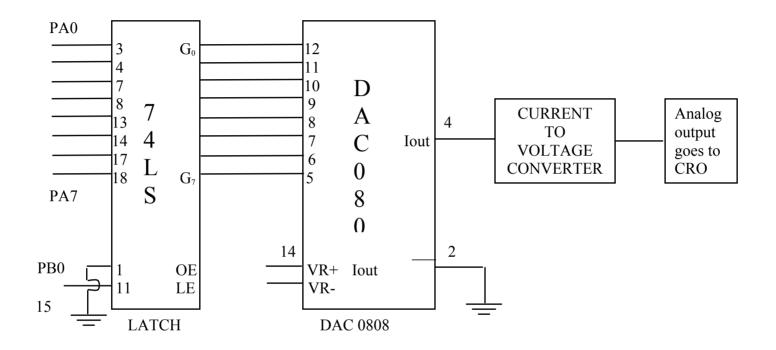


## SCHEMATIC HARDWIRED CIRCUIT DIAGRAM FOR A / D CONVERTER INTERFACE



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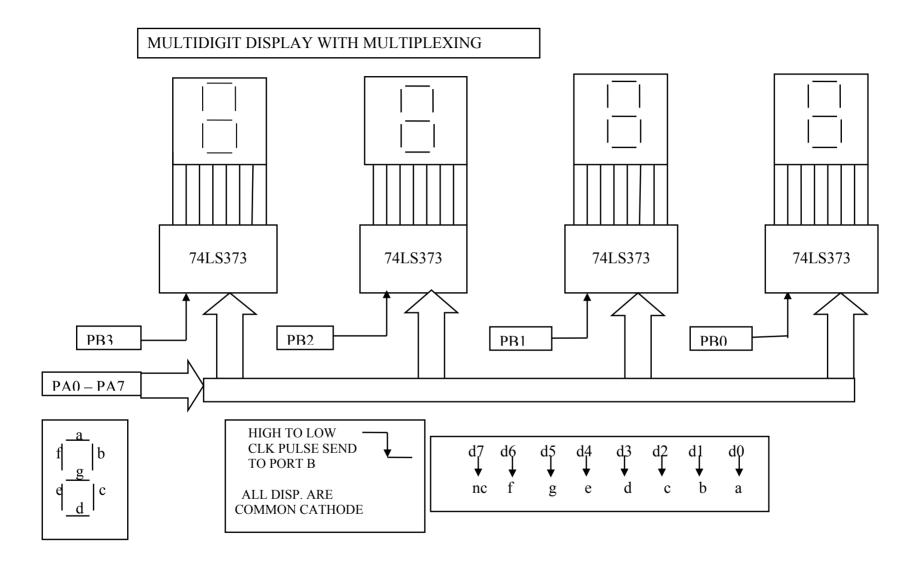
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## SCHEMATIC HARDWIRED CIRCUIT DIAGRAM FOR D / A CONVERTER INTERFACE



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