# Sick **Yoon**

PLATFORM ENGINEER · GOPHER

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## Skills

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|---|---|
| **Languages** | Go, Python3, C++, Javascript |
| **DevOp** | Kubernetes, Helm, Ksonnet, Operator, Docker, Ansible, Prometheus, Grafana |
| **Web** | Go, Tornado/Flask/Django, Rails, Express, React/Redux, HTML5/CSS3/SASS |

## Experience

**Platform Engineer** *Remote*
COMPOSE, AN IBM COMPANY *Jan. 2017 - Present*
- Database-as-a-Service platform for IBM Bluemix (PostgreSQL, MongoDB, Etcd, RabbitMQ and more)
- Microservices in Go to manage tens of thousands of database replicas distributed across multi-region cloud providers (GCP, AWS, Bluemix)
- Kubernetes operators, CRD, Jobs to manage database replicas
- Monitoring infrastructure through kube-prometheus (prometheus and grafana)

**Sergeant, Software Engineering** *Seoul, S.Korea*
REPUBLIC OF KOREA CYBER COMMAND *Feb. 2015 - Nov. 2016*
- Large scale microservices in Go/Python3/React for cybersecurity projects

**ASIC Design Engineer 2** *Markham, Canada*
ADVANCED MICRO DEVICES INC. *Aug. 2013 - Oct. 2014*
- PCIe post-silicon debugging
- PCS IP (PCIe, USB, SATA) design verification
- C++, Perl, System Verilog, Verilog, UVM

**Software Engineer (Java)** *Mississauga, Canada*
AVERSAN INC. *Nov. 2012 - May. 2013*
- Implemented reverse proxy and test automation framework for eHealth Ontario
- Java, Netty, Spring, Hibernate, Maven, Oracle 11g, Selenium

**AMD Design Engineer (PEY)** *Markham, Canada*
ADVANCED MICRO DEVICES INC. *May. 2010 - Sep. 2011*
- DFx IP (DFT/DFD) design verification
- IEEE standards (JTAG, P1500)
- C++, Perl, System Verilog, Verilog

## Education

**University of Toronto** *Toronto, Canada*
B.A.SC. COMPUTER ENGINEERING *2007 - 2012*
- Certificate in Entrepreneurship, Innovation and Small Business

## Open Source

★ 641  **GoCelery**, Celery Distributed Task Queue in Go - https://github.com/gocelery/gocelery