Prelab 2.

1. What is the purpose of the NVIC peripheral?
    a. The primary responsibilities of the NVIC are enabling and disabling interrupts, indicating requests waiting for servicing, canceling pending interrupt requests, and establishing how multiple interrupts interact through configurable priorities
2. What is the difference between interrupt tail-chaining and nesting?
    a. Tail chaining the interrupts are not interrupted so they chain them in order, while nesting can have other interrupts in an interrupt nesting them inside
3. In what file are the CMSIS libraries that control the NVIC?
    a. core_cm0.h
4. What is the purpose of the EXTI peripheral?
    a. allows non-peripheral sources to trigger interrupts
5. What is the purpose of the SYSCFG pin multiplexers?
    a. deals primarily with signal routing, and controls data transfer between peripherals and memory, remapping portions of memory, and some high-power communication modes
6. What file has the defined names for interrupt numbers?
    a. stm32f072xb.h
7. What file has the Vector table implementation?
    a. startup_stm32f072xb.s