```
/***************************************************************************
**
**    This file defines the Special Function Registers for
**    MANUFACTURER CHIPNAME
**
**    Used with ARM IAR C/C++ Compiler and Assembler.
**
**    (c) Copyright IAR Systems YEAR
**
**    $Revision:  $
**
***************************************************************************/

#ifndef __IOCHIPNAME_H
#define __IOCHIPNAME_H

#if (((__TID__ >> 8) & 0x7F) != 0x4F)    /* 0x4F = 79 dec */
#error This file should only be compiled using the ARM IAR compiler and assembler
#endif

#include "io_macros.h"

/***************************************************************************
 ***************************************************************************
**
**    CHIP_NAME SPECIAL FUNCTION REGISTERS
**
 ***************************************************************************
 ***************************************************************************/

/* Compiler-specific declarations ***********************************************/

#ifdef __IAR_SYSTEMS_ICC__

#ifndef _SYSTEM_BUILD
 #pragma system_include
#endif

//REPLACE WITH BIT STRUCTS

#endif   /* __IAR_SYSTEMS_ICC__ */


/* Declarations common to compiler and assembler ********************************/

//REPLACE WITH REGISTERS
```

```
/* Assembler-specific declarations ****************************************/

#ifdef __IAR_SYSTEMS_ASM__

//REPLACE WITH ASSEMBLER-SPECIFIC DECLARATIONS (IF THERE ARE ANY)

#endif   /* __IAR_SYSTEMS_ASM__ */

/***************************************************************************
 **
 **   CHIP_NAME + KIND OF (OFTEN INTERRUPT) VALUE  VALUES
 **
 ***************************************************************************/
//REPLACE WITH VALUES

#endif   /* __IOCHIPNAME_H */
```