

# Introduction to Digital Logic

## EECS/CSE 31L

### Assignment 0

EECS Department  
Henry Samueli School of Engineering  
University of California, Irvine

January, 5, 2015

Due on Saturday 1/12/2015 5:00pm. Note: this is a one-week assignment
---

## 1 Getting familiar with CAD tools [10 bonus points]

The goal of this assignment is to get familiar with Computer Aided Design (CAD) tools you will work with during this course.

### 1.1 Assignment Description

Follow the instructions in the tutorial for each tool appended to this text. CAD tools you will work with them in this assignment are:

**Tool 1:** ISE<sup>®</sup> WebPACK<sup>™</sup> design software from Xilinx

**Tool 2:** ModelSim from Mentor Graphics

**Tool 3:** Incisive Enterprise Simulator from Cadence

### 1.2 Assignment Deliverable

Your submission should include the following:

- A short report on what you have learned
- Compilation, Elaboration, and Simulation logs
- Waveform snapshot from each tool

**Note:** Remember to name your files as **assignment0\_STUDENT-ID\_report.pdf**.