UNIVERSITY OF ALASKA FAIRBANKS

COLLEGE OF ENGINEERING AND MINES

DEPARTMENT OF ELECTRICAL AND COMPUTER ENGINEERING

COURSE CODE	EE F102 F01 (CRN: 34544)		
COURSE NAME	INTRODUCTION TO ELECTRICAL AND COMPUTER ENGINEERING		
SEMESTER	SPRING	**YEAR**	2022
LABORATORY LOCATION	ELIF 331 (ELECTRONICS LAB)		
LAB SESSION DATE AND TIME	MONDAY 21 FEB 2022		
TYPE OF SUBMISSION	LABORATORY REPORT	**NUMBER OF SUBMISSION**	5
TITLE OF SUBMISSION	BOARD LAYOUT		
METHOD OF SUBMISSION	ONLINE TO: maher.albadri@alaska.edu		
DUE DATE OF SUBMISSION	MONDAY 28 FEB 2022	**DUE TIME OF SUBMISSION**	23:59

STUDENT NAME	Jacob Guenther

MAKE THIS FORM A "COVER PAGE" FOR YOUR REPORT SUBMISSION.

FOR THE TA USE ONLY

REMARKS:

1 Objective

The goal of this lab is to learn how to layout a simple PCB using Candance. We design the circuit then place the components and traces. Finally we export the .art files which describe the placement of traces, holes, and edges cuts.

2 Equipment

- Lab Computer Running Cadance

3 Observations and Results

Figure 1: Image of the completed PCB.

4 Conclusion

5 References

[1] Denise Thorsen, Maher Al-Badri, INTRODUCTION TO ELECTRICAL AND COMPUTER ENGINEERING, University of Alaska Fairbanks, 2022.