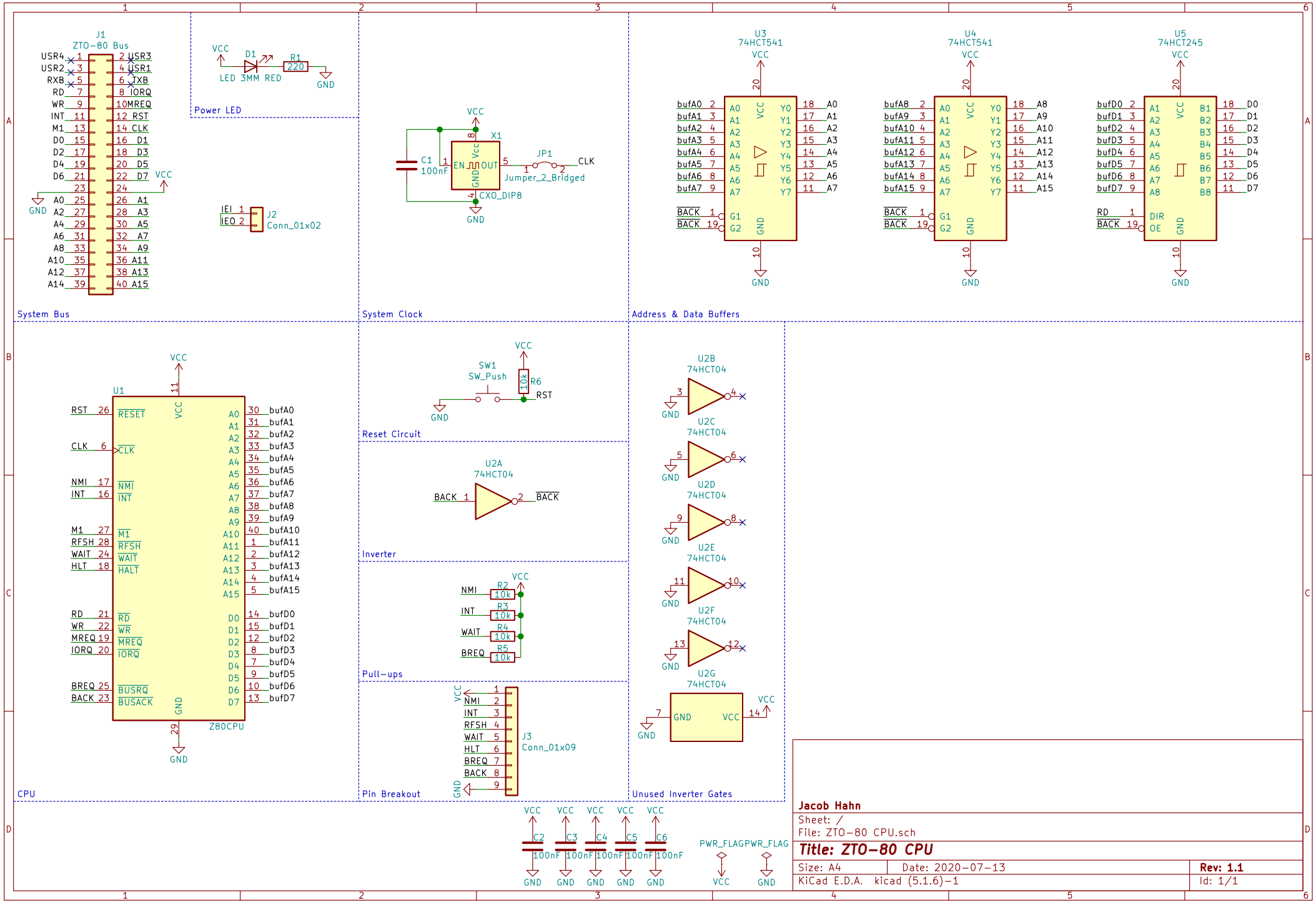
J1
ZTO-80 Bus
System Bus
Power LED
D1
R1
220
LED 3MM RED
VCC
GND
J2
Conn_01x02
IEI
IEO
System Clock
X1
C1
100nF
EN
OUT
CXO_DIP8
JP1
Jumper_2_Bridged
CLK
U3
74HCT541
U4
74HCT541
U5
74HCT245
Address & Data Buffers
U1
Z80CPU
CPU
Reset Circuit
SW1
SW_Push
R6
10k
RST
Inverter
U2A
74HCT04
BACK
Pull-ups
R2
R3
R4
R5
NMI
INT
WAIT
BREQ
Pin Breakout
J3
Conn_01x09
U2B
U2C
U2D
U2E
U2F
U2G
Unused Inverter Gates
C2
C3
C4
C5
C6
100nF
PWR_FLAG
Jacob Hahn
Sheet: /
File: ZTO-80 CPU.sch
Title: ZTO-80 CPU
Size: A4
Date: 2020-07-13
Rev: 1.1
KiCad E.D.A. kicad (5.1.6)-1
Id: 1/1