DS5110/CS5501 Caching Policy Worksheet

Problen	n 1 FIFO,	size 2:	1, 2 (full), 3, take out 1, put in 1 (full)	recent		
Data. Hit?						
1 miss, starting with an empty state						
2 miss						
hit, because it's there Hit ratio: 1/5 = 0.2						
1 miss						
Problen	1 2 LRU,	size 2:	A, B (full)	recent		
Data. Hit?						
<u> </u>	B miss					
Α _	A Hit ratio: $\frac{2/5 = 0.4}{}$					
C miss						
A hit						
Problem 3 LRU, siz		size 3:	W, X, Y, evict W and	recent		
Data. Hit?						
W miss (compulsory						
w [miss)					
	hit '	1 11441 -	. 4/8 = 0.5			
X	miss (compulsory)					
Y	miss (compulsory)		ency: 20 ms			
Υ	hit		ncy: 0.1 ms			
Z	miss	Average	e latency:			
Υ	hit					
Х						

Problem 4 LRU, size 4:	3, 4, 5, 6 (full), take out 3 and add 7, take out 4 and add 3, take out 5 and add 4, take out 6 and add 5, take out 7 and				
Data. Hit?	add 6, take out 3 and add 7				
3 miss (compulsory)					
4 miss (compulsory)					
5 miss (compulsory) Hit ration	0: 0				
6 miss (compulsory)	Delegate Anomaly when the westing act (av. (2. 4. 5.				
7 miss	Belady's Anomaly: when the working set (ex: {3, 4, 5, 6, 7}) is one more than the size (ex: 4) OR some				
3 miss	similar instance (like timing, etc)				
4 miss					
5 miss					
6 miss					
7 miss					
Problem 5 LRU, size 5: 3, 4, 5, 6, 7 (full), move 3, move 4, move 5, recent					
Data. Hit?	move 6, move 7				
3					
4					
	o: <u>0.5</u>				
6					
7 miss (compulsory)					
3 hit					
4 hit					
5 hit					
6 hit					
7 hit					