All save and load instructions were created in the same INUM since they have the same control outputs																								
				Inputs				Control Outputs										Input Bits				Output Bits	Actual	
INUM	Instruction	Mnemonic	Format	Opcode	funct3	funct7	funct6	RFW	ALUOp	ALUSrcA	ALUSrcB	PCsel	WDSel	BrOp	MR	STR	LD	opcode	funct3	funct7	funct6		Output Bits	
0	addi	ADDI	I .	0010011	000	XXXXXXXX	XXXXXX	1	000	0	001	0	0	110	0	0	0	060010011	06000	Obxxxxxxx	Obxxxxxx	0b1000000100110000	061000000100110000	
1	add	ADDR	R	0110011	000	0000000	XXXXXX	1	000	0	000	0	0	110	0	0	0	060110011	06000	060000000	Obxxxxxx	0b1000000000110000	06100000000110000	
2	jal	JAL	J	1101111	XXX	XXXXXXXX	X00000X	1	000	1	010	1	1	110	0	0	0	061101111	0bxxx	Obxxxxxxx	Obxxxxxx	0b1000101011110000	061000101011110000	
3	jalr	JALR	1	1100111	000	XXXXXXXX	X00000X	1	000	0	001	1	1	110	0	0	0	061100111	06000	Obxxxxxxx	Obxxxxxx	0b1000000111110000	061000000111110000	
4	sil	SLLR	R	0110011	001	0000000	X00000X	1	011	0	000	0	0	110	0	0	0	060110011	06001	060000000	Obxxxxxx	0b1011000000110000	0b1011000000110000	
5	slli	SLLI	I .	0010011	001	XXXXXXXX	000000	1	011	0	001	0	0	110	0	0	0	060010011	06001	Obxxxxxxx	06000000	0b1011000100110000	061011000100110000	
6	srl	SRLR	R	0110011	101	0000000	X00000X	1	100	0	000	0	0	110	0	0	0	060110011	0b101	060000000	Obxxxxxxx	0b1100000000110000	061100000000110000	
7	srli	SRLI	I	0010011	101	X000000X	000000	1	100	0	001	0	0	110	0	0	0	060010011	0b101	Obxxxxxxxx	06000000	0b1100000100110000	061100000100110000	
8	mul	MUL	R	0110011	000	0000001	X00000X	1	010	0	000	0	0	110	0	0	0	060110011	06000	060000001	Obxxxxxxx	0b1010000000110000	061010000000110000	
9	beq	BEQ	В	1100011	000	X000000X	X00000X	0	000	1	011	0	1	000	0	0	0	061100011	06000	Obxxxxxxxx	Obxxxxxxx	060000101101000000	060000101101000000	
9	bne	BNE	В	1100011	001	X000000X	X00000X	0	000	1	011	0	1	001	0	0	0	061100011	06001	Obxxxxxxxx	Obxxxxxxx	060000101101001000	060000101101001000	
9	bit	BLT	В	1100011	100	X000000X	X00000X	0	000	1	011	0	1	100	0	0	0	061100011	0b100	Obxxxxxxxx	Obxxxxxxx	060000101101100000	060000101101100000	
9	bge	BGE	В	1100011	101	X000000X	X00000X	0	000	1	011	0	1	101	0	0	0	061100011	0b101	Obxxxxxxxx	Obxxxxxxx	060000101101101000	060000101101101000	
10	lui	LUI	U	0110111	XXX	X000000X	X00000X	1	101	0	101	0	0	110	0	0	0	060110111	0bxxx	Obxxxxxxxx	Obxxxxxxx	061101010100110000	0b1101010100110000	
11	lb	LB	I .	0000011	000	X000000X	X00000X	1	000	0	001	0	0	110	1	0	1	060000011	06000	Obxxxxxxxx	Obxxxxxxx	0b1000000100110101	061000000100110101	
12	sb	SB	S	0100011	000	X000000X	X00000X	0	000	0	100	0	0	110	0	1	1	060100011	06000	Obxxxxxxxx	Obxxxxxx	060000010000110011	060000010000110011	
13	lw	LW	I	0000011	010	X000000X	X00000X	1	000	0	001	0	0	110	1	0	1	060000011	06010	Obxxxxxxxx	Obxxxxxxx	0b1000000100110101	061001000000110000	
14	sw	SW	S	0100011	010	X000000X	X00000X	0	000	0	100	0	0	110	0	1	1	060100011	06010	Obxxxxxxxx	Obxxxxxxx	060000010000110011	061110000000110000	
15	ld	LD	I	0000011	011	X000000X	X00000X	1	000	0	001	0	0	110	1	0	1	060000011	06011	Obxxxxxxxx	Obxxxxxxx	0b1000000100110101	061111000000110000	
16	sd	SD	S	0100011	011	X000000X	X00000X	0	000	0	100	0	0	110	0	1	1	060100011	06011	Obxxxxxxxx	Obxxxxxxx	060000010000110011		
17	sub	SUB	R	0110011	000	0100000	X00000X	1	001	0	000	0	0	110	0	0	0	060110011	06000	060100000	Obxxxxxxx	0b1001000000110000		
18	and	AND	R	0110011	111	0000000	X00000X	1	110	0	000	0	0	110	0	0	0	060110011	0b111	060000000	Obxxxxxxx	0b1110000000110000		
19	or	OR	R	0110011	110	0000000	X00000X	1	111	0	000	0	0	110	0	0	0	060110011	0b110	0ь0000000	Obxxxxxxx	0b1111000000110000		
																		1						