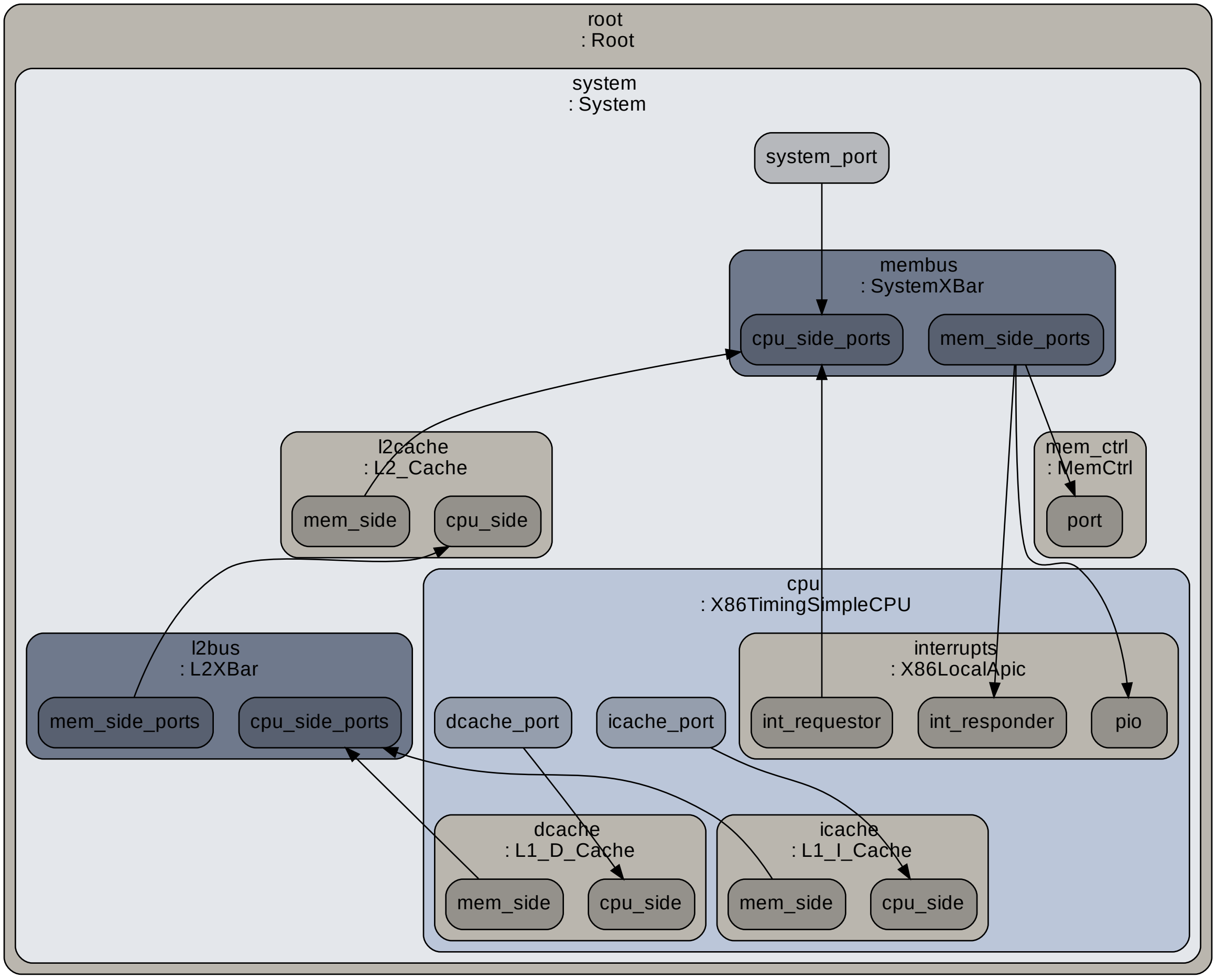
root
: Root
system
: System
system_port
membus
: SystemXBar
cpu_side_ports
mem_side_ports
l2cache
: L2_Cache
mem_side
cpu_side
mem_ctrl
: MemCtrl
port
cpu
: X86TimingSimpleCPU
l2bus
: L2XBar
mem_side_ports
cpu_side_ports
interrupts
: X86LocalApic
int_requestor
int_responder
pio
dcache_port
icache_port
dcache
: L1_D_Cache
mem_side
cpu_side
icache
: L1_I_Cache
mem_side
cpu_side