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# sched\_rr\_timeslice\_MS\_150

increased round robin time slice to 150 ms from 100ms

### **Test Systems:**

#### round robin 150ms

Processor: Intel Core i5-1145G7 (4 Cores), Motherboard: Intel 440BX (6.00 BIOS), Chipset: Intel 440BX/ZX/DX, Memory: 8GB, Disk: 107GB VMware Virtual S, Graphics: VMware SVGA II, Audio: Ensoniq ES1371/ES1373, Network: Intel 82545EM

OS: Ubuntu 18.04, Kernel: 5.4.0-148-generic (x86\_64), Desktop: GNOME Shell 3.28.4, Display Server: X Server 1.20.8, Compiler: GCC 7.5.0, File-System: ext4, Screen Resolution: 1718x878, System Layer: VMware

Kernel Notes: Transparent Huge Pages: madvise

Compiler Notes: --build=x86\_64-linux-gnu --disable-vtable-verify --disable-werror --enable-bootstrap --enable-checking=release --enable-clocale=gnu --enable-default-pie --enable-gnu-unique-object --enable-languages=c,ada,c++,go,brig,d,fortran,objc,obj-c++ --enable-libmpx --enable-libstdcxx-debug --enable-libstdcxx-time=yes --enable-multiilib --enable-nls --enable-objc-gc=auto --enable-offload-targets=nvptx-none --enable-plugin --enable-shared --enable-threads=posix --host=x86\_64-linux-gnu --program-prefix=x86\_64-linux-gnu --target=x86\_64-linux-gnu --with-abi=m64 --with-arch-32=i686 --with-default-libstdcxx-abi=new --with-gcc-major-version-only --with-multiilb-list=m32,m64,mx32 --with-target-system-zlib --with-tune=generic --without-cuda-driver -v



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Security Notes: itlb\_multihit: Not affected + I1tf: Mitigation of PTE Inversion + mds: Vulnerable: Clear buffers attempted no microcode; SMT Host state unknown + meltdown: Mitigation of PTI + mmio\_stale\_data: Not affected + retbleed: Mitigation of IBRS + spec\_store\_bypass: Mitigation of SSB disabled via prctl and seccomp + spectre\_v1: Mitigation of usercopy/swapgs barriers and \_\_user pointer sanitization + spectre\_v2: Mitigation of IBRS IBPB: conditional RSB filling PBRSB-eIBRS: Not affected + srbds: Not affected + tsx\_async\_abort: Not affected

#### round robin 150ms

Apache HTTP Server - 4 (Regs/sec) 13451

Standard Deviation 0.1%

Apache HTTP Server - 20 (Reqs/sec) 16491

Standard Deviation 0.4%

Apache HTTP Server - 100 (Reqs/sec) 18768

Standard Deviation 1.2%

Apache HTTP Server - 200 (Reqs/sec) 19184

Standard Deviation 0.4%

Apache HTTP Server - 500 (Reqs/sec) 17973

Standard Deviation 0.1%

Apache HTTP Server - 1000 (Reqs/sec) 17498

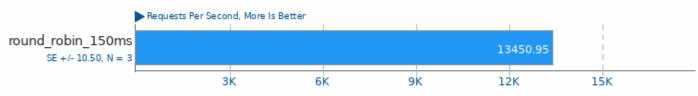
Standard Deviation 0.3%

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#### sched rr timeslice MS 150



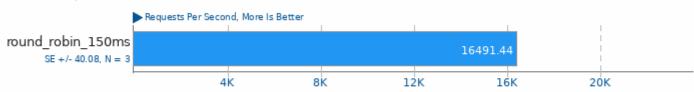




1. (CC) gcc options: -lluajit-5.1 -lm -lssl -lcrypto -lpthread -ldl -std=c99 -O2

#### Apache HTTP Server 2.4.56

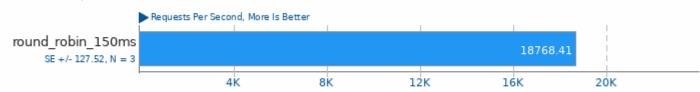
Concurrent Requests: 20



1. (CC) gcc options: -lluajit-5.1 -lm -lssl -lcrypto -lpthread -ldl -std=c99 -O2

## Apache HTTP Server 2.4.56

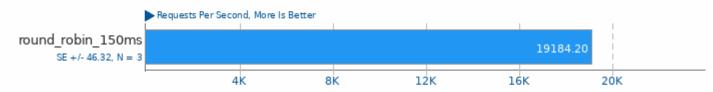
Concurrent Requests: 100



1. (CC) gcc options: -lluajit-5.1 -lm -lssl -lcrypto -lpthread -ldl -std=c99 -O2

#### Apache HTTP Server 2.4.56

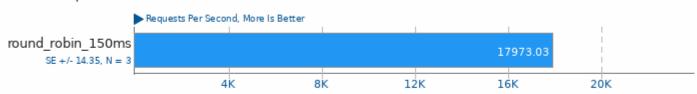
Concurrent Requests: 200



1. (CC) gcc options: -lluajit-5.1 -lm -lssl -lcrypto -lpthread -ldl -std=c99 -O2

# Apache HTTP Server 2.4.56

Concurrent Requests: 500

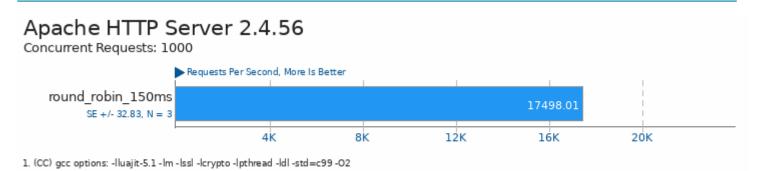


1. (CC) gcc options: -lluajit-5.1 -lm -lssl -lcrypto -lpthread -ldl -std=c99 -O2

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This file was automatically generated via the Phoronix Test Suite benchmarking software on Sunday, 7 May 2023 10:37.

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