

JAGADEESHWAR

Dedicated VLSI trainee | Quick Learner | Skilled in VLSI

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Summary

A highly motivated and skilled ECE student, As ECE student, I possess a solid foundation in electrical engineering principles and coding languages coupled with practical experience in computer engineering concepts. I am ready With a strong desire to contribute to innovative projects and learn from industry experts, I am confident that I can make a valuable contribution to the company.

SKILLS

Technical: c and Python languages with data structures .

Hardware: VLSI DESIGN, DIGITAL LOGIC DESIGN, VERILOG, SOC, and RTL Design,

CAD tools: MATLAB, XILINX VIVADO, and Cadence Virtuoso.

EXPERIENCE

AGILE (WORKSHOP)

VLSI DESIGN INTERNSHIP

INTEL UNNATI SUMMER INTERN PROJECT (PROTECTION OF PASSWORD USING SGX)

PROJECTS

1. PCB DESIGN (ELECTRICAL PIANO) .
2. VERILOG WITH FPGA (VOTING MACHINE AND 7 SEGMENT DISPLAY) .

EDUCATION

GITAM UNIVERSITY (UG IN Electrical and Communication Engineering) 2021-2025 (INDIA, Bangalore.)

NARAYANA SCHOOL OF TECHNOLOGIES (10TH with 9.8 CGPA and 12th with 91%)

LANGUAGES

ENGLISH, TELUGU, KANADA

CERTIFICATION

1. PROGRAMMING FOR EVERYBODY (GETTING STARTED WITH PYTHON) [link](#)
2. DIGITAL SYSTEMS: FROM LOGIC GATES TO PROCESSORS [link](#)
3. VLSI System-On-Chip Design - Overview (IN MAVEN SILICON)

📄 [VLSI_System_On_Chip_Design_-_Overview-Busireddy_Jagadeeshwar_Reddy.pdf](#)

