This section shows how the inverting, noninverting, and differential configurations are useful in such applications as summing, scaling and averaging amplifiers.

1. Inverting Configuration

Figure 1 shows the inverting configuration with three inputs Va, Vb and Vc. Depending on the relationship between the feedback resistor RF and the input resistors Ra, Rb, and Rc, the circuit can be used as either a summing amplifier, scaling amplifier, or averaging amplifier.

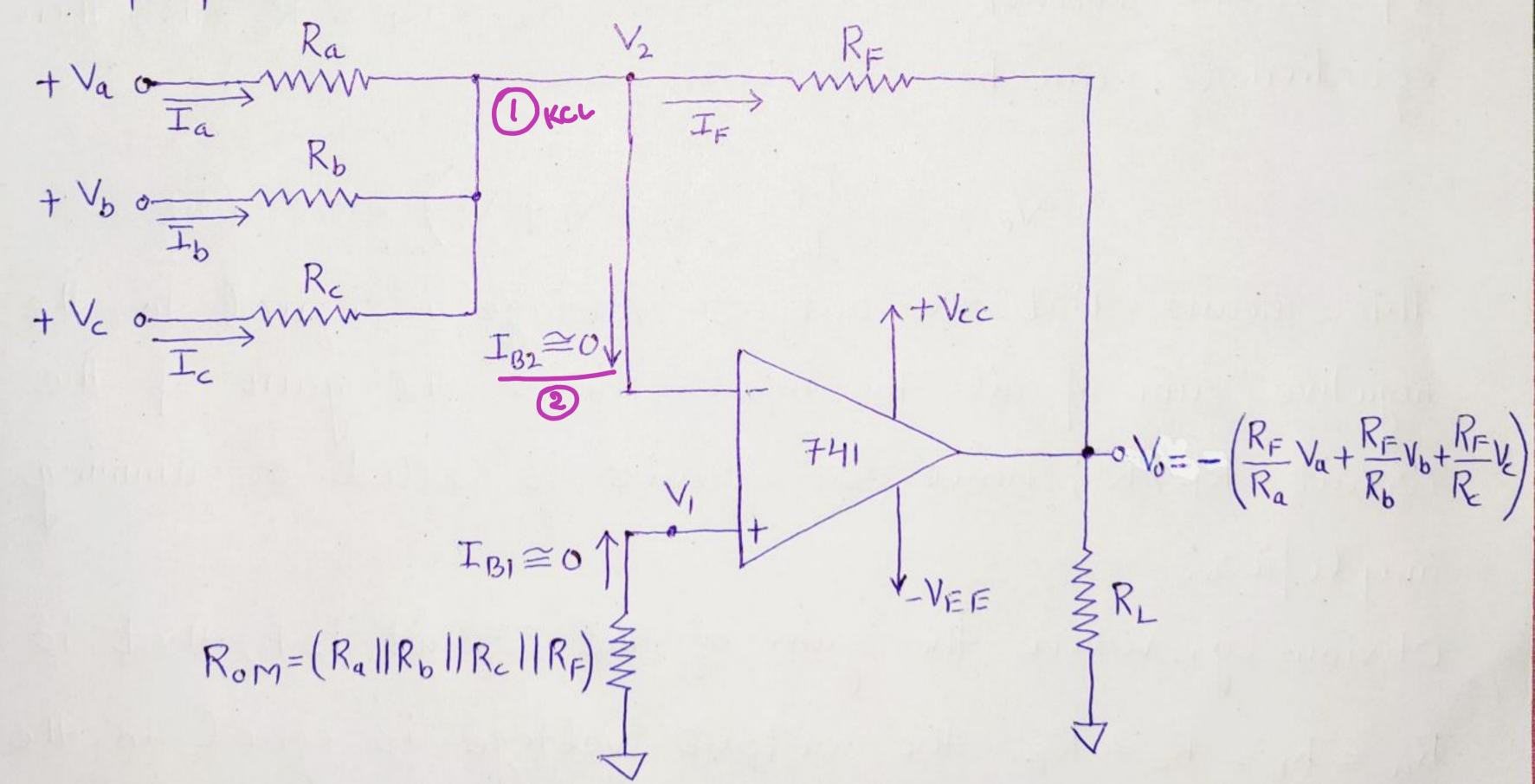


Figure 1. Inverting configuration with three inputs can be used as a summing amplifier, scaling amplifier, or averaging amplifier.

The circuit's function can be verified by examining the expression for the output voltage Vo, which is obtained

from Kirchoff's current equation written at node (2)

$$I_a + I_b + I_c = I_B + I_F - O$$

Since Ri and A of the op-amp are ideally infinity, $I_B = 0A$ and $V_1 = V_2 \cong 0 V$.

There fore

$$\frac{V_a}{R_a} + \frac{V_b}{R_b} + \frac{V_c}{R_c} = -\frac{V_o}{R_F}$$

0/2

$$V_o = -\left(\frac{R_F}{R_a}V_a + \frac{R_F}{R_b}V_b + \frac{R_F}{R_c}V_c\right) - (2)$$

Summing Amplifier

If in the circuit of Figure 1, $R_a = R_b = R_c = R$, then equation 2 can be rewritten as

This means that the output voltage is equal to the negative sum of all the inputs times the gain of the circuit R_F/R ; hence the circuit is called a summing amplifier.

Obviously, when the gain of the circuit is 1, that is $R_a = R_b = R_c = R_F$, the output voltage is equal to the negative sum of all input voltages. Thus

Scaling or weighted Amplifier

If each imput voltage is amplified by a different

factor, in other words, weighted differently at 3 the output, then the circuit in Figure 1 is then called a scaling or weighted amplifier. This condition can be accomplished if Ra, Rb, and Rc are different in value. Thus the output voltage of the scaling amplifier is

$$V_o = -\left(\frac{R_F}{R_a}V_a + \frac{R_F}{R_b}V_b + \frac{R_F}{R_c}V_c\right) - (5)$$

where

$$\frac{R_F}{R_a} + \frac{R_F}{R_b} + \frac{R_F}{R_c}$$

Average Circuit

The circuit of Figure I can be used as an averaging circuit, in which the output voltage is equal to the average of all the input voltages. This is accomplished by using all input resistors of equal value, i.e., $R_a = R_b = R_c = R.$

In addition, the gain by which each input is amplified must be equal to I over the number of inputs; that is

$$\frac{R_F}{R} = \frac{1}{n}$$

where n is the number of inputs.

Thus, if there are three inputs (as shown in Figure 1) we want $R_F/R = \frac{1}{3}$. Consequently, from equation (3) $V_0 = -\left(\frac{V_0 + V_0 + V_c}{2}\right) - \frac{1}{3}$

Remember that in the preceding applications, the 4 inputs Va, Vb, and Vc could be either ac or dc. These circuits are commonly used in analog computers and audio miners, in which a number of inputs is added up to produce a desired output.

In Figure 1, the offset minimizing resistor Rom is used to minimize the effect of input bias worrents on the output offset voltage.

2. Noninverting Configuration

If imput voltage sources and resistors are connected to the noninverting terminal as shown in Figure 2, the circuit can be used either as a summing or averaging amplifier through selection of appropriate values of resistors, that is, R, and RF.

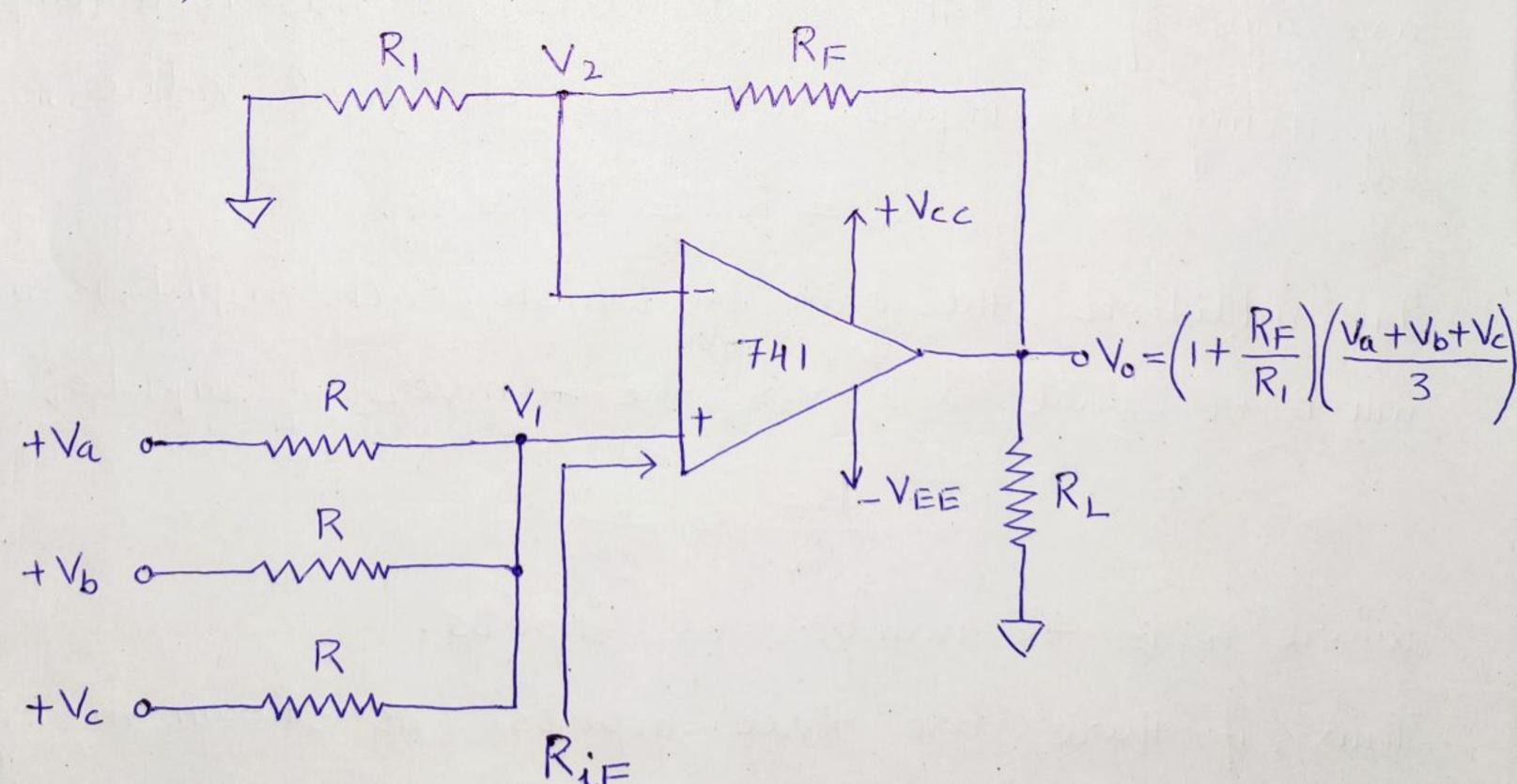


Figure 2. Noninverting configuration with three inputs can be used as an averaging or summing amplifier.

Recall that the input resistance Rif of the non- 5 inverting amplifier is very large. Therefore, using the superposition theorem, the voltage V, at the noninverting terminal is

$$V_{1} = \frac{R/2}{R + R/2} V_{a} + \frac{R/2}{R + R/2} V_{b} + \frac{R/2}{R + R/2} V_{c}$$
or,
$$V_{1} = \frac{V_{a}}{3} + \frac{V_{b}}{3} + \frac{V_{c}}{3} = \frac{V_{a} + V_{b} + V_{c}}{3} - 7$$

Hence the output voltage Vo is

$$V_{0} = \left(1 + \frac{R_{F}}{R_{I}}\right)V_{I}$$

$$V_{0} = \left(1 + \frac{R_{F}}{R_{I}}\right)\frac{V_{a} + V_{b} + V_{c}}{3} - \frac{8}{3}$$

Averaging amplifier

Equation (8) shows that the output voltage is equal to the average of all input voltages times the gain of the circuit (1+ RF/R1), hence the name averaging amplifier. Depending on the application requirement, the gain (1+ RF/R1) can be set to a specific value. Obviously, if the gain is 1, the output voltage will be equal to the average of all input voltages.

Note that there are two basic differences between this averaging amplifier and that using the inverting configuration:

- 1. No sign change or phase reversal occurs between 6 the average of the inputs and output.
- 2. The noninverting input Voltage V, is the average of all inputs, whereas in the inverting averaging amplifier, the output is the average of all inputs, with a negative sign.

Summing amplifier

A close examination of equation (8) reveals that if the gain (1+ RF/RI) is equal to the number of inputs, the output voltage becomes equal to the sum of all input voltages. That is, if (1+ RF/R1) = 3,

Vo = Va + Vb + Vc Hence the circuit is called a noninverting summing amplifier.

Q1. In the circuit of Figure 1, Va = +1 V, Vb = +2 V, Vc = +3V, Ra = Rb = Rc = 3 K_D, RF = 1K_D; Rom = 270-12, and supply voltages = ±15 V. Assuming that the op-amp is initially nulled, determine the output voltage Vo.

Solution: Using equation (3), we obtain, Vo = - RF (Va + Vb + Vc)

$$V_0 = -\frac{1}{3}(1+2+3) = -2V$$

This value is equal to the average of three (7) inputs with a negative sign.

Q2. In the circuit of Figure 2, supply voltages = $\pm 15 \text{ V}$, Va = +2 V, Vb = -3 V, Vc = +4 V, $R = R_1 = 1 \text{ K} \cdot \Omega$ and $R_F = 2 \text{ K} \cdot \Omega$. Determine the voltage V_1 at the non-inverting terminal and the output voltage Vo. Assume that the op-amp is initially nulled.

Solution: Using equation (7),

$$V_1 = \frac{2-3+4}{3} = 1 \vee$$

which is the average of three inputs: +2V, -3V, and +4V. From equation (8),

$$V_0 = \left(1 + \frac{2}{1}\right) \times 1 = 3 \vee$$

Note:
$$V_0 = \left(1 + \frac{R_F}{R_I}\right) \left(\frac{Va + V_b + V_c}{3}\right)$$

No is the sum of three inputs in this example.