

Standard Template for Academic Tasks (22231)

**Academic Task Number:** CA1**Date of allotment:** 14th Feb, 2024**Date of submission:** 14th Feb, 2024**Course code:** ECE310**Course title:** Fundamental of Microprocessor and Micro controller**Maximum Marks:** 30**Academic Task Type:** SETA

Question Number	Question Statement	Course Outcome	Bloom's level	Marks per Question
Q1	A) What is an interrupt. How many hardware interrupts are there in 8085? Explain priority interrupts in 8085. B) Steps involved to fetch a byte in 8085.	CO1	L1: Remember	5
Q2	What will be the functions of following instruction and also give how many bytes instruction is A. LDA 3000H B. XCHG C. SHLD	CO2	L3: Apply	5
Q3	Define different types of addressing Modes of 8085 with one-one examples?	CO3	L6: Create	5
Q4	What is the difference between MOV and MVI, explain with examples.	CO1	L1: Remember	5
Q5	A) Discuss how DMA data transfer is implemented in an 8085 microprocessor system. Explain the role of the 8085 in initiating and completing a DMA transfer, including how it interacts with peripheral devices and the DMA controller during the transfer process. B) List few applications of microprocessor-based system.	CO2	L3: Apply	10

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Academic Task Type: SETB

Question Number	Question Statement	Course Outcome	Bloom's level	Marks per Question
Q1	Explain any five instructions with examples from data transfer instruction Set? Write the few differences between Micro controller and Microprocessor.	CO1	L1: Remember	5
Q2	Give number of bytes and function of following instruction in detail? A. STA 3000H B. PCHL C. LHLD	CO2	L3: Apply	5
Q3	Discuss the steps involved in configuring the interrupts, the role of the interrupt service routine (ISR), and how priority is managed.	CO3	L6: Create	5
Q4	Define status flag? If we perform the ADD operation then what will be the status of the flags indicate with figure	CO1	L1: Remember	5
Q5	Explain how serial communication is achieved in the 8085 microprocessor, focusing on the use of SID and SOD lines. Discuss the configuration of the 8085 for serial communication, including any necessary programming steps and how data integrity is ensured during transmission.	CO2	L3: Apply	10

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