# Gate Processing Logic.

Review of gate processing

1. A NOT gate inverts its single input value
2. An AND gate produces 1 if both input values are 1
3. An OR gate produces 1 if one or the other or both input values are 1
4. An XOR gate produces 1 if one or the other (but not both) input values are 1
5. A NAND gate produces the opposite results of an AND gate
6. A NOR gate produces the opposite results of an OR gate