



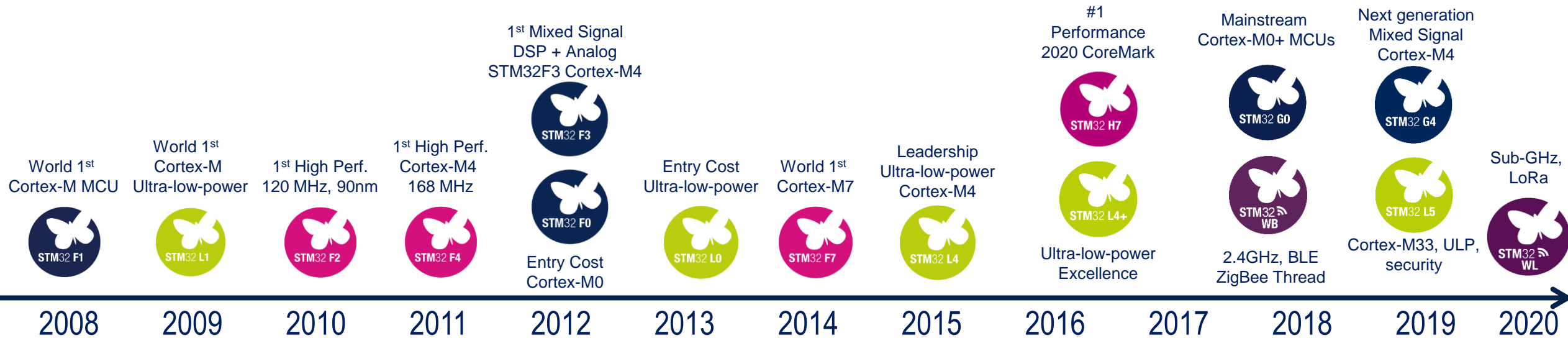
life.augmented

# STM32WL marketing presentation

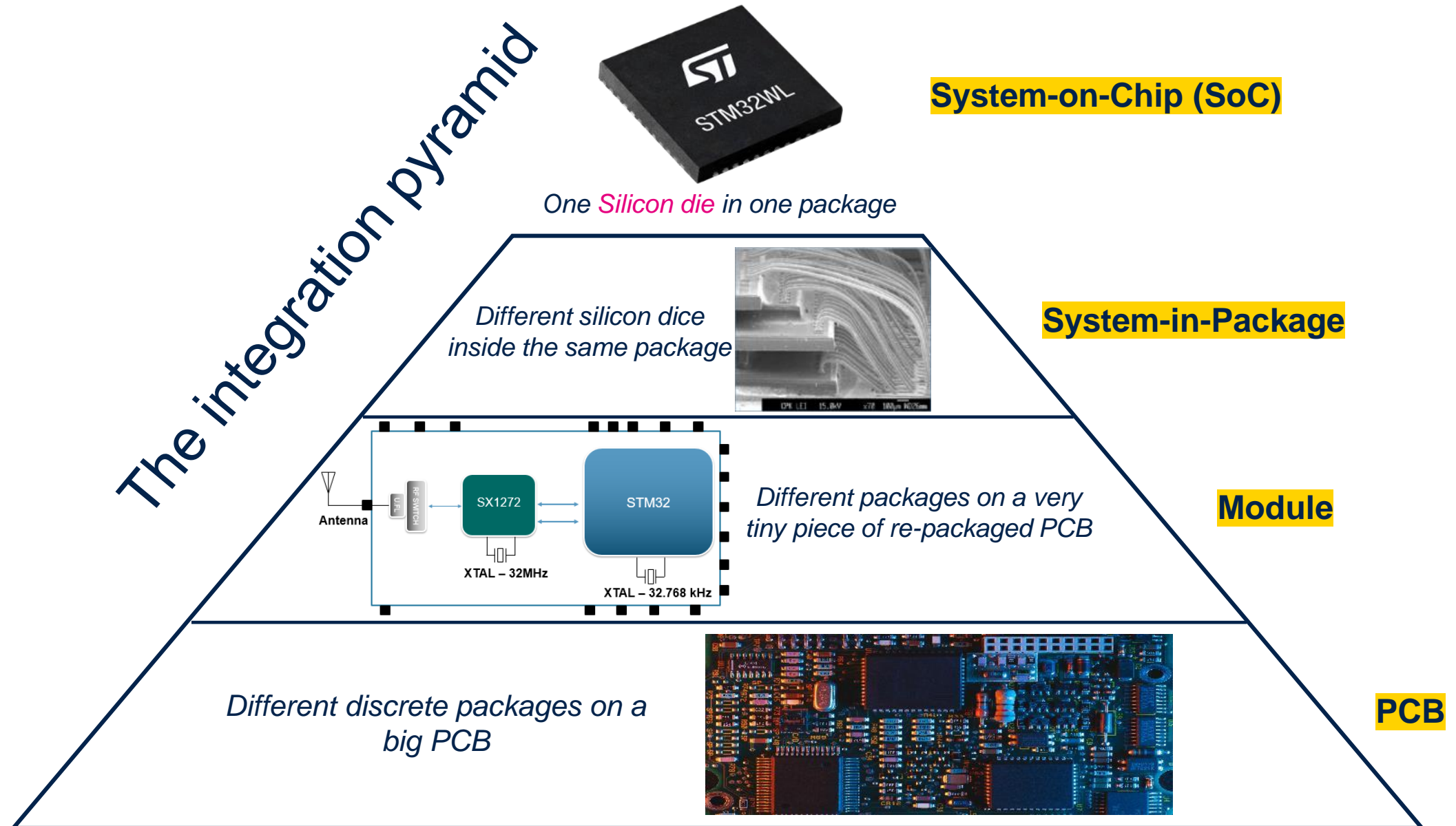
# Continuing the STM32 Success Story

## Leader in ARM Cortex-M 32-bit MCUs / SoCs

- Mainstream / General Purpose category
- High Performance category
- Ultra Low Power category
- Wireless category



# STM32WL: first LoRa-enabled SoC in the world

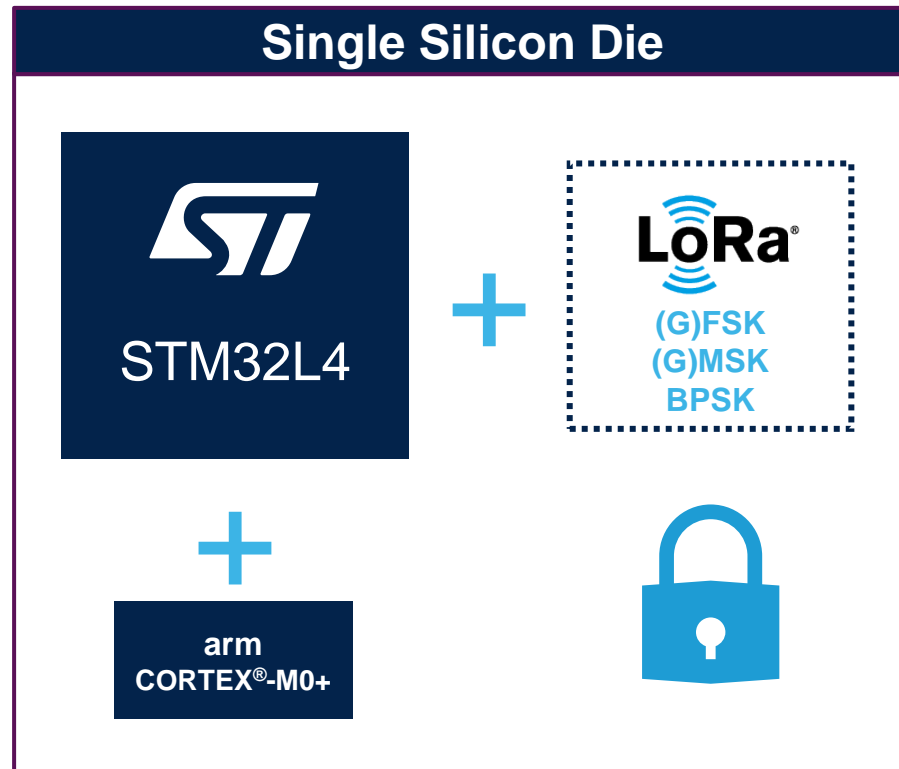




# System-on-chip made for versatility

A Long-Range Wireless Microcontroller:  
one die, many IoT possibilities

**World First!**



=



# 4 modulations - many protocols



Proprietary



Proprietary

# STM32WLEx Line - a rich feature set

<b>Control</b>	<b>Arm® Cortex®-M4 DSP 48 MHz</b>	<b>Memory</b>
Power supply 1.8 to 3.6 V w/ DCDC+ LDO POR/PDR/PVD/BOR	Nested vector interrupt controller (NVIC)	Up to 256-Kbyte Flash
Crystal oscillators 32 MHz (Radio + HSE) 32.768 KHz (LSE)	Memory protected unit (MPU)	Up to 64-Kbyte SRAM
Internal RC oscillators 32,768 KHz + 16 MHz + 48 MHz ± 1% acc. over V and T(°C)	JTAG/SW debug	Boot Lock
RTC/AWU/CSS	ART Accelerator™	Boot loader
PLL	AHB Bus matrix	
SysTick timer	2x DMA 7 channels	<b>Timers</b>
2 watchdogs (WWDG/IWDG)	<b>Radio</b>	1 x 32-bit timer
43 GPIOs	LoRa®, (G)FSK, (G)MSK, BPSK	3x 16-bit timers 3x ULP 16-bit timers
Cyclic redundancy check	+15dBm & +22dBm Power Outputs -148 dBm sensitivity (LoRa)	<b>Analog</b>
Voltage scaling (2 modes)	150 MHz to 960 MHz	1x 12-bit ADC SAR 2.5 Msps
<b>Security</b>		12-bit DAC
AES 256-bit + TRNG + PCROP		2x ULP comparators
Tamper detection		Temperature sensor
		<b>Connectivity</b>
		2x SPI, 3x I2C
		2x USART LIN, smartcard, IrDA, Modem control
		1x ULP UART

## KEY FEATURES

- Arm® Cortex®-M4 & DSP up to 48 MHz
- Up to 256 KB Flash and 64 KB SRAM
- **Sub-GHz Radio**
  - Multi-modulation: LoRa, (G)FSK, (G)MSK, BPSK
  - 2 embedded power amplifiers:
    - 1 output up to +15 dBm
    - 1 output up to +22 dBm
  - LoRa RX sensitivity: -148 dBm (SF12, BW=10.4kHz)
  - RX: 4.82mA and TX: 15mA (at 10dBm) / 87mA (at 20dBm) [3.3V]
- **Ultra-Low Power consumption**
  - < 71µA/MHz Active mode (3V - RF OFF)
  - 1 µA Stop2 mode with RAM retention
  - 390 nA Standby mode with RTC
  - 31 nA Shutdown mode
- **Peripherals**
  - 3xI<sup>2</sup>C, 2xUSART, 1xLP-UART, 2xSPI
  - 7x timers + 2x ULP Comparators
- 1.8 to 3.6V voltage range (DC/DC, LDO)
- -40 to up to +105°C temperature range

# STM32WL5x Line - a rich feature set



## Dual-core and enhanced security

<b>Control</b> <ul style="list-style-type: none"> <li>Power supply 1.8 to 3.6 V w/ DCDC+ LDO POR/PDR/PVD/BOR</li> <li>Crystal oscillators 32 MHz (Radio + HSE) 32.768 KHz (LSE)</li> <li>Internal RC oscillators 32,768 KHz + 16 MHz + 48 MHz ± 1% acc. over V and T(°C)</li> <li>RTC/AWU/CSS</li> <li>PLL</li> <li>SysTick timer</li> <li>2 watchdogs (WWDG/IWDG)</li> <li>43 GPIOs</li> <li>Cyclic redundancy check</li> <li>Voltage scaling (2 modes)</li> </ul>	<b>Arm® Cortex®-M4 DSP 48 MHz</b> <ul style="list-style-type: none"> <li>Nested vector interrupt controller (NVIC)</li> <li>Memory protected unit (MPU)</li> <li>JTAG/SW debug</li> </ul>	<b>Memory</b> <ul style="list-style-type: none"> <li>Up to 256-Kbyte Flash</li> <li>Up to 64-Kbyte SRAM</li> <li>CM4 or CM0 Boot Lock</li> <li>Boot loader</li> <li>Hide protect</li> </ul>
	<b>ART Accelerator™</b> <ul style="list-style-type: none"> <li>AHB Bus matrix</li> <li>2x DMA 7 channels</li> </ul>	<b>Timers</b> <ul style="list-style-type: none"> <li>1 x 32-bit timer</li> <li>3x 16-bit timers 3x ULP 16-bit timers</li> </ul>
	<b>Radio</b> <ul style="list-style-type: none"> <li>LoRa®, (G)FSK, (G)MSK, BPSK</li> <li>+15dBm &amp; +22dBm Power Outputs -148 dBm sensitivity (LoRa)</li> <li>150 MHz to 960 MHz</li> </ul>	<b>Analog</b> <ul style="list-style-type: none"> <li>1x 12-bit ADC SAR 2.5 Msps</li> <li>12-bit DAC</li> <li>2x ULP comparators</li> <li>Temperature sensor</li> </ul>
<b>Security</b> <ul style="list-style-type: none"> <li>AES 256-bit + TRNG + PCROP</li> <li>Tamper detection</li> <li>Secure Areas</li> <li>Secure FW Install</li> <li>Debug control</li> <li>Boot Selection</li> <li>Secure Sub-GHz, MAC Layer, SFI</li> <li>Key Management Services</li> </ul>	<b>Arm® Cortex®-M0+ 48 MHz</b> <ul style="list-style-type: none"> <li>Nested vector interrupt controller (NVIC)</li> <li>Memory protected unit (MPU)</li> <li>SW debug</li> </ul>	<b>Connectivity</b> <ul style="list-style-type: none"> <li>2x SPI, 3x I2C</li> <li>2x USART LIN, smartcard, IrDA, Modem control</li> <li>1x ULP UART</li> </ul>

### KEY FEATURES

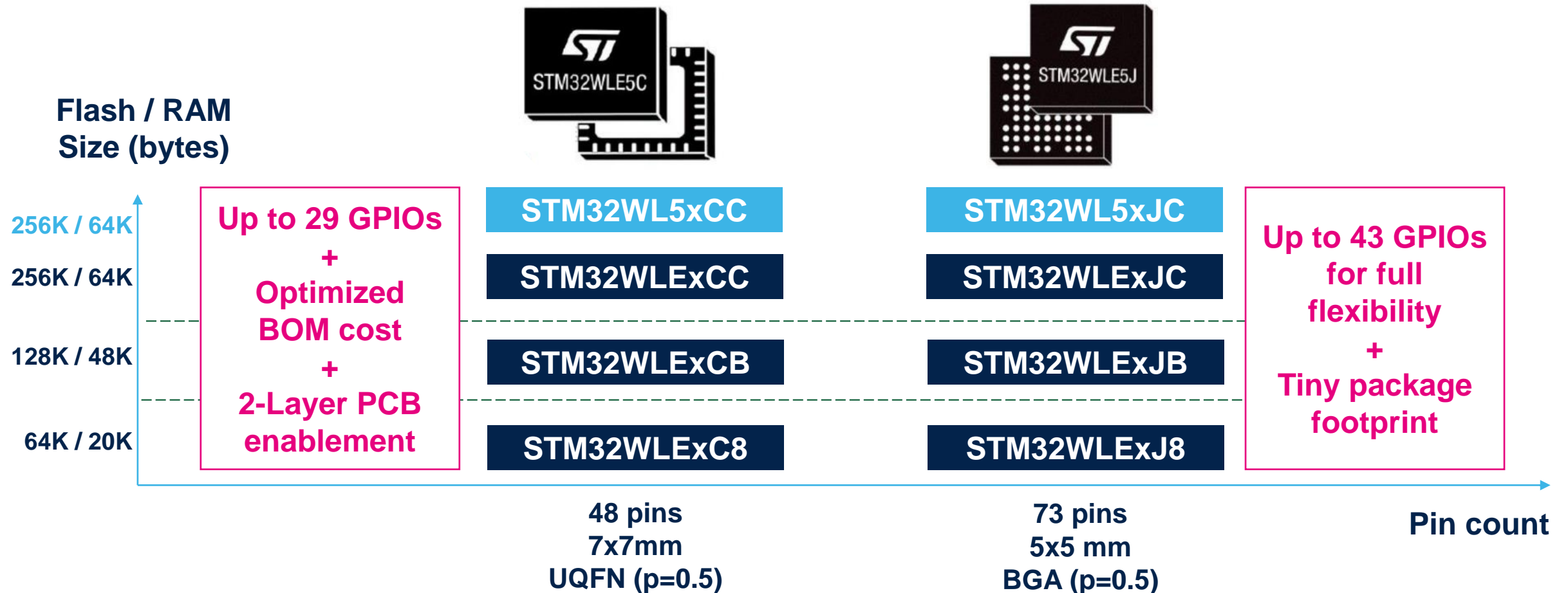
- Arm® Cortex®-M4 & DSP up to 48 MHz
- Up to 256 KB Flash and 64 KB SRAM
- Arm® Cortex®-M0+ up to 48 MHz
- **Sub-GHz Radio**
  - Multi-modulation: LoRa, (G)FSK, (G)MSK, BPSK
  - 2 embedded power amplifiers:
    - 1 output up to +15 dBm
    - 1 output up to +22 dBm
  - LoRa RX sensitivity: -148 dBm (SF12, BW=10.4kHz)
  - RX: 4.82mA and TX: 15mA (at 10dBm) / 87mA (at 20dBm) [3.3V]
- **Ultra-Low Power consumption**
  - < 71µA/MHz Active mode (3V - RF OFF)
  - 1 µA Stop2 mode with RAM retention
  - 390 nA Standby mode with RTC
  - 31 nA Shutdown mode
- **Peripherals**
  - 3x I²C, 2x USART, 1x LP-UART, 2x SPI
  - 7x timers + 2x ULP Comparators
- **Advanced security features**
  - 1.8 to 3.6V voltage range (DC/DC, LDO)
  - -40 to up to +105°C temperature range

# STM32WL product lines

ARM Cortex-M4 (DSP+ MPU) + M0+ (DSP+MPU) - 48 MHz	<ul style="list-style-type: none"><li>• <b>Freq. : 150 to 960MHz</b></li><li>• Sensitivity: -148 dBm</li><li>• ARM Cortex M4 &amp;</li><li>• CPU max 48MHz</li><li>• ART Accelerator</li><li>• USART, SPI, I2C</li><li>• 16- and 32- bit timer</li><li>• 1x ADC 12-bit</li><li>• 1x DAC 12-bit</li><li>• Temperature sensor</li><li>• Low voltage 1.8 to 3.6V</li><li>• LDO or DC/DC</li><li>• Internal RC +/- 1% [-20° to 85°C]</li><li>• Vbat mode</li><li>• Unique ID</li><li>• AES 128/256- bit</li><li>• Temperature range -40° to 105°C</li></ul>	Product line	Flash (KB)	RAM (KB)	Output power (dual output)	Modulation				CPUs	Security	
		LoRa	(G)FSK	(G)MSK	BPS K							
		STM32WL5x – Dual Core lines										
		STM32WL55	256	64	Up to 15 and 22dBm	■	■	■	■	CM0+: Radio	Secure Key Storage Secure Sub-GHz Mac Layer SFI, SFU	
										CM4: User app		
		STM32WL54	256	64	Up to 15 and 22dBm		■	■	■	CM0+: Radio	Secure Key Storage Secure Sub-GHz Mac Layer SFI, SFU	
										CM4: User app		
		STM32WLEx – Single Core lines										
		STM32WLE5	Up to 256	Up to 64	Up to 15 and 22dBm	■	■	■	■	CM4: Radio + user App	AES, TRNG, PCROP	
		STM32WLE4	64	20	Up to 15 and 22dBm		■	■	■	CM4: Radio + user App	AES, TRNG, PCROP	



# STM32WL Sub-GHz - portfolio



 Dual core

 Single core

Note:

x = 5 all modulations available

x = 4 all modulations available except LoRa

# Flexible power scheme FlexPowerControl

Typ with LDO @  $V_{DD} = 3\text{ V}$  @  $25\text{ }^{\circ}\text{C}$



\* Typical values with SMPS, RF OFF

\*\* with RTC on LSE Bypass

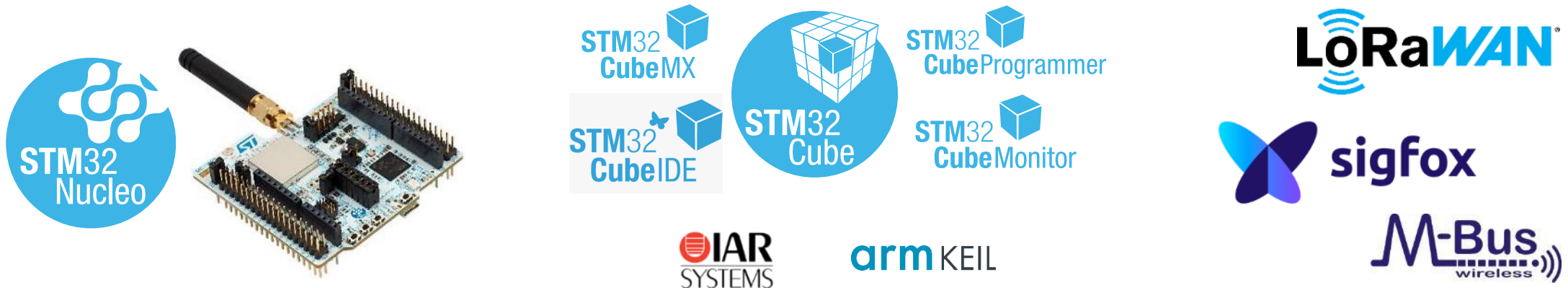
\*\*\* All OFF

## Benchmark scores

- High Efficiency  
→ CoreMark score = 162
- Ultra Low-Power Platform  
→ ULPBbench score  $\approx$  204

# STM32WL – ecosystem overview

Fully integrated into the rich and market-proven STM32 ecosystem



## STM32 Nucleo-64

Flexible prototyping

## Dev tools

STM32CubeMX  
STM32CubeWL  
STM32CubeMonitor  
STM32CubeProg  
STM32CubeIDE + Partners IDEs

## Stacks

LoRaWAN (ST)  
Sigfox (ST)  
Wireless-MBUS (Stackforce)

# Make the choice of the STM32WL series

## The 8 key points that make the difference



(G)FSK  
(G)MSK  
BPSK

Multi-modulation



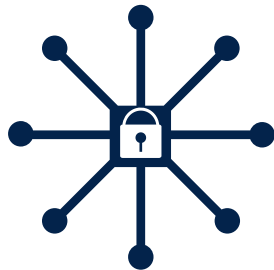
Massive integration  
Cost saving



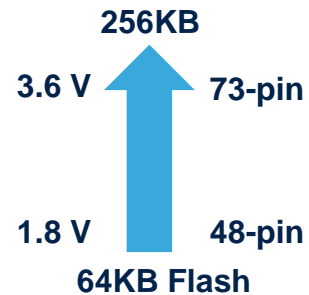
Open dual-core platform



Ultra-low-power



STM32 Security



A large offer



End-to-end ecosystem



No matter what!

# Releasing your creativity with the new STM32

