

STM32WL Lorawan & Sigfox

Coexistence is possible

Key learning

- Cover both Lorawan & Sigfox protocol stack in one app,
- Basic introduction to sw implementation
- Working Lorawan & Sigfox application example in practice,



Thanks to Sequencer it is easy to combine more than one app process

```
✓ IDE LoRaWAN_Sigfox_send_on_timer (in STM32CubeIDE)
  > 🐉 Binaries
  > 🛍 Includes

→ Application

   > 🗁 Core
                            while (1)
   > 🔒 app_lorawan.c
     > R lora_app.c
     > 🖟 lora_info.c
                                     if (active_app == ACTIVE_APP_LORAWAN)
   > 🔒 app_sigfox.c
     > 🖳 ee.c
     > R mcu_api.c
                                         MX_LoRaWAN_Process();
     > R mn_api.c
     > 🖟 rf_api.c
     > R se_nvm.c
     > R sgfx_app.c
     > 🖟 sgfx_credentials.c
                                     else
     > 🖟 sgfx_cstimer.c
     > R sgfx_eeprom_if.c
   > 📂 Startup
   > 🗁 Target
                                         MX_Sigfox_Process();
  > 🗁 Debug
  Drivers
 Middlewares
   > A LoRaWAN
   > 🗁 Sigfox
   > > SubGHz_Phy
 > 📂 Utilities
```



B1 button EXTI is used to switch active LPWAN protocol

```
void HAL_GPIO_EXTI_Callback(uint16_t GPIO_Pin)
switch (GPIO_Pin)
  case SYS_BUTTON1_PIN:
    if (active_app == ACTIVE_APP_LORAWAN)
      active_app= ACTIVE_APP_SIGFOX;
    else
      active_app= ACTIVE_APP_LORAWAN;
   NVIC_SystemReset();
    break;
```





App Sigfox/Lorawan control variable is placed in RAM2 and not initialized (no init) to keep value after MCU reset

```
UTIL_MEM_PLACE_IN_SECTION("MB_MEM2") uint32_t active_app;
```

Linker code excerpt

```
RAM2 (xrw) : ORIGIN = 0x20008000, LENGTH = 32K /* Backup SRAM2 */
.
.
MB_MEM2 (NOLOAD) : { *(MB_MEM2) } >RAM2
```

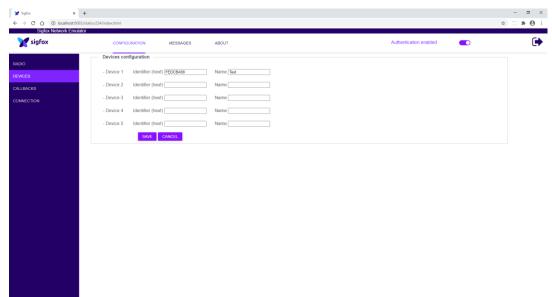


- Lorawan hands-on scenario is reused (uplink/downlink),
- Sigfox app (default) sends periodically to the network temperature value (2 bytes),
- Public Key is used for Sigfox app,

```
if (E2P_Read_KeyType()!=CREDENTIALS_KEY_PUBLIC)
{
    E2P_Write_KeyType(CREDENTIALS_KEY_PUBLIC);
}
```

- SDR Dongle and Sigfox Network Emulator is used www.sdrdongle.sigfox.com





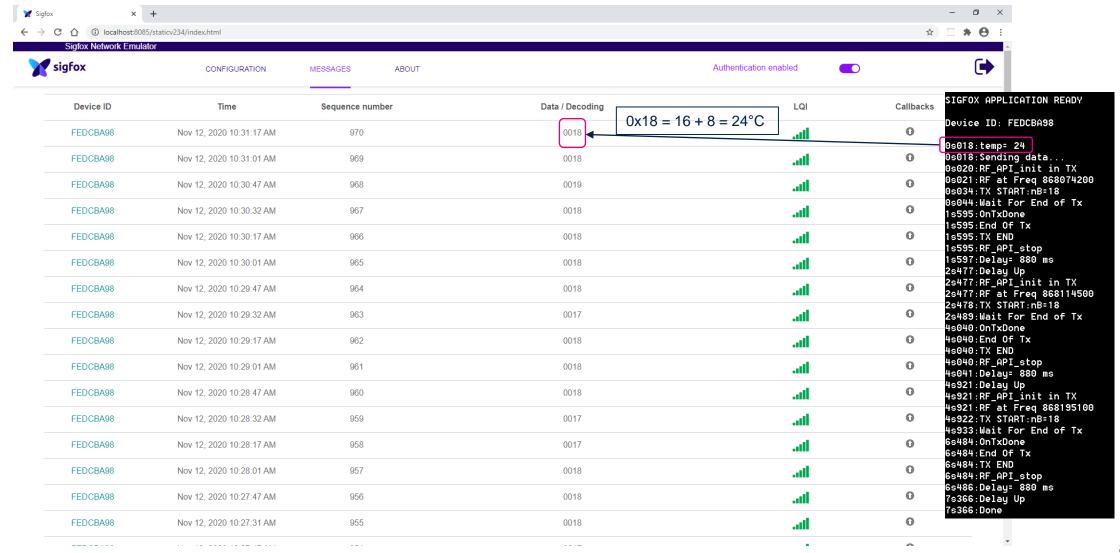


Memory footprint, STM32CubeIDE, Optimization level: NONE (-00)

Memory Regions	Memory Details					
Region	Start address	End address	Size	Free	Used	Usage (%)
■ ROM	0x08000000	0x08040000	256 KB	117,8 KB	138,2 KB	53,99%
SIGFOX_DATA	0x0803e500	0x0803e800	768 B	768 B	0 B	0,00%
ERAM1	0x20000000	0x20008000	32 KB	19,55 KB	12,45 KB	38,89%
ERAM2	0x20008000	0x20010000	32 KB	32 KB	4 B	0,01%



Sigfox Network Emulator, MESSAGES view





Sigfox app -

B1 button press

Lorawan app

```
4s933:Wait For End of Tx
6s484:OnTxDone
6s484:End Of Tx
6s484:TX END
6s484:RF_API_stop
6s486:Delay= 880 ms
7s366:Delay Up
7s366 : Done <
APP_UERSION:
                   V1.0.0
MW_LORAWAN_UERSION: U2.2.1
MW_RADIO_VERSION:
                   VO.6.1
###### OTAA ######
###### AppKey: 2B 7E 15 16 28 AE D2 A6 AB F7 15 88 09 CF 4F 3C
###### NwkKey: 2B 7E 15 16 28 AE D2 A6 AB F7 15 88 09 CF 4F 3C
###### ABP #####
###### AppSKey: 2B 7E 15 16 28 AE D2 A6 AB F7 15 88 09 CF 4F 3C
###### NwkSKey: 2B 7E 15 16 28 AE D2 A6 AB F7 15 88 09 CF 4F 3C
###### DevEui: 00-80-E1-15-00-01-94-0B
###### AppEui: 01-01-01-01-01-01-01
LORAWN APPLICATION READY
0s045:TX on freq 868500000 Hz at DR 0^{\circ}
1s550:MAC txDone
6s572:RX_1 on freq 868500000 Hz at DR 0
6s708:PRE OK
7s244:HDR OK
8s391:MAC rxDone
###### = JOINED = OTAA =============
15s068:temp= 25
15s069:TX on freq 868300000 Hz at DR 0
16s236:MAC txDone
17s257:RX_1 on freq 868300000 Hz at DR 0
17s394:PRE OK
17s930:HDR OK
18s421:MAC rxDone
###### ======== MCPS-Confirm ========
18s424:AppMsg -> INCREASE temperature.
```

Dual protocol

Sigfox uplink done

Lorawan uplink

Lorawan downlink



VCP: 115200,8,N,1

Thank you



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