

- **SEMESTER 3**

- VECTOR CALCULUS AND TRANSFORM TECHNIQUES
- ANALOG ELECTRONIC CIRCUITS
- DIGITAL SYSTEM DESIGN WITH VERILOG
- NETWORK THEORY
- SIGNALS AND SYSTEMS
- DIGITAL SYSTEM DESIGN LAB
- CONSTITUTION OF INDIA, PROFESSIONAL ETHICS
- ABILITY ENHANCEMENT COURSE – III (ENHANCING SELF COMPETENCE)
- UNIVERSAL HUMAN VALUES

- **SEMESTER 4**

- SIGNAL PROCESSING
- ANALOG COMMUNICATION
- CONTROL SYSTEMS
- ELECTROMAGNETIC WAVE THEORY
- SIGNAL PROCESSING LAB
- SYSTEM VERILOG LAB
- INTER / INTRA INSTITUTIONAL INTERNSHIP
- SAMSKRUTIKA KANNADA/BALAKE KANNADA
- ABILITY ENHANCEMENT COURSE – IV
- SOCIAL CONNECT & RESPONSIBILITY

- **SEMESTER 5**

- ANALOG COMMUNICATION
- COMPUTER NETWORK
- MICROPROCESSOR & MICROCONTROLLER
- VLSI DESIGN
- OPERATIONS RESEARCH
- ELECTIVE - I/II
- BASIC COMMUNICATION LAB.
- MICROPROCESSOR & MICROCONTROLLER LAB
- EMPLOYABILITY SKILL DEVELOPMENT - I

- **SEMESTER 6**

- ANTENNAS & MICROWAVE SYSTEMS
- DIGITAL COMMUNICATION
- INFORMATION THEORY & CODING
- ELECTIVE - I
- ELECTIVE-II
- ADVANCED COMMUNICATION LAB.
- VLSI LAB.
- EMPLOYABILITY SKILL DEVELOPMENT- II
- MINI PROJECT

- **SEMESTER 7**

- POWER ELECTRONICS
- ELECTIVE - IV
- ELECTIVE - V
- OPEN ELECTIVE - I

- COMPUTER NETWORKS LAB.
- POWER ELECTRONICS LAB.
- SEMINAR
- PROJECT PHASE - I

- **SEMESTER 8**
- ELECTIVE - VI
- OPEN ELECTIVE - II
- PROJECT PHASE - II
- INTERNSHIP