

* CMOS amplifiers :-

→ To design amplifiers using MOS devices, we need to learn how to turn the MOS devices ON such that they are in the saturation region. → This is called Biasing.

- * Biasing techniques :-
- ① Resistive divider Biasing
 - ② Self - Biasing.

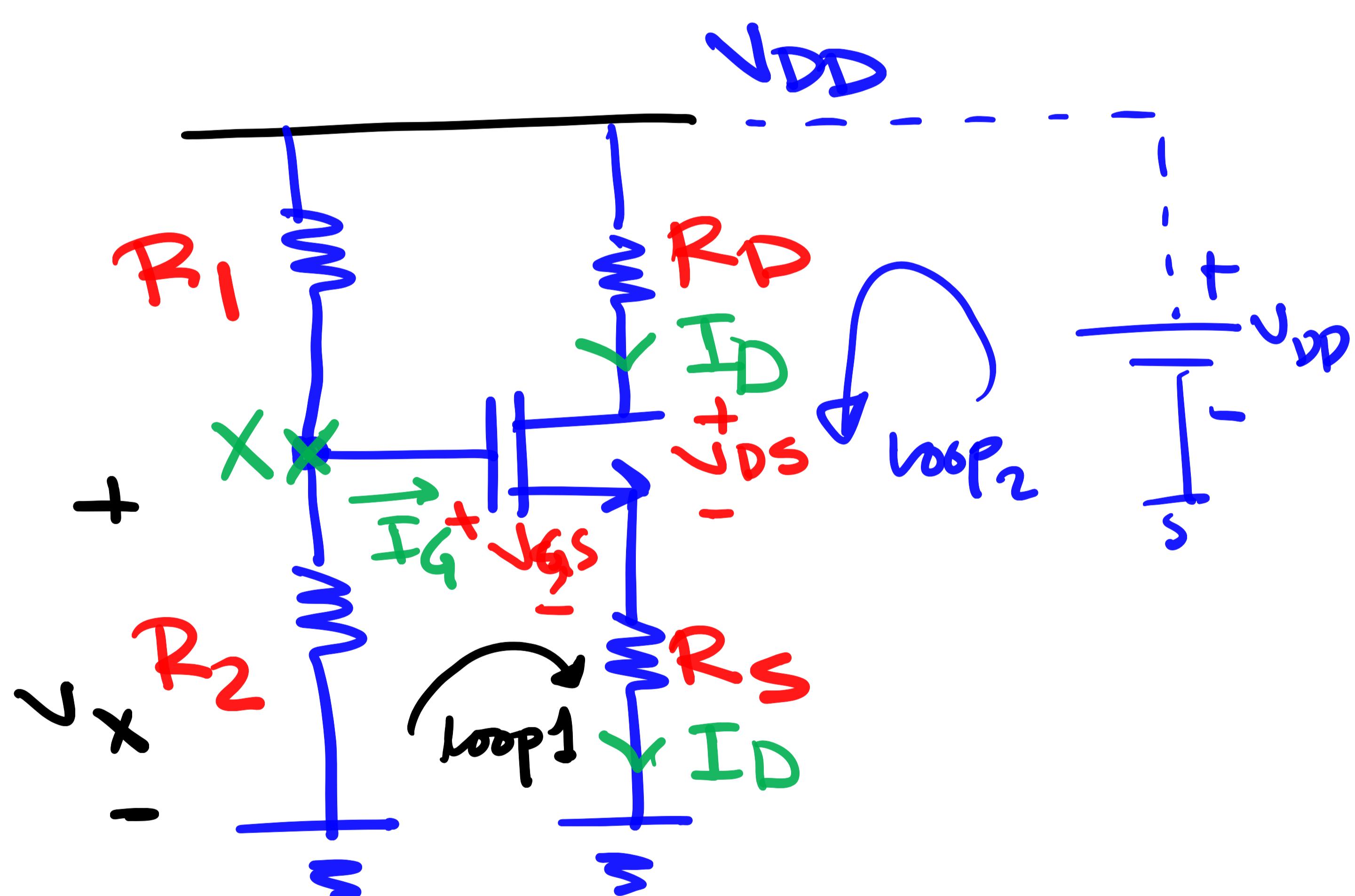
II Resistive divider Biasing :-

Note that $I_G = 0$

→ knowing that $I_G = 0$

$$\rightarrow V_x = \frac{R_2}{R_1 + R_2} \cdot V_{DD}$$

$$V_x = V_g = V_{R_2}$$



applying KVL @ loop 1

$$-V_x + V_{GS} + R_s \cdot I_D \rightarrow V_{GS} = V_x - R_s I_D \dots \textcircled{1}$$

if $V_{DS} > V_{HS} - V_{TH}$ then

$$I_D = \frac{1}{2} \mu_n C_{ox} \frac{W}{L} (V_{HS} - V_{TH})^2, \dots \textcircled{2}$$

To find V_{GS} , substitute I_D in ② in ①
and solve for V_{GS}

$$V_{GS} = - (V_1 - V_{TH}) + \sqrt{V_1^2 + 2V_1 \left(\frac{R_2 V_{DD} - V_{TH}}{R_1 + R_2} \right)}$$

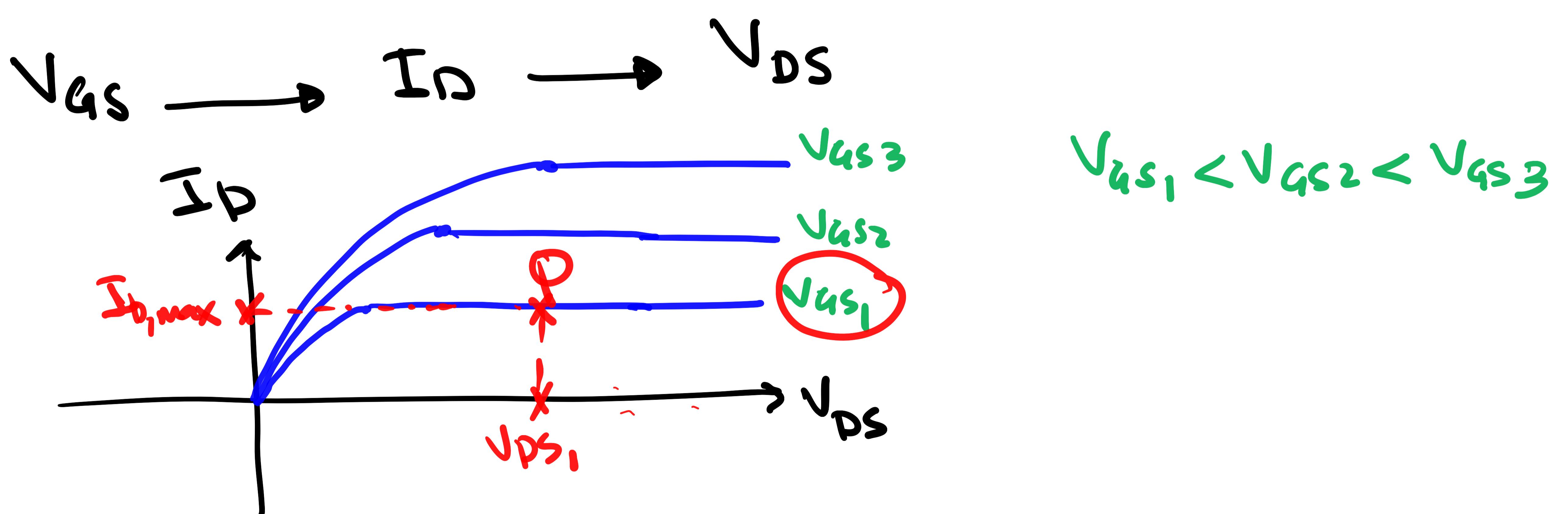
$$V_1 = \frac{1}{Mn \log \frac{W}{L} R_s}$$

after finding V_{GS} find I_D → then apply KVL
to loop 2 to find V_{DS}

$$V_{DS} = V_{DD} - I_D (R_s + R_D)$$

then check is $V_{DS} \geq V_{GS} - V_{TH}$

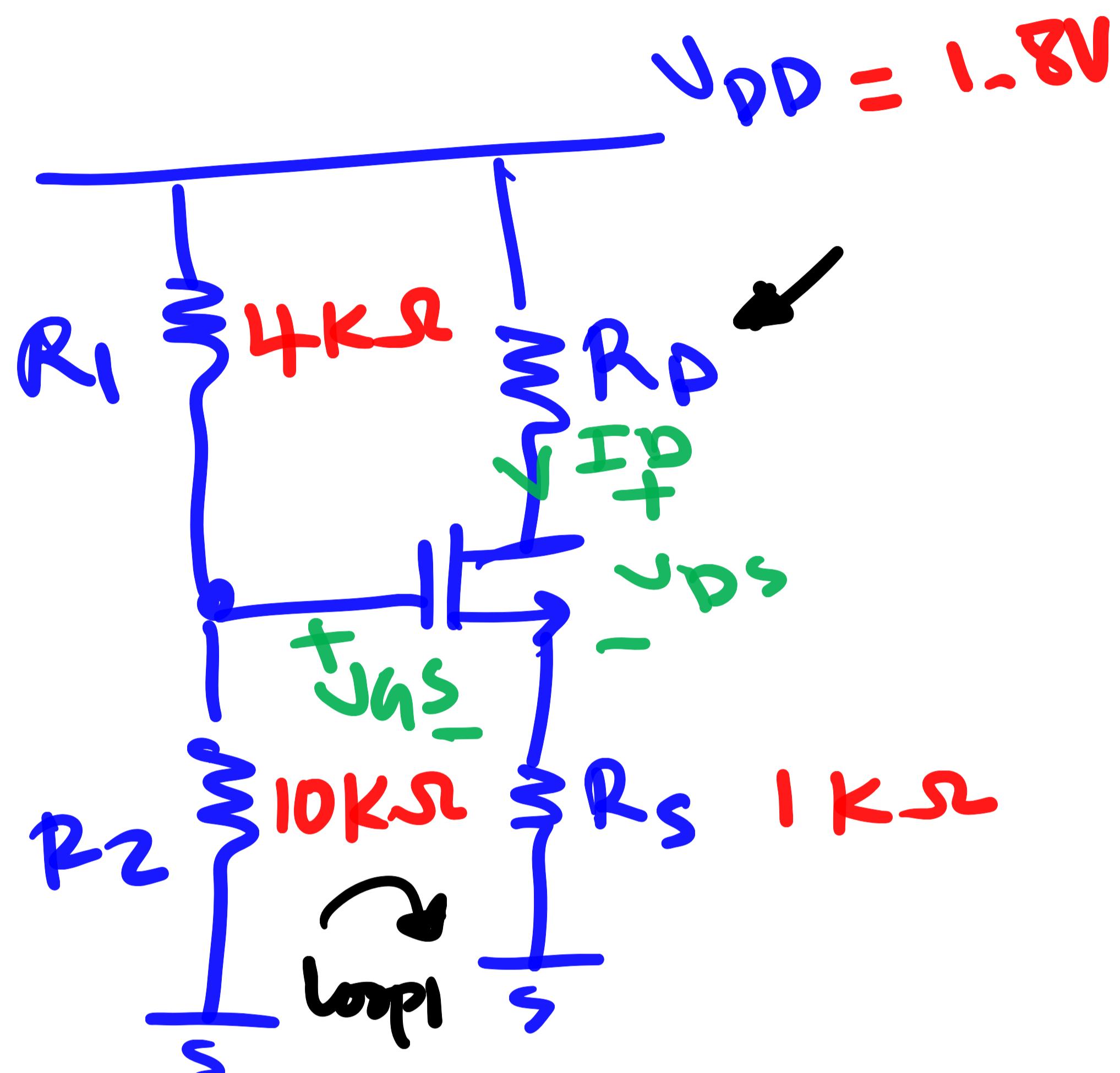
* Operating Point for a MOS :- Q-point.



Ex: Determine the bias current of M₁ in this cct. if V_{TH} = 0.5V, μ_nC_{ox} = 100 μA/V², w/L = 5/0.18 and λ = 0? what is the max. allowable R_D for M₁ to remain in saturation?

$$I_D = ?$$

$$I_D = \frac{1}{2} \mu n C_{ox} \frac{W}{L} (V_{GS} - V_{TH})^2$$



→ V_{GS} can be found using the equation in the previous section :

$$\rightarrow V_{GS} = 0.974 \text{ V} \rightarrow \text{check is } V_{GS} > V_{TH} \quad \checkmark$$

$$\rightarrow I_D = \frac{1}{2} \mu n C_{ox} \frac{W}{L} V_{GS}^2 = 312 \mu\text{A}$$

→ I_D ⇒ applying KVL to loop 1

$$-V_X + V_{GS} + I_D R_S = 0 \Rightarrow I_D = \frac{V_X - V_{GS}}{R_S}$$

$$V_X = \frac{R_2}{R_1 + R_2} \cdot V_{DD} = 1.286 \text{ V}$$

$$\Rightarrow I_D = 312 \mu\text{A}$$

⇒ check if M₁ is in saturation?

$$V_{DS} > V_{ov} \Rightarrow V_{DS} = V_{DD} - I_D (R_D + R_S)$$

$$V_{DD} - I_D (R_D + R_S) > V_{GS} - V_{TH} \rightarrow \text{solve for}$$

$$R_D \Rightarrow R_D < 3.24 \text{ k}\Omega$$

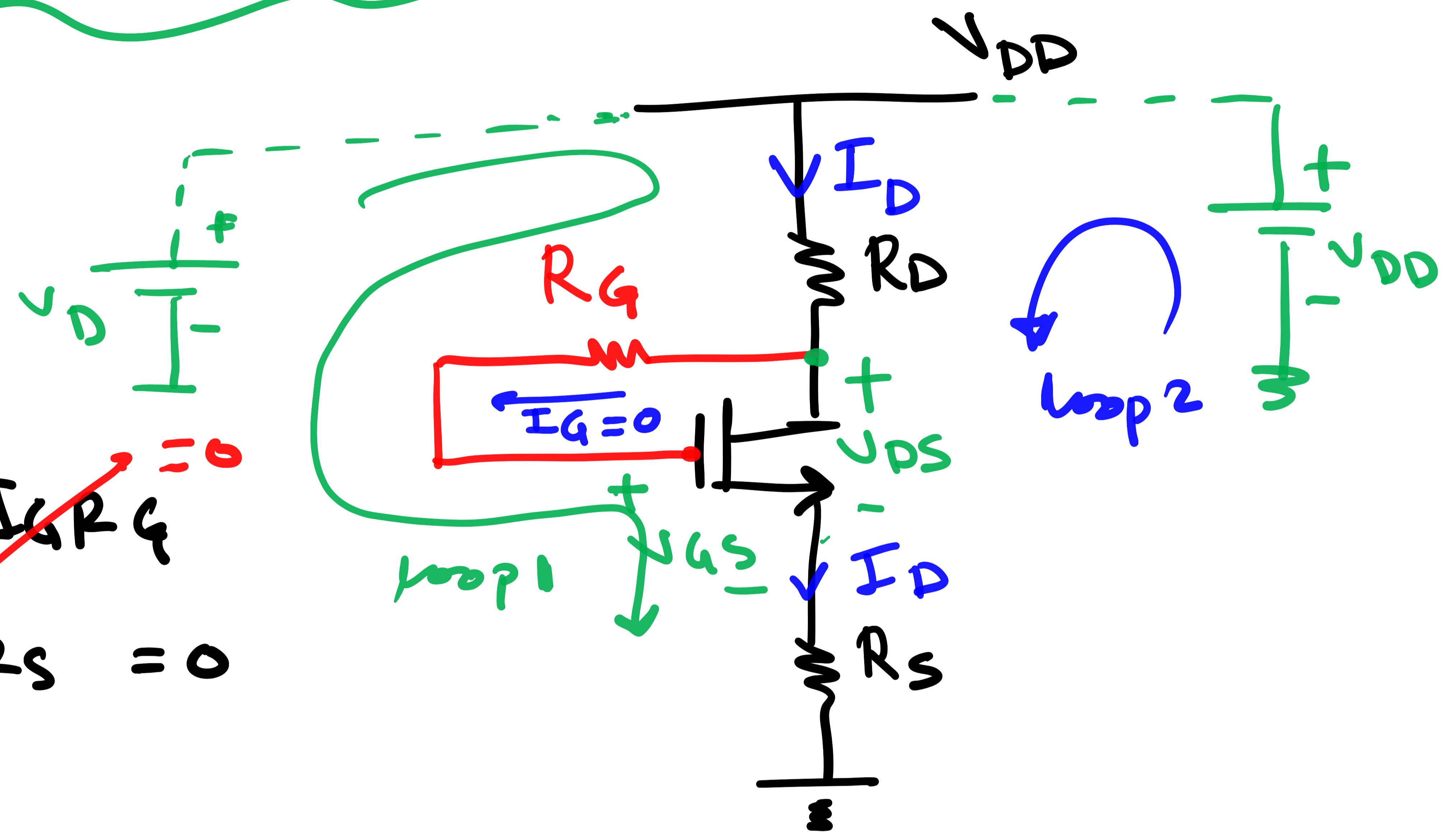
* Self-Biasing technique :-

Applying KVL @ loop 1 :

$$-V_{DD} + I_D R_D + I_G R_G = 0$$

$$+ V_{GS} + I_D R_S = 0$$

but $I_G = 0$



$$\Rightarrow V_{GS} = V_{DD} - (R_D + R_S) \cdot I_D$$

\Rightarrow Applying KVL @ Loop 2 :

$$-V_{DD} + I_D R_D + V_{DS} + I_D R_S = 0$$

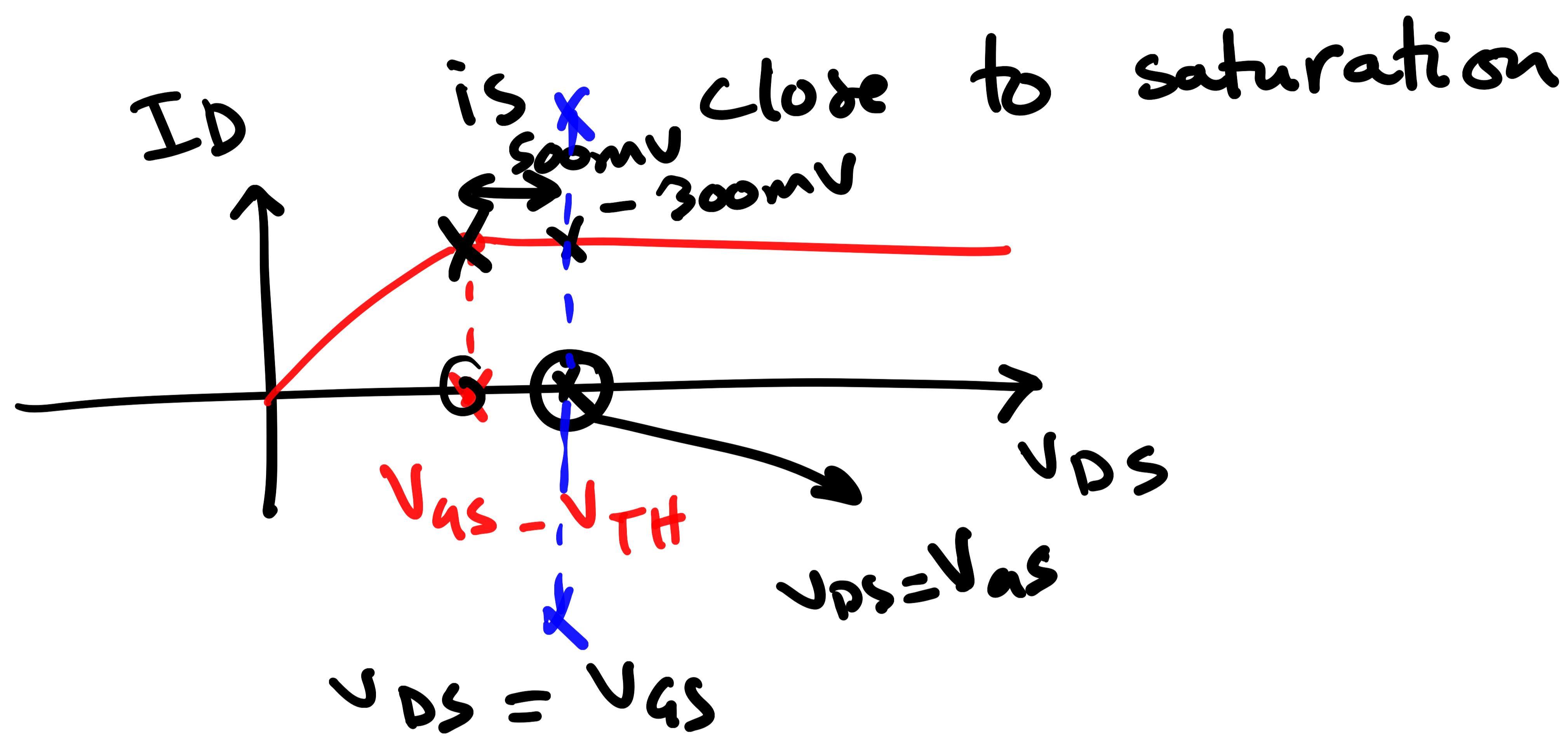
$$V_{DS} = V_{DD} - I_D (R_D + R_S)$$

with self biasing we conclude that

$$V_{DS} = V_{GS} \Rightarrow$$

For saturation operation

$V_{DS} > V_{GS} - V_{TH}$, However with self biasing Obviously the operation



* Deriving the current I_D :

→ Referring to the KVL loop 1

$$V_{GS} = V_{DD} - I_D (R_S + R_D) \quad , \quad \text{if } \lambda = 0$$

$$I_D = \frac{1}{2} \mu_n C_o x \frac{W}{L} (V_{GS} - V_{TH})^2$$

$$I_D = \frac{1}{2} \mu_n C_o x \frac{W}{L} (V_{DD} - I_D (R_S + R_D) - V_{TH})^2$$

$$\Rightarrow (R_S + R_D)^2 \cdot I_D^2 - 2 \left[(V_{DD} - V_{TH})(R_S + R_D) + \frac{1}{\mu_n C_o x \frac{W}{L}} \right] \cdot I_D + (V_{DD} - V_{TH})^2 = 0$$

The above eq. will lead to two I_D values
a negative and a positive.

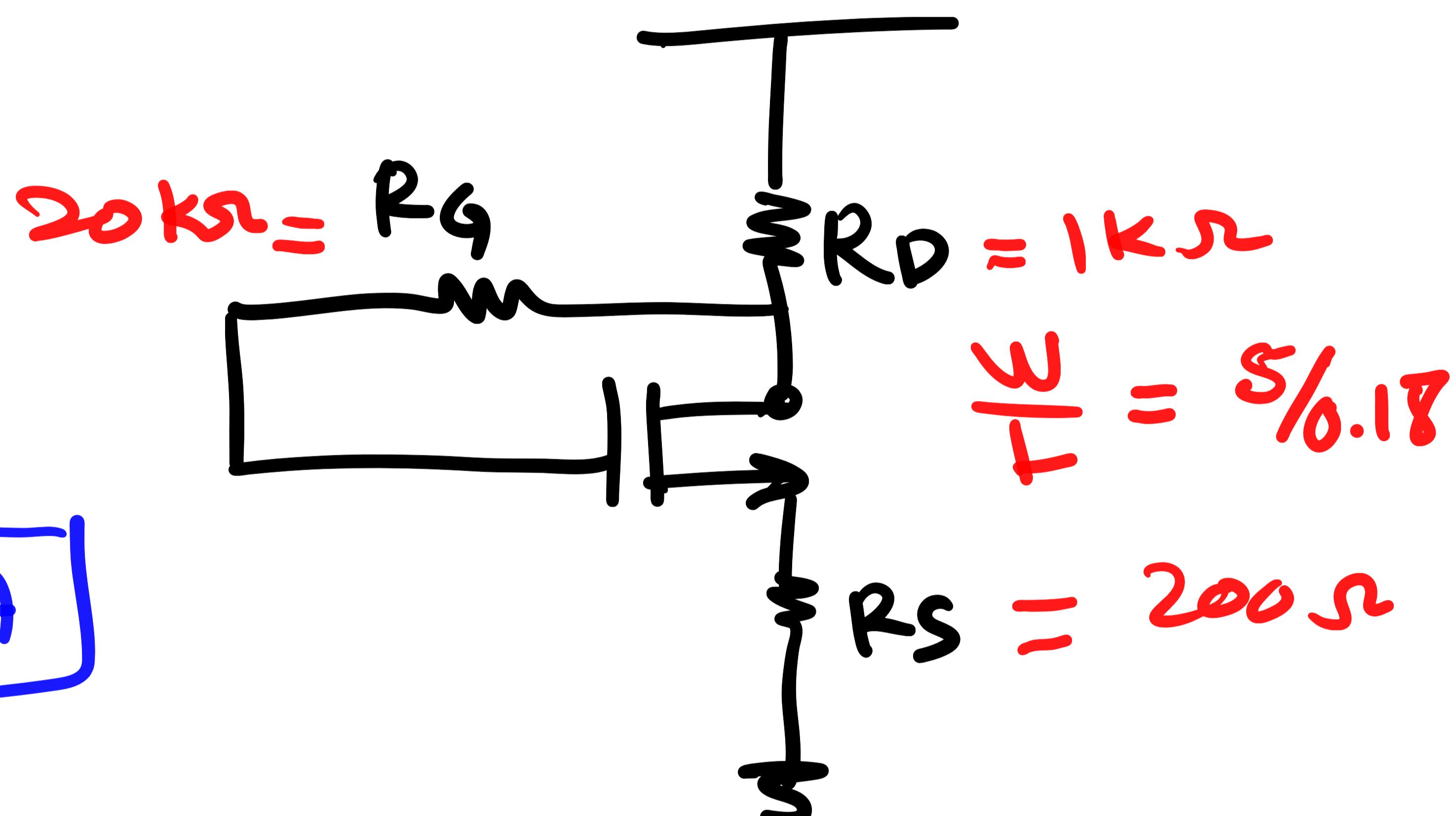
Pick the positive value.

Ex : Calculate the drain current of M_1 in
this cct. if $\mu_n C_{ox} = 100 \mu A/V^2$, $V_{TH} = 0.4V$,

$$V_{DD} = 1.8V$$

* Using the
above eq for I_D

$$\rightarrow I_D = 556 \mu A$$



* The other way to find I_D is as follows :-

→ Let's find I_D as $\frac{1}{2} \mu_n C_{ox} \frac{W}{L} V_{ov}^2$

→ We need to find V_{ov} ?

$$V_{GS} = V_{DD} - I_D (R_D + R_S) \Rightarrow V_{ov} = V_{GS} - V_{TH}$$

$$\Rightarrow V_{GS} = V_{ov} + V_{TH}$$

$$V_{ov} + V_{TH} = V_{DD} - I_D (R_D + R_S) \rightarrow$$

$$V_{ov} + V_{TH} = V_{DD} - \frac{1}{2} \mu_n C_{ox} \frac{W}{L} V_{ov}^2 (R_D + R_S)$$

$$\frac{1}{2} \mu_n C_o x \frac{w}{L} V_{DS}^2 (R_D + R_S) + V_{DS} + (V_{TH} - V_{DS}) = 0$$

Solve for (V_{DS})

$$\Rightarrow \text{Solving for } V_{DS} = 0.633V$$

$$\Rightarrow V_{GS} = 1.133V$$

$$\Rightarrow I_D = \frac{1}{2} \mu_n C_o x \frac{w}{L} V_{DS}^2 = 556 \mu A$$