Packager-XL Message Reference

Product Version 23.1
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Contents

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ERROR (5)

ERROR 5

ERROR (5): "Pin '%s' on the following primitive instance cannot be packaged in package of type '%s'. Primitive Instance: %s No description of the logical pin being used by a primitive instance could be found in the chips.prt file. Ensure that the chips.prt file for the part contains a description of the pin. This error can also occur if the selected symbol does not match the selected primitive. Make sure that the symbol you select matches the primitive specified by the PACK_TYPE property. "

Packager-XL generates the above error if it does not find a description of a logical pin, which is being used by a primitive instance, in the chips.prt file. Ensure that the chips.prt file for the physical part that generated the error contains a description for the logical pin mentioned in the error message.

2

ERROR (16)

ERROR 16

ERROR (16): "Expected '%c' while parsing added properties on line %d. Check the property assignment statement to ensure that you use the equal to (=) operator while assigning a value to the added or injected property and enclose the property name in quotation marks. "

This error occurs when an equal to operator is missing while assigning a value to an added/injected property in the ptf file or when you assign an added property without enclosing it within quotes in the ptf file. Ensure that you use an equal to operator while assigning values to added properties. Also make sure that added properties are enclosed within quotation marks.

3

ERROR (20)

ERROR 20

ERROR (20): "Keyword '%s' not found on line %d in the part table file, or the character ':' is missing from the beginning of the key property header. Correct the entry in the part table file. "

This error can occur for multiple reasons. A keyword FILE_TYPE, PART or END_PART may be missing on the line mentioned in the error in the ptf file. The LIBRARY_PARTS or some other keyword may not be there in the chips.prt file. Words that do not seem a property definition (name=value pair) exist after the keyword PART. Characters such as period or comma may have been placed after the keyword in the same PPT row. As a result of ERROR 20, Packager-XL is unable to load the ptf file properly. It may also generate ERROR 47. Ensure that you place keywords at the right place and that no extra words exist in the part definition. The END_PART keyword should be the only word in its line. The FILE_TYPE definition should be the first definition in the ptf file or the chips.prt file and should appear before any other part definition. Note: You should not hand edit the ptf or chips.prt files. Use Part Table Editor to create ptf files. For more information about defining a ptf file, see the documentation.

4

ERROR (21)

ERROR 21

ERROR (21): "Expected '=' character while parsing global props on line %d. "

If you do not begin the property header for a part (which lists key and injected properties) with a colon (:) in a ptf file, the ptf file will not be loaded properly. As a result of ERROR 21, Packager-XL will also display ERROR 47. If a colon is placed in the key/injected property list but is placed after a property, ERROR 24 is generated. Ensure that you place a colon as the first character in the line that lists all global properties. Do not hand edit ptf or chips.prt files. Use Part Table Editor to work with ptf files.

5

ERROR (22)

ERROR 22

ERROR (22): "The %s property for PPT %s is defined twice as a global property. Ensure that the part table file does not have duplicate property values. "

You have defined the same property twice as a global property. Packager-XL is unable to pick the right version and load the ptf file. As a result of this error, Packager-XL may also generate ERROR 47. Ensure that you do not have duplicate property values in any PPT row.

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ERROR (23)

ERROR 23

ERROR (23): "End of file encountered while reading global properties. "

This error is caused when you have an incomplete global properties section for a PPT part definition. You have used the PART keyword but have not defined it completely. Ensure that the ptf file ends with the END. keyword, where period (.) is part of the keyword.

7

ERROR (24)

ERROR 24

ERROR (24): "Extra property value found on line %d."

There may be an extra property in the line displayed in the error message. The = character or the property separator could be incorrectly placed. The name of the property may be missing in the header definition. As a result of this error, Packager-XL may also generate ERROR 47. Ensure that you do not have any extra property values in PPT rows.

8

ERROR (25)

ERROR 25

ERROR (25): "Multiple subtypes found on line %d. Ensure that you have specified only one subtype name for a particular PPT row. "

This error is caused when you assign two or more subtypes in the same PPT row. You cannot use more than one subtype for the same PPT row. As a result of this error, Packager-XL might also generate ERROR 47. Ensure that you have specified only one subtype name definition for a particular PPT row. You can define the subtype name for a part using one of three formats: !: Packager-XL uses the instance property value as the suffix. This is the default behavior. subtype_name_suffix: Packager-XL appends the suffix to the parent part name. ::complete_user_subtype_name: Packager-XL uses the entire subtype name that appears after the tilde (::) character. For more information about defining the part subtype names, see the documentation.

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ERROR (26)

ERROR 26

ERROR (26): "The closing round bracket is missing from the subtype name on line %d. Ensure that you have specified the closing round bracket for the subtype name definition. "

This error occurs when you do not use the closing round bracket ')' character in the subtype name. As a result of this error, Packager-XL also generates ERROR 47. Ensure that you specify the subtype name definition within both, opening, and closing round brackets. You can define the subtype name for a part using one of three formats: !: Packager-XL uses the instance property value as the suffix. This is the default behavior. subtype_name_suffix: Packager-XL appends the suffix to the parent part name. ::complete_user_subtype_name: Packager-XL uses the entire subtype name that appears after the tilde (::) character. Each of these subtype name definitions requires that you place the value between brackets.

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ERROR (27)

ERROR 27

ERROR (27): "The row separator on line %d does not match the header definition. The row separator should exactly match, in number and position, the header definition. You cannot use a pipe (|) symbol in the header row and a comma (,) in a PPT row as row separators. Ensure that the row separators match the separators defined in the header for the concerned PPT part. "

The row separator should exactly match in number and in its position with the header definition. The row separator should also be the same character. You cannot use a pipe (|) in the header row and a comma (,) in a PPT row as the row separator. You could have used a different number of row separators as listed in the header row of the PPT part which generated the problem or you could have forgotten to add the quote (') for the first property value. As a result, Packager-XL is unable to load the ptf file properly. Ensure that the row separators match the separators defined in the header for the concerned PPT part.

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ERROR (28)

ERROR 28

ERROR (28): "Extra '%c' character found on line %d. Ensure that there is only one such character in each PPT row and you have not used it as a property separator. "

This error occurs when you have added an extra '=' character on any PPT line. You may have defined '=' as the property separator, which is not allowed. As a result, Packager-XL is unable to load the ptf file properly. Ensure that there is only one '=' character in a PPT line. Do not use the '=' character as the property separator.

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ERROR (29)

ERROR 29

ERROR (29): "Values not found for all %s properties on line %d. Ensure that all properties are properly defined and the property header row lists the correct set of properties. In addition, do not use the '=' character as the property separator. "

This error occurs when injected properties are not properly defined for a line per the injected property definition in the header row. The problem could be in the PPT line listed in the error or in the injected property definition itself. Ensure that all injected properties are properly defined and the injected property header row lists the right set of injected properties. Also ensure that the = character is not used as the property separator.

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ERROR (30)

ERROR 30

ERROR (30): "PPT file terminated early while parsing part table rows. PPT file terminated early while parsing part table rows. The part table file was closed because the keyword END_PART was not found before the last keyword END. in the part table file. Check if some other character, such as a period (.) or a comma (,) is placed after END_PART. Ensure that the header property is defined only once for the PTF file and END_PART is placed before END. in the PTF file. "

This error is caused if the END_PART keyword is missing before the last keyword in a ptf file, that is END. The error can occur if the END_PART keyword is missing or if some other character such as period (.) or comma (,) is placed after the END_PART keyword. As a result of this error, Packager-XL may also generate ERROR 20 and ERROR 47. Ensure that the header property is defined only once for the PPT and the END_PART keyword is placed before the END keyword in a ptf file.

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ERROR (31)

ERROR 31

ERROR (31): "Duplicate header property %s found for PPT %s. The property might be defined both in the Global property declaration and as a key or injected property. Ensure that the property is defined only once for the part table file. "

A header property should be defined only once for each PPT. You could have added a property both, in the Global property declaration, and as a key or injected property. This error is also followed by ERROR 47, which states that the concerned ptf file was not loaded. Ensure that the header property is defined only once for the PPT.

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ERROR (32)

ERROR 32

ERROR (32): "Extra character(s) were found after %s on line %d. Ensure that no extra text is present after the END keyword or after the semicolon in the header property row definition. "

This error occurs when you place one or more extra characters after a keyword such as END or semicolon (;). For example, the END keyword is used to terminate a ptf file. Packager-XL expects the end of file delimiter after that keyword and no extra text. Similarly, the semicolon (;) is used to terminate the header property row definition, and any extra text after it causes Packager-XL to generate this error. Ensure that there is no text after the END keyword or after the semicolon in the header property row definition.

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ERROR (33)

ERROR 33

ERROR (33): "The OPT keyword is expected after the opening round bracket character '(' on line %d. This keyword defines whether a property is optional on an instance of a part. Place this keyword after the property name in the property header row for a PPT part. Ensure that you have defined all the properties and values in the property header row using the correct syntax. "

This error occurs if you add the opening round bracket '(' character in the property header row but do not follow it with the OPT keyword. The OPT keyword in a PTF file defines whether a property is optional for a part instance. The keyword is used after the property name in the property header row for a PPT part. Ensure that you have defined all properties and values in the property header row as defined in the following syntax: property_name [(OPT='value')] [separator property_name ...] = property_name [separator property_name ...] ; For more information about defining a ptf file, see the documentation.

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ERROR (35)

ERROR 35

ERROR (35): "The OPT property value on line %d is not enclosed within quotes. Ensure that you have defined all the properties and values in the property header row according to the required syntax. "

The syntax for defining the OPT keyword in the property header row is as follows: : property_name [(OPT='value')] [separator property_name ...] = property_name [separator property_name ...] ; If you add the OPT keyword, which is optional, and do not add quotes to define the OPT value, Ensure that you have defined all properties and values in the property header row as defined in the syntax described above. The OPT value must be enclosed within quotes. Packager-XL displays the above error.

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ERROR (36)

ERROR 36

ERROR (36): "The OPT keyword has been specified without a value on line %d. Ensure that you specify a value when you set the OPT keyword. "

The syntax for defining the OPT keyword in the property header row is as follows: If you define the OPT keyword, you must specify a value for it. If you do not add the OPT value, Packager-XL displays this error. As a result of this error, Packager-XL also generates ERROR 47. Ensure that you have defined all properties and values in the property header row as defined in the syntax. The OPT value must be enclosed within quotes.

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ERROR (37)

ERROR 37

ERROR (37): "Injected properties not found on header line %d. Check the part table file and ensure that each part definition has a header row consisting of key and injected properties separated by the '=' character. "

Packager-XL expects each part definition to have a header row consisting of key and injected properties separated by the "=" character. If you do not add any injected property, this error is displayed. As a result of this error, Packager-XL also generates ERROR 47. Ensure that you have added injected properties to the required header line.

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ERROR (40)

ERROR 40

ERROR (40): "File is not of type %s. A part table file must be of the type MULTI_PHYS_TABLE. Ensure that you have added FILE_TYPE=MULTI_PHYS_TABLE as the first keyword in the PTF file. If this keyword is not the first keyword in a PTF file, further errors will be generated. "

Packager-XL expects a ptf file to be of the type MULTI_PHYS_TABLE. If you have defined any other file type, this error is generated and the ptf file is closed. As a result of this error, Packager-XL also generates ERROR 47. If you add the MULTI_PHYS_TABLE keyword later in the file after other keywords, Packager-XL generates ERROR 40 and ERROR 20. Ensure that you have added the FILE_TYPE=MULTI_PHYS_TABLE keyword as the first keyword in the ptf file.

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ERROR (43)

ERROR 43

ERROR (43): "End of file encountered while reading PPT tables. The END keyword might be missing from the part table file. Ensure that all the part table files contain all keywords including END. Ensure that the END keyword is followed by a period. "

This error is generated when you have a ptf file that ends without the END keyword. This error also generates ERROR 47, which lists the file where the END keyword was not properly defined. Ensure that all ptf files contain all keywords including END. Ensure that the END keyword is followed by a period.

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ERROR (47)

ERROR 47

ERROR (47): "Packaging cannot be completed because the file %s could not be loaded. There might be syntax errors in this file. Ensure that the syntax is correct before proceeding. "

This error is generated along with another packaging error, which prevents Packager-XL from completing its operation. Fix the other errors that are generated in the pxl.log file and package the design again.

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ERROR (51)

ERROR 51

ERROR (51): "Part table file '%s' could not be opened because there is no read permission on the part table file. Assign the read permission to the file and package. "

This error can occur if read permissions have not been set for the ptf file. As a result, Packager-XL is not able to load the ptf file. Assign read permissions for the file and re-run Packager-XL.

24

ERROR (67)

ERROR 67

ERROR (67): "Old path name '%s' not relative for primitive instance '%s'. "

This error occurs because a BODY_NAME property is present with different values in a body file and chips.prt file. If a BODY_NAME property appears in a body file and the chips.prt file for any part, it should have the same property value. Ensure that if a BODY_NAME property appears in a body file and the chips file for any part, it should have the same property value.

25

ERROR (74)

ERROR 74

ERROR (74): "End of file encountered while parsing %s. "

This error is generated when the packager state file or a chips.prt file ended while a particular section was being parsed. You might have forgotten to terminate the chips.prt file with the END. keyword. Ensure that you have defined the chips.prt file properly and terminated it with the END.keyword. Note that a period (.) follows the END keyword. For more information about creating the chips.prt file, see the documentation.

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ERROR (75)

ERROR 75

ERROR (75): "Expected %s on line %d. "

This error is generated when Packager-XL expects a particular type of data while parsing the packager state file or a chips.prt file. The name of that data type is mentioned in the error. Ensure that you have used proper keywords such as end_primitive at the right places.

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ERROR (77)

ERROR 77

ERROR (77): "Could not open file %s. You might be trying to reuse a design with an inaccessible state file. To reuse a design, ensure that the design being reused is treated as a subdesign and the subdesign state file is accessible and readable. To create the subdesign state file, use the GEN_SUBDESIGN directive. "

This is a generic error message generated when the output file is missing. Ensure that the file mentioned in the error is available with the right access permissions. If you are trying to reuse a design, ensure that the design being reused is treated as a subdesign. The subdesign state file should be accessible and readable. To create the subdesign state file, use the GEN_SUBDESIGN directive. For instance, in the above example, run Packager-XL with the following directive: GEN_SUBDESIGN = DESIGN1 (where DESIGN1 represents the root design). This will create the pxl_DESIGN1.state file, which is required for effective design reuse.

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ERROR (79)

ERROR 79

ERROR (79): "Power net name expected before '%c' character in 'POWER_PINS' = '%s'. The ':' might be missing or there is an extra ';' character. To prevent such syntax problems, avoid manual editing of the property and use the Assign Power Pins dialog box. "

This error is generated when the POWER_PINS definition in the chips.prt file does not conform to the syntax. Ensure that the POWER_PINS property is properly defined. Check that you have used a colon to separate the supply name from the pin list and used a semicolon to separate each supply specification.

29

ERROR (84)

ERROR 84

ERROR (84): "Extraneous text found on line %d. "

This error occurs when you have added some extra unrecognizable characters on a line in the chips.prt file where Packager-XL is expecting a standard delimiter such as quote, semicolon, or colon. Ensure that you have removed all extra characters from the line reporting the error. If a standard delimiter is expected, add it.

30

ERROR (85)

ERROR 85

ERROR (85): "The '(' character for PIN_NUMBER is missing from line %d. Ensure the PIN_NUMBER range is enclosed within the '(' and ')' characters.To avoid such errors, use Part Developer instead of manually editing the library part definition. "

This error occurs when you do not add the quotes to separate the PIN_NUMBER range in the chips.prt file. As a result, Packager-XL is unable to load the chips.prt file. Ensure that you have separated the PIN_NUMBER range with the (and) characters.

31

ERROR (86)

ERROR 86

ERROR (86): "Null pin name found for PIN_NUMBER on line %d. To avoid such problems, use Part Developer to edit library parts instead of manually editing them. "

This error is generated when you have not defined any pin number in the PIN_NUMBER range in the chips.prt file. As a result, Packager-XL is unable to load the chips.prt file. Ensure that you specify at least one valid PIN_NUMBER or specify a valid PIN_NUMBER range in the chips.prt file.

32

ERROR (88)

ERROR 88

ERROR (88): "Invalid %s pin in PIN_NUMBER range on line %d. "

This error occurs when you use an invalid pin width in the PIN_NUMBER range. Ensure that you have defined a valid PIN_NUMBER range for the physical pin corresponding to the logical pin name.

33

ERROR (90)

ERROR 90

ERROR (90): "Invalid PIN_NUMBER repeat factor on line %d. Expecting an integer value after '*' character. "

This error is caused when you use an invalid character for defining the PIN_NUMBER range for vector pins. For instance, in the following example, an asterisk (*) is used as part of the PIN_NUMBER range. For vector pins, specify the PIN_NUMBER range explicitly, as shown below: PIN_NUMBER='(<13,14,15,1>,<13,14,15,1>,<13,14,15,1>,<13,14,15,1>)' However, you can use an asterisk character for defining the PIN_NUMBER range for scalar pins, as follows: 'ad'<0>: pin_number='(13*4)'; 'ad'<1>: pin_number='(14*4)'; 'ad'<2>: pin_number='(15*4)'; 'ad'<3>: pin_number='(1*4)'

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ERROR (100)

ERROR 100

ERROR (100): "State file not written for design %s. "

This error message appears with other error messages, which indicate why the state file was not written. To fix this error, you need to fix the other errors in the Packager-XL run.

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ERROR (101)

ERROR 101

ERROR (101): "Primitive %s is not packaged. "

This error message appears with other error messages, which indicate why the primitive instance was not packaged. To fix this error, you need to fix the other errors (ERROR 1131 and ERROR 1137) in the Packager-XL run. This error occurs because two pin numbers were swapped across two different pin groups, and one pin was not found in the physical part.

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ERROR (111)

ERROR 111

ERROR (111): "Phys part name information corrupt for: Schematic instance: %s This error can be corrected by performing the following steps: 1. Delete the schematic instance listed above 2. Package the design 3. Re-add the schematic instance 4. Package the design again "

This error occurs when you have accidentally deleted information from packaging files. Do not manually edit packaging files. You can correct this error by deleting the schematic instance listed in the error message, packaging the design, re-adding the schematic instance, and packaging the design again.

37

ERROR (128)

ERROR 128

ERROR (128): "Load state file error. Part name conflict found for '%s'. "

This error occurs when you change the logical part name in a subdesign after packaging it in the root design. This prevents Packager-XL from loading the subdesign state file. You can correct this error by deleting all instances of this subdesign from the root design, packaging the root design, then adding the subdesign instances to the root design and packaging again. Or, change the instance back to its original name and generate the subdesign. Then package the root design again.

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ERROR (143)

ERROR 143

ERROR (143): "Physical part name %s conflicts with logical part name %s for instance %s. %s not performed. "

This error is displayed when the design is packaged. This error occurs if the PART_NAME property is defined on the schematic part, but the value of this property is not the same as the logical name of the part. If PART_NAME is defined in the chips.prt file, this property need not be defined in the schematic. If the property is defined on the schematic part, update the value of this property so it is the same as the logical name of the part, as it appears in the chips.prt file.

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ERROR (153)

ERROR 153

ERROR (153): "Part name %s has multiple parent phys part names associated with it in the %s. The possible cause could be that a COMP_DEF_PROPERTY has been added in the reuse block which results in change of the part name in the root design. Increase the part name length to a larger value in the Part Type Length field on the Packager Setup dialog box and package again in preserve mode. "

This error will not occur if a state file generated by Packager-XL is used. But if you have modified the state file and changed the base physical part name for an alternate physical part, this error can occur. A possible cause for the error could be that a COMP_DEF_PROPERTY has been added in the reuse block which results in a change of the part name in the root design. Increase the part name length to a larger value in the Part Type Length field on the Packager Setup dialog box and package again in preserve mode.

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ERROR (162)

ERROR 162

ERROR (162): "PPT part name '%s' for PPT '%s' is not unique in the part_table view. "

This error message appears when multiple rows are defined in the part table with the same name. This can result from duplicate names or suffixes assigned to the row or duplicate key properties. As a result of this error, Packager-XL also generates Error 47.You can fix the problem by assigning a unique name or suffix to the PPT row. If you use different PPT definition formats such as :: or !, the part name created after merging the PPT rows is unique.

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ERROR (169)

ERROR 169

ERROR (169): "Physical part name '%s' exceeds the maximum set length of %d characters. If you have reduced the value for the PART_TYPE_LENGTH directive since the last time the design was packaged, the pxl.state file might contain part names that are longer than the new PART_TYPE_LENGTH. Increase the maximum permissible part name length in the Packager Setup dialog box. "

This error is generated if a physical part name exceeds the maximum specified length for net names, which is controlled by the PART_TYPE_LENGTH directive. The default PART_TYPE_LENGTH is 31. This error is typically generated when you have packaged the design once and then reduced the value in the PART_TYPE_LENGTH directive. This may cause data in the pxl.state file to contain part names whose length exceeds the PART_TYPE_LENGTH value. If you now package the design, Packager-XL will generate this error. Increase the maximum permissible part name length by entering a new value in the Part Type Length field in the Packager Setup - Layout tab.

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ERROR (170)

ERROR 170

ERROR (170): "Cannot create a unique %s name from '%s'. Increase the maximum permissible part name length in the Packager Setup dialog box. "

This error is generated when a unique name cannot be found within the maximum length allowed for defining a part name. Increase the maximum permissible part name length by entering a new value in the Part Type Length field in the Packager Setup - Layout tab.

43

ERROR (173)

ERROR 173

ERROR (173): "Error found while parsing POWER_PINS for %s. "

This error occurs when Packager-XL parses a POWER_PINS property value that has a syntax error. Ensure that the POWER_PINS property is defined using the following syntax: POWER_PINS=(supply:pin list; supply:pin list; ...)

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ERROR (174)

ERROR 174

ERROR (174): "Error found while parsing POWER_GROUP for %s. Property value - %s."

This error occurs when Packager-XL parses a POWER_GROUP property value that has a syntax error. Ensure that the POWER_GROUP property is defined using the following syntax: POWER_GROUP = supply=newsupply [;supply=newsupply...][(subtype_name)]

45

ERROR (178)

ERROR 178

ERROR (178): "Physical net name '%s' loaded from the state file exceeds the maximum set length of %d characters. You might have changed the maximum physical net name length since the state file was last generated. To correct this, change the net name length in the Packager Setup dialog box or select the option of regenerating physical net names while packaging. "

This error occurs if you change the net length option after successfully running Export Physical once. The error is reported in the pxl.log file. The Packager tries to preserve physical net names from the previous run (assuming the 'Preserve' option is selected). Ensure that the Regenerate Physical Net Names check box is selected in the Export Physical dialog box and re-run Export Physical. This will regenerate all the physical net names to comply with the new character length constraint.

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ERROR (183)

ERROR 183

ERROR (183): "Null property value must be specified using leading/closing quotes on line %d. "

This error is generated if a null value is specified as the key property for a part in a ptf file. You could have accidentally added two property separators back-to-back. You could also have accidentally added a carriage return (End of Line) in place of the tilde (::) character. This error generally prevents Packager-XL from loading the PTF file properly. As a result of this error, Packager-XL may also generate ERROR 47. If you need to assign a null value to a property, use leading/closing quotes to specify that value.

47

ERROR (187)

ERROR 187

ERROR (187): "Power pin '%s' also appears as a non-power pin in section %d for physical part '%s'. "

This error is generated when the same pin is defined as a power and non-power pin in a section of a physical part in the chips.prt file. As a result of this error, Packager-XL may also generate ERROR 1084. Ensure that for each section of a physical part, a pin is defined as either a power pin or a non-power pin but not as both.

48

ERROR (189)

ERROR 189

ERROR (189): "Vectored pin name '%s' terminated early on line %d. "

This error occurs when you have used a vector pin but have not terminated it with the closing angle bracket '>' character in the chips.prt file. Ensure that all vectored pin names conform to the following syntax: pin_name<higher_pin_number..lower_pin_number>

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ERROR (190)

ERROR 190

ERROR (190): "Illegal pin range notation in pin name '%s' on line %d. "

This error occurs when you have used a vector pin but have not specified the lower and upper bus range or forgot to use two periods (..) as the separator for the pin range. Ensure that all vectored pin names conform to the following syntax: pin_name<higher_pin_number..lower_pin_number>

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ERROR (202)

ERROR 202

ERROR (202): "Unexpected token '%s' found on line %d. "

This error occurs if the pxl.state file contains a wrong keyword. This error will not occur if you use a pxl.state file generated by the Packager-XL. Do not edit the pxl.state file. Use Packager-XL to generate the pxl.state and *.pst feedback files.

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ERROR (203)

ERROR 203

ERROR (203): "Cannot find PPT part %s when loading state file. "

This warning appears for all single node nets. The message is for information purposes. If you do not want Packager-XL to display the warning, assign the following property:
NO_SINGLE_CHECK=TRUE

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ERROR (204)

ERROR 204

ERROR (204): "Part name '%s' maps to '%s' and '%s' in the %s. "

This error occurs when LONG_PART_NAME is different for the part in the top level and in the subdesign files. To resolve this, package the subdesign blocks again then package the top-level block. If the problem persists, contact Cadence Customer Support.

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ERROR (206)

ERROR 206

ERROR (206): "Expected '%c' character in 'POWER_PINS' = '%s'. "

This error appears when the starting delimiter, the (character, is missing from the POWER_PINS property definition in a chips.prt file. As a result of this error, Packager-XL can also generate more errors such as ERROR 206 and ERROR 1084. Ensure that you have defined the POWER_PINS property properly using the following syntax: POWER_PINS=(supply:pin list; supply:pin list; ...) For details about the POWER_PINS property, see the documentation.

54

ERROR (207)

ERROR 207

ERROR (207): "'POWER_PINS' property value '%s' terminated early. "

This error appears when the ending delimiter, the) character, is missing from the POWER_PINS property definition in a chips.prt file. Ensure that you have defined the POWER_PINS property properly using the following syntax: POWER_PINS=(supply:pin list; supply:pin list; ...)

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ERROR (208)

ERROR 208

ERROR (208): "Extra text after '%c' character in 'POWER_PINS' = '%s'. "

This error appears when you have placed extra text after the ')' character in the POWER_PINS property in a chips.prt file. The ')' character is the ending delimiter. As a result of this error, Packager-XL can also generate more errors such as ERROR 210 and ERROR 1084. Ensure that you have no extra text after the) character in the POWER_PINS property definition.

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ERROR (210)

ERROR 210

ERROR (210): "Cannot load power pins for alternate phys part '%s'. There might be syntax errors in the power pins value assignment. To prevent such problems, use the Assign Power Pins dialog box to edit properties. "

This error message appears with other error messages, which indicate why Packager-XL is unable to read power signals. Fix the cause of the other error that prevents Packager-XL from reading properties correctly.

57

ERROR (211)

ERROR 211

ERROR (211): "Token '%s' was terminated by a carriage return on line %d. "

This error occurs when you forget to place a semicolon (;) as the last character in the property header row in a ptf file. As a result of this error, Packager-XL also generates Error 47. Ensure that the property header row for a part is terminated with a semicolon (;) and fix the error in the ptf file listed in ERROR 47.

58

ERROR (213)

ERROR 213

ERROR (213): "%s property name:value '%s:%s' clashes with '%s' for PPT '%s'. Multiple property name:value pairs might exist for a particular PPT property in the part table files that are being merged. Ensure that each PPT has a unique property name:value pair. Fix one of the property name:value pairs listed in this message. "

This error occurs in situations where multiple property name:value pairs exist for a particular property of a PPT in a ptf file. Often, this error causes another error such as ERROR 47. For example, an injected property for a PPT has two values, such as 1K and 1000, or the same PPT is loaded from two different files and both files have different values for the CLASS property. In such a scenario, injected property values of 1K and 1000 were specified in two or more part rows, which should be merged because the key properties on the corresponding rows can be merged. Ensure that each PPT has a unique property name:value pair. Fix one of the property name:value pairs listed in the error.

59

ERROR (214)

ERROR 214

ERROR (214): "Multiple PPT part '%s' found when merging PPT '%s'. If multiple rows exist by the same name, multiple values of a key property must match and multiple values of an injected property must match. "

This error occurs when multiple PPT parts with the same name exist and this prevents Packager-XL from merging PPT parts. If multiple rows exist by the same name, multiple values of a key property must match and multiple values of the injected property must also match. Check the key property values for VALUE. Check the injected property values for PART_NUMBER.

60

ERROR (217)

ERROR 217

ERROR (217): "Following part row for PPT '%s' was not merged: "

This error occurs when two or more PPT rows cannot be merged together for the same PPT part. This error can occur if there are multiple tables in one file preventing the merging of a PPT part row. Using a dump of what the part row looks like and the file from which it is loaded, you can determine where the table is located and fix the error. It is a good practice to have PPT rows for a part in one table to reduce the chance of PPT merging errors. Ensure that if PPT rows are defined for the same part in multiple files, they have a consistent definition, and each PPT row has a unique PPT value.

61

ERROR (218)

ERROR 218

ERROR (218): "Property '%s' appears as both '%s' and '%s' for PPT '%s'. "

The above error occurs when a property is defined as global in one PPT row for a part and as injected in another row for the PPT part in the ptf files. This error may occur if you have two sets of PPT row definitions for the same part in one ptf file or across two ptf files. If the error occurs because of different definitions for the same part in two files, Packager-XL also generates ERROR 221. Define the PPT definition for a part in only one file and in one part definition. This will ensure that you have only one set of global properties.

62

ERROR (221)

ERROR 221

ERROR (221): "PPT '%s' was loaded from the following files: "

This error occurs when a PPT is defined in multiple files. Packager-XL is unable to resolve the PPT to one set of values. As a result of this error, Packager-XL can also generate ERROR 213 and ERROR 47. Define the PPT definition for a part in only one file by listing all required properties and remove the PPT definitions for the part from other files.

63

ERROR (228)

ERROR 228

ERROR (228): "Cannot package the following primitive instance in any section of the physical part '%s'. Primitive Instance: %s The primitive instance has pins which does not match the section definition in chips.prt. Check the pin definitions for each section in the chips.prt file. Regenerate the netlist and rerun packager. "

This error occurs when the entity/verilog.v file is not in sync with the physical part used in the design and Packager-XL fails to package the primitive instance in any of the sections of the physical part. Rewrite the part symbols with the new name. This will create a new entity/verilog.v file and then package the design.

64

ERROR (229)

ERROR 229

ERROR (229): "Part selection not performed. Refer to previous error(s) regarding attempt to merge PPT '%s'. "

This error is followed by error 217, when the Packager fails to merge two or more PPT rows together for the same PPT part. There could be multiple tables in one file preventing the merging of a PPT part row. As a result, part selection cannot be performed. You should have all the PPT rows for a part in one table. This reduces the chances of any PPT merging problems. If PPT rows are defined for the same part in multiple files, ensure that they have a consistent definition and each PPT row results in a unique PPT value.

65

ERROR (239)

ERROR 239

ERROR (239): "Conflicting directives '%s' and '%s' were specified. Directive '%s' will be ignored. "

You have defined the same file in both, the INCLUDE_PPT, and EXCLUDE_PPT directives. This causes Packager-XL to ignore the EXCLUDE_PPT directive for that file. You cannot include and exclude the same ptf file while packaging a design. If a file is listed in the INCLUDE_PPT and EXCLUDE_PPT directives, the file is considered to be included for packaging. The EXCLUDE_PPT directive is ignored. If you want the file to be excluded while packaging, remove its name from the INCLUDE_PPT list.

66

ERROR (241)

ERROR 241

ERROR (241): "Power nets %s and %s have same power pin %s on physical part %s. "

This error occurs when multiple power nets have a common power pin for a physical part. To correct the error, remove the common pin from one of the power nets in the chips.prt file for the given part.

67

ERROR (242)

ERROR 242

ERROR (242): "Cannot determine legal range for pins '%s' and '%s'. "

This error is caused when you use an invalid PIN_NUMBER range. For instance, when a negative number is used as part of the PIN_NUMBER range. As a result of this error, Packager-XL can also generate ERROR 88. Ensure that you have defined a valid PIN_NUMBER range for the physical pin corresponding to the logical pin name.

68

ERROR (246)

ERROR 246

ERROR (246): "No physical pins found for logical pin '%s' on physical part '%s'. "

This error message occurs when you have specified a logical pin name but have not specified a corresponding physical pin. To specify a physical pin, use the following syntax: pin 'logical_pin_name' <bit_number> : PIN_NUMBER='(<physical_pin_number>, <physical_pin_number2>, ...)'; property1='(value1, value2, ...)'; property2='(value1, value2, ...)'; end_pin; To fix the problem, ensure that a physical pin is specified for the logical pin. For more information about the chips.prt file, see the documentation.

69

ERROR (248)

ERROR 248

ERROR (248): "Part '%s' cannot be used because there are no unique physical pins in section '%d'. Open and edit the part to ensure that there is at least one unique pin in each section of the physical part."

This error occurs when all pins for a section are common. Ensure that you have at least one non-common pin for each section of a physical part.

70

ERROR (251)

ERROR 251

ERROR (251): "Design not loaded. "

This error message is always generated with another error message that is more informative. To fix the problem, fix the issue listed in the other error message.

71

ERROR (262)

ERROR 262

ERROR (262): "Expected %s while parsing 'NC_PINS' = '%s'. "

This error occurs when you have defined the NC_PINS property but have not assigned the leading or closing quote. Depending on whether the leading or closing quote is missing, the error message will show the appropriate missing character. While checking for the cause of this error, check the assignment for the NC_PINS or MERGE_NC_PINS property. To avoid this error, ensure that all NC_PINS or MERGE_NC_PINS property value definitions are enclosed within leading and closing quotes.

72

ERROR (263)

ERROR 263

ERROR (263): "NC_PINS properties were not merged for %s '%s'. "

This error occurs when you have defined an incorrect MERGE_NC_PINS property. You may not have assigned the leading or closing quote. This error may often appear with ERROR 262. Ensure that all NC_PINS or MERGE_NC_PINS property value definitions are enclosed within leading and closing quotes.

73

ERROR (269)

ERROR 269

ERROR (269): "The following block instances have same value '%s' specified for the property 'SUBDESIGN_SUFFIX': '%s(MODULE: %s)' and '%s(MODULE: %s)'. The 'SUBDESIGN_SUFFIX' property value needs to be unique across the design. "

This error occurs when you have specified the same value for the SUBDESIGN_SUFFIX property for two different modules. Packager-XL will use the value of the module it reads first and ignore the value assigned to the second module. To avoid this error, ensure that you have defined different values for the SUBDESIGN_SUFFIX property across different modules.

74

ERROR (285)

ERROR 285

ERROR (285): "NC pin '%s' also defined as a %s for physical part '%s'. "

This error occurs when you have defined a NC_PINS property to contain a pin that is already defined as a physical pin (as a PIN_NUMBER property in the chips.prt file). As a result of this error, Packager-XL also generates ERROR 286. Ensure that you have different values for NC pins and physical pins for a physical part.

75

ERROR (286)

ERROR 286

ERROR (286): "NC pins not unique for %s '%s'. "

The above error occurs when you have defined a NC pin and one or more of those pins are defined as physical pins. This error generally occurs with ERROR 285. Example: ERROR(285): NC pin '1' also defined as a physical pin for physical part ':: 74LS00-(1,2,3,4,5,6,7)'. ERROR(286): NC pins not unique for alternate physical part '74LS00-(1,2,3,4,5,6,7)'. ERROR(1084): The alternate physical part '74LS00-(1,2,3,4,5,6,7)' for schematic instance @PROJ_LIB.DESIGN(SCH_1):PAGE1_I1@MYTTL.LS00(CHIPS)(MODULE: DESIGN; PART: 74LS00) cannot be created. The physical part might be incorrectly defined. Check the component definition properties (COMP_DEF_PROP) defined on the instance in Design Entry HDL for any possible syntax error. For more information on COMP_DEF_PROP, refer to the Packager documentation. Description of Example: The above error was caused as both, the NC_PINS, and PIN_NUMBER property, had a common pin, 1, for part 74LS00. Ensure that you have different values for NC pins and physical pins for the same physical part.

76

ERROR (288)

ERROR 288

ERROR (288): "Line %d for '%s' is too long. "

This error occurs when a property name definition is longer than the supported length. For example, this error may occur for NC_PINS and POWER_PINS if they have a long definition. If the description of a property is long and you need to retain the entire description, you can break the property definition into two or more lines.

77

ERROR (305)

ERROR 305

ERROR (305): "Cannot find file '%s' in lib.cell:view '%s.%s:%s'. Expected a chips file for cell '%s' because this cell has no hierarchy. Verify you have saved all schematic changes for this cell and provide a chips file if appropriate. Schematic instance: %s "

This error message is generated when there are either all upper case or mixed case values in the cds.lib file used by the design. Modify the cds.lib file to contain all lower case values.

78

ERROR (314)

ERROR 314

ERROR (314): "Occurred while reading cds.lib: "

This error occurs when the cds.lib file includes a reference to a file, which is not available or which cannot be accessed. Ensure that all INCLUDE file entries in the cds.lib file point to valid files. Also ensure that you have at least read-access to the files being included.

79

ERROR (316)

ERROR 316

ERROR (316): "EDB%s reports: "

This error displays the messages generated by design expansion library functions. The errors can occur when global buses such as DATA<7..0>\G are used in a design and individual bits of the bus (say, DATA<3>\G) are connected to components. When the design is saved, these connections may be incorrectly written to the verilog.v netlist and/or the glbl module.

80

ERROR (331)

ERROR 331

ERROR (331): "Unknown keyword '%s' found on line %d. "

This error occurs if you use an unrecognized keyword in the cdsprop.paf file, which specifies the case sensitivity of properties. Valid keywords include permit, inherit, uppercasevalue, preservename, and parameter. Ensure that you have defined a valid keyword in the cdsprop.paf file.

81

ERROR (339)

ERROR 339

ERROR (339): "The PIN section for part '%s' contains properties but no pin names. Add the appropriate pin name(s) to the chips.prt file for this part. "

This error occurs when you have specified properties for the PIN section corresponding to a part in the chips.prt file but have not specified pin names corresponding to that part. Ensure that you use the following syntax while defining the PIN section for any part: pin 'logical_pin_name' <bit_number>: PIN_NUMBER='(<physical_pin_number>, <physical_pin_number2>, ...)'; property1='(value1, value2, ...)'; property2='(value1, value2, ...)'; end_pin; Ensure that you have defined a logical pin name in the PIN section for the part that is specified in the error.

82

ERROR (341)

ERROR 341

ERROR (341): "A short property name was found instead of the long name. %s property name: %s %s property name: %s Net name: '%s' "

SCALD designs have a limitation (16) on the number of characters allowed in property names. When Packager-XL is run on a newly migrated SCALD design, errors are generated and the truncated property names are identified as the cause. After design migration, run the "s2l design" command in the Design Entry HDL console window. Do not substitute the word "design" with the actual design name. This command updates all short properties on HDL to long properties and the design should then package without problems.

83

ERROR (346)

ERROR 346

ERROR (346): "Unable to expand this design for packaging. The chips view was not found for cell '%s' in library '%s'. "

The above problem is caused when there is a cell in the design that does not have the chips view defined. You need to define a chips view containing the chips.prt file for all cells that require packaging. If a cell does not need packaging, then assign the PACK_IGNORE property to it.

84

ERROR (355)

ERROR 355

ERROR (355): "The part '%s' found in library '%s' during design expansion has been instantiated from library '%s'. The part '%s' has been instantiated from more than one library. "

If a part moves from one library to another, Packager-XL will not flag any error and use the part from the updated library. But if a part is instantiated from more than one library, Packager-XL generates this fatal error. Ensure that you instantiate a part from only one library.

85

ERROR (356)

ERROR 356

ERROR (356): "'REUSE_INSTANCE'='%s' present on design '%s' is found on more than one design. It should be unique across the instances of reused blocks. "

This error occurs when you add the REUSE_INSTANCE property with the same value on multiple reuse modules. To correct this problem, you must ensure that the REUSE_INSTANCE property has unique values when assigned to each module.

86

ERROR (362)

ERROR 362

ERROR (362): "Two global signals are shorted. If you want to short these global signals, add them in Allowed Global Shorts list in editor setup options. Net name: '%s' Net name: '%s' "

This error occurs when Packager-XL finds an illegal global signal short. It displays the two net names that are being shorted, and exits with error status 2. You should ensure that there is no shorting of global signals. If you want a global signal to be shorted, add it in the ALLOWED_GLOBAL_SIGNALS list.

87

ERROR (409)

ERROR 409

ERROR (409): "Error at line %d while loading the %s file. Unable to create the following new pin instance, %s, as the pin definition was not found. Reference Designator: %s. Schematic Instance: %s. Check the symbol and ensure that the pin definition is consistent. "

Packager-XL generates this error when there is a mismatch between the pins in the pstxnet.dat file and those in the pstchip.dat file. To resolve this error, generate the pstxnet.dat and pstchip.dat files again by running Export > Physical. If the problem persists, contact Cadence Customer Support.

88

ERROR (413)

ERROR 413

ERROR (413): " Error at line %d in file %s. Reference designators are inconsistent in the xprt and xnet files. Reference designator in xprt file: %s. Reference designator in xnet file: %s. Schematic instance :%s. "

To resolve this error, package the design again by running Export > Physical. If the problems persists, contact Cadence Customer Support.

89

ERROR (570)

ERROR 570

ERROR (570): "SUBDESIGN_SUFFIX = '%s' is attached to module %s. Module %s is not specified in the FORCE_SUBDESIGN or USE_SUBDESIGN directive design name list. "

This error is generated when design reuse has not been enabled for a design with an instance that has the SUBDESIGN_SUFFIX property. The block name has not been specified in the FORCE_SUBDESIGN or USE_SUBDESIGN directive. Specify the block name in the FORCE or USE_SUBDESIGN list by adding the name of the subdesign (base_level) in the USE_SUBDESIGN or FORCE_SUBDESIGN fields in the Packager Setup - Subdesign tab.

90

ERROR (636)

ERROR 636

ERROR (636): "Part definition for the component %s could not be loaded from the library %s. There are syntax errors in the part definition. Ensure that the syntax is correct for the part definition. "

This error is generated along with another packaging error, which prevents Packager-XL from completing its operation. Fix the other errors that are generated in the pxl.log file and package the design again.

91

ERROR (1001)

ERROR 1001

ERROR (1001): "Global project file: '%s' not found. Packager-XL is unable to execute from the command-line prompt as you may have misspelled the -proj directive or have specified a <project_filename> that does not exist. Use the -proj option correctly and specify the correct <project_filename>. Also ensure that the following environment variable is set: setenv CDS_CONCEPT_HDL TRUE Set the environmental variable, CDS_CONCEPT_HDL, to TRUE and run Packager-XL again.

92

ERROR (1003)

ERROR 1003

ERROR (1003): "Error(s) found while reading the project file. The cpm file might have been manually edited. Correct the syntax errors in the project file and rerun the program. "

This error message is always generated with other error conditions that prevent Packager-XL from reading the project file. For example, if you do not have proper REF_DES_PATTERN directive assignment, Packager-XL will not read the project file. Or the cpm file might have been manually edited. Correct the syntax errors in the project file and rerun the program.

93

ERROR (1006)

ERROR 1006

ERROR (1006): "%s is an invalid command line argument. "

If Packager-XL generates an invalid argument for -proj, although -proj is a valid argument, and is unable to run from the command line prompt, this might indicate that you have not have set the environment to Design Entry HDL. This error often appears with Error 1001. To resolve the error, set the environmental variable, CDS_CONCEPT_HDL, to TRUE and run Packager-XL again.

94

ERROR (1051)

ERROR 1051

ERROR (1051): "Cannot find the physical part %s for Schematic instance: %s The chips.prt file might have been accidentally edited, or the PACK_TYPE property might be missing from the ptf file or the schematic. Ensure that a physical part corresponding to the instance used in the schematic exists and the PACK_TYPE property is defined in the schematic or the part table file. "

This error occurs when Packager-XL does not find a physical part in the chips.prt file when an instance of that part is being used in the schematic. This error can occur because of accidental editing of the chips.prt file. Another reason for this error is a missing PACK_TYPE property in the ptf file or the schematic. As a result of this error, Packager-XL may also generate ERROR 1052. Ensure that a physical part corresponding to the instance used in the schematic exists.

95

ERROR (1052)

ERROR 1052

ERROR (1052): "No ppt part selected because the instance has no physical part. Schematic instance: %s Check the PACK_TYPE or PART_NAME property assigned to the instance and ensure that the part exists in the part table file. "

This error occurs when Packager-XL is unable to find a physical part corresponding to a schematic instance. As a result, Packager-XL is unable to select any PPT part. This error generally occurs with ERROR 1051. Ensure that a physical part corresponding to the instance used in the schematic exists.

96

ERROR (1069)

ERROR 1069

ERROR (1069): "Invalid POWER_GROUP property value: %s Schematic instance: %s The correct syntax is PWR_NET1=NEW_NET1;PWR_NET2=NEW_NET2(SUBTYPE) To avoid syntax errors, use the Assign Power Pins dialog box in Design Entry HDL for assigning the POWER_GROUP property to the schematic. "

You have specified an invalid value for the POWER_GROUP property. The correct value for the property must conform to the following syntax: POWER_GROUP = supply=newsupply [;supply=newsupply...][(subtype_name)] Ensure that you have defined the correct value for the POWER_GROUP property.

97

ERROR (1072)

ERROR 1072

ERROR (1072): "Net %s is present in more than one POWER_GROUP properties in the schematic instance. Schematic instance: %s Correct the POWER_GROUP property and rerun Packager-XL. To avoid such errors, use Assign Power Pins dialog box to specify power pin details. "

This error appears when you define a POWER_GROUP property multiple times. Packager-XL in that case cannot decide which value is the right value.

98

ERROR (1078)

ERROR 1078

ERROR (1078): "PACK_TYPE value %s of the part %s is not defined in chips.prt for the schematic instance. "

This error occurs if the pack_type value of a part in the schematic is not defined in the chips.prt file. Ensure that the value of the PACK_TYPE property for a part on the schematic is defined in the chips.prt file.

99

ERROR (1082)

ERROR 1082

ERROR (1082): "Cannot assign physical part to schematic instance. Schematic instance: %s Physical part: %s Add PART_NAME='%s' to chips.prt entry for %s. "

This error message is generated when Packager-XL is unable to associate logical and physical parts based on the definitions in the chips.prt file and schematic instances. Packager-XL determines the logical part name for each primitive section and determines the physical parts that are linked to this logical part. The PART_NAME property supplies the name of the logical part. If this property is not present, the shortest name is used for the logical name. This shortest name may be the same for two primitives. To solve this error, you need to add a PART_NAME=TL431C property to the BODY section of the second primitive in the chips.prt file.

100

ERROR (1084)

ERROR 1084

ERROR (1084): "The alternate physical part '%s' for the schematic instance cannot be created. Schematic instance: %s The physical part might be incorrectly defined. Check the component definition properties (COMP_DEF_PROP) defined on the instance in Design Entry HDL for any possible syntax error. For more information on COMP_DEF_PROP, refer to the Packager documentation. "

This error occurs when Packager-XL is unable to create an alternate physical part for a schematic instance. The cause of this error could be some syntax error in any property definition such as NC_PINS, MERGE_NC_PINS, POWER_PINS, or MERGE_POWER_PINS. Ensure that you fix the other errors that appear with ERROR 1084.

101

ERROR (1085)

ERROR 1085

ERROR (1085): "No key property found in the ppt file for the part '%s'. "

The above error occurs when you have not defined a key property in the ptf file for this part. Ensure that you have defined a key property in the ptf file for the specified part.

102

ERROR (1088)

ERROR 1088

ERROR (1088): "%s=%s property on the following instance having physical part '%s' is ignored by Packager-XL. Packager-XL considers only one PHYS_DES_PREFIX value per physical part on the schematic for assigning new reference designators. Schematic Instance: %s "

Packager-XL generates this error when multiple PHYS_DES_PREFIX property values are found on the schematic for the same physical parts. It exits with error status 1. Ensure that you have only one PHYS_DES_PREFIX property value defined for a physical part.

103

ERROR (1105)

ERROR 1105

ERROR (1105): "Properties on instance and package do not match. Schematic instance: %s Package: %s Property: %s Value on instance: %s Value on package: %s This error occurred because multiple instances have the same LOCATION property value but have different values for the property name(s) listed above. Ensure the following: that instances with different component instance properties do not have the same reference designator assigned or that the properties listed in the error match for the instances with same reference designator assigned. "

Packager-XL compares the property value assigned on the schematic instance with the values in the package. If discrepancies exist, it displays this error. It also lists the name of the package and property, and the value of the property in the schematic instance and the package instance. Ensure that the value of property listed in the error message for both instances is the same.

104

ERROR (1109)

ERROR 1109

ERROR (1109): "Cannot package instance into package %s. Schematic instance: %s The instances have the same location property value. Change the location property value for any one of the instances in the Design Entry HDL Schematic. "

This error occurs if you try to package an invalid instance or an instance that does not exist into a package. Error 1109 can also occur if an instance has a user-defined reference designator (Hard Location property) defined in a block and that block is instantiated multiple times but is not reused in the design. Change the reference designator value or reduce the value of the SIZE property defined. Note: In case of a block instantiated multiple times, change the reference designator value.

105

ERROR (1120)

ERROR 1120

ERROR (1120): "Reference designator %s for the following instance is longer than the one specified by the REF_DES_LENGTH = %d directive. This instance will not be packaged. Change the ref_des_pattern or REF_DES_LENGTH directive and rerun the packager. Schematic Instance: %s "

Packager-XL uses the REF_DES_LENGTH directive to determine the maximum permissible length of reference designators. By default, this value is 31, which is usually enough to represent the reference designator. You may have set this value to a lower value. In such cases, you may increase the REF_DES_LENGTH value. To address this error, change the REF_DES_PATTERN value by entering a new value in the Ref Des Length field in the Packager Setup - Layout tab and rerun Packager-XL. Alternatively, you can change the LOCATION property on the schematic and rerun Packager-XL.

106

ERROR (1129)

ERROR 1129

ERROR (1129): "Multiple '%s' properties found on: Schematic instance: %s Ignoring all '%s' properties on this object. "

This error can occur if instances within a hierarchical block are assigned two values for each soft packaging property. This may happen if you assign soft packaging properties (CDS_LOCATION, CDS_SEC) to a hierarchical block that has already been backannotated. Assigning soft packaging property to the hierarchical block causes these properties to be inherited down to all instances within the block thereby causing two values for each soft packaging property. If you remove the soft packaging properties from the hierarchical block, the design will package correctly.

107

ERROR (1134)

ERROR 1134

ERROR (1134): "PN %s does not belong to SEC %d. Pin: %s Schematic instance: %s There might be a mismatch between the subdesign state file and the schematic of the subdesign. To update the subdesign state file, set the module as root and run Packager-XL. Once the subdesign state file is corrected, set the top as root and run Packager-XL again. "

This error occurs when a particular pin does not belong to its associated section. This error may be generated if you make changes to a block that contains subdesigns and then package it using the FORCE_SUBDESIGN directive. If this error occurs in reused blocks, ensure that you use the USE_SUBDESIGN directive and not the FORCE_SUBDESIGN directive to package the design.

108

ERROR (1137)

ERROR 1137

ERROR (1137): "Hard PN property value %s on the schematic not found on phys part %s in chips.prt file. Pin: %s Schematic instance: %s "

You may get this error if you have added the PN=number property to a pin on a symbol body to make the property invisible. To make the pin number property invisible, add the $PN=number property to the pin and not the PN=number property.

109

ERROR (1138)

ERROR 1138

ERROR (1138): "A hard location was found on instances of different physical part types. Refer to error (%d) for a complete list of instances assigned this location. Schematic instance: %s Instance physical part: %s Package: %s Package physical part: %s The same value for location on instances of different physical part names is not supported. Correct the location for the appropriate instance(s) in Design Entry HDL. "

This error occurs due to the assignment of the LOCATION value to two different types of instances, such as LS00 or LS241. This error often occurs (as is the present case) with ERROR 1149. Refer to ERROR 1149 for a complete list of instances assigned to this location. To address the error, change the LOCATION value of one of the instances. Other likely cause: This error may also occur if a new (unpackaged) symbol does not have a placeholder for the PATH property. When you save a design with such a symbol, the PATH property of the symbol is replaced by the original value of the PATH property as no placeholder can be found. In addition, during backannotation, the canonical path of the part is not found and as a result, the value of the LOCATION property on the board is not honored. This leads to a duplicate LOCATION property on parts and results in this error. Add the PATH placeholder on new symbols to avoid this problem.

110

ERROR (1147)

ERROR 1147

ERROR (1147): "Empty %s property value found on : Schematic Instance: %s "

You must always specify a value for the LOCATION property. When you define a LOCATION property, Packager-XL does not assign a location for that instance and creates a reference designator based on the value you define. If you leave the LOCATION property with a NULL value, Packager-XL will not be able to define a reference designator for that instance. Ensure that you have specified a proper value for the LOCATION property.

111

ERROR (1149)

ERROR 1149

ERROR (1149): "Found a hard location '%s' for Schematic instance: %s A hard location on instances of different physical part names is not supported. Correct the location for the appropriate instance(s). "

This error message occurs along with ERROR 1138. The message shows the LOCATION value, which is the same for two different physical parts. For instance, the LOCATION value of U1 is defined for both, LS00, and LS241. Packager-XL does not support the same hard location on instances of different physical part names. Therefore, correct the location for the appropriate instance(s). For example, change the LOCATION value of U1 for either LS00 or LS241.

112

ERROR (1175)

ERROR 1175

ERROR (1175): "REF_DES_PATTERN range must be terminated with a ']'. "

The REF_DES_PATTERN range begins with the '[' character and is terminated with the ']' character. If you forget to terminate the REF_DES_PATTERN range with ']', this error occurs. As a result of this error, Packager-XL may also generate ERROR 1003. For example, if you use U($DRAWING)X[1-9(1) as the REF_DES_PATTERN, Packager-XL will generate this error. Ensure that the REF_DES_PATTERN range is terminated with the ']' character.

113

ERROR (1176)

ERROR 1176

ERROR (1176): "REF_DES_PATTERN range specifiers must both be characters or integers. "

The default REF_DES_PATTERN directive supports a number range from 0-9. You can change this number range to say, 3-9, or use characters such as A-H to specify the number range. However, you cannot specify a combination of numbers and characters (for example, 0-S or D-8). Ensure that the REF_DES_PATTERN range consists of either numbers or characters but not both. The range should be ascending and should not contain a space.

114

ERROR (1177)

ERROR 1177

ERROR (1177): "The REF_DES_PATTERN number range is not in the increasing order. Specify the number range in the increasing order, for example, 0-9, by editing the REF_DES_PATTERN directive from the Layout tab of the Packager Setup dialog box. "

The default REF_DES_PATTERN directive supports a number range from 0-9. Note that this range is always ascending. If you specify a descending range, this message will be displayed. Ensure that the REF_DES_PATTERN value range is ascending. For example, you can specify 1-9 as the range.

115

ERROR (1178)

ERROR 1178

ERROR (1178): "The REF_DES_PATTERN value is invalid. Use only alphanumeric characters: letters (a-z or A-Z) or numbers (0-9). Other characters, such as $ or %, are not supported. Edit the REF_DES_PATTERN directive from the Layout tab of the Packager Setup dialog box. "

The REF_DES_PATTERN directive supports either characters (a-z or A-Z) or numbers (0-9) for defining the number range. If you use a non-alphanumeric character such as $ or % in REF_DES_PATTERN, this error occurs. For example, if you use U($DRAWING)X[$-*](6) as REF_DES_PATTERN, Packager-XL will generate the above error. Ensure that the REF_DES_PATTERN value range consists of alphanumeric characters.

116

ERROR (1179)

ERROR 1179

ERROR (1179): "The format for REF_DES_PATTERN is ['character/digit'-'character/digit']. "

The REF_DES_PATTERN directive supports a standard pattern for defining the format of reference designators. This pattern is of the format [character/digitcharacter/digit]. If you do not use this format, this error is generated. For example, if you use U($DRAWING)X[$-*](6) as the REF_DES_PATTERN, Packager-XL will generate this error. If you add any spaces or use a non-alphanumeric character, you can get this error message. Ensure that the REF_DES_PATTERN definition conforms to the format [character/digit-character/digit] and that there are no spaces in the range definition.

117

ERROR (1180)

ERROR 1180

ERROR (1180): "The starting value for REF_DES_PATTERN is not terminated with ')' character. "

The REF_DES_PATTERN directive supports a number range and a starting value in that number range. This starting value begins with the '(' character and terminates with the ')' character. If you do not terminate the starting value with the ')' character, this error is displayed. For example, if you use U($DRAWING)X[1-9](6 as the REF_DES_PATTERN, Packager-XL will generate this error. Ensure that the REF_DES_PATTERN value range consists of alphanumeric characters.

118

ERROR (1181)

ERROR 1181

ERROR (1181): "The starting value for REF_DES_PATTERN must be an alphanumeric character. "

The starting value in the REF_DES_PATTERN directive should either be an alphabet or a number. For example, if you use U($DRAWING)X[1-9]($) as the REF_DES_PATTERN, Packager-XL will generate this error. Ensure that the starting value in the REF_DES_PATTERN directive is either an alphabet or a number. This starting value should be within the specified range. For example, in the instance illustrated in this case, specify the starting value as a number between 1 and 9.

119

ERROR (1183)

ERROR 1183

ERROR (1183): "The starting value for REF_DES_PATTERN should follow the specified range. Edit the REF_DES_PATTERN directive from the Layout tab of the Packager Setup dialog box. "

The starting value in the REF_DES_PATTERN directive should always follow the number range. For example, the default REF_DES_PATTERN definition has the following value: ($PHYS_DES_PREFIX)[0-9](1) Note that the starting value 1 lies within the acceptable range 0-9. Ensure that the starting value in the REF_DES_PATTERN directive always follows the number range.

120

ERROR (1189)

ERROR 1189

ERROR (1189): "Invalid starting value specified in REF_DES_PATTERN. "

This error message is generated when you specify an invalid starting value for the REF_DES_PATTERN directive. For example, if you specify the following REFDES_PATTERN value, REFDES_PATTERN ($PHYS_DES_PREFIX)[1-9](0), this message will be generated because the starting value 0 is not defined in the range 1-9. Ensure that the starting value defined in the REFDES_PATTERN directive is part of the value range you have specified.

121

ERROR (1318)

ERROR 1318

ERROR (1318): "Errors found while processing "%s". Ignoring line: %d "

This error is generated when Packager-XL is unable to complete the operation because of issues in the pstsecx.dat file. Example: Errors found while processing "U1 1 U1 3". Ignoring line: 2 In this example, U1(LS04) has two sections, section5 (pin1), and section6 (pin3). The problem arises because the pstsecx.dat file shows the following data: U1 1 U1 3 This data requires Packager-XL to change the net name from U1 of pin 1 to U1 of pin 3. However, Packager-XL is unable to complete the operation as pin 3 is already being used in the design. To address the error, change the data in the pstsecx.dat file to some other pin (section), which is not being used in the design.

122

ERROR (1458)

ERROR 1458

ERROR (1458): "Unable to package the design as the design connectivity data is in use by another user '%s' "

This error occurs when the design is in use by another user or if there are lock files in the design. The other user may be packaging the design, has launched Constraint Manager, or is saving the design. If there are lock files, delete them, save the design in Design Entry HDL and package the design again.

123

ERROR (2026)

ERROR 2026

ERROR (2026): "Feedback part %s is missing and the schematic instance cannot be updated with feedback key properties. Schematic instance: %s The part might have been added using a part table file that has been accidentally removed from the PPT directive during the feedback. Ensure the part added in the schematic exists and the appropriate part table files containing all the part definitions are specified in the PPT directive in the Part Table section of the Project Setup form. "

This error occurs when you have added a ppt part using a ptf file, which during the feedback phase, has been accidentally removed from the PPT directive or the ppt part added in the design is removed from the ptf file from which it was added before the properties have been fed back using Import Physical. To address this error, ensure that the ppt part added in a schematic is not altered or removed and the appropriate ptf files containing all part definitions are specified in the PPT directive in the Part Table section of the Project Setup form. The Import Physical procedure should have access to the ppt that will be used during feedback.

124

ERROR (2030)

ERROR 2030

ERROR (2030): "Part %s is missing and the the schematic instance cannot be updated with feedback key properties. Schematic instance: %s The part might have been added using a part table file that has been accidentally removed from the PPT directive during the feedback. Ensure the part added in the schematic exists and the appropriate part table files containing all the part definitions are specified in the PPT directive in the Part Table section of the Project Setup form. "

The issue is caused by a space in a KEY property field in the part table. To resolve the issue, remove the space in the part table file and then use the part manager tool to update the parts in Design Entry HDL.

125

ERROR (2050)

ERROR 2050

ERROR (2050): "Too many tokens in line %d File Name: %s There must be %d tokens. "

This error is caused whenever any feedback file (compview.dat, pinview.dat, netview.dat, funcview.dat) has more tokens than required. Each feedback file uses an exclamation mark (!) as a token to separate two properties in the same line. If you use more tokens than the one defined in the first line, this error occurs. To address this error, ensure that all lines in the feedback files have the same number of tokens. Avoid editing the feedback file because it may cause errors. Use File > Export > Logic in PCB Editor to generate the feedback files.

126

ERROR (2052)

ERROR 2052

ERROR (2052): "! is not the last character on line %d File Name: %s "

Packager-XL expects the token (!) to be the last character of each line of all feedback files. If you do not use the token (!) as the last character of any line of a feedback file, such as pinview.dat, Packager-XL generates this error. To address this error, ensure that the token (!) is used as the last character for each line of all feedback files.

127

ERROR (2053)

ERROR 2053

ERROR (2053): "Too few tokens in line %d File Name: %s Only %d are found. %d required. The file might have been manually edited. Run the Import Physical command with the Generate Feedback File check box selected. "

This error is caused whenever any feedback file (compview.dat, pinview.dat, netview.dat, funcview.dat) has fewer tokens than required. Each feedback file uses the exclamation mark (!) as a token that separates two properties in the same line. Ensure that all lines in the feedback files have the same number of tokens. Avoid editing the feedback file because it may cause errors. Use File > Export > Logic in PCB Editor to generate the feedback files.

128

ERROR (2056)

ERROR 2056

ERROR (2056): "Cannot feedback properties of type %s in %s. "

This error is generated when you are mixing releases, for example using Design Entry 13.6 and Packager 14.0. As a result, while backannotating the design, Packager-XL might corrupt the pxl.state file generating this error. This error may also occur if you have hand-edited the pxlBA.txt file. Ensure that you are not mixing releases. You may also delete the pxl.state file and re-package the design using the same Design Entry HDL version as that of PCB Editor.

129

ERROR (2064)

ERROR 2064

ERROR (2064): "Instance in pinView.dat refers to multiple packages. Schematic instance: %s Line Number: %d Packages: %s and %s "

This error occurs while backannotating a swap operation from Allegro PCB Editor on a design that uses resistor packs with the HAS_FIXED_SIZE property attached. Any schematic body with the HAS_FIXED_SIZE = n property is automatically assigned the SWAP_GROUP = x property in PCB Editor, where x is the logical path name to the schematic symbol in Design Entry HDL. Functions within the same swap group can be swapped with one another, but not outside the swap group. If the assigned SWAP_GROUP property is manually changed to override this limitation, the backannotation process breaks. To avoid this error, it is recommended that you use single resistor symbol versions instead of resistor packs.

130

ERROR (2070)

ERROR 2070

ERROR (2070): "%s must be specified in line %d of %s Cannot process pin %s on pkg %s connected to net. Physical net name: %s "

This error may occur if you try to change a part definition on the fly. For example, if you try to change a 9-pin part to an 11-pin part (by defining a pin count of 11 and adding a new JEDEC_TYPE in PCB Editor) and then backannotate the schematic, this error may occur. In such a scenario, you can do the following to avoid the problem: Make a local copy of the pxlBA.txt file. (This is for backup.) Add the following line after LOGICAL_PIN: FUNC_LOGICAL_PATH != '' Backannotate the schematic by creating new feedback files and generate back annotation files (run Import Physical). These steps will create a pinview.dat file without the power pins in it.

131

ERROR (2073)

ERROR 2073

ERROR (2073): "The first line of %s has bad format. Must be:
A!NET_NAME!REFDES!PIN_NUMBER!FUNC_LOGICAL_PATH!COMP_DEVICE_TYPE! and
then be followed by any other user specific %s properties. "

This error occurs when you do not have the first line of the pinview.dat file in the standard format.
Ensure that the first line of the pinview.dat file has the following format:
A!NET_NAME!REFDES!PIN_NUMBER!FUNC_LOGICAL_PATH!COMP_DEVICE_TYPE! and
that the following lines have any other user specific properties.

132

ERROR (2074)

ERROR 2074

ERROR (2074): "The first line of %s has bad format. The line must begin with %s and then be followed by any other user specific %s properties. The file might have been manually edited. Run the Import Physical command with the Generate Feedback File check box selected. "

This error occurs when you do not begin the first line of the compview.dat file with A!REFDES! and then follow it with any other component properties that you may have defined. Ensure that the second line of all feedback lines begins with the J character.

133

ERROR (2077)

ERROR 2077

ERROR (2077): "The second line of %s does not start with J. The line must begin with J The file might have been manually edited. Run the Import Physical command with the Generate Feedback File check box selected. "

This error occurs when you do not begin the second line of any feedback file, such as netview.dat or funcview.dat, with the J character. You may have accidentally put a carriage return and separated the first line into two lines. To address the error, ensure that the second line of all feedback lines begins with the J character.

134

ERROR (2080)

ERROR 2080

ERROR (2080): "Feedback failed because %s file is missing. To complete feedback from PCB Editor, Packager-XL requires four feedback files: pinview.dat, netview.dat, compview.dat, and funcview.dat. Generate the feedback files from the Import Physical dialog box. "

Packager-XL requires four feedback files to complete feedback from PCB Editor. These files are pinview.dat, netview.dat, compview.dat, and funcview.dat. If the pinview.dat file is not present in the packaged view, this error appears. Ensure that the pinview.dat file is in the packaged view of the design. Generate the feedback files from the Import Physical dialog box.

135

ERROR (2082)

ERROR 2082

ERROR (2082): "Cannot find %s in feedback view. Name: %s File Name: %s Ignoring line: %d "

This error occurs if there are parts on the board that no longer exist in the schematic. When you backannotate the schematic from the board to the schematic, it results in this error. To address this error, backannotate data from the board to the schematic to include any changes from the board before you make any modifications to the schematic. You should then make the required changes to the schematic and forward annotate to bring the board in sync.

136

WARNING (34)

WARNING 34

WARNING (34): "Expected '%c' character on line %d. Check the name and value syntax for invalid characters in the primitive definition before the line number. "

Packager-XL expects ptf files and chips.prt file to conform to a standard syntax. If you use PPT Editor to define part definitions, you will not get such errors. However, if you manually edit the ptf file and forget to place a standard character or change a standard character, Packager-XL will not be able to parse the file or load it. Therefore, Packager-XL will exit with error status 2. You may also get this error if you edit the pxl.state file manually and remove an expected character from it. Ensure that you have added the specified characters as required to create a ptf file.

137

WARNING (38)

WARNING 38

WARNING (38): "Terminating character '%c' not found on line %d. "

This error is displayed while running Design Differences. The error occurs due to the equal to ("=") operator being used in the reference designator (LOCATION property) value on a component. For example, LOCATION = U1=1. Rename the reference designator (REF DES) and remove the "=" operator.

138

WARNING (41)

WARNING 41

WARNING (41): "Opening quotation mark is missing for the part name on line %d. Ensure that all part names are enclosed in quotation marks. "

This warning is generated when you have defined a part name without using the leading quote ('). This warning is generally followed by WARNING 42. Ensure that all part names are enclosed within quotes.

139

WARNING (42)

WARNING 42

WARNING (42): "The closing quotation mark not found for part name on line %d. Ensure that all part names are enclosed within quotation marks. "

This warning is generated when you have defined a part name without using the closing quote ('). Ensure that all part names are enclosed within leading and closing quotes.

140

WARNING (66)

WARNING 66

WARNING (66): "Could not find property %s for %s %s. "

This error typically occurs because certain properties are missing in the property file. To resolve this error, run File > Save Hierarchy in Design Entry HDL then package the design. If the problem persists, contact Cadence Customer Support.

141

WARNING (124)

WARNING 124

WARNING (124): "Pin '%s' not found in design for primitive instance '%s'. "

This error might also occur when incorrect logical pin names are assigned in the PACK_SHORT property. Update the property value with the correct logical pin names. Example: Pin 'A0' not found in design for the primitive instance '@TEST_LIB.D1(SCH_::1):PAGE1_I3@LSTTL.LS00(CHIPS)'. Explanation: In the design, the correct logical pin names are A<0>, B<0>. However, the PACK_SHORT property is assigned as follows: PACK_SHORT = (A0, B0) Solution: Update the property value with the correct logical pin names. In the given example, the correct logical pin names should be assigned as: PACK_SHORT = (A<0>, B<0>)

142

WARNING (150)

WARNING 150

WARNING (150): "Name '%s' found in '%s'. Name not added to '%s'. "

You can add a subdesign name in either the FORCE SUBDESIGN list or the USE SUBDESIGN list but not in both lists. If you add a subdesign name in both, the FORCE SUBDESIGN, and USE SUBDESIGN lists, Packager-XL uses the FORCE SUBDESIGN directive to package the subdesign.

143

WARNING (185)

WARNING 185

WARNING (185): "The bit size of pin '%s'<%d..%d> is not equal to the section size of %d derived from the 'PIN_NUMBER' property on line %d in file '%s'. The derived section size will be used. "

This warning is generated when you have defined a pin that does not have the same section size as used in the PIN_NUMBER property for that part. If you need to change any properties, edit the chips.prt file.

144

WARNING (198)

WARNING 198

WARNING (198): "Phys part '%s' not found for primitive instance '%s' with property 'PACK_TYPE' = '%s'. "

This warning occurs when you have assigned the value NULL to the PACK_TYPE property and have also defined the SIZE property. If you have a sizeable part, ensure that the PACK_TYPE property has a valid value.

145

WARNING (283)

WARNING 283

WARNING (283): "Deleting property '%s' = '%s' on this mechanical part: "

Mechanical parts with no pins do not require section assignment. As a result, SEC/$SEC properties are deleted for this part.

146

WARNING (351)

WARNING 351

WARNING (351): "The %s directive has become obsolete. "

This warning occurs when you define a directive in the project file, which is not being used in the current version of Packager-XL. Remove the directive from the .cpm file to fix the warning.

147

WARNING (354)

WARNING 354

WARNING (354): "Invalid module name in GEN_SUBDESIGN directive '%s'. GEN_SUBDESIGN can only be used for root design. "

This warning occurs when you define a non-root design in the GEN_SUBDESIGN directive. To correct this problem, you must ensure that the root design name matches with the design name defined in the GEN_SUBDESIGN directive.

148

WARNING (357)

WARNING 357

WARNING (357): "The %s signal in the property %s=%s of the instance %s is not global or is not present in the design. Add the signal name to the POWER_GROUP property for the instance or declare the signal as global. "

In the POWER_GROUP property, you should have a global signal. If a global signal is not present, this warning is issued. Ensure that the signal defined in the POWER_GROUP property is a valid global signal.

149

WARNING (360)

WARNING 360

WARNING (360): "PPT file "%s" not found in any of the directories specified by the PPT directive. "

You have entered a ptf file name (my_parts.ppt), which is not available in any of the directories specified by the PPT directive. Ensure that the name of the ptf file is correct, and it is located in one of the directories specified by the PPT directive.

150

WARNING (361)

WARNING 361

WARNING (361): "Length of PartName "%s", existing in design, is more than the specified part_type_length. part_type_length specified in project file is %d. One possibility could be that the part_type_length was reduced after the previous run. Regenerating the PartName based on the specified part_type_length. "

If the length of any part name is greater than the specified PART_TYPE_LENGTH, Packager-XL regenerates the part name based on the specified PART_TYPE_LENGTH and displays this warning message.

151

WARNING (1046)

WARNING 1046

WARNING (1046): "Invalid View_Packager "%s". Changing it to "packaged". "

This error occurs when Packager-XL finds an invalid value for the VIEW_PACKAGER directive. If white space is used as VIEW_PACKAGER, Packager-XL will change the value to packaged and generate this warning.

152

WARNING (1047)

WARNING 1047

WARNING (1047): "Invalid View_Constraints "%s". Changing it to "constraints". "

This error occurs when Packager-XL finds an invalid value for the VIEW_CONSTRAINTS directive. If white space is used as VIEW_CONSTRAINTS, Packager-XL will change the value to constraints and generate this warning.

153

WARNING (1048)

WARNING 1048

WARNING (1048): "Invalid value '%s' specified for directive 'sd_suffix_separator' in the project file (cpm file). Defaulting it to '_'. "

This error occurs when Packager-XL finds an invalid value for the SD_SUFFIX_GENERATOR directive. If white space is used as SD_SUFFIX_GENERATOR, Packager-XL will generate reference designators with white spaces, which will prevent Netrev from importing the design in PCB Editor. You should not use white space as the value for SD_SUFFIX_GENERATOR.

154

WARNING (1066)

WARNING 1066

WARNING (1066): "Duplicate Part Subtype Name %d: %s Duplicate Part Subtype Name %d: %s Schematic instance: %s Fix the POWER_GROUP property and rerun Packager-XL. "

Multiple sub type values are defined on the instance that is specified in the warning. These values are from different component definition properties. Packager-XL will assign any one of these values to the instance and continue. Ensure that only one sub type value is specified for the instance. Example: WARNING(1066): Duplicate Part Subtype Name 1: ::GOOD Duplicate Part Subtype Name 2: BAD Schematic instance: @TEST_LIB.D1(SCH_1):PAGE1_I8@LSTTL.LS373(CHIPS) Ignoring 1. Using 2. Because COMP_NAME has more precedence. Explanation: In the above warning, the two subtypes are defined in the following way: POWER_GROUP = VCC=VDD(::GOOD) and COMP_NAME = BAD Both, the POWER_GROUP, and COMP_NAME properties, are of component definition type. COMP_NAME will overwrite the POWER_GROUP subtype. As a result, Packager-XL will assign the part name as BAD.

155

WARNING (1186)

WARNING 1186

WARNING (1186): "Warning - REF_DES_PATTERN exhausted - will attempt to increase width. "

The number of digits specified in the REF_DES_PATTERN have been exhausted. Packager-XL will assign new reference designators going beyond the specified limit.

156

WARNING (1207)

WARNING 1207

WARNING (1207): "The local net name is same as the power name Power net name: %s Logical net name: %s "

This warning is generated when you are shorting specific logical nets with power nets.

157

WARNING (2000)

WARNING 2000

WARNING (2000): "The following COMP_INST_PROP property is being fed back. Property name: %s This property is present in compview.dat but not defined as a component instance property. Add the property name to the COMP_INST_PROP directive in order for future runs of the Packager-XL to treat it correctly. "

Packager-XL requires certain properties that apply to reference designators or packages on your board to be specified as component instance properties. These properties include ROOM, REUSE_NAME, REUSE_INSTANCE, and REUSE_ID. If you do not include a component instance property in the Component Instance list in the Packager Setup - Properties tab, this warning message is displayed. Specify the property listed in the warning as a component instance property. This will ensure that all packages are assigned correct reference designators.

158

WARNING (2004)

WARNING 2004

WARNING (2004): "Feedback instance has been deleted from the design. Feedback instance: %s "

When running Packager-XL in the Feedback mode, a warning may appear that a part has been deleted from the design. This warning is often followed with WARNING 2005. This warning may occur if you are migrating from PE 13.0 to a later release. Solution: Make sure that the design is fully in sync with PE 13.0. Next, switch to PSD 14.2. Finally, run Export Physical before running Import Physical.

159

WARNING (2005)

WARNING 2005

WARNING (2005): "Instance has been added to the design. Design instance: %s "

When running Packager-XL in the Feedback mode, a warning may appear that a part has been added to the design. This warning is often preceded by WARNING 2004. This warning may occur if you are migrating from PE 13.0 to a later release. To address this warning, make sure that the design is fully in sync with PE 13.0. Next, switch to PSD 14.2. Finally, run Export Physical before running Import Physical.

160

WARNING (2006)

WARNING 2006

WARNING (2006): "Net %s has been deleted from the design. The net might have been renamed in PCB Editor. Changed net names cannot be fed back to the schematic because net renaming from PCB Editor is not supported in Packager-XL. To avoid this, rename nets in Design Entry HDL and not in PCB Editor. Run Packager-XL in the Forward mode to transfer the net names to the board. "

This warning occurs when the schematic and board are not in sync. Before you run Packager-XL in the Feedback mode, ensure that you have first run Packager-XL in the Forward mode.

161

WARNING (2007)

WARNING 2007

WARNING (2007): "Net has been added to the design. Net name: %s Physical net name: %s The net might have been renamed in PCB Editor. Changed net names cannot be fed back to the schematic because net renaming from PCB Editor is not supported in Packager-XL. To avoid this, rename nets in Design Entry HDL and not in PCB Editor. Run Packager-XL in the Forward mode to transfer the net names to the board. "

When running Packager-XL in the Feedback mode, a warning may appear that a part has been added to the design. This warning is often preceded by WARNING 2006. Before you run Packager-XL in the Feedback mode, ensure that you have first run Packager-XL in the Forward mode.

162

WARNING (2019)

WARNING 2019

WARNING (2019): "Connectivity has changed between the design and feedback. Feedback changes to PN for the Pin %s have been ignored. Design instance: %s "

This warning is caused when there is a change in connectivity between the schematic and the layout. For example, it might be possible that there is a termination resistor on a net in the schematic that does not need a terminator in PCB Editor. Solution: Ensure that you place termination resistors ONLY on those nets in the schematic that are to be assigned a terminator. In Design Entry HDL, remove termination resistors from nets where they are wrongly assigned, then run Export Physical followed by Import Physical to ensure that the design goes through a front-to-back then a back-to-front flow. There may be times when the changes in connectivity are deliberate and you would like to retain those changes. You can use Design Association to feedback connectivity changes to the schematic. For more information about Design Association, see the documentation.

163

WARNING (2048)

WARNING 2048

WARNING (2048): "Feedback rejected for at least one %s, SEC or PN. "

This warning is caused whenever feedback from PCB Editor is rejected by Packager-XL. The warning is informational in nature and is generated only once for a design, regardless of the number of feedback rejections. The details of all rejected feedback values are generated as information messages in the pxl.log file. You may individually handle each rejected feedback.