

Topology Workbench Tcl Command Reference

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Using the Topology Workbench Tcl Commands: An Overview

Topology Workbench includes a vast collection of Tcl commands that are used internally when designers perform tasks using its interface menu options. These Tcl commands are also available to anyone who needs to create scripts to modify the default built-in functionality, or wants to accomplish design tasks without clicking and navigating the user interface.

Scope of this Document

This document lists the Tcl commands that are available and describes how to use them. Knowledge of Tcl concepts is mandatory to be able to use and extend the Topology Workbench Tcl commands.

To learn the basics of Tcl commands, as in the language syntax, operands, assignments, substitutions, and so on, refer to <https://www.tcl.tk/man/tcl8.6/TclCmd/info.html>. Many of the Tcl commands are for Internal use only and details for their behavior and parameters are not disclosed. Do not use these commands directly on your designs. If you require the functionality these commands offer, contact a Cadence representative for information and help. It is recommended you use only those commands that are listed in this document.

Prerequisites

Before you implement Topology Workbench Tcl commands, you should be familiar with the following:

- Basics of schematic design tasks
- Fundamentals of Topology Workbench
- Basics of Tcl scripting

Using the Tcl Commands

The standard Tcl commands are used to extract the details from the built-in Tcl tools and commands within Topology Workbench. Only the relevant commands and features for System Capture are listed and explained. Topology Workbench Tcl commands are based on Tcl version 8.6. There is no Tk support.

Running Tcl Commands

The Command Window in the Topology Workbench window lets you run the supported Tcl commands and view the ones that are run in the background when you perform a corresponding GUI action. To open this panel,

1. Choose the *View – Command Window* menu.
2. Type the command on the Tcl> prompt.

The commands specific to Topology Workbench use the following general syntax:

```
topxp::<command_name> <arguments/values>
```

Related Documents

In addition to the Topology Workbench User Guide, you can refer to the following documents:

Tutorials

- [Topology Workbench: Topology Explorer Tutorial](#)

This tutorial captures the step-by-step instructions on exploring the Topology Workbench canvas, creating a topology from scratch, doing pre-layout extraction and post-layout routed interconnect extraction, and updating the ECSet using Constraint Manager.

- [Topology Workbench: Parallel Bus Analysis Tutorial](#)

This tutorial covers typical steps you will perform to create, edit, and simulate a parallel bus interface by using a default template from the install hierarchy and by creating a topology from scratch.

Frequently Asked Questions

- [Topology Workbench Frequently Asked Questions](#)

This document can be read to find answers to a few commonly encountered questions related to Topology Workbench.

Additional Learning Resources

Cadence offers training courses that enable you to understand the applications better. For specific information about the courses available in your region, visit [Cadence Training](#) or write to training_enroll@cadence.com.

Note: The links in this section open in a separate web browser window when clicked in Cadence Help.

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Topology Workbench Tcl Command Reference

Using the Topology Workbench Tcl Commands: An Overview

Topology Workbench Tcl Commands

Topology Workbench supports use of Tcl commands to perform the following tasks:

- [Managing Topology Workbench Environment](#)
- [Adding Elements to a Topology](#)
- [Enabling Elements in a Topology](#)
- [Editing Elements in a Topology](#)
- [Displaying Elements in a Topology](#)
- [Controlling Simulation and Analysis](#)
- [Exporting from Topology Workbench](#)
- [Managing Projects](#)
- [Generating Via Models](#)
- [Managing AMI Models](#)
- [Handling Constraint Manager](#)
- [Toggling in Topology Workbench](#)

Managing Topology Workbench Environment

You can use the following Tcl commands in the Command Window panel:

- [setEnv](#)
- [setSigrityEDADir](#)
- [testEnv](#)
- [unsetEnv](#)

setEnv

Syntax	topxp::setEnv \ {<environment>} \ {<value>}	
Description	Set runtime environment value as {environment} = {value}	
Arguments	{<environment>}	
		Name of the environment.
	{<value>}	
		The value that will replace the current environment value.
GUI Equivalent	None.	

Related Topics

[Using the Topology Workbench Tcl Commands: An Overview](#)

Topology Workbench Tcl Command Reference

Topology Workbench Tcl Commands

setSigrityEDADir

Syntax	topxp::setSigrityEDADir {<sigrity_installation_path>}	
Description	<p>Sets the path to the Sigrity installation directory during a running session to allow access to the latest Sigrity engines and functionality.</p> <p>You should alternatively set the <code>SIGRITY_EDA_DIR</code> environment variable before starting Topology Workbench. The approach of setting this environment variable is recommended because the path set using the <code>setSigrityEDADir</code> Tcl command is retained only in the current Topology Workbench session.</p>	
Arguments	{<sigrity_installation_path>}	
		Specifies the path to the Sigrity installation.
GUI Equivalent	None.	

Related Topic

[Prerequisites for Running Topology Workbench and Other SystemSI Products](#)

[Using the Topology Workbench Tcl Commands: An Overview](#)

testEnv

Syntax	topxp::testEnv \ {environment}	
Description	Displays the runtime environment value.	
Arguments	{<environment>}	
		Specifies the name of the environment.
GUI Equivalent	None.	

Related Topic

[Using the Topology Workbench Tcl Commands: An Overview](#)

Topology Workbench Tcl Command Reference

Topology Workbench Tcl Commands

unsetEnv

Syntax	topxp::unsetEnv \ {environment}	
Description	Unset runtime environment.	
Arguments	{<environment>}	
		Specifies the name of the environment.
GUI Equivalent	None.	

Related Topic

[Using the Topology Workbench Tcl Commands: An Overview](#)

Adding Elements to a Topology

You can use the following Tcl commands in the Command Window panel:

- [addAMI](#)
- [addNewBlocksWidthDiff](#)
- [addRepeaterConnection](#)
- [addToAMM](#)
- [addWhatIf](#)
- [appendTopology](#)
- [newBlockWithSinglePin](#)

Topology Workbench Tcl Command Reference

Topology Workbench Tcl Commands

addAMI

Syntax	<pre>topxp::addAMI \ {<block_name>} \ {<executable_file>} \ {<parameter_file>}</pre>	
Description	Adds an AMI model to a block with the specified executable file and parameter file.	
Arguments	<block_name>	
		Specifies the block name.
	<executable_file>	
		Specifies the path to the executable file.
	<parameter_file>	
		Specifies the path to the parameter file.
GUI Equivalent	None	

Related Topics

[Using the Topology Workbench Tcl Commands: An Overview](#)

addNewBlocksWidthDiff

Syntax	<pre>topxp::addNewBlocksWidthDiff</pre>	
Description	When toggled, adds new blocks with differential signals.	
Arguments	None	
GUI Equivalent	The <i>Diff Signals</i> toggle button in the <i>Settings</i> option of the floating toolbar.	

Related Topics

[Using the Topology Workbench Tcl Commands: An Overview](#)

Topology Workbench Tcl Command Reference

Topology Workbench Tcl Commands

addRepeaterConnection

Syntax	<code>topxp::addRepeaterConnection</code>
Description	Adds a repeater connection between the Tx and Rx blocks.
Arguments	None
GUI Equivalent	Context Menu - Add Repeater Connection

Related Topics

[Using the Topology Workbench Tcl Commands: An Overview](#)

addToAMM

Syntax	<code>topxp::addToAMM</code>
Description	Adds or updates models of the selected block to AMM. Currently Spice model and IBIS model are supported.
Arguments	None
GUI Equivalent	Context Menu - Add To AMM

Related Topics

[Using the Topology Workbench Tcl Commands: An Overview](#)

Topology Workbench Tcl Command Reference

Topology Workbench Tcl Commands

addWhatIf

Syntax	topxp::addWhatIf \ <block name>:<pin name>	
Description	Adds a what-if decap.	
Arguments	<block_name>	
		Specifies the block name.
	<pin name>	
		Specifies the name of the pin.
GUI Equivalent	Context Menu - "Add What-If Decaps"	

Related Topics

[Using the Topology Workbench Tcl Commands: An Overview](#)

appendTopology

Syntax	topxp::appendTopology	
Description	Appends an existing topology (.topx or legacy .top, .ssix) to current topology.	
Arguments	None.	
GUI Equivalent	In the Topology Workbench window, the <i>Topology – Append...</i> menu.	

Related Topics

[Appending to a Topology](#)

[Using the Topology Workbench Tcl Commands: An Overview](#)

Topology Workbench Tcl Command Reference

Topology Workbench Tcl Commands

newBlockWithSinglePin

Syntax	<code>topxp::newBlockWithSinglePin</code>
Description	Toggles adding new blocks with block-based connectivity.
Arguments	None.
GUI Equivalent	The <i>Block-Based</i> toggle button in the <i>Settings</i> option of the floating toolbar.

Related Topics

[Using the Topology Workbench Tcl Commands: An Overview](#)

Enabling Elements in a Topology

You can use the following Tcl commands in the Command Window panel:

- [enableConfigurePortsAutomatically](#)
- [enableDistributedComputing](#)
- [enableUseChannelSimulator](#)

enableConfigurePortsAutomatically

Syntax	<code>topxp::enableConfigurePortsAutomatically</code>
Description	Toggles <i>Configure Ports Automatically</i> .
Arguments	None.
GUI Equivalent	In the <i>Workflow</i> pane of Custom Compliance Kit, the <i>Configure Ports Automatically</i> check box in the <i>Simulation Setup</i> schema.

Related Topic

[Using the Topology Workbench Tcl Commands: An Overview](#)

Topology Workbench Tcl Command Reference

Topology Workbench Tcl Commands

enableDistributedComputing

Syntax	<code>topxp::enableDistributedComputing</code>
Description	Toggles <i>Enable Distributed Computing</i> .
Arguments	None.
GUI Equivalent	In the <i>Workflow</i> pane of supported workflows, the <i>Enable Distributed Computing</i> check box in the <i>Distributed Computing Setup</i> schema.

Related Topic

[Using the Topology Workbench Tcl Commands: An Overview](#)

enableUseChannelSimulator

Syntax	<code>topxp::enableUseChannelSimulator</code>
Description	Toggles the <i>Use Channel Simulator</i> check box.
Arguments	None.
GUI Equivalent	In the <i>Workflow</i> pane of Parallel Bus Analysis, the <i>Use Channel Simulator</i> check box in the <i>Simulation Setup</i> schema.

Related Topic

[Using the Topology Workbench Tcl Commands: An Overview](#)

Editing Elements in a Topology

- [editAnalysisOption](#)
- [editDieModel](#)
- [editFrequencyResponse](#)
- [editProbePoints](#)
- [editSParamExtraction](#)
- [editTimingBudget](#)
- [editWriteLeveling](#)

Topology Workbench Tcl Command Reference

Topology Workbench Tcl Commands

- makeReceiver
- makeTransmitter
- renameComponent
- restoreDefaultPins
- setupDefaultPinLocation
- shortToGND
- shortToPower
- shortUnConnectedGroundNode2GND

Topology Workbench Tcl Command Reference

Topology Workbench Tcl Commands

editAnalysisOption

Syntax

```
editAnalysisOption \
    -autotimestep <Boolean> \
    -timestep <step> \
    -timestop <stop> \
    -busType <bus_type> \
    -datarate <data_rate> \
    -datarate [list {Block <block>} {BusGroup <busgroup>} {Signal
    <signal>} {<data_rate>}} \
    -baudrate [list {Block <block>} {BusGroup <busgroup>} {Signal
    <signal>} {<baud_rate>}} \
    -numberofbits [list {Block <block>} {BusGroup <busgroup>}
    {Signal <signal>} {<bit_num>}} \
    -minnumberofbits <bit_num> \
    -corner {<corner_type> <Boolean>} \
    -writedirection <Boolean> \
    -readdirection <Boolean> \
    -memoryiomodelshared <Boolean> \
    -wloenabled <Boolean> \
    -clockingtype <clocking_type> \
    -memoryrank [list {Rank <mem_name> ... <mem_name> <IsActive>}
    {Rank <mem_name> ... <mem_name> <IsActive>}} \
    -ignoretime [list {Time <time>} {Unit <unit>}} \
    -channelminnumberofbits <bit_num> \
    -bitsamplingrate <bit_sampling_rate> \
    -checkberfloor <Boolean> \
    -lberfloor <lber_floor> \
    -channelnumberofbitsfordisplay <bit_num> [{Option
    <option>}} \
    -timescale <Boolean> \
    -voltagescale <Boolean> \
    -bothtimeandvoltage <Boolean> \
    -lber {<Log_ber>} \
    -eyeDistributionMethod <method> \
    -stimuluspattern [list {Block <block>} {BusGroup <busgroup>}
    {StimulusType <stimulus_type>} {Seed <seed>} {Poly <poly>}
    {DataFile <datafile>} {BitPattern <bit_pattern>} {DCD <dcd>}
    {PulseType <pulse_type>} {Init <init>} {SwitchTimes
    <switch_time>} {DataCodingEnabled <bool>} {DataCoding
    <data_coding>} {RiseFallTimeEnabled <bool>} {RiseTime
    <rise_time>} {FallTime <fall_time>}} \
    -standbyiomodel [list {Block <block>} {Bus <bus>} {BusGroup
    <busgroup>} {Signal <signal>} {<iomodel>}} \
```

Topology Workbench Tcl Command Reference

Topology Workbench Tcl Commands

	<pre> -transmitjitternoise [list {Block <block>} {BusGroup <busgroup>} {JitterSinusoidalEnabled <bool>} {JitterSinusoidalFrequency <value>} {JitterSinusoidalAmplitude <value>} {JitterFrequencyOffsetEnabled <bool>} {JitterFrequencyOffset <value>} {JitterTransitionRjEnabled <bool>} {JitterTransitionRj <value>} {JitterTransitionDjEnabled <bool>} {JitterTransitionDj <value>} {JitterDCDEnabled <bool>} {JitterDCD <value>} {NoiseSinusoidalEnabled <bool>} {NoiseSinusoidalFrequency <value>} {NoiseSinusoidalAmplitude <value>} {NoiseTransitionEnabled <bool>} {NoiseTransition <value>}} \ -receivejitternoise [list {Block <block>} {BusGroup <busgroup>} {Signal <signal>} {JitterRandomEnabled <bool>} {JitterRandom <value>} {JitterDeterministicEnabled <bool>} {JitterDeterministic <value>} {NoiseRandomEnabled <bool>} {NoiseRandom <value>} {NoiseDeterministicEnabled <bool>} {NoiseDeterministic <value>}} \ -check [list {Block <block>} {BusGroup <busgroup>} {Signal <signal>} {<bool>}} \ -chsimAnalMode <mode> \ -simulator spdsim hspice spectre \ -circuitSimulatorOption <option> \ -channelSimulatorControl <option> \ -spectrePerformanceOption [list <option> ... <option>] \ -spectreSimulatorOption [list <option> ... <option>] \ -spectreTransientOption [list <option> ... <option>] \ -characterizationTimeStep <value> \ -launchDelay <value> \ -stepDuration <value> \ -vmeas <value> \ -busStimuliConfiguration Random Even \ -seedControl <value> \ -adaptiveTimeStep \ -restoredefaults </pre>	
Description	Sets the simulation options in the <i>Analysis Options</i> panel.	
Arguments	-autotimestep <boolean>	
		<p>Selects or deselects the <i>Auto</i> check box for time step and time stop.</p> <p>Note: This argument applies only to the Topology Explorer workflow.</p>

Topology Workbench Tcl Command Reference

Topology Workbench Tcl Commands

	<code>-timestep <step></code>	
		<p>Specifies the time step, which is a positive number.</p> <p>Note: This argument applies only to the Topology Explorer workflow.</p>
	<code>-timestop <stop></code>	
		<p>Specifies the time stop, which is a positive number.</p> <p>Note: This argument applies only to the Topology Explorer workflow.</p>
	<code>-busType <bus_type></code>	
		<p>Specifies the <i>Bus Type</i>.</p> <p>The valid <code><bus_type></code> can be one of the following: Data, Ctrl, AddCmd</p> <p>Note: This argument applies only to the Parallel Bus Analysis (PBA) workflow.</p>
	<code>-datarate <data_rate></code>	
		<p>Specifies the <i>Data Rate</i>, which is a positive number.</p> <p>Note: This argument applies only to the PBA workflow.</p>
	<code>-datarate [list {Block <block>} {BusGroup <busgroup>} {Signal <signal>} {<data_rate>}]</code>	
		<p>Sets the <i>Data Rate</i> for the specified signal.</p> <p>In this argument's syntax, the following values can be set:</p> <ul style="list-style-type: none"> ■ <code><block></code>, a string to specify the block name. ■ <code><busgroup></code>, a string to specify the bus group name. ■ <code><signal></code>, a string to specify the signal name. ■ <code><data_rate></code>, a positive number. <p>Note: This argument applies to the Topology Explorer and Serial Link Analysis (SLA) workflows.</p>

Topology Workbench Tcl Command Reference

Topology Workbench Tcl Commands

	-baudrate [list {Block <block>} {BusGroup <busgroup>} {Signal <signal>} {<baud_rate>}]	
		<p>Sets the <i>Baud Rate</i> for the specified signal.</p> <p>In this argument's syntax, the following values can be set:</p> <ul style="list-style-type: none"> ■ <block>, a string to specify the block name. ■ <busgroup>, a string to specify the bus group name. ■ <signal>, a string to specify the signal name. ■ <baud_rate>, a positive number. <p>Note: This argument applies only to the SLA workflow.</p>
	-numberofbits [list {Block <block>} {BusGroup <busgroup>} {Signal <signal>} {<bit_num>}]	
		<p>Sets the <i># of Bits</i> value for the specified signal.</p> <p>Here, <bit_num> can be an integer value, [1, 5000]</p> <p>Note: This argument applies only to the Topology Explorer workflow.</p>
	-minnumberofbits <bit_num>	
		<p>Sets the <i>Minimum # of Bits</i> value.</p> <p>Note: This argument applies only to the PBA workflow.</p>
	-corner {<corner_type> <Boolean>}	
		<p>Select or deselect the corner type check box.</p> <p>Here, <corner_type> can be set to one of the following: Slow, Typ, Fast, Fast/Slow, Slow/Fast</p>
	-writedirection <Boolean>	
		<p>Select or deselect the <i>Write</i> direction check box.</p> <p>Note: This argument applies only to the PBA workflow.</p>

Topology Workbench Tcl Command Reference

Topology Workbench Tcl Commands

	<code>-readdirection <Boolean></code>	
		<p>Select or deselect the <i>Read</i> direction check box.</p> <p>Note: This argument applies only to the PBA workflow.</p>
	<code>-memoryiomodelshared <Boolean></code>	
		<p>Select or deselect the <i>Memory Block Shared IO Models</i> check box.</p> <p>Note: This argument applies only to the PBA workflow.</p>
	<code>-wloenabled <Boolean></code>	
		<p>Select or deselect the <i>WLO/CktMeasDelay</i> check box.</p> <p>Note: This argument applies only to the PBA workflow.</p>
	<code>-clockingtype <clocking_type></code>	
		<p>Sets the <i>Signal Clocking</i> value.</p> <p>Here, <code><clocking_type></code> can be set to one of the following values: 1T, 2T, 3T</p> <p>Note: This argument applies only to the PBA workflow.</p>
	<code>-memoryrank [list {Rank <mem_name> ... <mem_name> <IsActive>} {Rank <mem_name> ... <mem_name> <IsActive>}]</code>	
		<p>Sets the <i>Memory Rank</i>.</p> <p>In this argument's syntax, the following values can be set:</p> <ul style="list-style-type: none"> ■ <code><mem_name></code>, a string to specify the memory name. ■ <code><IsActive></code>, a Boolean value of <code>true</code> or <code>false</code>. <p>Note: This argument applies only to the PBA workflow.</p>

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	-ignoretime [list {Time <time>} {Unit <unit>}]	
		<p>Sets the <i>Ignore Time or Ignore Bits</i> value.</p> <p>In this argument's syntax, the following values can be set:</p> <ul style="list-style-type: none"> ■ <time>, a positive number. ■ <unit>, the unit as ns or bits. <p>Note: This argument applies only to the PBA channel simulation mode and SLA workflow.</p>
	-channelminnumberofbits <bit_num>	
		<p>Sets the <i>Minimum # of Bits</i> value.</p> <p>Here, <bit_num> is a positive integer.</p> <p>Note: This argument applies only to the PBA channel simulation mode and SLA workflow.</p>
	-bitsamplingrate <bit_sampling_rate>	
		<p>Sets the <i>Bit Sampling Rate</i> value.</p> <p>Here, <bit_sampling_rate> is a positive number.</p> <p>Note: This argument applies only to the PBA channel simulation mode and SLA workflow.</p>
	-checkberfloor <Boolean>	
		<p>Select or deselect the <i>Log BER Floor</i> check box.</p> <p>Note: This argument applies only to the PBA channel simulation mode and SLA workflow.</p>
	-lberfloor <lber_floor>	
		<p>Sets the <i>Log BER Floor</i> value.</p> <p>Here, <ber_floor> is an integer value, [-21, -3].</p> <p>Note: This argument applies only to the PBA channel simulation mode and SLA workflow.</p>

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	-channelnumberofbitsfordisplay <bit_num> [{Option <option>}]	
		<p>Sets the <i># of Bits for Display</i> value to define the number of bits worth of raw waveforms to be saved to disk and displayed.</p> <p>Here, <bit_num> is a positive integer value.</p> <p>If {Option <option>} is specified, <option> can be set as <i>first</i> or <i>last</i> for <i># of Bits for Display</i>. This value specifies whether to save and display the last or the first number of bits. For example, if you choose to save the last number of bits, the first several bits from the very beginning are ignored while viewing the eye diagram or generating the report.¹</p> <p>Note: This argument applies only to the PBA channel simulation mode and SLA workflow.</p>
	-timescale <Boolean>	
		<p>Selects or clears the <i>Time Scale</i> check box in the <i>Channel Simulation</i> tab of the Analysis Options pane to specify the type of <i>BER_Eyes</i> to be generated.</p> <p>Note: This argument applies to the PBA channel simulation mode and SLA workflow.</p>
	-voltagescale <Boolean>	
		<p>Selects or clears the <i>Voltage Scale</i> value.</p> <p>Note: This argument applies to the PBA channel simulation mode and SLA workflow.</p>
	-bothtimeandvoiltage <Boolean>	
		<p>Selects or clears the <i>Both time and voltage</i> value.</p> <p>Note: This argument applies to the PBA channel simulation mode and SLA workflow.</p>

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Topology Workbench Tcl Commands

	-lber {<log_ber>}	
		<p>Sets the <i>Log BERs</i> value.</p> <p><log_ber> sets one of the following values:</p> <ul style="list-style-type: none"> ■ A single log BER [-12] ■ Multiple log BERs [-6, -9, -12] ■ A range of log BERS [-12, -15] <p>Note: Ensure that all <log_ber> values are negative integer values in the range of [-21, -3].</p>
	-eyeDistributionMethod <method>	
		<p>Sets the <i>Eye Distribution Method</i>.</p> <p>Here, <method> can be <i>Time Domain Waveform</i> or <i>Statistical</i>.</p>
	-xtalk <option>	
		<p>Sets the <i>xTalk Mode</i>.</p> <p>For time domain, <option> can be one of the following:</p> <ul style="list-style-type: none"> ■ <i>Ignore xTalk</i> ■ <i>Invert All Aggressor Stimulus</i> ■ <i>Use Aggressor Stimulus As Defined</i> ■ <i>Random Aggressor Stimulus</i> <p>For statistical, <option> can be one of the following:</p> <ul style="list-style-type: none"> ■ <i>Ignore xTalk</i> ■ <i>Include xTalk</i> <p>Note: This argument applies to the PBA simulation mode, SLA workflow. The <i>Ignore xTalk</i> and <i>Include xTalk</i> options are available only for the statistical mode.</p>

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	-signaling [list {Block <block>} {Signal <signal>} {<signal_type>}]	
		<p>Sets <i>Signaling</i> for the specified signal.</p> <p><signal_type> can be one of the following: NRZ, PAM3, PAM4, PAM6, PAM8, or MIPI-C.</p> <p>Note: MIPI-C is valid for only MIPI-C compliance.</p>
	-leadingbits [list {Block <block>} {Signal <signal>} {<leadingbits_file>}]	
		Sets <i>Leading Bits</i> for the specified signal.
	-defaultstimuluspattern [list {Block <block>} {BusGroup <busgroup>}]	
		Resets to default stimulus pattern for the specified signal.
	<pre>-stimuluspattern [list {Block <block>} {BusGroup <busgroup>} {StimulusType <stimulus_type>} {Seed <seed>} {Poly <poly>} {DataFile <datafile>} {BitPattern <bit_pattern>} {DCD <dcd>} {PulseType <pulse_type>} {Init <init>} {SwitchTimes <switch_time>} {DataCodingEnabled <bool>} {DataCoding <data_coding>} {RiseFallTimeEnabled <bool>} {RiseTime <rise_time>} {FallTime <fall_time>}]</pre>	
	<p>Sets <i>Stimulus Pattern</i> for the specified signal.</p> <p>Here, <stimulus_type> can be one of the following:</p> <ul style="list-style-type: none"> ■ For SLA workflow: Random, PRBS, Data File, Sinusoidal Waveform, Sawtooth, or Clock ■ For PBA workflow: Bit Pattern, Random, PRBS, Data File, or Worst Case ■ For Topology Explorer workflow: Random, PRBS, Data File, Pulse, PWL <p>The other values that can be specified are:</p> <ul style="list-style-type: none"> ■ <seed> is a positive integer for Random in PBA simulation mode. ■ <poly> specifies 1 integer value for PRBS. 	

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	<ul style="list-style-type: none"> ■ <bit_pattern> specifies the bit pattern, for example "01.." ■ <dcd> is a double value for Pulse in Topology Explorer workflow ■ <pulse_type> can be set for Pulse in Topology Explorer workflow to Pulse, Rise, Fall, QuietHi, or QuietLo. ■ <init> can be set to 0 or 1 for PWL in Topology Explorer workflow ■ <switch_time> specifies the list of time such as 2e-8 4e-8. ■ <data_coding> can be one of the following: 8b10b, 64b66b, 64b67b, 128b130b, 128b132b, 16b18b, or Gray Coding ■ <rise_time> is a positive number and less than Bit Period/2. ■ <fall_time> is a positive number and less than Bit Period/2.
	-stimulusoffset [list {Block <block>} {BusGroup <busgroup>} {offset}]
	<p>Sets the <i>Stimulus Offset</i> for the specified signal.</p> <p>Here, <offset> is a positive number.</p>
	-transmitiomodel [list {Block <block>} {Bus <bus>} {BusGroup <busgroup>} {Signal <signal>} {<iomodel>}]
	<p>Sets the <i>Tx IO Model</i> for the specified signal.</p> <p>Here, <iomodel> is a ibis model.</p>
	-receiveiomodel [list {Block <block>} {Bus <bus>} {BusGroup <busgroup>} {Signal <signal>} {<iomodel>}]
	<p>Sets the <i>Rx IO Model</i> for the specified signal.</p> <p>Here, <iomodel> is a ibis model.</p>
	-standbyiomodel [list {Block <block>} {Bus <bus>} {BusGroup <busgroup>} {Signal <signal>} {<iomodel>}]
	<p>Sets the <i>Standby IO Model</i> for the specified signal.</p> <p>Here, <iomodel> is a ibis model.</p>

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	<pre>-transmitjitternoise [list {Block <block>} {BusGroup <busgroup>} {JitterSinusoidalEnabled <bool>} {JitterSinusoidalFrequency <value>} {JitterSinusoidalAmplitude <value>} {JitterFrequencyOffsetEnabled <bool>} {JitterFrequencyOffset <value>} {JitterTransitionRjEnabled <bool>} {JitterTransitionRj <value>} {JitterTransitionDjEnabled <bool>} {JitterTransitionDj <value>} {JitterDCDEnabled <bool>} {JitterDCD <value>} {NoiseSinusoidalEnabled <bool>} {NoiseSinusoidalFrequency <value>} {NoiseSinusoidalAmplitude <value>} {NoiseTransitionEnabled <bool>} {NoiseTransition <value>}]</pre>	
		<p>Sets the <i>Tx Jitter & Noise</i> for the specified signal.</p> <p>Here, <value> is a positive number.</p>
	<pre>-receivejitternoise [list {Block <block>} {BusGroup <busgroup>} {Signal <signal>} {JitterRandomEnabled <bool>} {JitterRandom <value>} {JitterDeterministicEnabled <bool>} {JitterDeterministic <value>} {NoiseRandomEnabled <bool>} {NoiseRandom <value>} {NoiseDeterministicEnabled <bool>} {NoiseDeterministic <value>}]</pre>	
		<p>Sets the <i>Rx Jitter & Noise</i> for the specified signal.</p> <p>Here, <value> is a positive number.</p>
	<pre>-check [list {Block <block>} {BusGroup <busgroup>} {Signal <signal>} {<bool>}]</pre>	
		<p>Selects or deselects the specified Tx signal.</p> <p>Here, <value> is a positive number.</p>

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	-chsimAnalMode <mode>	
		<p>Sets the <i>Advanced Simulation Controls</i>.</p> <p>Here, <mode> specifies one of the following:</p> <ul style="list-style-type: none"> ■ Liberal ■ Moderate ■ Conservative <p>Note: This argument applies to the PBA simulation mode and SLA workflow.</p>
	-simulator spdsim hspice spectre	
		Sets the <i>Circuit Simulator</i> .
	-circuitSimulatorOption <option>	
		Sets the <i>Circuit Simulator Option</i> .
	-channelSimulatorControl <option>	
		Sets the <i>Circuit Simulator Controls</i> .
	-spectrePerformanceOption [list <option> ... <option>]	
		Sets the <i>Spectre Performance Option</i> .
	-spectreTransientOption [list <option> ... <option>]	
		Sets the <i>Spectre Transient Option</i> .
	-characterizationTimeStep <value>	
		Sets the <i>Time Step</i> for characterization.
	-launchDelay <value>	
		Sets the <i>Launch Delay</i> for characterization.
	-stepDuration <value>	
		Sets the <i>Step Duration</i> for characterization.
	-vmeas <value>	
		Sets the <i>Vmeas</i> for characterization.

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	-busStimuliConfiguration Random Even	
		Sets the <i>Bus Stimuli Seed Control</i> for characterization when <i>PRBS</i> is selected in <i>Advanced Bus Characterization Options</i> .
	-seedControl <value>	
		Sets the <i>Seed</i> for characterization when <i>PRBS</i> is selected in <i>Advanced Bus Characterization Options</i> .
	-adaptiveTimeStep	
		Controls the value of the TOPXP_ADAPTIVE_TIME_STEP environment variable.
	-restoredefaults	
		Resets all modified values to their default settings.
GUI Equivalent	<i>Analysis Options</i> panel that opens when you click the <i>Setup – Analysis Options...</i> menu in the Topology Workbench window. Alternatively, you can click <i>Set Analysis Options</i> in the Workflow panel to open this panel.	

Related Topics

[Using the Topology Workbench Tcl Commands: An Overview](#)

Topology Workbench Tcl Command Reference

Topology Workbench Tcl Commands

editBlock

Syntax	<pre>editBlock \ <block> -newname <new_name> -blockgroup <group> <block_type_specific_params></pre>	
Description	Edits the properties of the specified block.	
Arguments	<block>	Specifies the name of the block for which the properties need to be edited.
	-newname <new_name>	
		Specifies the new name for the block.
	-blockgroup <group>	
		Specifies the name of the group for controller, memory, and EBD blocks.
	<block_type_specific_params>	
		<p>This argument can be one of the following types of parameters:</p> <ul style="list-style-type: none"> ■ <spice_block_params> ■ <via_block_params> ■ <sparam_block_params> ■ <device_block_params> ■ <ebd_block_params> ■ <trace_block_params> ■ <layout_block_params> ■ <descrete_block_params> ■ <terminator_block_params> ■ <vrn_block_params> ■ <clarity_block_params> ■ <source_block_params>

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	<spice_block_params>	
		<p>Following is the syntax for SPICE block parameters:</p> <pre>-spicefile <spice_file> -subcircuit <subcircuit> -sweepfilemodels <sweepfilemodels></pre> <p>Where,</p> <ul style="list-style-type: none"> ■ <spice_file> is the name of the SPICE file. ■ <subcircuit> is the name of the subcircuit. ■ <sweepfilemodels> is a list of SPICE files given as following: <pre>[list "spice_file1" subcircuit1 "spice_file2" subcircuit2 ...]</pre>
	<via_block_params>	
		<p>Following is the syntax for Via block parameters:</p> <pre>-spicefile <spice_file> -subcircuit <subcircuit></pre> <p>Where,</p> <ul style="list-style-type: none"> ■ <spice_file> is the name of the SPICE file. ■ <subcircuit> is the name of the subcircuit.

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	<sparam_block_params>
	<p>Following is the syntax for S Parameter block parameters:</p> <pre>-sparameterfile <sparameterfile> -2n <2n> -sweepfiles <sweepfiles> -removedcblockingcomponents <removedcblockingcomponents> -checkpassivity <checkpassivity> -optimizez0 <optimizez0></pre> <p>Where,</p> <ul style="list-style-type: none"> ■ <sparameterfile> is the name of the S Parameter file of .bnp, .s*p, or .ts file format. ■ <2n> is a Boolean, n+1, or 2n model where n is a port number. ■ <sweepfiles> is a list of S Parameter files given as following: [list {sparameterfile1} {sparameterfile2} ...{sparameterfileN}] ■ <removedcblockingcomponents> is a Boolean to remove DC blocking components. ■ <checkpassivity> is a Boolean to enforce passivity. ■ <optimizez0> is a Boolean that is used only in SystemPI.

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	<device_block_params>
	<p>Following is the syntax for device block parameters:</p> <pre>-ibisfile <ibisfile> -component <component> -ignoreVTCurves <ignoreVTCurves> -onDieParasitic <onDieParasitic> -pkgParasitic <pkgParasitic> -shortResistor <shortResistor> -pdsResistor <pdsResistor> -pdsCapacitor <pdsCapacitor> -packageModelFile <packageModelFile> -packageModelName <packageModelName> -interconnectModeling <interconnectModeling> -interconnectModelGroup <interconnectModelGroup> -ioOnly <ioOnly> -singleendedpin <singleendedpin> -diffpin <diffpin> -extModelParam <extModelParam> -extModelConverterParam <extModelConverterParam></pre> <p>Where,</p> <ul style="list-style-type: none"> ■ <ignoreVTCurves> is a Boolean, that is, 1 or 0. ■ <onDieParasitic> can be None or OnDie RC ■ <pkgParasitic> can be None, Pin RLC, or Package Model. ■ <shortResistor> is a number. ■ <pdsResistor> is a number. ■ <pdsCapacitor> is a number. ■ <interconnectModeling> can be None or Interconnect Modeling. ■ <ioOnly> is a Boolean, that is, 1 or 0. ■ <singleendedpin> is the name of a single-ended pin. This argument value supports wildcard characters asterisk (*) and question mark (?). For example: <pre>editBlock {Tx1} -singleendedpin {A*}</pre> <p>selects the single-ended pins with names beginning with A (case-insensitive).</p>

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		<p><code>editBlock {Tx1} -singleendedpin {*}</code> selects all the available single-ended pins.</p> <p><code>editBlock {Tx1} -singleendedpin {A?}</code> selects the single-ended pins with names beginning with A (case-insensitive), and of maximum two characters length.</p> <ul style="list-style-type: none"> ■ <code><diffpin></code> is a list of positive and negative-edge pin names specified as: [list <pos_pin_name> <neg_pin_name>] ■ <code><extModelParam></code> is a list of external model parameter names and their values specified as: [list <external_model_param_name> <external_model_param_value>] ■ <code><extModelConverterParam></code> is a list of external model converter parameter names and their values specified as: [list <external_model_converter_param_name> <external_model_converter_param_value>]
	<code><ebd_block_params></code>	
		<p>Following is the syntax for EBD block parameters:</p> <pre>-ebdAsPkg <ebdAsPkg> -ebdfile <ebdfile> \ -ebdboard <ebdboard> \ -ebdnodeoption <ebdnodeoption></pre> <p>Where,</p> <ul style="list-style-type: none"> ■ <code><ebdAsPkg></code> is a Boolean, that is, 1 or 0. ■ <code><ebdnodeoption></code> is a list of IBIS files given as following: [list <ibis_node> <ebd_dc_pin>]

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	<trace_block_params>
	<p>Following is the syntax for Trace block parameters:</p> <pre>-trcLayer <trcLayer> \ -trcLayerRemove <trcLayerRemove> \ -trcTrace <trcTrace> \ -trcTraceRemove <trcTraceRemove> \ -trcTraceData <trcTraceData> \ -trcSweepParamSet <trcSweepParamSet> \ -cableData <cableData> \ -cableSweepParamSet <cableSweepParamSet> \ -coaxCableData <coaxCableData> \ -coaxCableSweepParamSet <coaxCableSweepParamSet> \ -trcGlobalData <trcGlobalData> \ -trcRoughnessDataSet <trcRoughnessDataSet></pre> <p>Where,</p> <ul style="list-style-type: none"> ■ <trcLayer> has the following syntax: <pre>-roughnessLower <roughness_name> -roughnessUpper <roughness_name> -roughnessLeft <roughness_name> -roughnessRight <roughness_name> -xhatchUsed <xhatchUsed> -xhatchLayer <xhatchLayer> -conductivity <conductivity> -er <er> -loss <loss> -thickness <thickness> -angle <angle> -layerNum <layerNum> -layerName <layerName> -layerType <layerType> -layerMaterial <layerMaterial> -layerMetalMaterial <layerMetalMaterial></pre> <p>Here,</p> <p><xhatchUsed> and <xhatchLayer> are defined as a Boolean, that is, 1 or 0.</p> <p><conductivity>, <er>, <loss>, <thickness>, and <angle> are numbers.</p> <p><layerNum> is an integer.</p> <ul style="list-style-type: none"> ■ <trcLayerRemove> is an integer, layer index. ■ <trcTraceRemove> is an integer, trace index.

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- **<trcTrace> has the following syntax:**

```
-traceSpace <traceSpace> -traceWidth
<traceWidth> -traceLayer <traceLayer>
-traceType <traceType> -traceName
<traceName>
```

Here,

<traceSpace> and <traceWidth> are numbers.

<traceLayer> is an integer, layer index.

<traceType> is enum, 0:SE, 1:DIFF, 2:GND.
- **<trcTraceData> has the following syntax:**

```
-length <length> -upperRef <upperRef>
-lowerRef <lowerRef> -includepower
<includepower> -addvrm <addvrm> -pdsr <pdsr>
-pdsc <pdsc> -materialfile <materialfile>
-currentunit <currentunit>
```

Here,

<length> is a number.

<includepower> and <addvrm> are defined as a Boolean, that is, 1 or 0.
- **<cableData> has the following syntax:**

```
-isShield <isShield> -c2cPitch <c2cPitch>
-cableDia <cableDia> -condConn <condConn>
-connDia <connDia> -insuEr <insuEr> -insuLoss
<insuLoss> -insuThick <insuThick>
-shealthFillEr <shealthFillEr>
-shealthFillLoss <shealthFillLoss>
-shealthLoss <shealthLoss> -shealthEr
<shealthEr> -shieldConn <shieldConn>
-thickness <thickness> -twistPitch
<twistPitch> -condMaterial <condMaterial>
-insuMaterial <insuMaterial>
-shealthFillMaterial <shealthFillMaterial>
-shealthMaterial <shealthMaterial>
-shieldMaterial <shieldMaterial>
```


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		<ul style="list-style-type: none"> ■ <coaxCableData> has the following syntax: <code>-condConn <condConn> -dieEr <dieEr> -dieLoss <dieLoss> -innerR <innerR> -outerR <outerR> -thickness <thickness> -shieldConn <shieldConn> -condMaterial <condMaterial> -dieMaterial <dieMaterial> -shieldMaterial <shieldMaterial></code> ■ <trcGlobalData> has the following syntax: <code>-cutoffFreq <cutoffFreq> -calTemperature <calTemperature></code> ■ <trcRoughnessDataSet> has the following syntax: <code>-trcRoughData <trcRoughData></code>
	<layout_block_params>	
		<p>Following is the syntax for Layout block parameters:</p> <pre>-extractionengine <extractionengine> \ -layoutfile <layoutfile> \ -commandlineswitches <commandlineswitches></pre> <p>Where, <extractionengine> is POWERSI, SPEEDEM, or Clarity.</p>
	<VRM_block_params>	
		<p>Following is the syntax for VRM block parameters:</p> <pre>-powernet <powernet> -groundnet <groundnet> -numberofpowerpins <numberofpowerpins> -numberofgroundpins <numberofgroundpins> -renamepowerpin <renamepowerpin> -renamegroundpin <renamegroundpin> -setpowerpin <setpowerpin> -setgroundpin <setgroundpin> -parameter <parameter></pre> <p>Where, <parameter> is a list of parameter names and their values specified as:</p> <pre>[list <param_name> <param_value>]</pre> <p>Here, <param_name> can be TypVoltage, MaxVoltage, MinVoltage, IsSpice, or SpiceFile.</p>

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Topology Workbench Tcl Commands

<clarity_block_params>		
		<p>Following is the syntax for Clarity block parameters:</p> <pre>-layoutfile <layoutfile></pre> <p>Where, <layoutfile> is a file of the following supported file formats: *.3dem, *.spd, *.brd, *.mcm, and *.sip.</p>
<source_block_params>		
		<p>Following is the syntax for source block parameters:</p> <pre>-modeltype <modeltype> -numberofsignals <numberofsignals> -includepower <includepower> -parameter <parameter></pre> <p>Where,</p> <ul style="list-style-type: none"> ■ <modeltype> is Gaussian, PWL, Pulse, or Sinesquare. ■ <parameter> is a list of parameter names and values specified as: <pre>[list <param_name> <param_value>]</pre> <p>Here,</p> <p><param_name> can be:</p> <ul style="list-style-type: none"> ❑ Rs, Cs, SourceImpedance, InitValue, PeakValue, Delay, GaussPulseWidth, or GaussPeriod for Gaussian ❑ R, repeat, PWLFormType, PWLFile, Time, or Value for PWL ❑ InitValue, PeakValue, Delay, RiseTime, FallTime, PulsePulseWidth, PulsePeriod for Pulse and Sinesquare

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Topology Workbench Tcl Commands

	<terminator_block_params>
	<p>Following is the syntax for terminator block parameters:</p> <pre>-modeltype <modeltype> -numberofsignals <numberofsignals> -includepower <includepower> -parameter <parameter></pre> <p>Where,</p> <ul style="list-style-type: none"> ■ <modeltype> is ShuntR, ShuntRC, SeriesR, Thevenin, DualClamp, HiClamp, LowClamp, or VoltageSource. ■ <numberofsignals> is an integer, greater than zero. ■ <includepower> is a Boolean. ■ <parameter> is a list of parameter names and values specified as: [list <param_name> <param_value>] <p>Here,</p> <p><param_name> can be:</p> <ul style="list-style-type: none"> ❑ Resistance, Delay, Z0, or Voltage for ShuntR ❑ Resistance, Capacitance, Delay, Z0, or Voltage for ShuntRC ❑ Delay, Z0, or SeriesResistance for SeriesR ❑ ResistanceUp, ResistanceDown, VoltageUp, VoltageDown, Delay, or Z0 for Thevenin ❑ CutoffVoltageUp, CutoffVoltageDown, VoltageUp, VoltageDown, Delay, or Z0 for DualClamp ❑ CutoffVoltage, Voltage, Delay, or Z0 for HiClamp and LowClamp ❑ Voltage for VoltageSource

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	<discrete_block_params>
	<p>Following is the syntax for Discrete block parameters:</p> <pre>-ispowergroup <ispowergroup> \ -modeltype <modeltype> \ -numberofsignals <numberofsignals> \ -includepower <includepower> \ -parameter <parameter></pre> <p>Where,</p> <ul style="list-style-type: none"> ■ <ispowergroup> and <includepower> are defined as a Boolean, that is, 1 or 0. ■ <modeltype> is Resistor, Inductor, Capacitor, SeriesRLC, ParallelRLC, or IdealDiode. ■ <numberofsignals> is an integer, greater than zero. ■ <parameter> is a list of parameter names and values specified as: [list <param_name> <param_value>] <p>Here,</p> <p><param_name> can be:</p> <ul style="list-style-type: none"> ❑ Z0, Delay, or CutoffVoltage for IdealDiode ❑ Resistance for Resistor, SeriesRLC, and ParallelRLC ❑ Inductance for Inductor, SeriesRLC, and ParallelRLC ❑ Capacitance for Capacitor, SeriesRLC, and ParallelRLC
GUI Equivalent	None

Related Topics

[Using the Topology Workbench Tcl Commands: An Overview](#)

Topology Workbench Tcl Command Reference

Topology Workbench Tcl Commands

editDieModel

Syntax	topxp::editDieModel
Description	Opens the Die Model Editor for editing the die model.
Arguments	None.
GUI Equivalent	Die Model Editor dialog box

Related Topics

[Using the Topology Workbench Tcl Commands: An Overview](#)

Topology Workbench Tcl Command Reference

Topology Workbench Tcl Commands

editFrequencyResponse

Syntax	<pre>editFrequencyResponse \ [-maxfreq <double>] \ [-freqpoint <int>] \ [-txSignal [list {Mode SingleEnded} {Signal <block> <signal_net> <gnd_net>}]] \ [-txSignal [list {Mode Diff} {Signal <block> <pos_net> <neg_net>}]]</pre>	
Description	Edits the frequency response.	
Arguments	-maxfreq <double>	
		Specifies the maximum frequency.
	-freqpoint <int>	
		Sets # of Frequency Point.
	-txSignal [list {Mode SingleEnded} {Signal <block> <signal_net> <gnd_net>}]	
		Sets the Tx signal for single-ended mode.
	-txSignal [list {Mode Diff} {Signal <block> <pos_net> <neg_net>}]	
		Sets the Tx signal for differential mode.
GUI Equivalent	<i>Frequency Response</i> panel that opens when you click the <i>Tools – Frequency Response</i> menu in the Topology Workbench window.	

Related Topics

[Using the Topology Workbench Tcl Commands: An Overview](#)

Topology Workbench Tcl Command Reference

Topology Workbench Tcl Commands

editMCPConnection

Syntax	<pre>editMCPConnection \ -mcp [list {<Cable>} {<to_Left>} {<Tx>} {<to_Right>}] \ -autoconnect {<type>} [-pttn [list \$pttn1 \$pttn2]]</pre>	
Description	Edits the MCP connection between the specified blocks.	
Arguments	-mcp [list {<Cable>} {<to_Left>} {<Tx>} {<to_Right>}]	
		Defines the MCP connection that needs to be edited.
	-autoconnect {<type>} [-pttn [list \$pttn1 \$pttn2]]	
		<p>Auto-connects blocks with the same pins.</p> <p>Here,</p> <ul style="list-style-type: none"> ■ <type> can be one of the following: default, node, pin, or net ■ \$pttn1, \$pttn2 can be a regular expression or a wild string.
GUI Equivalent	<i>Connection Definition</i> panel in the Parallel Bus Analysis workflow	

Example

```
editMCPConnection \
    -mcp [list {LSI_1} {%LSI1_1%RW_Interposer%fRDL_1%PM0_sbump} {RW_IP}
    {%RW_Interposer%fRDL_1%LSI1_1%TIV_PSM}]
    -autoconnect cktfile -file {map5.csv}
```

Related Topics

[Using the Topology Workbench Tcl Commands: An Overview](#)

Topology Workbench Tcl Command Reference

Topology Workbench Tcl Commands

editOptions

Syntax	<pre> editOptions \ [-maxcpuforcircuit <int>] \ [-enableuseramp <Boolean>] \ [-enablerskin <Boolean>] \ [-rskin <double>] \ [-shortAllGround <Boolean>] \ [-maxcpuforcircuit <int>] \ [-enableuseramp <Boolean>] \ [-enablerskin <Boolean>] \ [-rskin <double>] \ [-shortAllGround <Boolean>] \ [-addhspice <Boolean>] \ [-optimizeSpdsim <Boolean>] \ [-showwarningmessage <Boolean>] \ [-realtimeplots <Boolean>] \ [-verifyTimingBudget <Boolean>] \ [-runGoldenCheck <Boolean>] \ [-result <option>] \ [-simsavefiles <Boolean>] \ [-showmostrecentcurves <Boolean>] \ [-hidepreviouscurves <Boolean>] \ [-stimulustype {Cycle}] \ [-stimulustype {PRBS} -prbs_option 0 -poly <value> -numofbits <value> -prbs_seed <value>] \ [-stimulustype {PRBS} -prbs_option 1] \ [-stimulustype {Ramp}] \ [-captureXTalk {Boolean}] \ [-ignoreamiclk <Boolean>] \ [-probealleyes <Boolean>] \ [-wavecnt <Boolean>] \ [-nuseblkflt <value>] \ [-logfile <file>] \ [-linktotd <Boolean>] \ [-wingcc <Boolean>] \ [-vspath <path>] </pre>	
Description	Sets the specified general simulation settings for Topology Workbench.	
Arguments	Simulation > General	
	<code>-maxcpuforcircuit <int></code>	
		<p>Sets the maximum CPU numbers for each circuit simulation.</p> <p>Note: This setting does not apply to Spectre.</p>

Topology Workbench Tcl Command Reference

Topology Workbench Tcl Commands

	-enableuseramp <Boolean>	
		Selects the <i>Always use [Ramp] data when no VT curves are available or used</i> option. Note: This setting only applies to SPDSIM.
	-enablerskin <Boolean>	
		Activates the <i>Skin Resistance</i> option.
	-rskin <double>	
		Sets the <i>Skin Resistance</i> option. Note: This setting only applies to the Pin RLC for Package Parasitics.
	-shortAllGround <Boolean>	
		Selects the <i>Short All Ground Pins to GND</i> .
	-addhspice <Boolean>	
		Selects the <i>Add Hspice</i> , if it is selected HPSICE can be used as circuit simulator in <i>Analysis Option</i> .
	-optimizeSpdsim <Boolean>	
		Selects the <i>Enabling Additional Performance Improvements</i> .
	Simulation > Messages and Windows	
	-showwarningmessage <Boolean>	
		Select the <i>Show Warning Messages</i> .
	-realtimeplots <Boolean>	
		Selects the <i>Show Real-Time Plots</i> .
	-verifyTimingBudget <Boolean>	
		Selects the <i>Run Golden Parser Check Before Simulation</i> .
	-runGoldenCheck <Boolean>	
		Selects the <i>Verify Timing Specs Before Simulation</i> .

Topology Workbench Tcl Command Reference

Topology Workbench Tcl Commands

	Simulation > Result	
	<code>-result <option></code>	
		<p>Here, <option> can be one of the following:</p> <ul style="list-style-type: none"> ■ <code>movetohistory</code> to move the previous simulation results under the <i>result</i> folder to the <i>history</i> folder. ■ <code>keep</code> to keep the previous results under the <i>result</i> folder. ■ <code>delete</code> to delete the previous results under the <i>result</i> folder.
	<code>-simsavefiles <Boolean></code>	
		Selects the <i>Save Simulation Files</i> .
	<code>-showmostrecentcurves <Boolean></code>	
		Selects the <i>Show Curves of Most Recent Simulation</i> .
	<code>-hidepreviouscurves <Boolean></code>	
		Selects the <i>Hide Curves of Previous Simulation</i> .
	Simulation > Channel Simulation > Advanced Bus Characterization Options	
	<code>-stimulustype {Cycle}</code>	
		Sets the stimulus type to <i>Cycle</i> .
	<code>-stimulustype {RampBothRiseFall}</code>	
		Sets the characterization stimulus type to <i>Consider both rising and falling ramp response</i> .
	<code>-stimulustype {PRBS} -prbs_option 0 -poly <value> -numofbits <value> -prbs_seed <value></code>	
		Sets the characterization stimulus type to <i>PRBS</i> .
	<code>-stimulustype {PRBS} -prbs_option 1</code>	
		Sets the characterization stimulus type to <i>Stimulus Pattern</i> as set in <i>Analysis Option</i> .

Topology Workbench Tcl Command Reference

Topology Workbench Tcl Commands

	-stimulustype {Ramp}	
		Sets the characterization stimulus type to <i>Ramp</i> .
	-captureXTalk {Boolean}	
		Sets characterization stimulus type to <i>Capture xTalk on individual signal basis</i> .
Simulation > Channel Simulation > Advance AMI options		
	-ignoreamiclk <Boolean>	
		Checks <i>Ignore clock ticks from AMI models</i> .
	editOptions -probealleyes <Boolean>	
		Checks <i>Probe all eyes (Contours at Tx Input, Tx Output and Rx Input)</i> .
	-wavecnt <Boolean>	
		Checks <i>Output last 1000 bits at Tx Input, Tx Output and Rx Input</i> . Note: The Tx waveform is printed only if the Tx AMI model has a GetWave.
	-nuseblkflt <value>	
		Sets the bits for <i>Get Wave</i> block size.
Measurement Report > Generate Report		
	-logfile <file>	
		Sets the <i>Logo File</i> in the HTML report.
	-linktotd <Boolean>	
		Selects <i>Interface to Timing Designer</i> .
AMI Builder > General		
	-wingcc <Boolean>	
		Specifies the compiler for the AMI builder. <i>true</i> sets the compiler as Windows GCC and <i>false</i> sets it as Microsoft Visual Studio.
	-vspath <path>	
		Sets the bin path for Microsoft Visual Studio.

Topology Workbench Tcl Command Reference

Topology Workbench Tcl Commands

GUI Equivalent	The <i>Options</i> dialog box that opens when you click the <i>Tools – Options</i> menu in the Topology Workbench window.
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Related Topics

[Using the Topology Workbench Tcl Commands: An Overview](#)

Topology Workbench Tcl Command Reference

Topology Workbench Tcl Commands

editProbePoints

Syntax	<pre> editProbePoints \ [-addVoltageProbe [list {Block <block>} {Connection <cnt>} {PosNode <pos_node>} {NegNode <neg_node>}]] [-updateVoltageProbe [list {Block <block>} {Connection <cnt>} {PosNode <pos_node>} {NegNode <neg_node>} {CustomName <custom_name>} {IsEnabled <Boolean>}]] [-deleteVoltageProbe [list {Block <block>} {Connection <cnt>} {PosNode <pos_node>} {NegNode <neg_node>}]] [-addCurrentProbe [list {Block1 <block>} {Connection1 <cnt>} {Node1 <node>} {Block2 <block>} {Connection2 <cnt>} {Node2 <node>}]] [-updateCurrentProbe [list {Block1 <block>} {Connection1 <cnt>} {Node1 <node>} {Block2 <block>} {Connection2 <cnt>} {Node2 <node>} {IsReversed <Boolean>} {CustomName <custom_name>} {IsEnabled <Boolean>}]] [-deleteCurrentProbe [list {Block1 <block>} {Connection1 <cnt>} {Node1 <node>} {Block2 <block>} {Connection2 <cnt>} {Node2 <node>}]] </pre>	
Description	Opens the <i>Probe Points</i> panel where you can edit the probes.	
Arguments	editProbePoints	
		Opens the <i>Probe Points</i> panel when the command is run without any arguments.
	<pre> editProbePoints -addVoltageProbe [list {Block <block>} {Connection <cnt>} {PosNode <pos_node>} {NegNode <neg_node>}] </pre>	
		Adds a voltage probe.
	<pre> editProbePoints -updateVoltageProbe [list {Block <block>} {Connection <cnt>} {PosNode <pos_node>} {NegNode <neg_node>} {CustomName <custom_name>} {IsEnabled <Boolean>}] </pre>	
		<p>Updates voltage probe. Here,</p> <p>CustomName <custom_name> specifies a custom name.</p> <p>IsEnabled <Boolean> activates a probe point when set to True or deactivates a probe point when set to False.</p>

Topology Workbench Tcl Command Reference

Topology Workbench Tcl Commands

	<code>editProbePoints -deleteVoltageProbe [list {Block1 <block>} {Connection <cnt>} {PosNode <pos_node>} {NegNode <neg_node>}]</code>
	Deletes voltage probe.
	<code>editProbePoints -addCurrentProbe [list {Block1 <block>} {Connection1 <cnt>} {Node1 <node>} {Block2 <block>} {Connection2 <cnt>} {Node2 <node>}]</code>
	Adds the current probe.
	<code>editProbePoints -updateCurrentProbe [list {Block1 <block>} {Connection1 <cnt>} {Node1 <node>} {Block2 <block>} {Connection2 <cnt>} {Node2 <node>} {IsReversed <Boolean>} {CustomName <custom_name>} {IsEnabled <Boolean>}]</code>
	<p>Updates the current probe. Here,</p> <p><code>CustomName <custom_name></code> specifies a custom name.</p> <p><code>IsEnabled <Boolean></code> activates a probe point when set to <code>True</code> or deactivates a probe point when set to <code>False</code>.</p> <p><code>IsReversed <Boolean></code> reverses the direction of the current probe.</p>
	<code>editProbePoints -deleteCurrentProbe [list {Block1 <block>} {Connection1 <cnt>} {Node1 <node>} {Block2 <block>} {Connection2 <cnt>} {Node2 <node>}]</code>
	Deletes the current probe.
GUI Equivalent	<p><i>Probe Points</i> panel that opens using:</p> <ul style="list-style-type: none"> ■ shortcut menu on the canvas -or- ■ the <i>Setup – Probe Points</i> menu in the Topology Workbench window

Related Topics

[Using the Topology Workbench Tcl Commands: An Overview](#)

Topology Workbench Tcl Command Reference

Topology Workbench Tcl Commands

editSParamExtraction

Syntax	<pre>editSParamExtraction \ [-add [list {Port <block> <node> ref} {Name <port_name>}}]] { -add [list {DiffPort <block> <pos_node> <neg_node> ref} {Name <diff_port_name>}}]] [-delete {Port <port_name>}]] [-delete {DiffPort <diff_port_name>}]] [-checkblock [list {Mode <mode>} {Block <block>}}]] [-uncheckblock [list {Mode <mode>} {Block <block>}}]] [-freqranges [list {<start_freq>,<end_freq>,<#ofpoints>,<points_decade>,<sweep _mode>} ... {<start_freq>,<end_freq>,<#ofpoints>,<points_decade>,<sweep _mode>}}]] [-filename {<file_name>}] [-fileformat {<format>}]</pre>	
Description	Opens the <i>S Parameter Extraction</i> panel that lets you extract the S Parameter definition.	
Arguments	editSParamExtraction	
		Opens the <i>S Parameter Extraction</i> panel when the command is run without any arguments.
	editSParamExtraction -add [list {Port <block> <node> ref} {Name <port_name>}]	
		Adds the specified single-ended port.
	editSParamExtraction -add [list {DiffPort <block> <pos_node> <neg_node> ref} {Name <diff_port_name>}]	
		Adds the specified differential port.
	editSParamExtraction -delete {Port <port_name>}	
		Deletes the specified single-ended port.
	editSParamExtraction -delete {DiffPort <diff_port_name>}	
		Deletes the specified differential port.
	editSParamExtraction -checkblock [list {Mode <mode>} {Block <block>}]	
		Includes the specified block in the S Parameter extraction.
		Here, <mode> can be single-ended or diff.

Topology Workbench Tcl Command Reference

Topology Workbench Tcl Commands

	<code>editSParamExtraction -uncheckblock [list {Mode <mode>} {Block <block>}]</code>	
		Excludes the specified block in the S Parameter extraction.
	<code>editSParamExtraction -freqranges [list {<start_freq>,<end_freq>,<#ofpoints>,<points_decade>,<sweep_mode>} ... {<start_freq>,<end_freq>,<#ofpoints>,<points_decade>,<sweep_mode>}]</code>	
		Specifies the setting of frequency ranges.
	<code>editSParamExtraction -filename {<file_name>}</code>	
		Specifies the S Parameter file.
	<code>editSParamExtraction -fileformat {<format>}</code>	
		Specifies the format of the S Parameter file. Here, <format> can be <code>touchstone</code> or <code>bnp</code> .
GUI Equivalent	<i>S Parameter Extraction</i> panel that opens when you click the <i>Tools – S Parameter Extraction</i> menu in the Topology Workbench window.	

Related Topics

[Using the Topology Workbench Tcl Commands: An Overview](#)

Topology Workbench Tcl Command Reference

Topology Workbench Tcl Commands

editTimingBudget

Syntax	<pre>editTimingBudget \ -spec [list {<block_type>} {BusType <bus>} {<spec> <value>}} \ -default {BusType <bus>}</pre>	
Description	Sets the maximum frequency.	
Arguments	<block_type>	Specifies the block type. Use one of the following values: Controller or Memory
	<bus>	Specifies the bus type. Use one of the following values: Data, Ctrl, or AddCmd
	<spec>	<p>Specifies the type of specification being defined. The following values are accepted:</p> <ul style="list-style-type: none"> ■ MinTransmitSetup ■ MinTransmitHold ■ MinReceiveSetup ■ MinReceiveHold ■ MaxReceiveSkewPlus ■ MaxReceiveSkewMinus ■ MinStrobeSetup ■ MinStrobeHold ■ MaxStrobeClockSkewPlus ■ MaxStrobeClockSkewMinus ■ tDQSQ ■ tQH
	<value>	Specifies the value of the given specification.
	-default {BusType <bus>}	
		Resets the values to default specifications for the specified bus type.
GUI Equivalent	<i>Timing Budget</i> panel that opens when you click the <i>Tools – Timing Budget...</i> menu in the Topology Workbench window while analyzing a topology in the Parallel Bus Analysis workflow.	

Topology Workbench Tcl Command Reference

Topology Workbench Tcl Commands

Related Topics

[Using the Topology Workbench Tcl Commands: An Overview](#)

editWriteLeveling

Syntax	<pre>editWriteLeveling \ [-resolution <resolution>] \ [-clkmeasdelay [list {Memory <memory>} {BusGroup <busgroup>} {<delay>}}] \ [-strobemeasdelay [list {Memory <memory>} {BusGroup <busgroup>} {<delay>}}] \ [-wlo [list {Memory <memory>} {BusGroup <busgroup>} {<delay>}}] \ [-default]</pre>	
Description	Opens the <i>Write Leveling</i> panel in the Parallel Bus Analysis workflow for specifying and calculating Write Leveling Offset (WLO).	
Arguments	-resolution <resolution>	
		Sets the resolution.
	-clkmeasdelay [list {Memory <memory>} {BusGroup <busgroup>} {<delay>}]	
		Sets the clock delay (<i>ClkMeasDelay</i>).
	-strobemeasdelay [list {Memory <memory>} {BusGroup <busgroup>} {<delay>}]	
		Sets strobe delay (<i>StrobeMeasDelay</i>).
	-wlo [list {Memory <memory>} {BusGroup <busgroup>} {<delay>}]	
		Sets the WLO value.
	-default	
		Resets the modified values to the default settings.
GUI Equivalent	<i>Write Leveling</i> panel that opens when you click the <i>Tools – Write Leveling...</i> menu in the Topology Workbench window while analyzing a topology in the Parallel Bus Analysis workflow.	

Related Topics

[Using the Topology Workbench Tcl Commands: An Overview](#)

Topology Workbench Tcl Command Reference

Topology Workbench Tcl Commands

makeReceiver

Syntax	topxp::makeReceiver <block>	
Description	Makes the selected IO block the receiver.	
Arguments	<block>	Name of the block.
GUI Equivalent	<i>Make Receiver</i> option clicked from the shortcut menu displayed when an I/O (IBIS) transmitter block is right-clicked.	

Related Topics

[Using the Topology Workbench Tcl Commands: An Overview](#)

makeTransmitter

Syntax	topxp::makeTransmitter <block>	
Description	Makes the selected IO block the transmitter.	
Arguments	<block>	Name of the block.
GUI Equivalent	<i>Make Transmitter</i> option clicked from the shortcut menu displayed when an I/O (IBIS) receiver block is right-clicked.	

Related Topics

[Using the Topology Workbench Tcl Commands: An Overview](#)

Topology Workbench Tcl Command Reference

Topology Workbench Tcl Commands

renameComponent

Syntax	topxp::renameComponent \ -name <old_name> \ -newname <new_name>	
Description	Renames the selected component.	
Arguments	-name <old_name>	
		Specifies the current name of the block.
	-newname <new_name>	
		Specifies the new name of the block.
GUI Equivalent	None	

Related Topics

[Using the Topology Workbench Tcl Commands: An Overview](#)

restoreDefaultPins

Syntax	topxp::restoreDefaultPins	
Description	Restores the selected block's pins to default location and names.	
Arguments	None	
GUI Equivalent	<i>Restore Default Pins</i> option from the shortcut menu displayed when an unconnected block is right-clicked.	

Related Topics

[Using the Topology Workbench Tcl Commands: An Overview](#)

Topology Workbench Tcl Command Reference

Topology Workbench Tcl Commands

setupDefaultPinLocation

Syntax	<pre>topxp::setupDefaultPinLocation \ -group <group_param> \ [-groundnodes <gnd_nodes>] \ [-powernodes <power_nodes>]</pre>	
Description	Sets up the default pin location for the selected block.	
Arguments	-group <group_param>	
		<p>Specifies the group parameters in the following format: <code>[list <group_name> <direction> <nodes>]</code></p> <p>Where,</p> <ul style="list-style-type: none"> ■ <group_name> specifies the group's name. ■ <direction> specifies the direction as one of the following: Left, Right, Up, or Down ■ <nodes> specifies the nodes to be used in the following format: {node1 node2 ... nodeN}
	-groundnodes <gnd_nodes>	
		<p>Optional</p> <p>Specifies a list of ground nodes in the following format: <code>[list node1 node2 ... nodeN]</code></p>
	-powernodes <power_nodes>	
		<p>Optional</p> <p>Specifies a list of power nodes in the following format: <code>[list node1 node2 ... nodeN]</code></p>
GUI Equivalent	Set up Default Pin Location option from the shortcut menu displayed when a block is right-clicked.	

Related Topics

[Using the Topology Workbench Tcl Commands: An Overview](#)

Topology Workbench Tcl Command Reference

Topology Workbench Tcl Commands

shortToGND

Syntax	<code>topxp::shortToGND</code>
Description	Shorts the selected pin to GND.
Arguments	None.
GUI Equivalent	<i>Short to GND</i> option from the shortcut menu displayed when a selected pin is right-clicked.

Related Topics

[Using the Topology Workbench Tcl Commands: An Overview](#)

shortToPower

Syntax	<code>topxp::shortToPower</code>
Description	Shorts the selected pin to a power supply.
Arguments	None.
GUI Equivalent	<i>Short to Power</i> option from the shortcut menu displayed when a selected pin is right-clicked.

Related Topics

[Using the Topology Workbench Tcl Commands: An Overview](#)

Topology Workbench Tcl Command Reference

Topology Workbench Tcl Commands

shortUnConnectedGroundNode2GND

Syntax	<code>topxp::shortUnConnectedGroundNode2GND</code>
Description	Shorts all unconnected ground nodes to global GND.
Arguments	None.
GUI Equivalent	<i>Short Unconnected Ground Pins to GND</i> option from the shortcut menu displayed when a selected pin is right-clicked.

Related Topics

[Using the Topology Workbench Tcl Commands: An Overview](#)

Displaying Elements in a Topology

You can use the following Tcl commands in the Command Window panel:

- [displayPin](#)
- [displayUnConnectedGroundNode](#)
- [displayUnConnectedPowerNode](#)
- [displayUnConnectedSignalPins](#)

displayPin

Syntax	<code>topxp::displayPin < net pin node none ></code>	
Description	Displays information about the specified type of pin.	
Arguments	< net pin node none >	
		Specifies the type of pin for which information needs to be displayed.
GUI Equivalent	<i>Display Pin Info</i> option from the shortcut menu displayed when the canvas is right-clicked.	

Related Topics

[Using the Topology Workbench Tcl Commands: An Overview](#)

Topology Workbench Tcl Command Reference

Topology Workbench Tcl Commands

displayUnConnectedGroundNode

Syntax	<code>topxp::displayUnConnectedGroundNode</code>
Description	Toggles the visibility of unconnected ground pins.
Arguments	None
GUI Equivalent	<i>Display Unconnected Ground Pins</i> option from the shortcut menu displayed when the canvas is right-clicked.

Related Topics

[Using the Topology Workbench Tcl Commands: An Overview](#)

displayUnConnectedPowerNode

Syntax	<code>topxp::displayUnConnectedPowerNode</code>
Description	Toggles the visibility of unconnected power pins.
Arguments	None
GUI Equivalent	<i>Display Unconnected Power Pins</i> option from the shortcut menu displayed when the canvas is right-clicked.

Related Topics

[Using the Topology Workbench Tcl Commands: An Overview](#)

displayUnConnectedSignalPins

Syntax	<code>topxp::displayUnConnectedSignalPins</code>
Description	Toggles the visibility of unconnected signal pins for the selected block.
Arguments	None
GUI Equivalent	<i>Hide Unconnected Pins</i> option from the shortcut menu displayed when a block is right-clicked.

Related Topics

[Using the Topology Workbench Tcl Commands: An Overview](#)

Controlling Simulation and Analysis

You can use the following Tcl commands in the Command Window panel:

- [`checkConnectivity`](#)
- [`deriveGelementCurrent`](#)
- [`deriveTargetImpedance`](#)
- [`runJitterTolerance`](#)
- [`setOption`](#)
- [`showResult`](#)
- [`startACAnalysis`](#)
- [`startBlockSensitivity`](#)
- [`startBusChannelCharacterization`](#)
- [`startBusSimulation`](#)
- [`startChannelCharacterization`](#)
- [`startChannelSimulation`](#)
- [`startCircuitChannelCorrelation`](#)
- [`startConnectivityCheckSimulation`](#)
- [`startDCAnalysis`](#)

Topology Workbench Tcl Command Reference

Topology Workbench Tcl Commands

- startImpedanceSimulation
- startPowerRippleSimulation
- startSParamExtraction
- startSimulation
- startSweepSimulation
- startTransientAnalysis
- startVltusModelExtraction
- startWriteLevelingSimulation
- stopSimulation
- terminateUnconnectedNodes

Topology Workbench Tcl Command Reference

Topology Workbench Tcl Commands

checkConnectivity

Syntax	<pre>checkConnectivity [-victimRx {RxSignal <signal>}] \ [-maxFrequency {<freq>}] \ [-numberOfFrequencyPoint {<point>}] \ [-couplingThreshold {<threshold>}] \ [-couplingThreshold {Apply}] \ [-includeinchannelsimulation [list {TxSignal <signal>} {RxSignal <signal>} {IsChecked <Boolean>}]]</pre>	
Description	Checks the connectivity of the receiver block with its intended transmitter.	
Arguments	-victimRx {RxSignal <signal>}	
		Sets the <i>Victim Rx</i> signal.
	checkConnectivity -maxFrequency {<freq>}	
		Sets the <i>Max Frequency</i> .
	checkConnectivity -numberOfFrequencyPoint {<point>}	
		Sets the (number) # of <i>Frequency Points</i> .
	checkConnectivity -couplingThreshold {<threshold>}	
		Sets the coupling threshold.
	checkConnectivity -couplingThreshold {Apply}	
		Applies the coupling threshold to set the check status.
	checkConnectivity -includeinchannelsimulation [list {TxSignal <signal>} {RxSignal <signal>} {IsChecked <Boolean>}]	
		<p>Selects or deselects the <i>Include In Channel Simulation</i> check box in the <i>Check Connectivity</i> panel.</p> <p>Here, <signal> can be a differential signal such as RX_LOWER::pos-neg or a single-ended signal such as Mem_U1::DQ2.</p>
GUI Equivalent	Check Connectivity panel that opens when you click the <i>Check Connectivity</i> option in the <i>Simulation Setup</i> schema.	

Related Topics

[Using the Topology Workbench Tcl Commands: An Overview](#)

Topology Workbench Tcl Command Reference

Topology Workbench Tcl Commands

deriveGelementCurrent

Syntax	<pre>topxp::deriveGelementCurrent \ -file <file> \ [-pwr <pwr>] \ [-gnd <gnd>]</pre>	
Description	Extract the g-element current profile.	
Arguments	{<file>}	
		The name of the subcircuit file.
	{<pwr>}	
		Optional Specifies the power node whose current profile will be extracted. If not specified, all the power nodes will be lumped.
	{<gnd>}	
		Optional Specifies the ground node to be terminated to global ground. If not specified, all the ground nodes will be terminated to global ground
GUI Equivalent	"Extract G Element Current"	

Related Topics

[Using the Topology Workbench Tcl Commands: An Overview](#)

Topology Workbench Tcl Command Reference

Topology Workbench Tcl Commands

deriveTargetImpedance

Syntax	<pre>topxp::deriveTargetImpedance \ -block <block> \ -maxpowerripple <maxpowerripple> \ -maxpowerripple <maxpowerripple> \ -resistances <resistances> \ -startfreq <startfreq> \ -endfreq <endfreq> \ -timestep <timestep> \ -timestep <timestep></pre>	
Description	Derives the target impedance.	
Arguments	-block <block>	
		The name of the block.
	-maxpowerripple <maxpowerripple>	
		Type of ripple. The accepted values are peak-to-peak or undershoot.
	-maxpowerripple <maxpowerripple>	
		Specifies the max power ripple in V.
	-resistances <resistances>	
		Specifies the resistance values for optimization. This argument accepts double values separated by a comma (,) or a range specified by "max-min".
	-startfreq <startfreq>	
		Specifies the starting frequency in Hz.
	-endfreq <endfreq>	
		Specifies the ending frequency in Hz.
	-timestep <timestep>	
		Specifies the time step in seconds. This is automatically extracted from the current profile.
	-timestep <timestep>	
		Specifies the simulation time in seconds.
GUI Equivalent	None.	

Topology Workbench Tcl Command Reference

Topology Workbench Tcl Commands

Related Topics

[Using the Topology Workbench Tcl Commands: An Overview](#)

runJitterTolerance

Syntax	<code>topxp::runJitterTolerance</code>
Description	Starts the Jitter Tolerance simulation.
Arguments	None.
GUI Equivalent	None.

Related Topics

[Using the Topology Workbench Tcl Commands: An Overview](#)

setOption

Syntax	<code>setOption \ -namedsim <boolean></code>	
Description	Checks the <i>Simulation Name</i> option.	
Arguments	<code>-namedsim <boolean></code>	
		<p>Specifies a Boolean value, <code>true</code> or <code>false</code>.</p> <p>When set to <code>true</code>, the simulation's name is customizable. If <code>false</code>, a name is automatically assigned to the simulation.</p>
GUI Equivalent	Analysis Option panel – Circuit Simulation tab	

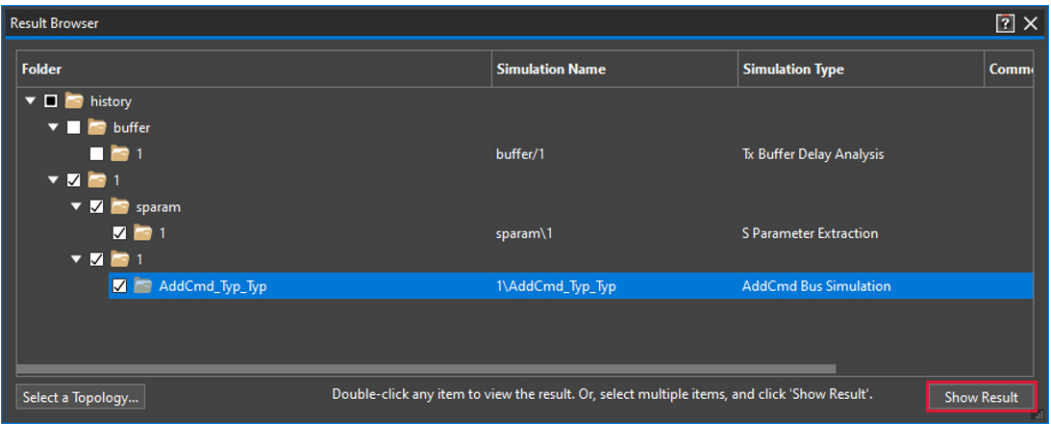
Related Topics

[Using the Topology Workbench Tcl Commands: An Overview](#)

Topology Workbench Tcl Command Reference

Topology Workbench Tcl Commands

showResult

Syntax	topxp::showResult \ [list {\$path}]	
Description	Shows the simulation results that are saved on the specified path.	
Arguments	list {\$path}	
		<p>Specifies the path from where the identified simulation results need to be shown.</p> <p>\$path can be the exact path, such as:</p> <ul style="list-style-type: none"> ■ history/buffer/1 ■ 1/sparam/1 ■ 1/1/AddCmd_Typ_Typ <p>\$path can have wildcards, such as:</p> <ul style="list-style-type: none"> ■ *sparam* ■ 1/sparam/? <p>The separator for \$path can be / or \.</p>
GUI Equivalent	<p>The <i>Show Result</i> button in the <i>Result Browser</i> panel.</p>  <p>The screenshot shows the 'Result Browser' window with a tree view on the left and a table on the right. The tree view shows a hierarchy of folders: history, buffer, 1, sparam, 1, and AddCmd_Typ_Typ. The 'AddCmd_Typ_Typ' folder is selected. The table on the right has columns for 'Folder', 'Simulation Name', 'Simulation Type', and 'Comm'. It lists three simulation results: 'buffer/1' (Tx Buffer Delay Analysis), 'sparam\1' (S Parameter Extraction), and '1\AddCmd_Typ_Typ' (AddCmd Bus Simulation). The '1\AddCmd_Typ_Typ' row is highlighted. At the bottom right, there is a 'Show Result' button.</p>	

Example

```
topxp::showResult [list {history\1\sparam\1} {history\1\1\AddCmd_Typ_Typ}]
```

Topology Workbench Tcl Command Reference

Topology Workbench Tcl Commands

Related Topics

[Using the Topology Workbench Tcl Commands: An Overview](#)

startACAnalysis

Syntax	topxp::startACAnalysis
Description	Starts the frequency response simulation. Will work for single-ended or diff-pair connections.
Arguments	None.
GUI Equivalent	None.

Related Topic

[Using the Topology Workbench Tcl Commands: An Overview](#)

startBlockSensitivity

Syntax	topxp::startBlockSensitivity
Description	Starts to run the block sensitivity simulations.
Arguments	None.
GUI Equivalent	None.

Related Topic

[Using the Topology Workbench Tcl Commands: An Overview](#)

Topology Workbench Tcl Command Reference

Topology Workbench Tcl Commands

startBusChannelCharacterization

Syntax	<code>topxp::startBusChannelCharacterization</code>
Description	Starts channel characterization in Parallel Bus Analysis.
Arguments	None.
GUI Equivalent	None.

Related Topic

[Using the Topology Workbench Tcl Commands: An Overview](#)

startBusSimulation

Syntax	<code>topxp::startBusSimulation</code>
Description	Starts bus simulation.
Arguments	None.
GUI Equivalent	None.

Related Topic

[Using the Topology Workbench Tcl Commands: An Overview](#)

Topology Workbench Tcl Command Reference

Topology Workbench Tcl Commands

startChannelCharacterization

Syntax	<code>topxp::startChannelCharacterization</code>
Description	Starts channel characterization in Serial Link Analysis.
Arguments	None.
GUI Equivalent	None.

Related Topic

[Using the Topology Workbench Tcl Commands: An Overview](#)

startChannelSimulation

Syntax	<code>topxp::startChannelSimulation</code>
Description	Starts channel simulations in Serial Link Analysis.
Arguments	None.
GUI Equivalent	None.

Related Topic

[Using the Topology Workbench Tcl Commands: An Overview](#)

Topology Workbench Tcl Command Reference

Topology Workbench Tcl Commands

startCircuitChannelCorrelation

Syntax	<code>topxp::startCircuitChannelCorrelation</code>
Description	Starts circuit channel correlation.
Arguments	None.
GUI Equivalent	None.

Related Topic

[Using the Topology Workbench Tcl Commands: An Overview](#)

startConnectivityCheckSimulation

Syntax	<code>topxp::startConnectivityCheckSimulation</code>
Description	Starts connectivity check simulation.
Arguments	None.
GUI Equivalent	None.

Related Topic

[Using the Topology Workbench Tcl Commands: An Overview](#)

Topology Workbench Tcl Command Reference

Topology Workbench Tcl Commands

startDCAnalysis

Syntax	<code>topxp::startDCAnalysis</code>
Description	Starts the DC IR Drop Analysis.
Arguments	None.
GUI Equivalent	None.

Related Topic

[Using the Topology Workbench Tcl Commands: An Overview](#)

startImpedanceSimulation

Syntax	<code>topxp::startImpedanceSimulation</code>
Description	Starts the PDN impedance simulation run.
Arguments	None.
GUI Equivalent	None.

Related Topic

[Using the Topology Workbench Tcl Commands: An Overview](#)

Topology Workbench Tcl Command Reference

Topology Workbench Tcl Commands

startPowerRippleSimulation

Syntax	<code>topxp::startPowerRippleSimulation</code>
Description	Starts the power ripple simulation.
Arguments	None.
GUI Equivalent	None.

Related Topic

[Using the Topology Workbench Tcl Commands: An Overview](#)

startSParamExtraction

Syntax	<code>topxp::startSParamExtraction <mode></code>	
Description	Starts the S Parameter extraction.	
Arguments	<code><mode></code>	Specifies the mode of S Parameter extraction, that is, single-ended or diff.
GUI Equivalent	None.	

Related Topic

[Using the Topology Workbench Tcl Commands: An Overview](#)

Topology Workbench Tcl Command Reference

Topology Workbench Tcl Commands

startSimulation

Syntax	<code>topxp::startSimulation</code>
Description	Starts the default simulation in the current workflow.
Arguments	None.
GUI Equivalent	None.

Related Topic

[Using the Topology Workbench Tcl Commands: An Overview](#)

startSweepSimulation

Syntax	<code>topxp::startSweepSimulation</code>
Description	Starts the sweep simulation.
Arguments	None.
GUI Equivalent	None.

Related Topic

[Using the Topology Workbench Tcl Commands: An Overview](#)

Topology Workbench Tcl Command Reference

Topology Workbench Tcl Commands

startTransientAnalysis

Syntax	<code>topxp::startTransientAnalysis</code>
Description	Starts transient analysis in SLA Tolerance.
Arguments	None.
GUI Equivalent	None.

Related Topic

[Using the Topology Workbench Tcl Commands: An Overview](#)

startVoltusModelExtraction

Syntax	<code>topxp::startVoltusModelExtraction</code>
Description	Starts Voltus model extraction.
Arguments	None.
GUI Equivalent	None.

Related Topic

[Using the Topology Workbench Tcl Commands: An Overview](#)

Topology Workbench Tcl Command Reference

Topology Workbench Tcl Commands

startWriteLevelingSimulation

Syntax	<code>topxp::startWriteLevelingSimulation</code>
Description	Starts simulation for calculating write leveling.
Arguments	None.
GUI Equivalent	None.

Related Topic

[Using the Topology Workbench Tcl Commands: An Overview](#)

stopSimulation

Syntax	<code>topxp::stopSimulation</code>
Description	Stops the current ongoing simulation.
Arguments	None.
GUI Equivalent	None.

Related Topic

[Using the Topology Workbench Tcl Commands: An Overview](#)

Topology Workbench Tcl Command Reference

Topology Workbench Tcl Commands

terminateUnconnectedNodes

Syntax	<pre>terminateUnconnectedNodes \ -signal <value> \ -power <value> \ -ground <value></pre>	
Description	Sets the termination resistor for un-connected nodes.	
Arguments	-signal <value>	
		Specifies the signals that are to be terminated.
	-power <value>	
		Specifies the power pins that are to be terminated.
	-ground <value>	
		Specifies the ground pins that are to be terminated.
GUI Equivalent	Terminate Unconnected Pins dialog box.	

Related Topics

[Using the Topology Workbench Tcl Commands: An Overview](#)

Exporting from Topology Workbench

You can use the following Tcl commands in the Command Window panel:

- [exportBlockSensitivityResults](#)
- [exportReport](#)
- [exportToEcsetPage](#)
- [extractECSet](#)

Topology Workbench Tcl Command Reference

Topology Workbench Tcl Commands

exportBlockSensitivityResults

Syntax	<pre>topxp::exportBlockSensitivityResults \ <result index> \ <target file></pre>	
Description	Exports the selected block sensitivity result to a file.	
Arguments	{<result index>}	
		The selected index of result to be exported.
	{<target file>}	
		The file name to export the result.
GUI Equivalent	None.	

Related Topic

[Using the Topology Workbench Tcl Commands: An Overview](#)

Topology Workbench Tcl Command Reference

Topology Workbench Tcl Commands

exportReport

Syntax	<pre>topxp::exportReport -path <target file>\ <-csv -json> \ -noparam</pre> <p>topxp::exportReport -path <target file> -csv -json -noparam</p> <p>-csv - the target file format will be csv.</p> <p>-json - the target file format will be json.</p> <p>-noparam - sweep parameters will not be exported.</p>	
Description	Exports sweep simulation result to a file.	
Arguments	<-csv -json>	
		Specifies the file format of the target file, that is, CSV or JSON.
	{<noparam>}	
		Sweep parameters will not be exported.
GUI Equivalent	None.	

Related Topic

[Using the Topology Workbench Tcl Commands: An Overview](#)

Topology Workbench Tcl Command Reference

Topology Workbench Tcl Commands

exportToEcsetPage

Syntax	<code>topxp::exportToEcsetPage</code>
Description	Exports constraint topology.
Arguments	None.
GUI Equivalent	None.

Related Topics

[Using the Topology Workbench Tcl Commands: An Overview](#)

Topology Workbench Tcl Command Reference

Topology Workbench Tcl Commands

extractECSet

Syntax	<pre>topxp::extractECSet \ <tx> \ <tx pos signal> \ <tx neg signal> \ <rx> \ <rx pos signal> \ <rx neg signal></pre>	
Description	Exports the constraint topology from connectivity checkers.	
Arguments	{<tx>}	
		Name of transmitter.
	{<tx pos signal>}	
		The positive signal name for a diff-pair connectivity, and signal name for single-ended ones.
	{<tx neg signal>}	
		Leave this argument empty for a single-ended signal.
	{<rx>}	
		Name of receiver.
	{<rx pos signal>}	
		The positive receiver signal.
	{<rx neg signal>}	
		The negative receiver signal.
GUI Equivalent	None.	

Related Topics

[Using the Topology Workbench Tcl Commands: An Overview](#)

Managing Projects

You can use the following Tcl commands in the Command Window panel:

- [archiveProject](#)

Topology Workbench Tcl Command Reference

Topology Workbench Tcl Commands

- [assignSimName](#)
- [checkCompliance](#)
- [cleanup](#)
- [connectToPowerSupply](#)
- [convertToWireBased](#)
- [copyProjectAs](#)
- [defineBlockGroup](#)
- [dumpPropertyWindow](#)
- [editPinArg](#)
- [getLicensesUsed](#)
- [ibisEditor](#)
- [launchAbout](#)
- [launchAMM](#)
- [log](#)
- [purgeUnusedModels](#)
- [queryLicenseUsage](#)
- [saveEcsetPageAsTopFile](#)

archiveProject

Syntax	<pre>archiveProject [-default] [-files [list { <i>paths</i> } ...>]] [-zip {true false}] [-o { <i>output_path</i> }]</pre>	
Description	<p>Archives all default project files or only the specified project files.</p> <p>When this Tcl command is run without any arguments, it opens the Archive Topology dialog box.</p>	
Arguments	-default	Adds the default project files to archive. These are the files included by default when the Archive Project dialog box is displayed.

Topology Workbench Tcl Command Reference

Topology Workbench Tcl Commands

	<code>-files [list] <paths ...></code>	
		<p>Adds the project files available at the specified path to the archive list.</p> <p>The <code>list</code> keyword is required, only if there are more than one paths to add.</p> <p>Each path can be of a folder or a file. A folder is included recursively to add all files that exist in it.</p> <p>If a path is ended with '/' (or '\' in Windows), the folder itself is not added, but changes the current directory. All the files and folders therein are treated as relative to the current directory.</p> <p>Each new current directory setting is progressed to the existing setting. The '<' character can be used to regress the current directory such that each less-than sign indicates to regress one level. The level is counted by the specified path items, not physical directories. A single '/' character returns to the root directory at once. For example,</p> <pre>-files [list /abc/ x1 dir1/ x2 dir2/dir3/ x3 x4 < x5 / x6]</pre> <p>is the same as (each X_n represents a sub-path name):</p> <pre>-files /abc/x1 -files /abc/dir1/x2 -files /abc/dir1/dir2/dir3/x3 -files /abc/dir1/dir2/dir3/x4 -files /abc/dir1/x5 -files x6</pre>
	<code>-zip {true false}</code>	
		<p>Enables or disables zip package of the archived project files. This feature is enabled by default.</p>

Topology Workbench Tcl Command Reference

Topology Workbench Tcl Commands

	-o <output_path>	
		<p>The default output path is the current project's folder name with the postfix <code>_copy</code> appended to it. With the <code>-o</code> argument you can specify another full path where the archived output should be saved. Otherwise, you can specify only the directory or file name and Topology Workbench uses the default path setting for the omitted part.</p> <p>If <code>-zip</code> is set to <code>false</code>, the output will always be a directory. However, when <code>-zip</code> is set to <code>true</code>, the path must be ended with <code>' / '</code> if you want to state a directory. The extension <code>.zip</code> will be added automatically to the directory name in this case.</p>
GUI Equivalent	In the Topology Workbench window, the <i>Topology – Archive</i> menu.	

Related Topic

[Archiving a Topology](#)

[Using the Topology Workbench Tcl Commands: An Overview](#)

assignSimName

Syntax	topxp::assignSimName {<name>}	
Description	Set a custom name for the current simulation.	
Arguments	<name>	
		Specifies the name of the simulation.
GUI Equivalent	<i>Assign Simulation Name</i> dialog box that opens when the simulation is started after setting the <i>Simulation Name</i> option in the <i>Circuit Simulation</i> tab of the Analysis Option panel to <i>Custom</i> .	

Related Topics

[Using the Topology Workbench Tcl Commands: An Overview](#)

Topology Workbench Tcl Command Reference

Topology Workbench Tcl Commands

checkCompliance

Syntax	<code>topxp::checkCompliance</code>
Description	Starts the check compliance simulations in the Serial Link Analysis workflow.
Arguments	None.
GUI Equivalent	<i>Check Compliance</i> in the <i>Simulation</i> schema of the <i>Workflow</i> panel displayed when using <i>Custom Compliance Kit</i> .

Related Topics

[Using the Topology Workbench Tcl Commands: An Overview](#)

cleanup

Syntax	<code>topxp::cleanup</code>
Description	Replaces all the blocks in the canvas and automatically routes all the connections.
Arguments	None
GUI Equivalent	The <i>Cleanup Canvas</i> option in the shortcut menu displayed on a right-click on the canvas.

Related Topics

[Using the Topology Workbench Tcl Commands: An Overview](#)

Topology Workbench Tcl Command Reference

Topology Workbench Tcl Commands

connectToPowerSupply

Syntax	<code>topxp::connectToPowerSupply</code>
Description	Connects the selected pins to power supply.
Arguments	None
GUI Equivalent	Context Menu - "Connect to Power Supply"

Related Topics

[Using the Topology Workbench Tcl Commands: An Overview](#)

convertToWireBased

Syntax	<code>topxp::convertToWireBased</code>
Description	<p>Converts legacy SystemSI (old canvas) topologies having MCP-based connectivity to wire-based connectivity.</p> <p>All legacy SystemSI topologies use MCP-based connectivity. Therefore, when you import them into Topology Workbench (new canvas), the legacy topologies continue to hold their MCP-based connectivity property. In addition, for large topologies, like the coupled buses with many signals, this is a great way to represent it. However, some topologies, for aspects like a single signal, or a diff pair, or other cases with a small number of signals, it is easier to work with a wire-based connectivity instead. In such scenarios, you can use this Tcl command that automatically converts all the MCP-based connectivity into wire-based connectivity and conversely.</p>
Arguments	None
GUI Equivalent	None

Related Topic

[Using the Topology Workbench Tcl Commands: An Overview](#)

Topology Workbench Tcl Command Reference

Topology Workbench Tcl Commands

copyProjectAs

Syntax	topxp::copyProjectAs <file>	
Description	Save a copy of the current topology project.	
Arguments	<file>	
		Specifies the name to be assigned to the saved copy.
GUI Equivalent	In the Topology Workbench window, the <i>Copy Topology As...</i> menu.	

Related Topics

[Using the Topology Workbench Tcl Commands: An Overview](#)

defineBlockGroup

Syntax	topxp::defineBlockGroup \ {<block_type>} \ [list {<group_name>} ... {<group_name>}]	
Description	Defines the block groups of Memory or EBD in PBA.	
Arguments	{<block_type>}	
		Can be from Memory or MemSPICEBlock or EBD
	{<group_name>}	
		Specifies the name of a group. This is in list format in case of multiple groups.
GUI Equivalent	None.	

Related Topics

[Using the Topology Workbench Tcl Commands: An Overview](#)

Topology Workbench Tcl Command Reference

Topology Workbench Tcl Commands

dumpPropertyWindow

Syntax	topxp::dumpPropertyWindow {output_file_path}	
Description	<p>Prints the properties of the block to the specified file.</p> <p>Running the simulation and comparing the result is one way to make sure that the setups and connections are correct. To get quick data for comparison, you can use this command to print the properties of various blocks in the topology to a text file.</p>	
Arguments	{output_file_path}	
		Specifies the path where the output file needs to be saved.
GUI Equivalent	None	

Related Topic

[Using the Topology Workbench Tcl Commands: An Overview](#)

Topology Workbench Tcl Command Reference

Topology Workbench Tcl Commands

editPinArg

Syntax	<pre>topxp::editPinArg \ -block <block name> \ -pin <pin name> \ -value <value></pre>	
Description	Edit the specified pin property's value.	
Arguments	{<block name>}	
		Specifies the name of the block.
	{<pin name>}	
		Specifies the name of the pin.
	{<value>}	
		Value of voltage for shorted to power pins / Value of resistance for terminated with resistor pins / Values of voltages for connected to power supply pins.
GUI Equivalent	None.	

Related Topic

[Using the Topology Workbench Tcl Commands: An Overview](#)

getLicensesUsed

Syntax	topxp::getlicensesUsed
Description	Returns current checked out license.
Arguments	None.
GUI Equivalent	In the Topology Workbench window, the <i>Help – License Used...</i> menu.

Related Topics

[Using the Topology Workbench Tcl Commands: An Overview](#)

Topology Workbench Tcl Command Reference

Topology Workbench Tcl Commands

ibisEditor

Syntax	<code>topxp::ibisEditor</code>
Description	Opens the IBIS editor.
Arguments	None.
GUI Equivalent	In the Topology Workbench window, the <i>Tools – IBIS Editor...</i> menu.

Related Topics

[Using the Topology Workbench Tcl Commands: An Overview](#)

launchAbout

Syntax	<code>topxp::launchAbout</code>
Description	Shows the <i>About</i> dialog box.
Arguments	None.
GUI Equivalent	In the Topology Workbench window, the <i>Help – About</i> menu.

Related Topics

[Using the Topology Workbench Tcl Commands: An Overview](#)

Topology Workbench Tcl Command Reference

Topology Workbench Tcl Commands

launchAMM

Syntax	topxp::launchAMM \ -libraries -browsing \ -save <file> -load <file>	
Description	Displays the Sigrity™ Analysis Model Manager (AMM) main window.	
Arguments	-libraries	Displays the AMM Library Management window.
	-browsing	Displays the AMM Browse Models window.
	-save <file>	Specifies the file name to be used to save the AMM library.
	-load <file>	Imports AMM library from the specified file.
GUI Equivalent	None.	

Related Topics

[Using the Topology Workbench Tcl Commands: An Overview](#)

log

Syntax	topxp::log <message>	
Description	Adds a message to the log file.	
Arguments	<message>	Message to be appended to the file
GUI Equivalent	None.	

Related Topics

[Using the Topology Workbench Tcl Commands: An Overview](#)

Topology Workbench Tcl Command Reference

Topology Workbench Tcl Commands

purgeUnusedModels

Syntax	<code>topxp::purgeUnusedModels</code>
Description	Purges unused models in <code>asi_models</code> .
Arguments	None.
GUI Equivalent	In the Topology Workbench window, the <i>Topology – Purge Unused Models...</i> menu.

Related Topics

[Using the Topology Workbench Tcl Commands: An Overview](#)

queryLicenseUsage

Syntax	<code>topxp::queryLicenseUsage <feature></code>	
Description	Queries the status of the licenses.	
Arguments	<code><feature></code>	<p>Specifies the license string for which status needs to be retrieved. The result is displayed in the following format:</p> <p><code><feature>: <total> available, <used> used</code></p> <p>Where,</p> <ul style="list-style-type: none">■ <code><feature></code> is the specified license string.■ <code><total></code> displays the total number of available licenses.■ <code><used></code> displays the number of licenses that are currently checked out.
GUI Equivalent	None.	

Related Topics

[Using the Topology Workbench Tcl Commands: An Overview](#)

Topology Workbench Tcl Command Reference

Topology Workbench Tcl Commands

saveEcsetPageAsTopFile

Syntax	topxp::saveEcsetPageAsTopFile <file>	
Description	Save the constraint topology as a SigXplorer .top file.	
Arguments	<file>	Name of the file
GUI Equivalent	None.	

Related Topics

[Using the Topology Workbench Tcl Commands: An Overview](#)

terminateWithR

Syntax	topxp::terminateWithR	
Description	Exits the selected pin with resistor.	
Arguments	None.	
GUI Equivalent	None.	

Related Topics

[Using the Topology Workbench Tcl Commands: An Overview](#)

Generating Via Models

You can use the following Tcl commands in the Command Window panel:

- [auroraViaWizard](#)
- [sigrityViaWizard](#)

Topology Workbench Tcl Command Reference

Topology Workbench Tcl Commands

auroraViaWizard

Syntax	topxp::auroraViaWizard <block_name>	
Description	Opens the Aurora Via Wizard that helps to generate via models in the pre-layout phase for the specified block.	
Arguments	<block_name>	Specifies the name of the block for which the via model needs to be generated
GUI Equivalent	The <i>Aurora Via Wizard</i> button in the <i>Edit Properties</i> panel displayed when a Via block is double-clicked in the Topology Workbench canvas.	

Related Topic

[Aurora Via Wizard](#)

[Using the Topology Workbench Tcl Commands: An Overview](#)

sigrityViaWizard

Syntax	topxp::sigrityViaWizard <block_name>	
Description	Opens the Sigrity Via Wizard that helps to generate via models in the pre-layout phase for the specified block.	
GUI Equivalent	The <i>Sigrity Via Wizard</i> button in the <i>Edit Properties</i> panel displayed when a Via block is double-clicked in the Topology Workbench canvas.	

Related Topic

[Sigrity Via Wizard](#)

[Using the Topology Workbench Tcl Commands: An Overview](#)

Managing AMI Models

You can use the following Tcl commands in the Command Window panel:

- [deleteAMI](#)
- [modelEnable](#)

Topology Workbench Tcl Command Reference

Topology Workbench Tcl Commands

deleteAMI

Syntax	topxp::deleteAMI \ {<block>} \ {<index>}	
Description	Delete the AMI of index {index} from the block.	
Arguments	{<block>}	
		Specifies the block that contains the AMI to be deleted.
	{<index>}	
		Specifies the index of the AMI in the block.
GUI Equivalent	None.	

Related Topics

[Using the Topology Workbench Tcl Commands: An Overview](#)

modelEnable

Syntax	topxp::modelEnable \ <true false>	
Description	Enable/disable the currently selected AMI model.	
Arguments	{<true false>}	
		If input is true, then the selected AMI model is enabled. If input is false, then the selected model is disabled.
GUI Equivalent	None.	

Related Topics

[Using the Topology Workbench Tcl Commands: An Overview](#)

Handling Constraint Manager

You can use the following Tcl commands in the Command Window panel:

- [setDesignPrecision](#)
- [setOptionalPins](#)
- [updateCM](#)

setDesignPrecision

Syntax	topxp::setDesignPrecision <precision>:<unit>	
Description	Sets the precision and unit for a constraint design.	
Arguments	<precision>:<unit>	
		<p>Specifies the precision and unit.</p> <p>The valid values for <precision> can be:</p> <ul style="list-style-type: none"> ■ an integer range in [2,4] for cm, inch ■ [0,4] for um ■ [1-4] for mm ■ [0,2] for mil <p>For <unit>, valid values can be one of the following: cm, in, um, mm, or mil</p>
GUI Equivalent	<i>Design Units and Precision</i> dialog box that opens when <i>Set Precision</i> option is clicked in the <i>Workflow</i> panel of the exported constraint topology.	

Related Topics

[Using the Topology Workbench Tcl Commands: An Overview](#)

Topology Workbench Tcl Command Reference

Topology Workbench Tcl Commands

setOptionalPins

Syntax	<code>topxp::setOptionalPins</code>
Description	Marks the pins on a selected component as optional when Ecset is updated in Constraint Manager.
Arguments	None.
GUI Equivalent	<i>Optional Pins</i> from the shortcut menu displayed on right-clicking a selected block in the ECSet page.

Related Topics

[Using the Topology Workbench Tcl Commands: An Overview](#)

updateCM

Syntax	<code>topxp::updateCM</code>
Description	Updates the constraint manager for the selected object.
Arguments	None.
GUI Equivalent	None.

Related Topic

[Using the Topology Workbench Tcl Commands: An Overview](#)

Toggling in Topology Workbench

You can use the following Tcl commands in the Command Window panel:

- [`toggleAutoShort`](#)
- [`toggleBackChannel`](#)
- [`togglePauseBeforeSim`](#)
- [`toggleShowNewMCP`](#)
- [`toggleTxRxJitterNoise`](#)

Topology Workbench Tcl Command Reference

Topology Workbench Tcl Commands

toggleAutoShort

Syntax	<code>topxp::toggleAutoShort</code>
Description	Toggles auto-short for the selected block.
Arguments	None.
GUI Equivalent	None.

Related Topics

[Using the Topology Workbench Tcl Commands: An Overview](#)

toggleBackChannel

Syntax	<code>topxp::toggleBackChannel</code>
Description	Toggles the back channel in SLA.
Arguments	None.
GUI Equivalent	None.

Related Topic

[Using the Topology Workbench Tcl Commands: An Overview](#)

Topology Workbench Tcl Command Reference

Topology Workbench Tcl Commands

togglePauseBeforeSim

Syntax	topxp::togglePauseBeforeSim
Description	Toggles “Pause before Simulation”.
Arguments	None.
GUI Equivalent	In the Topology Workbench window, the <i>Setup – Pause before Simulation</i> menu.

Related Topic

[Using the Topology Workbench Tcl Commands: An Overview](#)

toggleShowNewMCP

Syntax	topxp::toggleShowNewMCP
Description	Toggles the <i>Show new connection definition</i> option.
Arguments	None.
GUI Equivalent	None.

Related Topics

[Using the Topology Workbench Tcl Commands: An Overview](#)

Topology Workbench Tcl Command Reference

Topology Workbench Tcl Commands

toggleTxRxJitterNoise

Syntax	<code>topxp::toggleTxRxJitterNoise</code>
Description	Toggles transmitter (Tx) and receiver (Rx) jitter noise.
Arguments	None.
GUI Equivalent	None.

Related Topic

[Using the Topology Workbench Tcl Commands: An Overview](#)