

Allegro® X System Capture: What's New in Release 23.1

Product Version 23.1
September 2023

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This chapter contains the following sections:

■ New features

- ❑ [Update Available Notification](#)
- ❑ [Thermal Analysis and Celsius Integration](#)
- ❑ [Topology Workbench Integration](#)
- ❑ [New Analysis Modes Available in Design Integrity](#)
 - [MTBF \(Mean Time Between Failure\) Analysis](#)
 - [Power Topology Analysis](#)
- ❑ [Adding Parts from External Content Providers to Libraries](#)
- ❑ [Library Authoring Enhancements](#)
 - [Templates Introduced for Libraries](#)
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 - [Edit Multiple Parts in a Spreadsheet](#)
 - [Support for Custom Shapes and Pin Shapes](#)
 - [Enhanced Validation Checks Added](#)
- ❑ [New Features in the Multi-User Environment](#)
 - [Managed Layouts in Project Explorer](#)
 - [Pulse Web Dashboard Updates](#)
 - [Pulse Status Icons](#)
 - [Comparing Versions](#)
 - [Working in the Offline Mode](#)

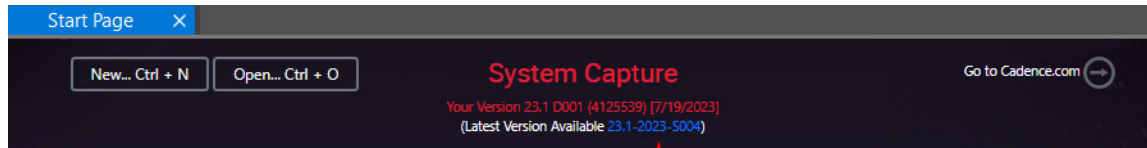
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- ☐ Design Search and Reuse
- Changed features
 - ☐ AWR MWO-Allegro RF Design Flow on page 30
 - ☐ Variant Editor Enhancements on page 32
 - ☐ PDF/A Support Added on page 36
 - ☐ Updated Bill of Materials Dialog Box on page 37
- Documentation Updates on page 39

Update Available Notification

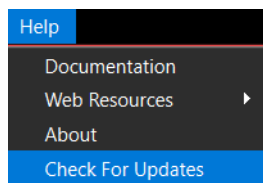
The *Start Page* shows the latest version available for System Capture. Click this link to download the newer version. If you are in an enterprise setup or do not have the required permissions, downloading and updating the installed version might require administrator support.



Link to the latest version

The version numbers in your installation might differ from those in the image.

A new *Help – Check for Updates* option is also added to check if a more recent update are available.



Thermal Analysis and Celsius Integration

Important

Requires a compatible Sigrity installation as listed in the *README.pdf* on downloads.cadence.com. System Capture 23.1 is compatible with Sigrity 23.1.

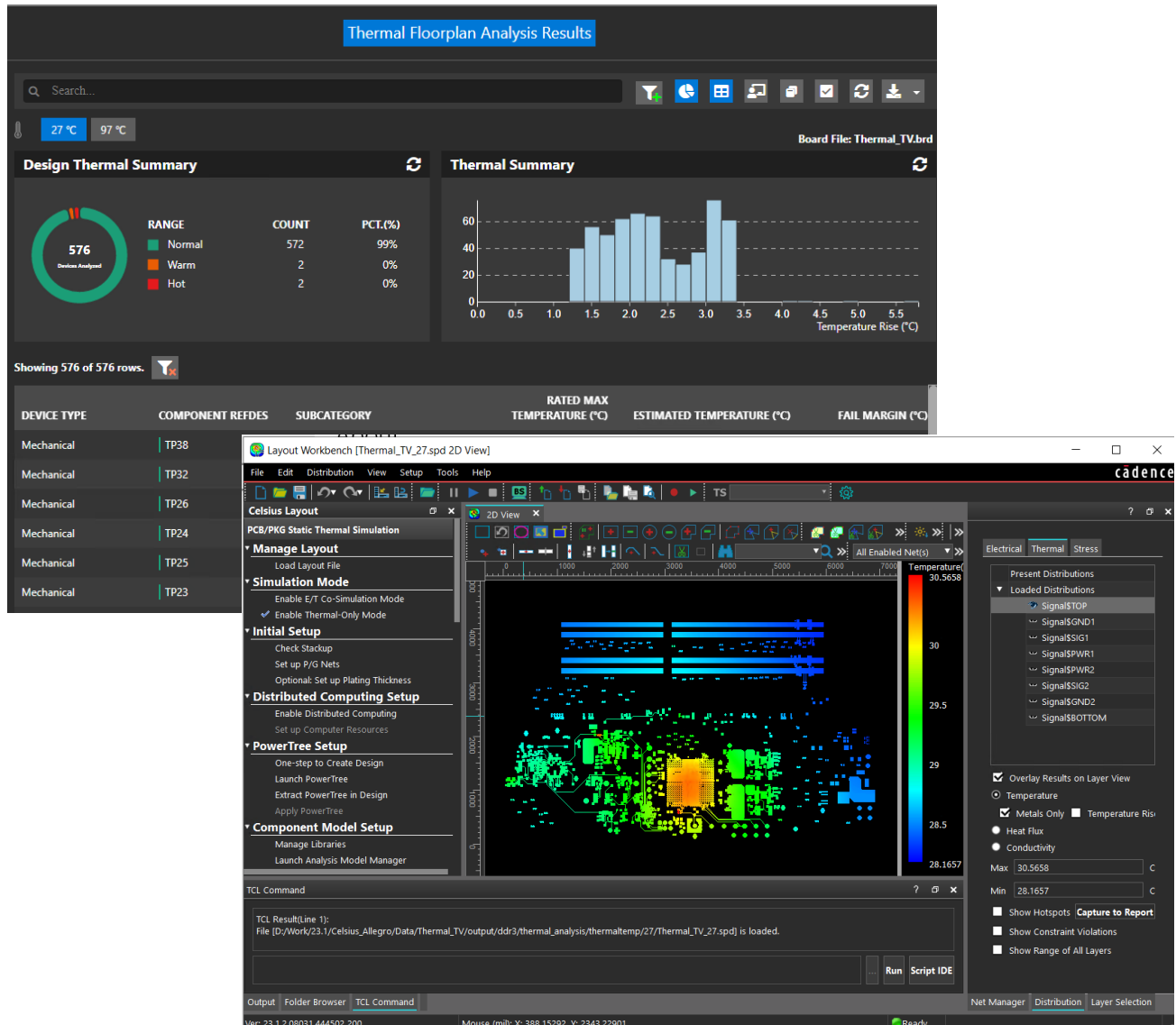
You can now estimate the life of a PCB design by generating a thermal floorplan for a board file in System Capture. This helps with improved placement of components based on the thermal map early in the design cycle. You can estimate the stress of schematic components based on the thermal map. You can launch Celsius Thermal Solver from the Electrical Analysis dashboard.

To access this functionality, create an environment variable called `SIGRITY_EDA_DIR` and

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set it to the *<path to Celsius executable file>*.



Related Documentation

[Thermal Analysis with Celsius Thermal Solver](#)

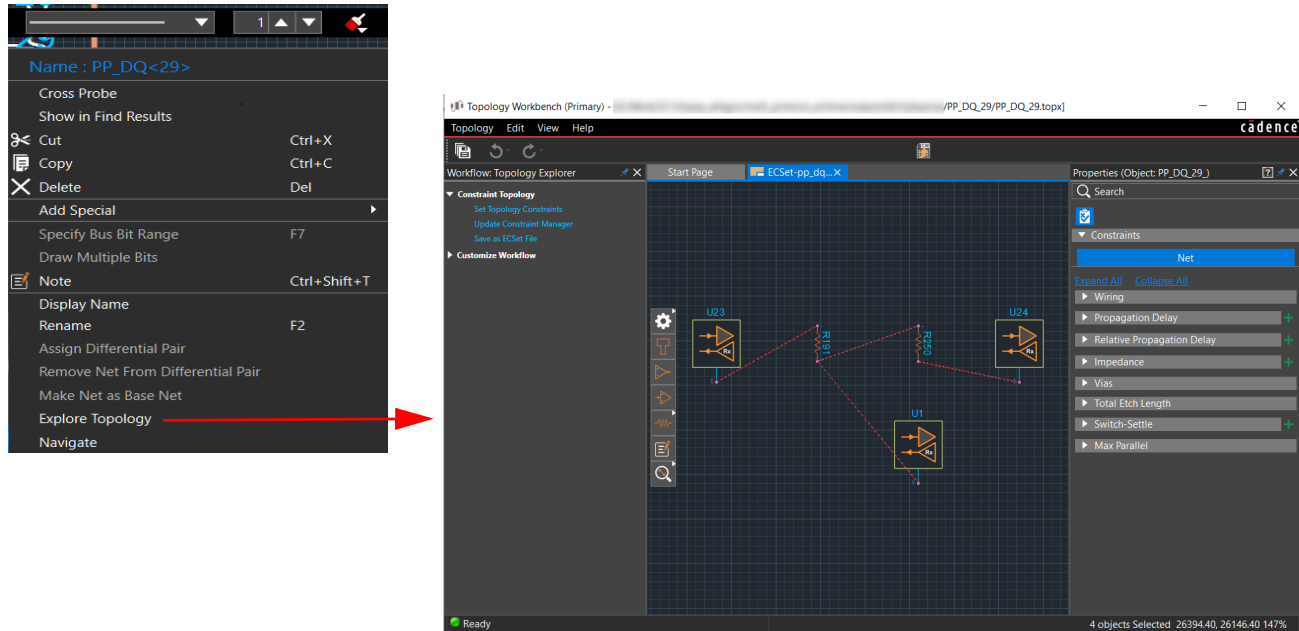
Topology Workbench Integration

Topology Workbench has replaced Signal Explorer for analyzing the signal integrity of high-speed nets at the schematic, floorplan, and layout stages. All the Constraint Manager-based

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ECSet creation and modification flows and simulation flows, continue to be supported. You can launch Topology Workbench directly from System Capture Canvas for a single net, XNet, or from Constraint Manager.



Related Documentation

[Extracting Topology in Topology Workbench](#)

New Analysis Modes Available in Design Integrity

Design Integrity includes two new analyses:

- [MTBF \(Mean Time Between Failure\) Analysis](#)
- [Power Topology Analysis](#)

MTBF (Mean Time Between Failure) Analysis

System Capture is enhanced to support MTBF analysis on a design to estimate the performance and safety of electrical, mechanical, and electro-mechanical parts. MTBF analysis is available with the following licenses:

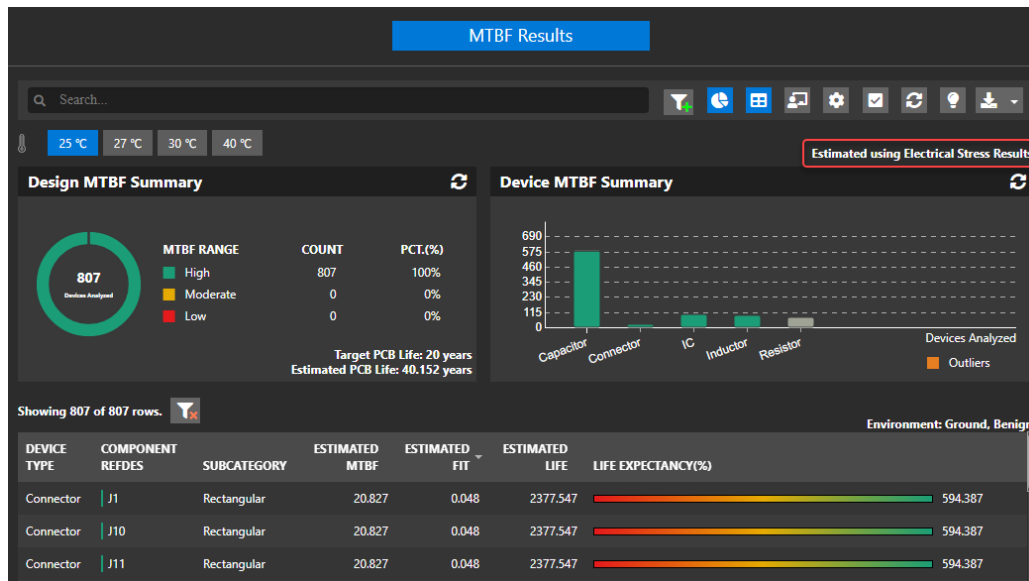
- Allegro X Designer

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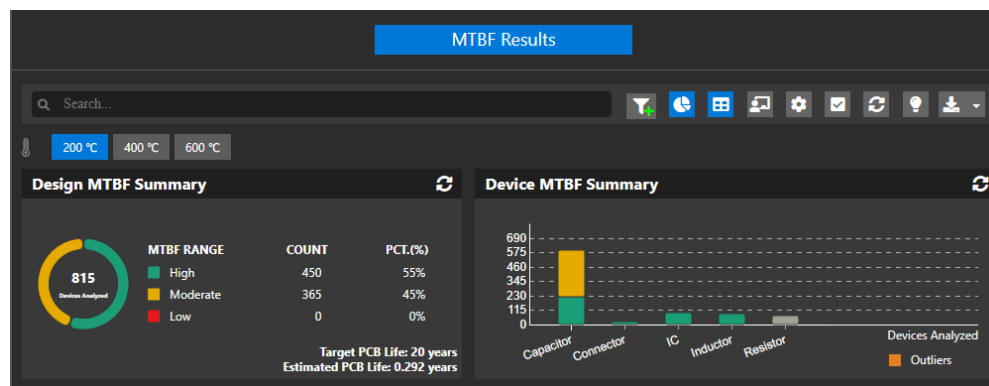
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- Allegro X Designer Plus
- Allegro X Venture

System Capture can calculate MTBF results with or without using the electrical stress data generated in the last run.



You can also configure the ambient temperatures used for MTBF analysis.



Related Documentation

MTBF Analysis

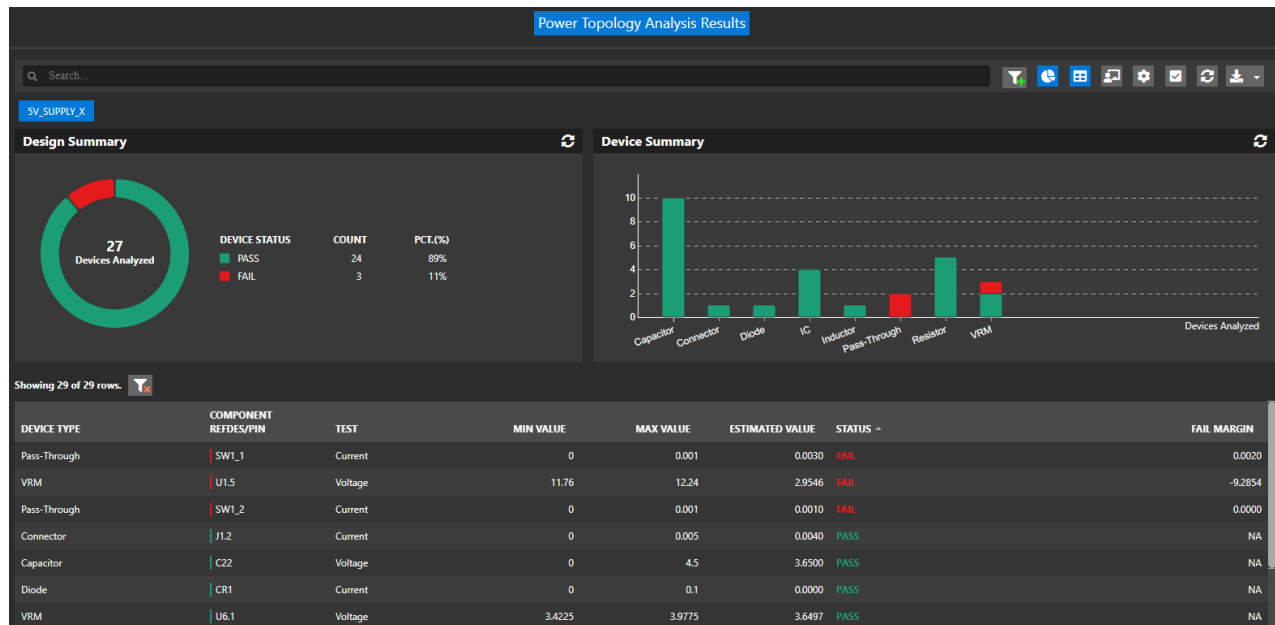
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Power Topology Analysis

Power Topology analysis sets up a power distribution network on the schematic and estimates the DC power consumption by the PCB design components. This enables the verification of all the components in a design in the early stages of design development. Power Topology analysis is available with the following licenses:

- Allegro X Designer
- Allegro X Designer Plus
- Allegro X Venture



Related Documentation

[Power Topology Analysis](#)

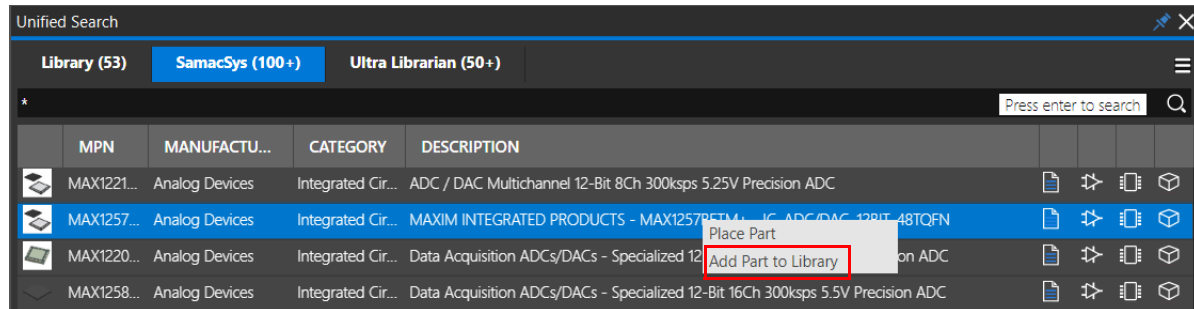
Adding Parts from External Content Providers to Libraries

Until the previous release, adding parts from external content providers, such as *SamacSys* or *Ultra Librarian*, downloaded parts to a provider-specific library and then placed them in

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designs. Now, you can add these parts to an existing library different from a provider-specific library, such as `local_part_lib`.



Related Documentation

[Adding Parts from External Content Providers](#)

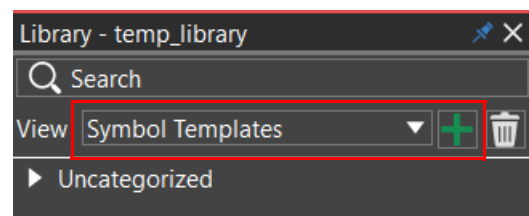
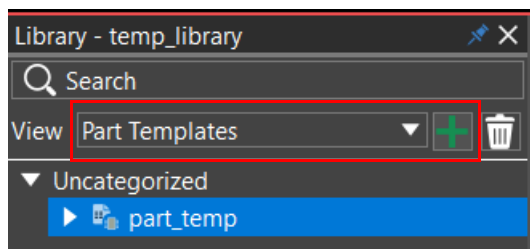
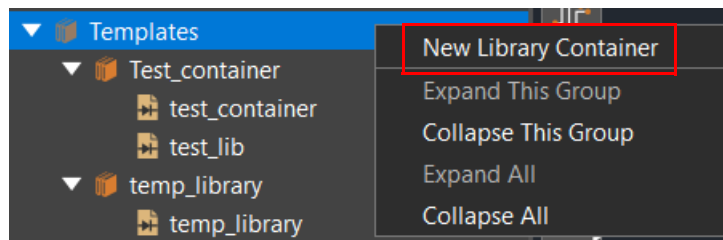
Library Authoring Enhancements

Allegro Unified library authoring includes the following new features:

- [Templates Introduced for Libraries](#)
- [Creating Categories in a Library](#)
- [Edit Multiple Parts in a Spreadsheet](#)
- [Support for Custom Shapes and Pin Shapes](#)
- [Enhanced Validation Checks Added](#)

Templates Introduced for Libraries

You can create a new template library container under the *Templates* node, and create a new part or symbol template in this container. In addition to the default templates available for creating a part or a symbol, custom part and symbol templates are also available.



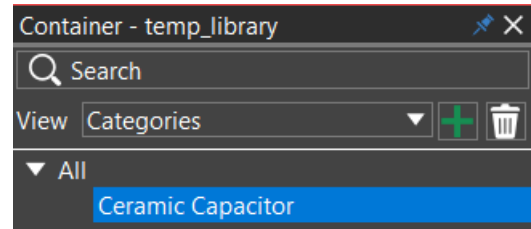
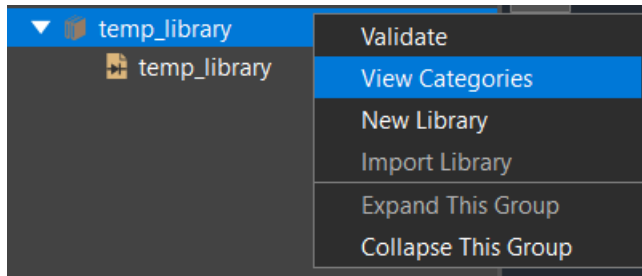
Creating Categories in a Library

New categories and subcategories in the template library improve component searching. For example, you can categorize a schematic model, resistor, as *Discrete*, which is further

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subclassified as *Resistor*. You can search for a keyword, *Discrete* or *Resistor*, and assign it to a part or a symbol.



Edit Multiple Parts in a Spreadsheet

Modifying part names, descriptions, and properties is simpler in the spreadsheet view for part editing. In this spreadsheet view, you can edit multiple part properties, schematic models, footprints, simulation models, datasheets, and other columns.

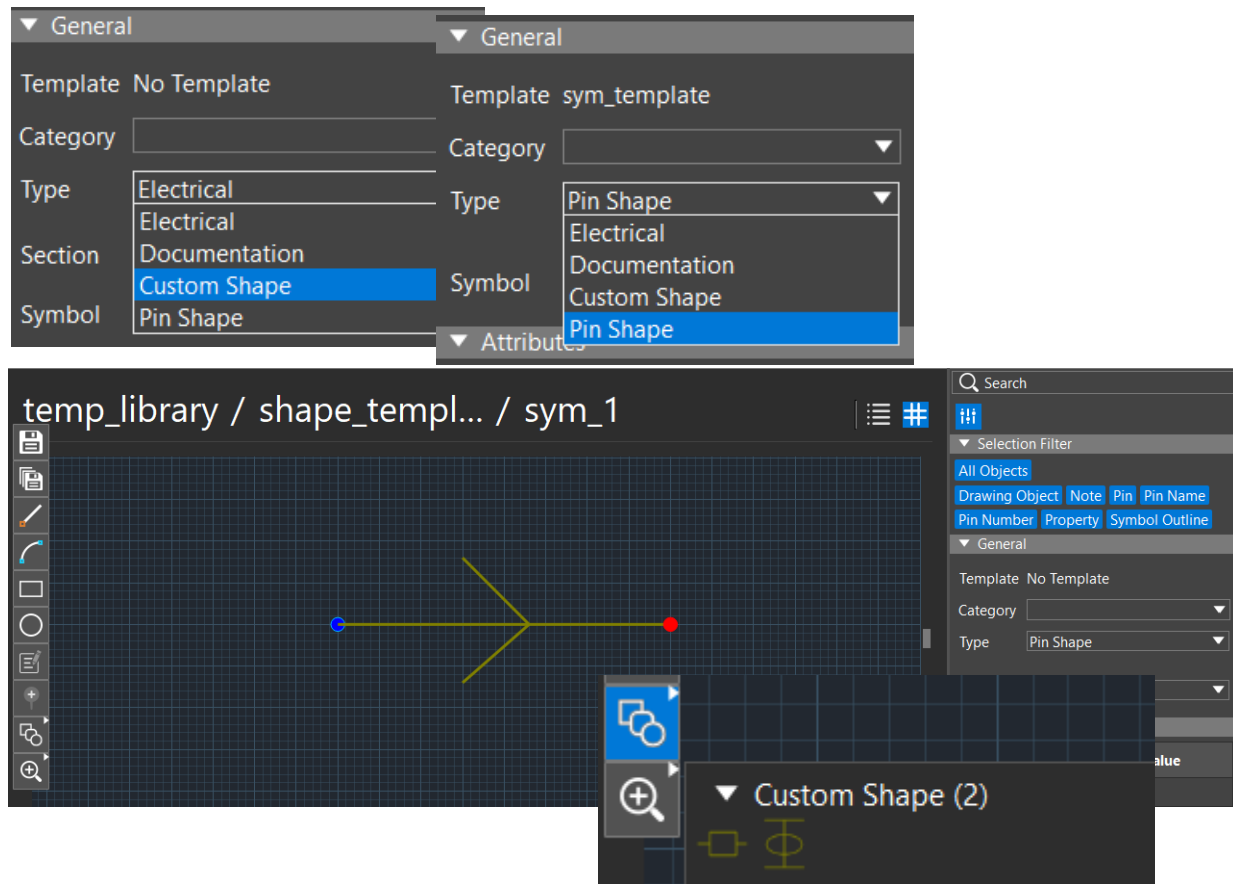
userlib : userlib Parts loaded : 4									
Save Cancel Load more Filters									
STATUS	NAME	CLASSIFICATI...	SUBTYPE	SYMBOL	MARKED DELETED	PART TEMPL...	DESCRIPTION	COMMENT	SYMBOL
	part_01		Electrical	sym1			new_part		SYM1
	new_part02	Capacitor	Electrical						
	new_part		Electrical	sym_templ...		part_temp	Part Template		SYM_TEMPLATE
	Part_SymL...		Electrical	sym_templ...					SYM_TEMPLATE-COPY

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Support for Custom Shapes and Pin Shapes

You can create a new custom or pin shape under a template library and attach it to a pin or the complete symbol. This feature is available for projects based on Allegro Unified or DE-HDL libraries.



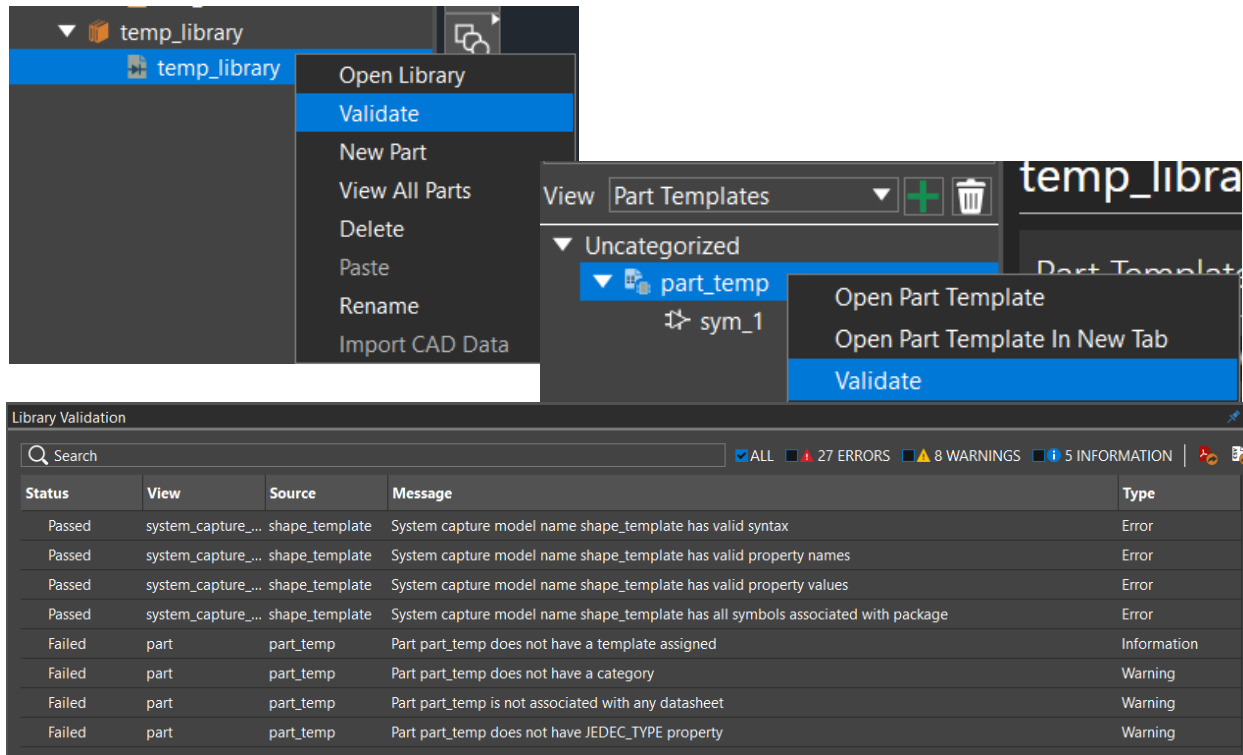
Enhanced Validation Checks Added

Validate libraries and data models, including parts, schematic models, footprints, and data sheets, to avoid common errors in a design with the *Library Validation* window. Libraries,

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parts, and schematic models are validated based on the rules defined in *Library Audit Settings*.



Related Documentation

[Library Authoring and Editing in Allegro X System Capture](#)

New Features in the Multi-User Environment

When connected to a remote Pulse server, the following additional features are available:

- [Managed Layouts in Project Explorer](#)
- [Pulse Web Dashboard Updates](#)
- [Pulse Status Icons](#)
- [Comparing Versions](#)
- [Working in the Offline Mode](#)

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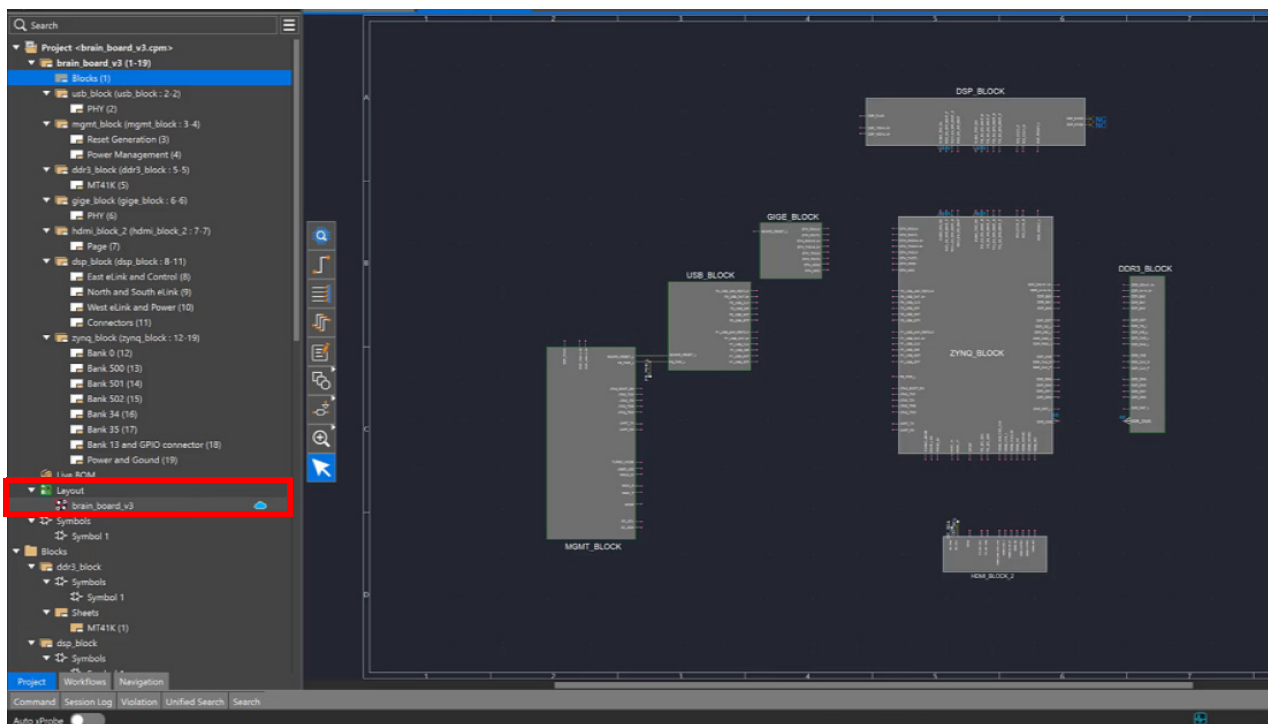
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■ Design Search and Reuse

Managed Layouts in Project Explorer

System Capture users in a multi-user environment can stay updated with the latest changes in layouts associated with schematics.

Pulse-managed layouts include boards, packages, and modules. The layouts are displayed in *Project* explorer if you have read or edit permissions for the layout files.



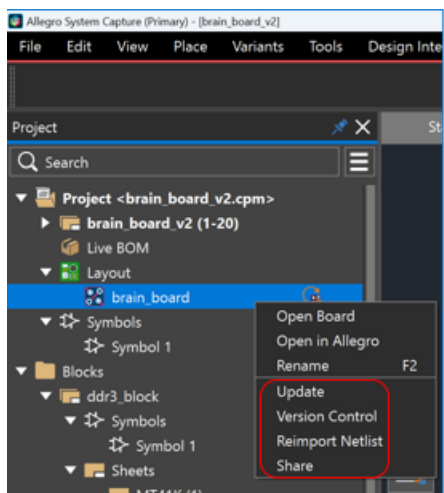
Tooltips displayed on hovering the mouse over layouts display the layout status, such as:

- Sync status with the associated schematic
- Whether a new revision is available
- Downloaded layouts that are removed from the design in the Pulse web dashboard

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Additional tasks are available from the context menu for Pulse-managed layouts, as shown in the following image.



Related Documentation

[Design Data Management in Allegro X System Capture](#)

Pulse Web Dashboard Updates

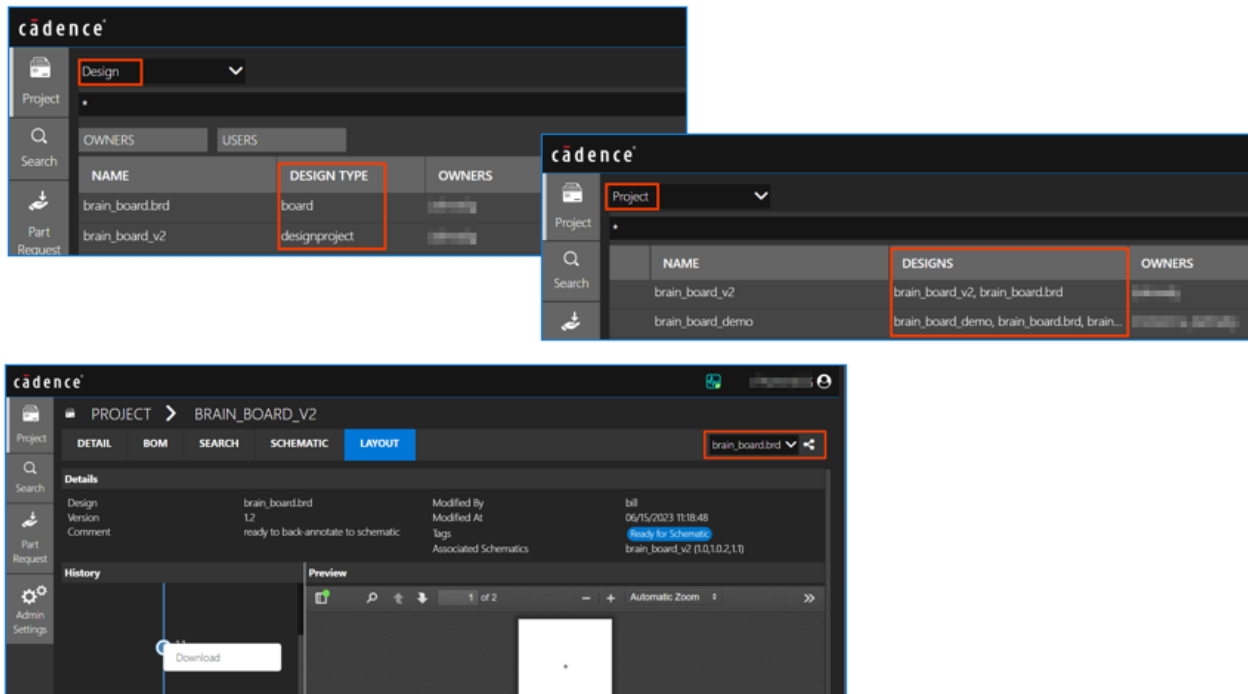
Changes in the Pulse web dashboard are as follows:

- The *Part Search* tab on the left of the web page is renamed to *Search*.
- There is a *Project* and *Design* view. The *Project* view displays project containers and the designs contained within. The *Design* view shows all Pulse-managed designs, which

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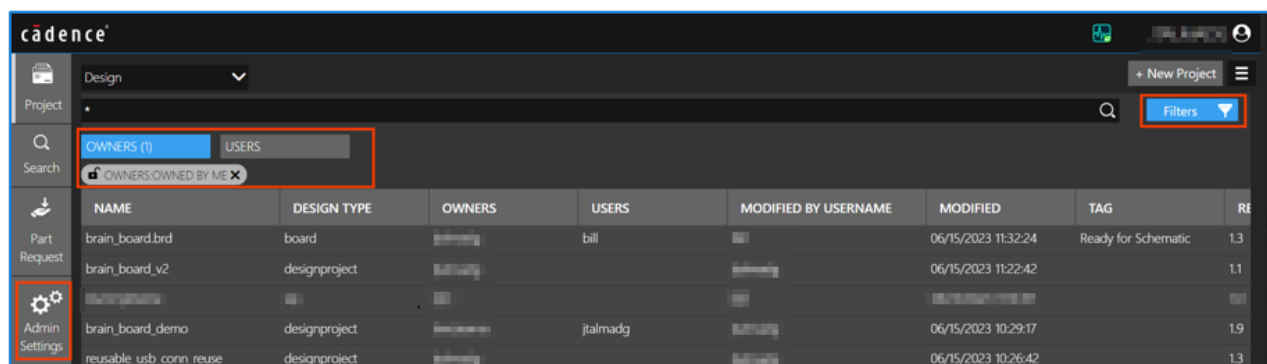
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includes schematics and layouts. This enables layout visibility in the *Layout* tab by double-clicking a project in the *Design* or *Project* list.



You can share a layout, view the layout versions, and download layout versions from *Version Control*.

- The administrator tasks have moved from the user profile icon to the left side of the web page.

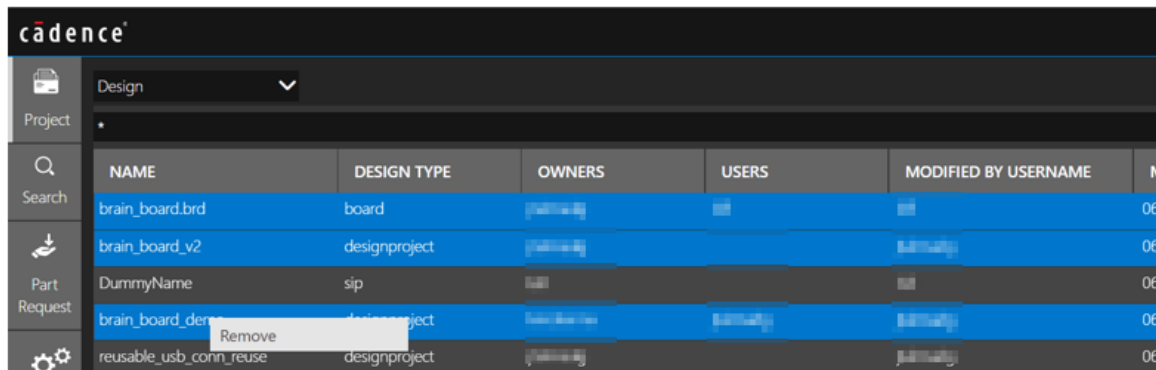


- Instead of clicking, you must now double-click to open a project or design from the list. Click selects a project or design instead of opening it.

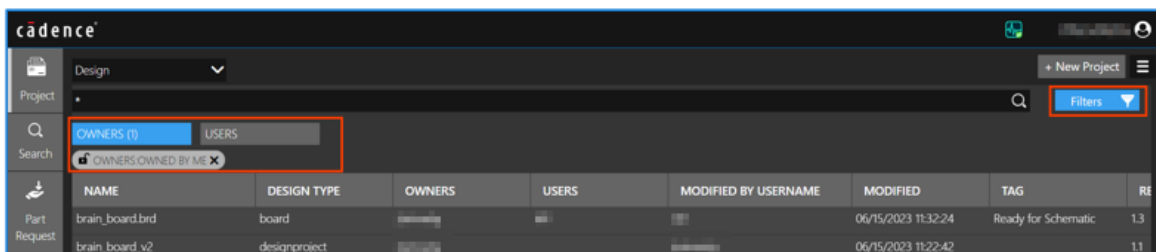
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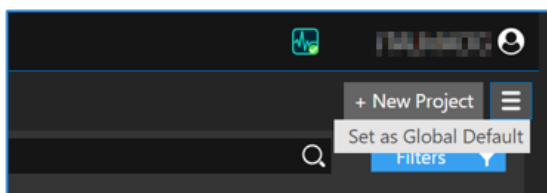
- Selecting and removing multiple entries simultaneously from the Design list is now supported.



- The *Owned by me* and *shared with me* filters enable easier viewing of projects and designs.



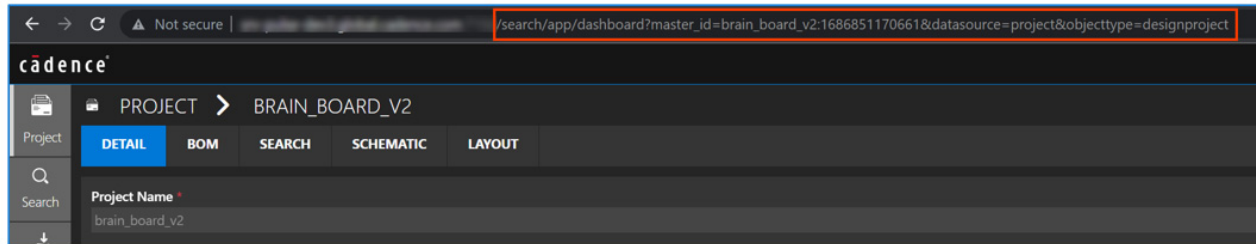
- Pulse administrators can save column visibility, the column order, and a filter for a project list as the global default.



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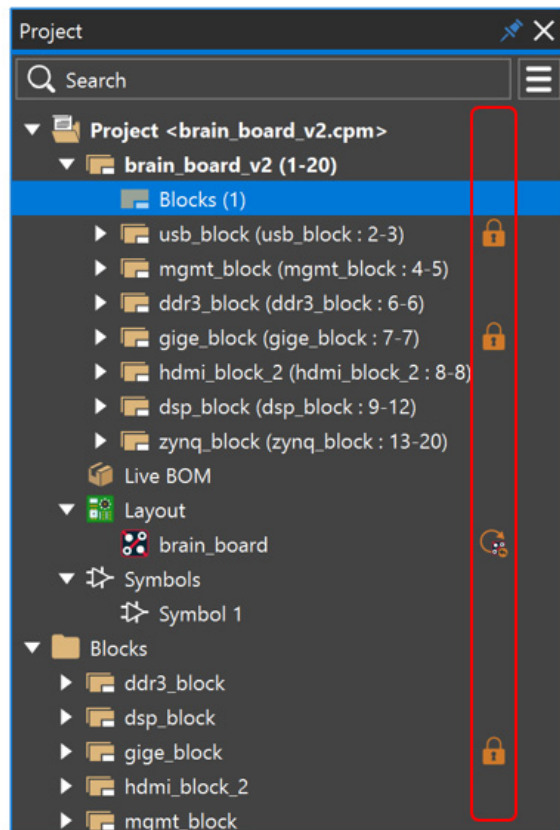
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- Project-specific URLs are now available for easy bookmarking. This simplifies opening a design or layout or sharing it.



Pulse Status Icons

The Pulse status icons, which provide notifications for team design, layout, and design reuse are now on the right side of the Project explorer pane. This enables easy viewing of the object type in the project.



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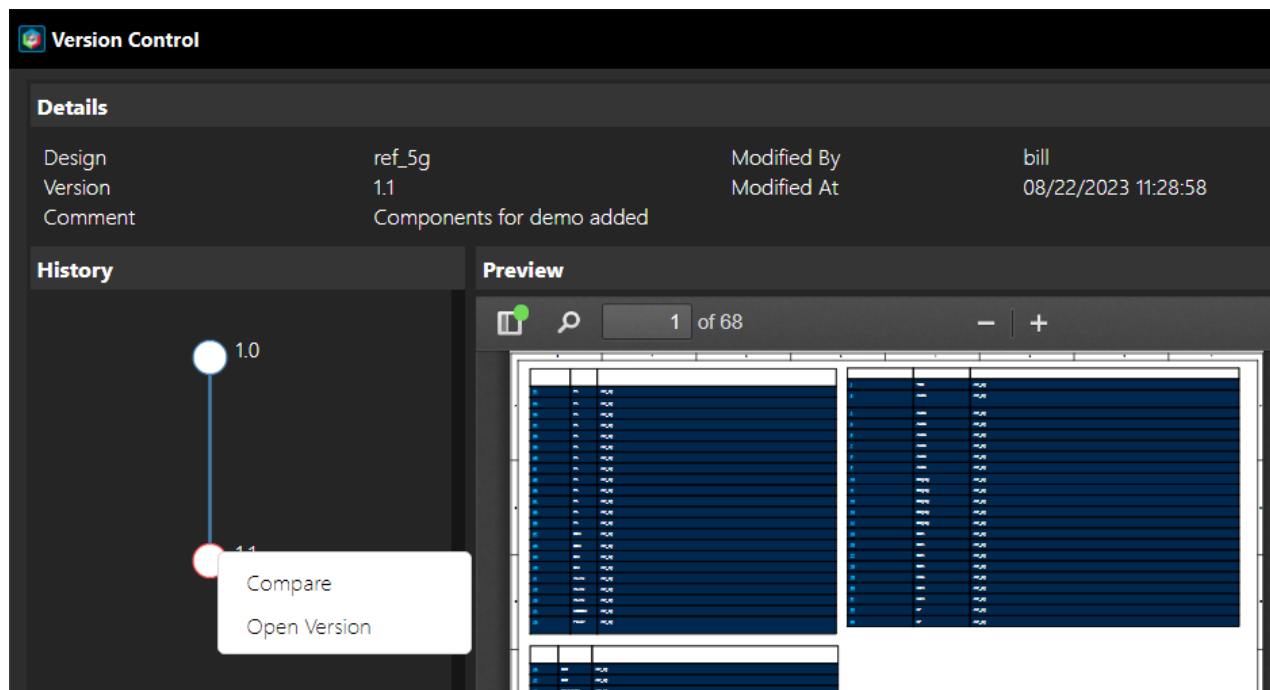
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Comparing Versions

To enable easier decision-making on whether to accept a team design update, you can now compare:

- Two design versions in *Version Control*.

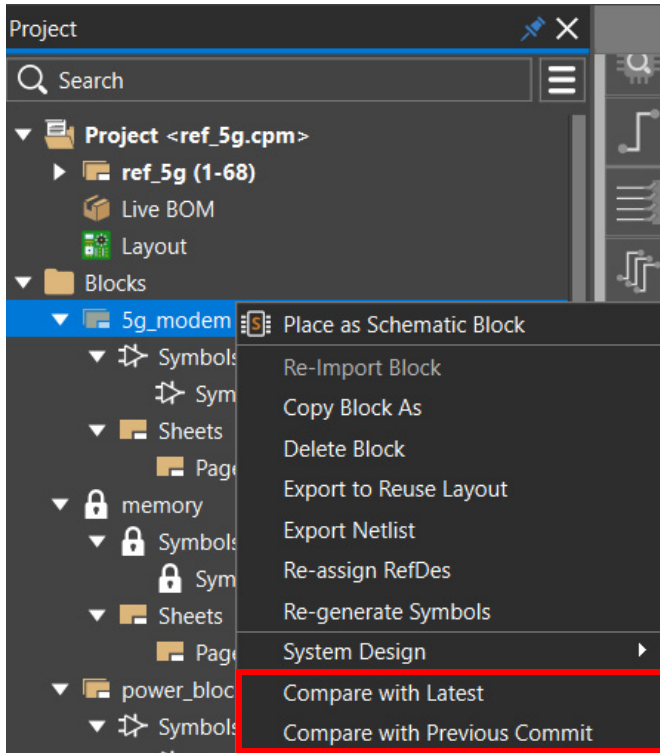
Right-click the first version and select *Compare* then click the second version.



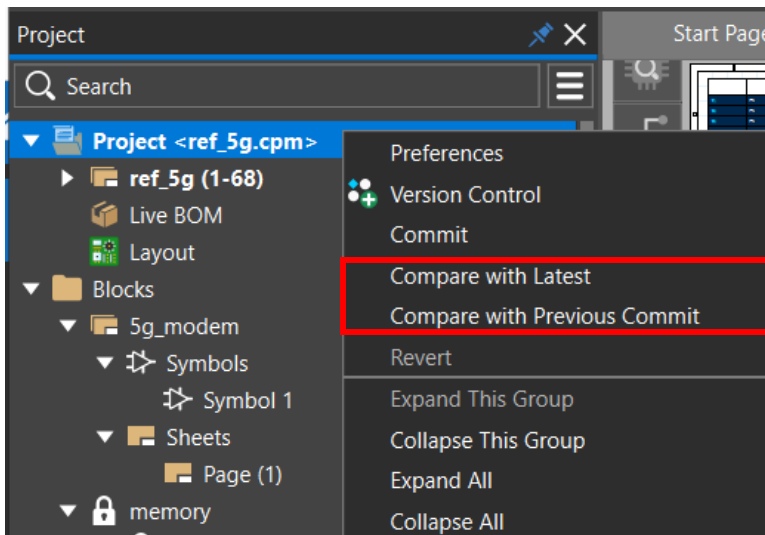
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- Individual blocks with the previous or latest commit versions.



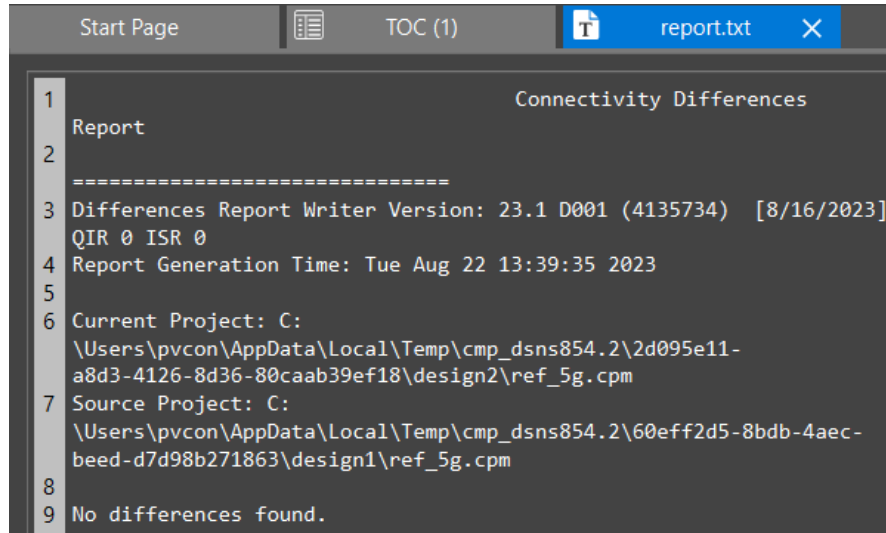
- The design with the latest version or any earlier version.



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The differences are displayed in a new tab.



The screenshot shows a software interface with a tab labeled 'report.txt'. The content of the report is as follows:

```
1                                     Connectivity Differences
2  Report
3  =====
4  Differences Report Writer Version: 23.1 D001 (4135734) [8/16/2023]
5  QIR 0 ISR 0
6  Report Generation Time: Tue Aug 22 13:39:35 2023
7  Current Project: C:
8  \Users\pvcon\AppData\Local\Temp\cmp_dsns854.2\2d095e11-
9  a8d3-4126-8d36-80caab39ef18\design2\ref_5g.cpm
10 Source Project: C:
11 \Users\pvcon\AppData\Local\Temp\cmp_dsns854.2\60eff2d5-8bdb-4aec-
12 beed-d7d98b271863\design1\ref_5g.cpm
13 No differences found.
```

Related Documentation

Comparing Designs

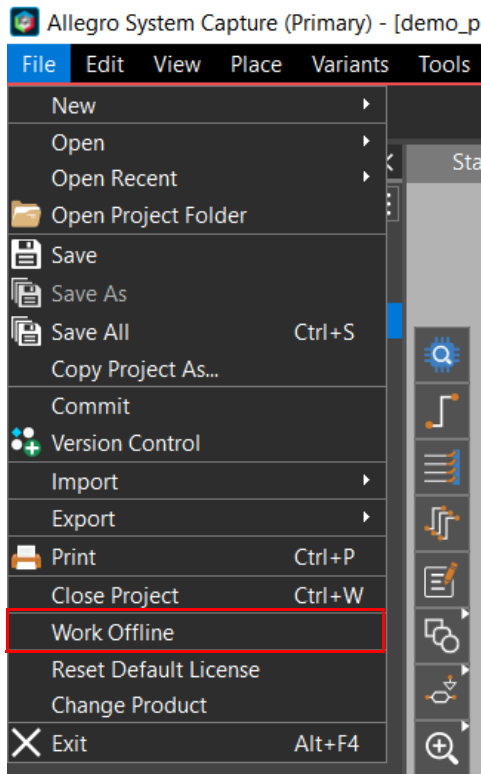
Working in the Offline Mode

If network connectivity is slow or the Pulse server is temporarily unavailable, users in a multi-user environment can disconnect from the Allegro Pulse server services and work in the

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offline mode. This ensures uninterrupted work using parts in the design cache. You can connect to the server again, when available.

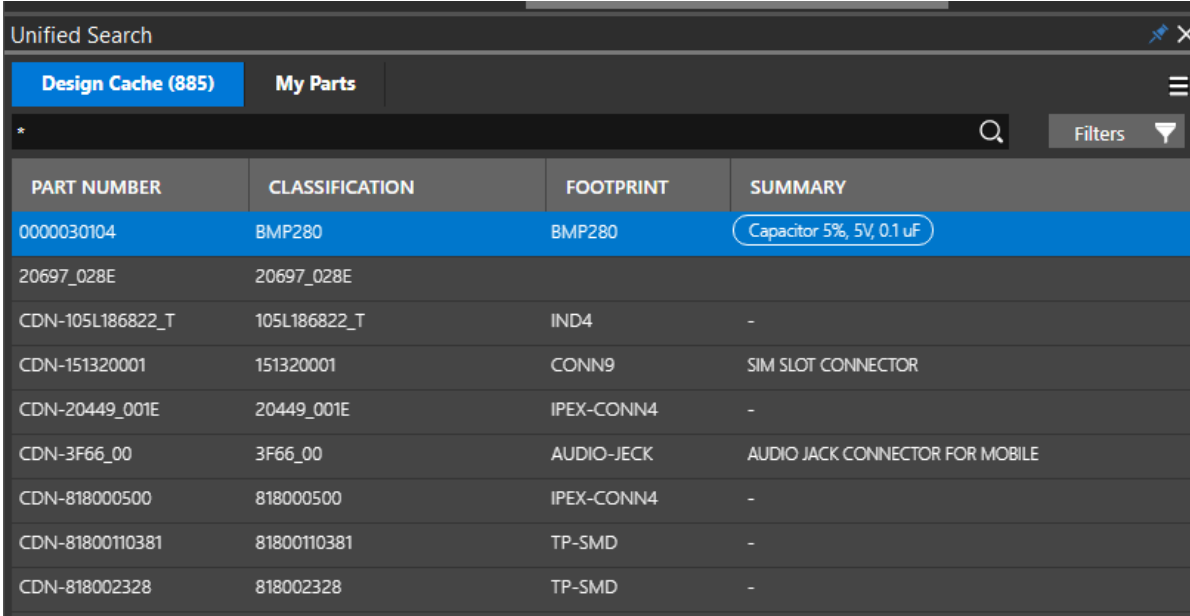


The offline mode is only supported for Allegro System Capture projects with Design Entry HDL or OrCAD Capture libraries.

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In the offline mode, you can only work on pages and blocks that you have explicitly locked before disconnecting from the Pulse server. Only parts cached in your local libraries are available in the offline mode in a *Design Cache* tab in Unified Search.



The screenshot shows the 'Unified Search' window with the 'Design Cache (885)' tab selected. Below the tabs is a search bar with a magnifying glass icon and a 'Filters' button with a funnel icon. The main area contains a table with four columns: PART NUMBER, CLASSIFICATION, FOOTPRINT, and SUMMARY. The first row is highlighted in blue and shows a capacitor part. Subsequent rows show various connectors and components.

PART NUMBER	CLASSIFICATION	FOOTPRINT	SUMMARY
0000030104	BMP280	BMP280	Capacitor 5%, 5V, 0.1 uF
20697_028E	20697_028E		
CDN-105L186822_T	105L186822_T	IND4	-
CDN-151320001	151320001	CONN9	SIM SLOT CONNECTOR
CDN-20449_001E	20449_001E	IPEX-CONN4	-
CDN-3F66_00	3F66_00	AUDIO-JECK	AUDIO JACK CONNECTOR FOR MOBILE
CDN-818000500	818000500	IPEX-CONN4	-
CDN-81800110381	81800110381	TP-SMD	-
CDN-818002328	818002328	TP-SMD	-

Related Documentation

[Disconnecting from Pulse Server](#)

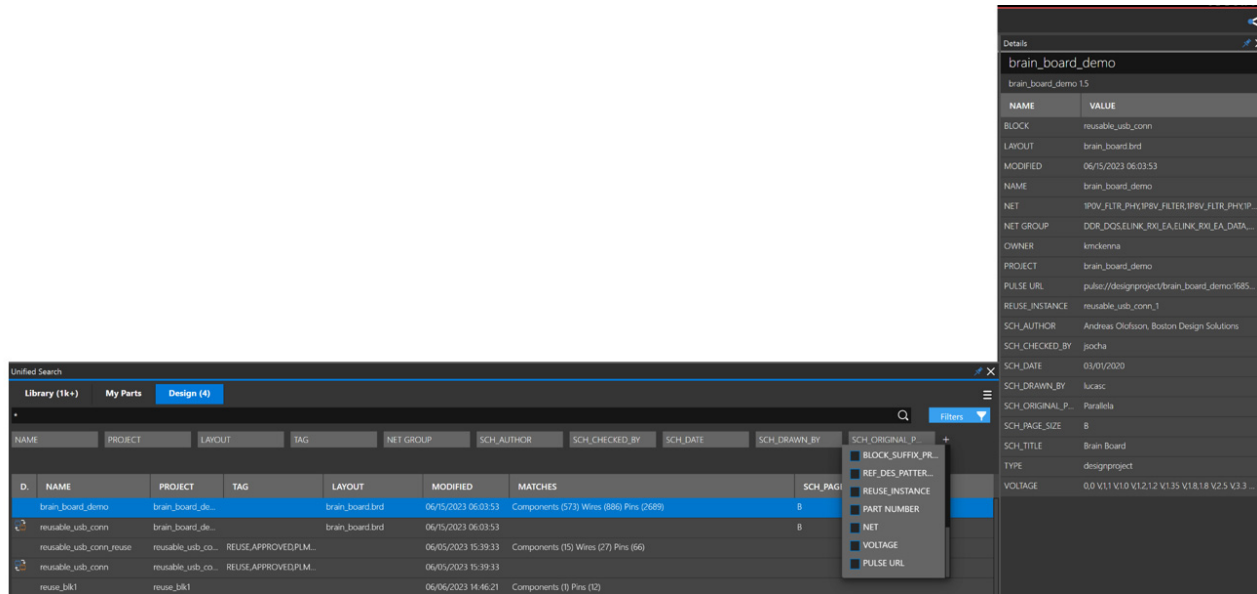
Design Search and Reuse

Unified Search supports searching data across designs and within design metadata. A new tab, *Design*, has been introduced for the multi-user environment. Search results display data

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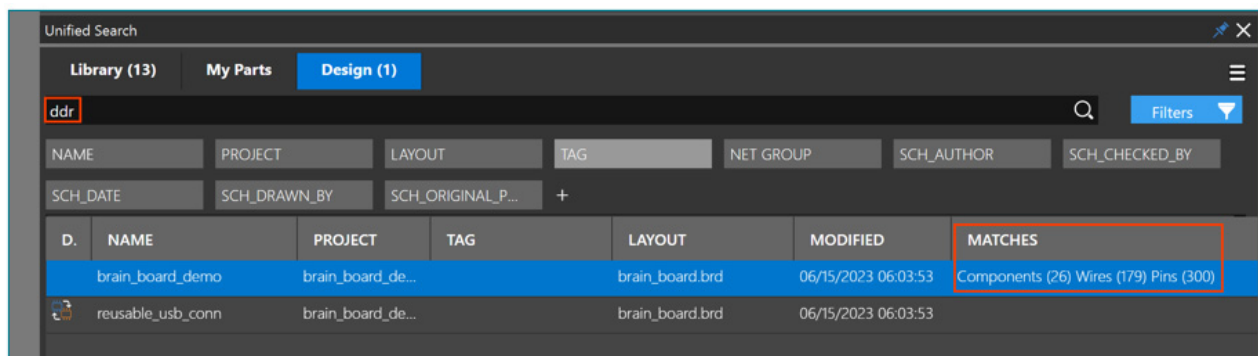
from the latest committed versions of the designs available on the shared server. You can also perform a parametric search of a design row in the *Details* pane.



Searchable data includes:

- Pulse metadata: Content in the project details form, version, the last modified date, design owner names, users with read-only access to a design, commit tags, comments, the Pulse URL
- Design information: Nets, NetGroups, buses, components, pins, voltages, CUSTOMVARs, variant names, block attributes, and layouts associated with schematics

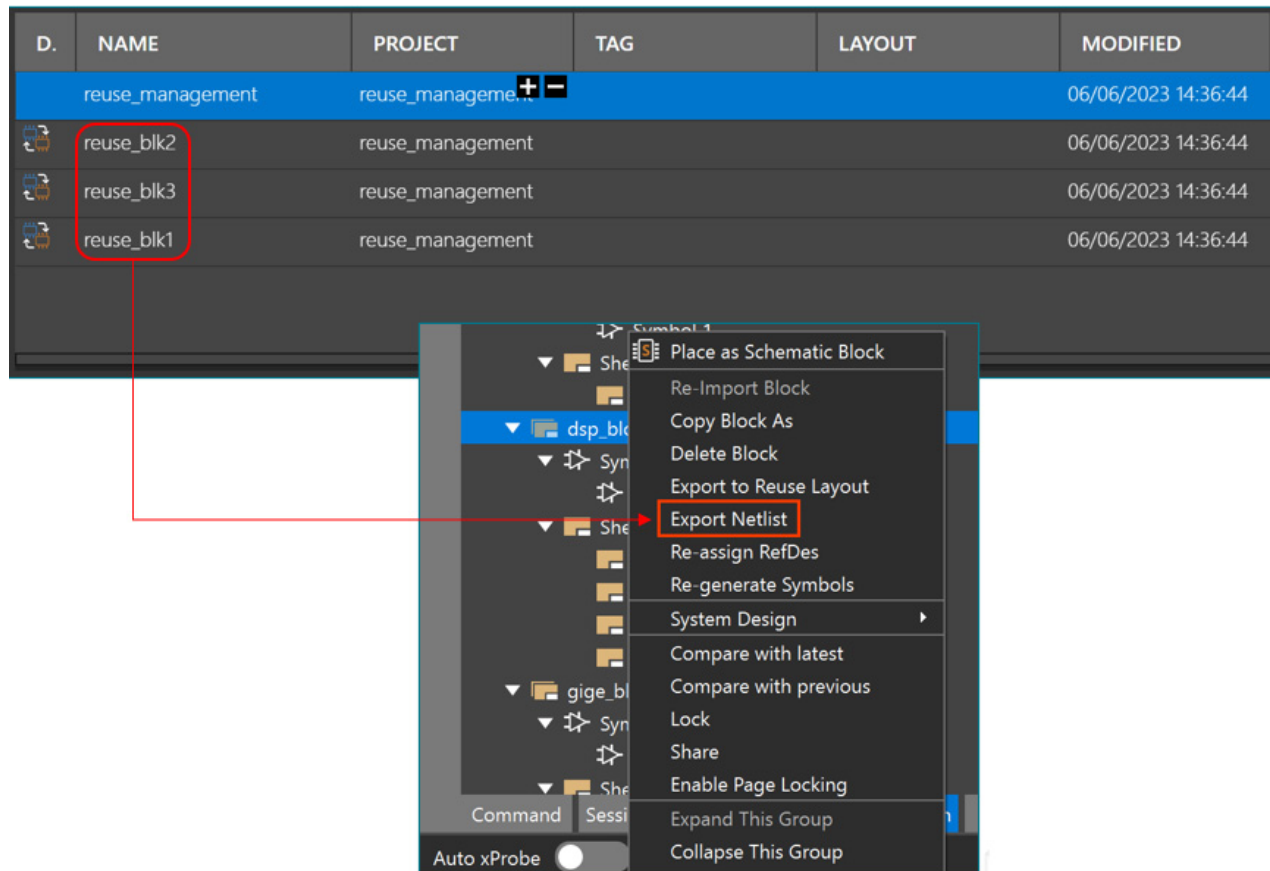
Search results are aggregated into matches.



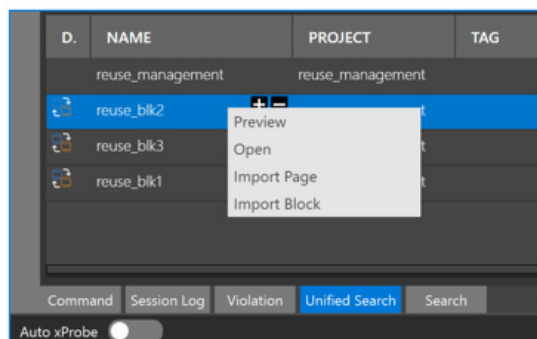
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To enable easy reuse of a design, blocks where netlists are exported for reuse are identified with an icon in the first column.



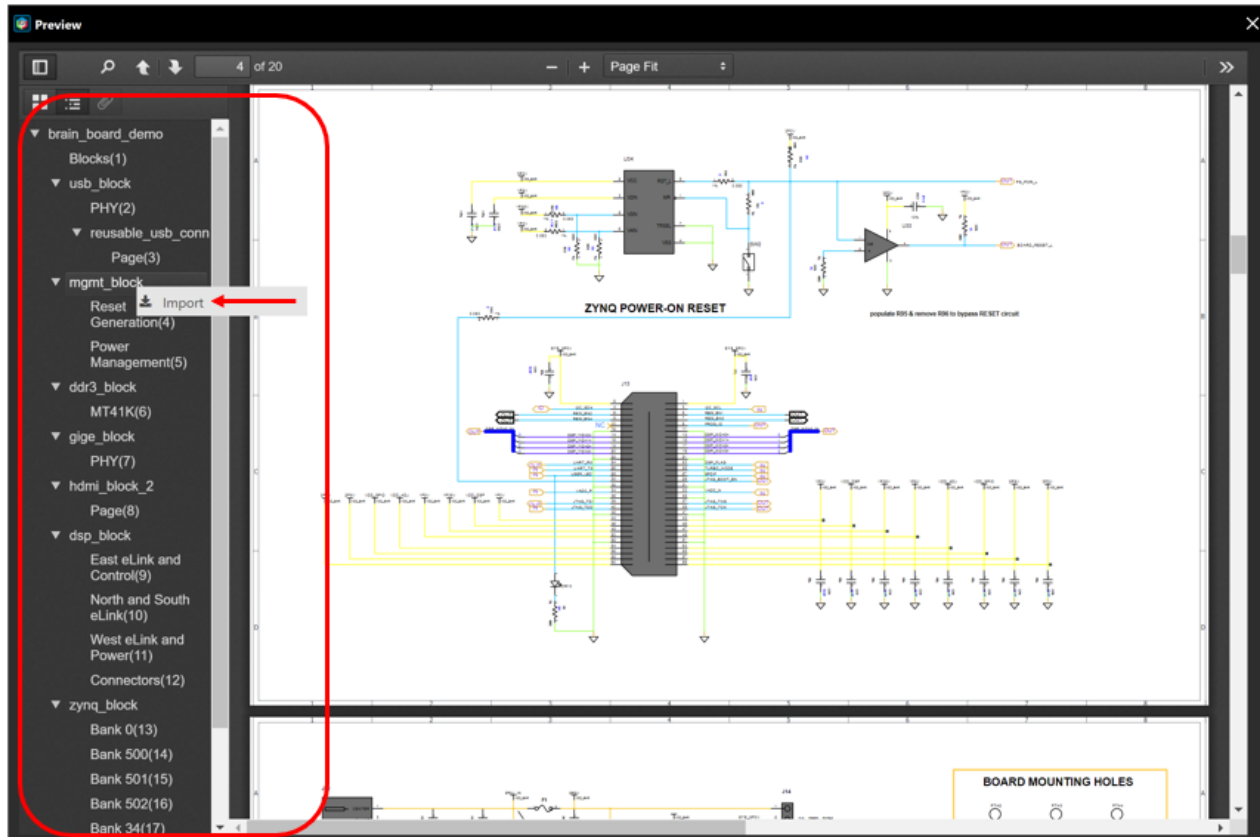
All the designs and projects that Pulse manages are displayed. You can only preview or import the designs that you have read or edit rights for.



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If you have at least read-only access to a design or project, you can open the *Preview*, which includes a PDF with hierarchical navigation. You can right-click an item in the hierarchy navigation and import a block or page.

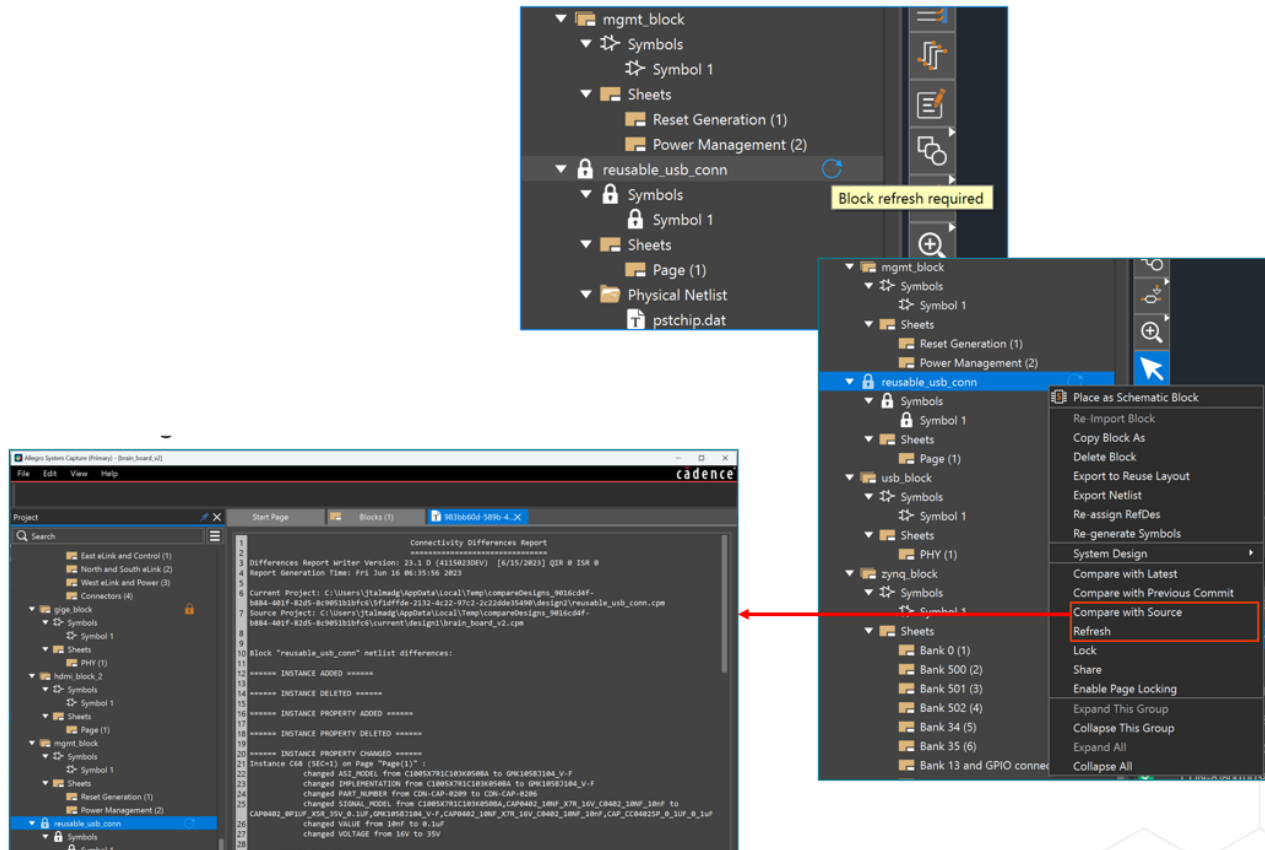


When a source design is committed and a change in it impacts a reused block, System Capture displays reuse update notifications for the blocks. You can view the differences to

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assess the impact and click *Refresh* to update your design with the latest version of the reused block.



Related Documentation

[Searching for Designs for Reuse](#)

AWR MWO-Allegro RF Design Flow

Important

This functionality is currently a limited-release feature that is available only on Windows. To enable the flow, create an environment variable `MWO_ALLEGRO` and set it to 1.

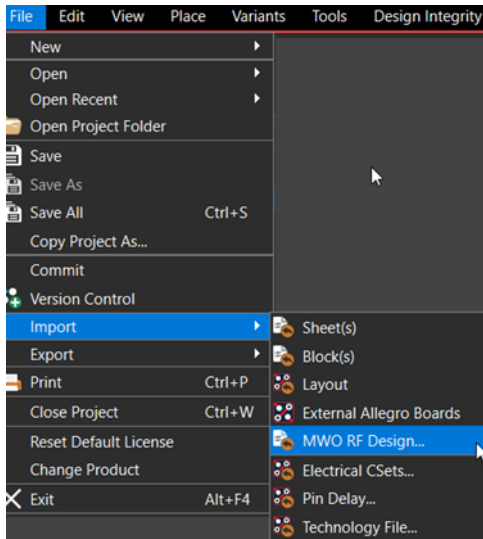
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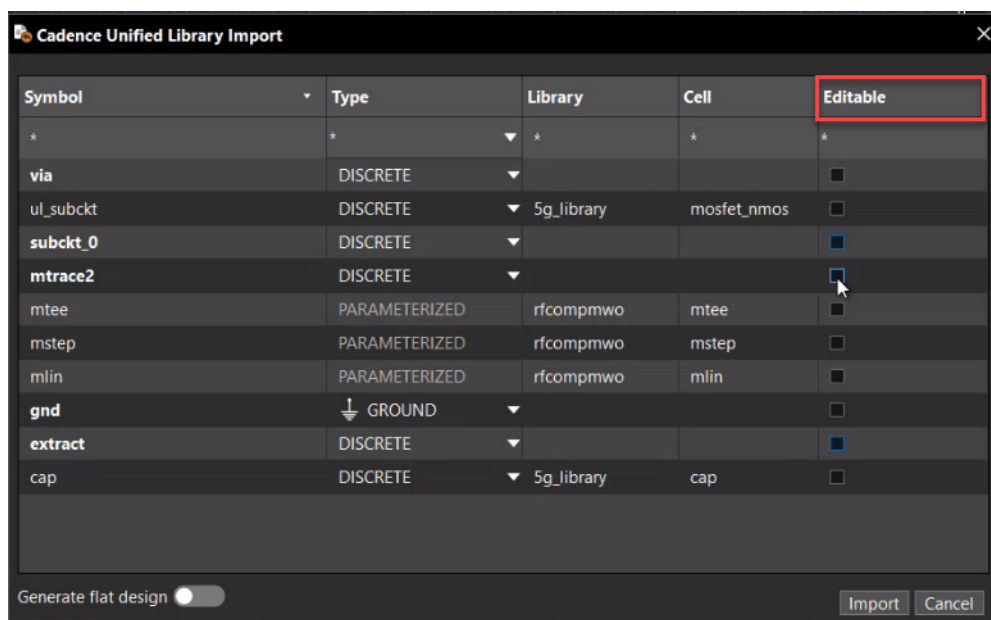
The integration of AWR® Microwave Office® (AWR MWO) and Allegro® PCB Design applications provides a way to create complex PCBs with RF design. Release 23.1 is compatible with the MWO 17.03 version.

Enhancements in this release include the following:

- Single container import from AWR MWO into System Capture.



- Multi-primitives in the scope of components in the RFIP transfer flow.



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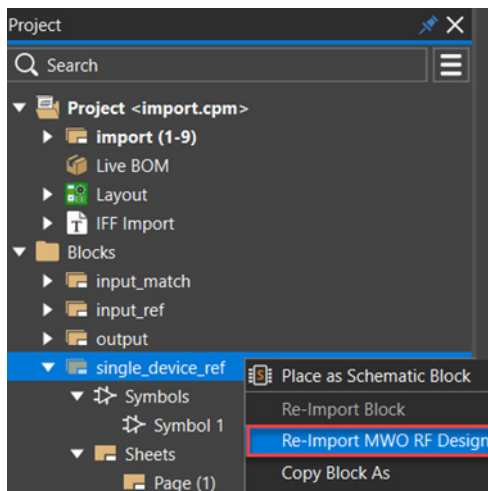
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- Unmapped components from a single container are no longer locked and can be replaced with components from an Allegro library. This enables the use of split, symmetric, and asymmetric parts.

- Import RFIP into existing non-RF schematic.

A single container can be imported into an existing schematic. This allows an RF block to be reused both in the logical design as well as in the layout.

- Re-import support is added for RF blocks.



Related Documentation

[Importing RF Design in Allegro System Capture](#)

Variant Editor Enhancements

This release includes many enhancements in Variant Editor. In addition to viewing the variants summary, you can now perform the following tasks:

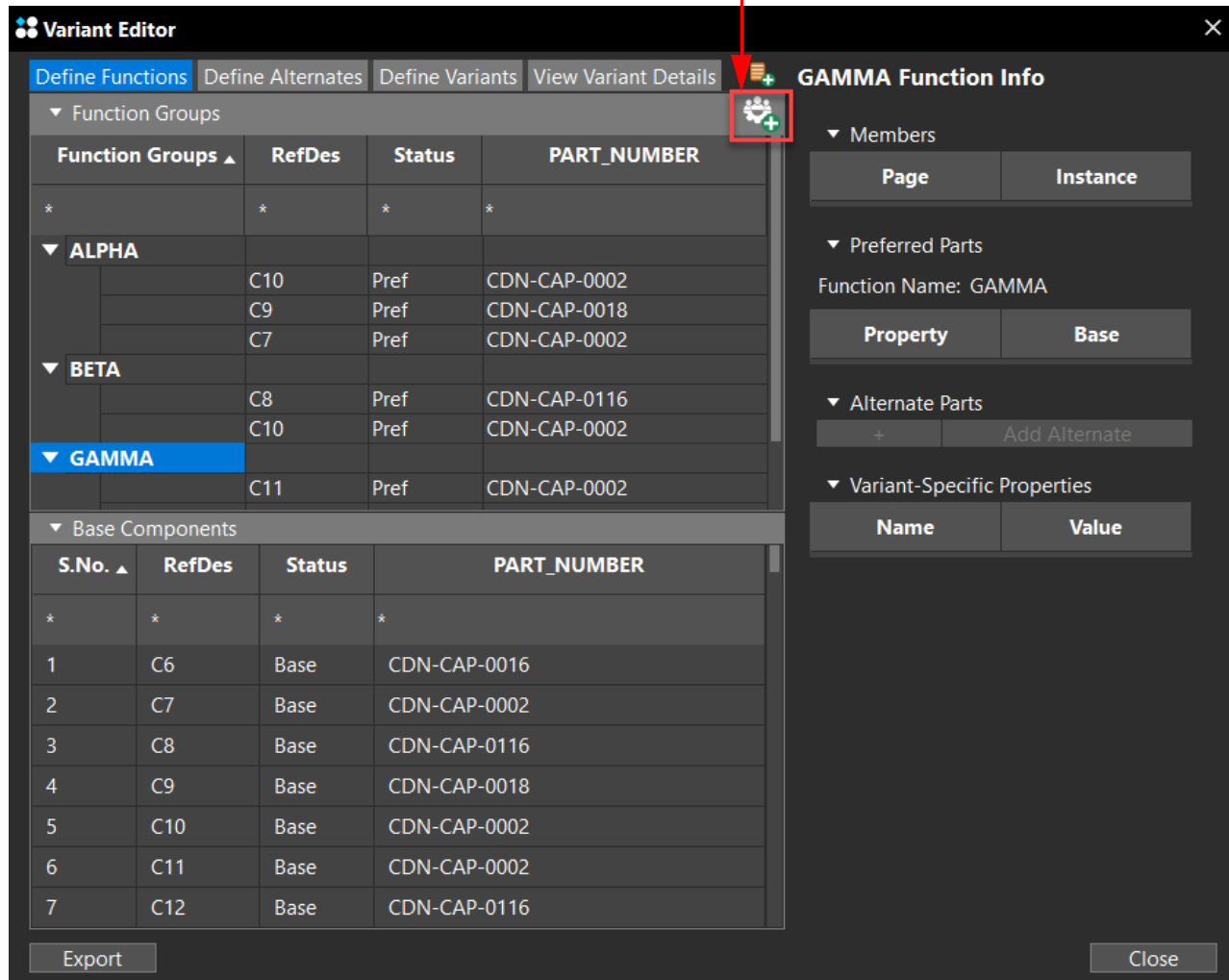
- Create function groups

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A function group is a collection of related components that can be added to a variant.

Create a Function Group



Variant Editor

Define Functions | Define Alternates | Define Variants | View Variant Details

Function Groups

Function Groups ▲	RefDes	Status	PART_NUMBER
*	*	*	*
▼ ALPHA			
	C10	Pref	CDN-CAP-0002
	C9	Pref	CDN-CAP-0018
	C7	Pref	CDN-CAP-0002
▼ BETA			
	C8	Pref	CDN-CAP-0116
	C10	Pref	CDN-CAP-0002
▼ GAMMA			
	C11	Pref	CDN-CAP-0002

Base Components

S.No. ▲	RefDes	Status	PART_NUMBER
*	*	*	*
1	C6	Base	CDN-CAP-0016
2	C7	Base	CDN-CAP-0002
3	C8	Base	CDN-CAP-0116
4	C9	Base	CDN-CAP-0018
5	C10	Base	CDN-CAP-0002
6	C11	Base	CDN-CAP-0002
7	C12	Base	CDN-CAP-0116

Export

GAMMA Function Info

Members

Page	Instance
------	----------

Preferred Parts

Function Name: GAMMA

Property	Base
----------	------

Alternate Parts

+ Add Alternate

Variant-Specific Properties

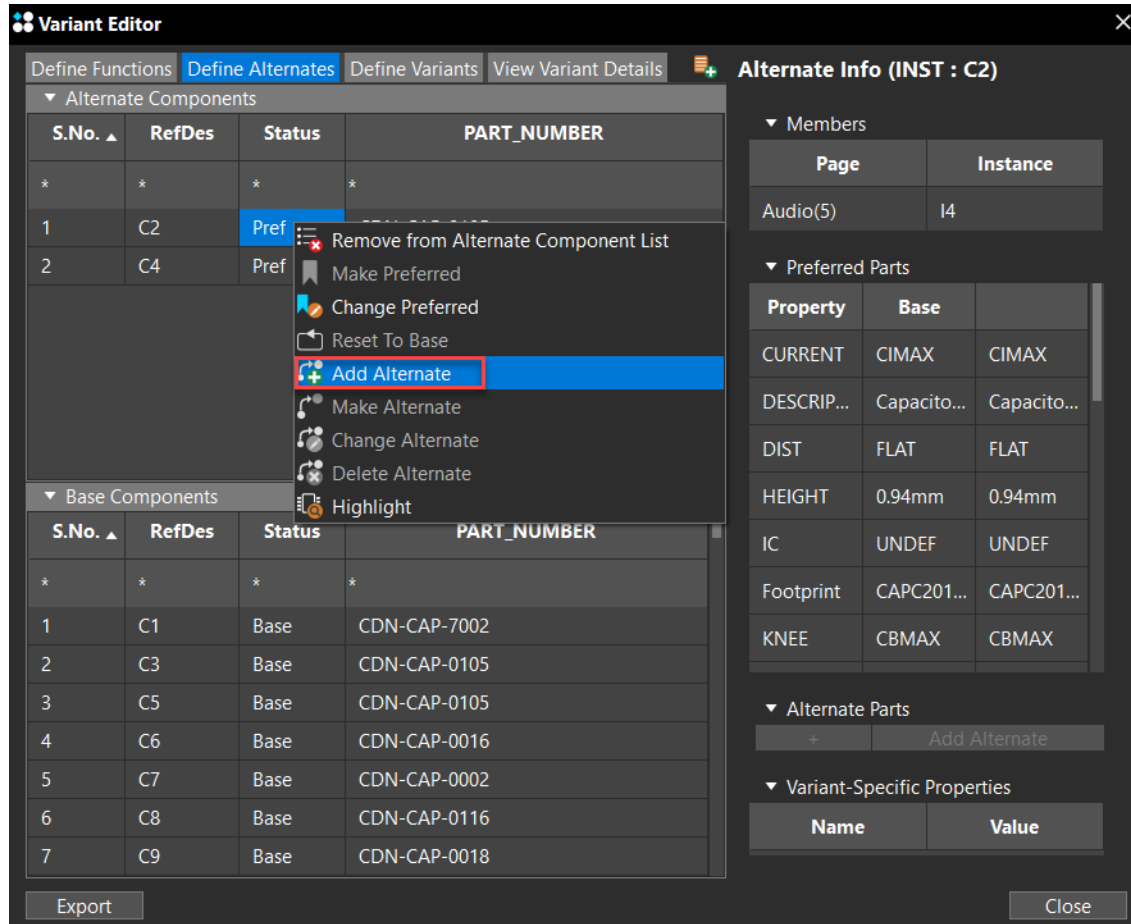
Name	Value
------	-------

Close

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■ Define alternate parts



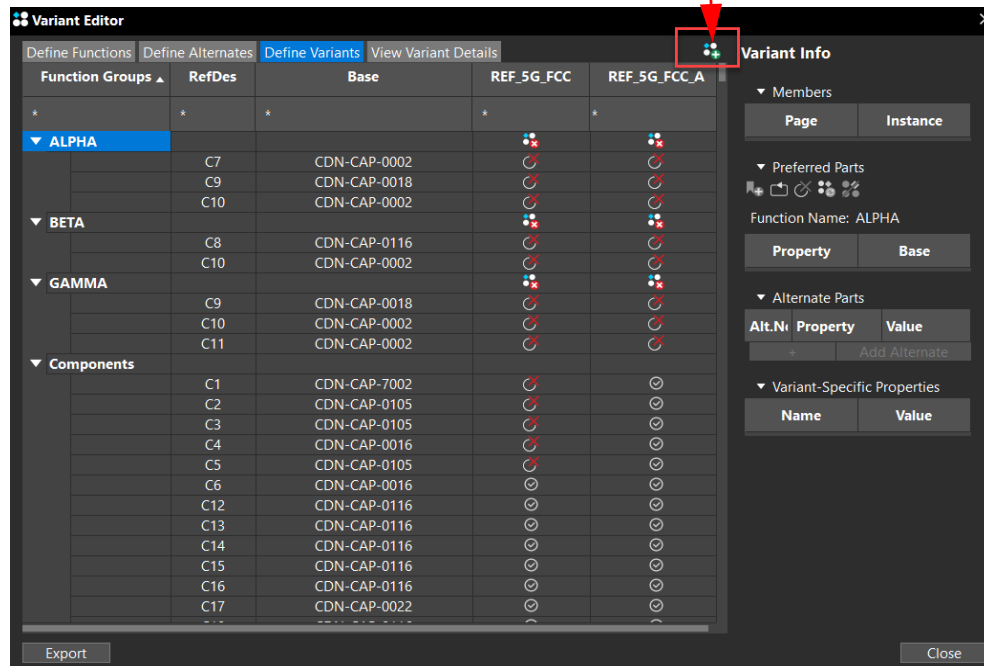
■ Define variants

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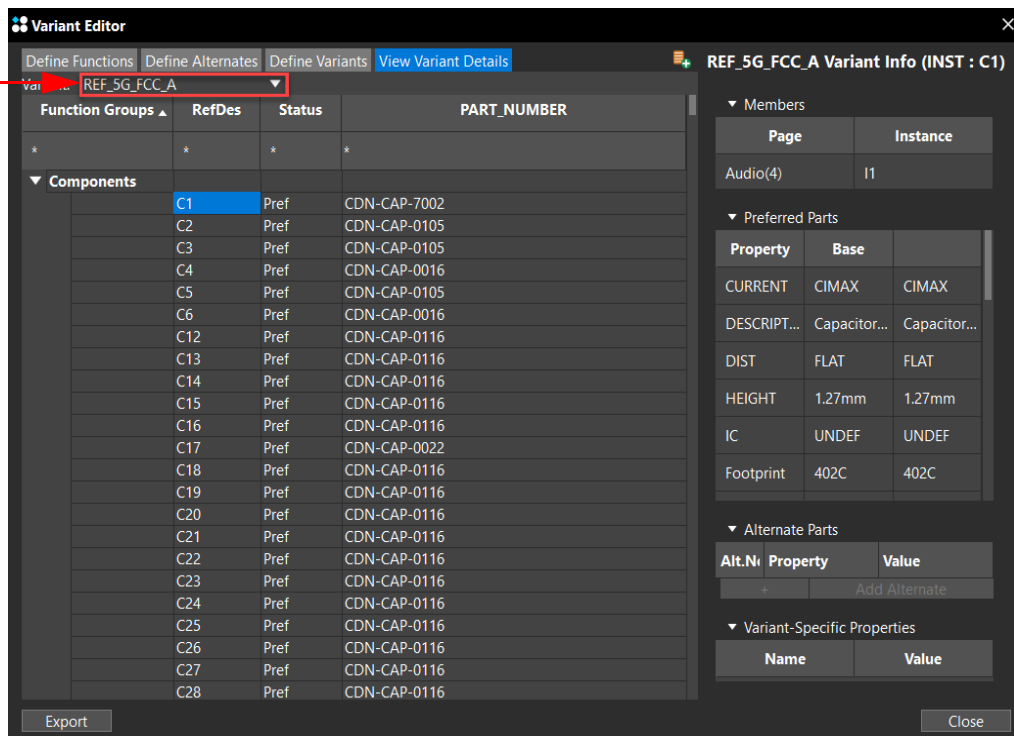
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You can switch to a variant to view its details.

Create a Variant



Choose a Variant



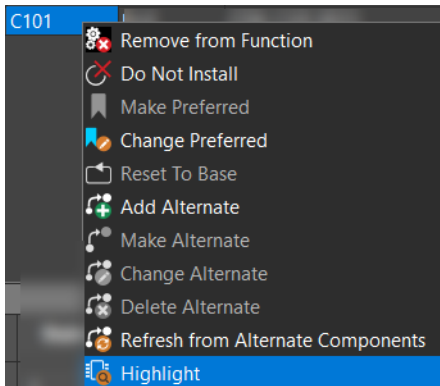
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- Column customization is introduced

For example, you can filter content based on a property value and then work on that reduced data set for adding to a variant or function.

- Cross-probing components between the schematic and Variant Editor is introduced.



When you select an instance on the canvas, it is highlighted in Variant Editor. You can then add it to a variant or function.

Related Documentation

[Variant Editor](#)

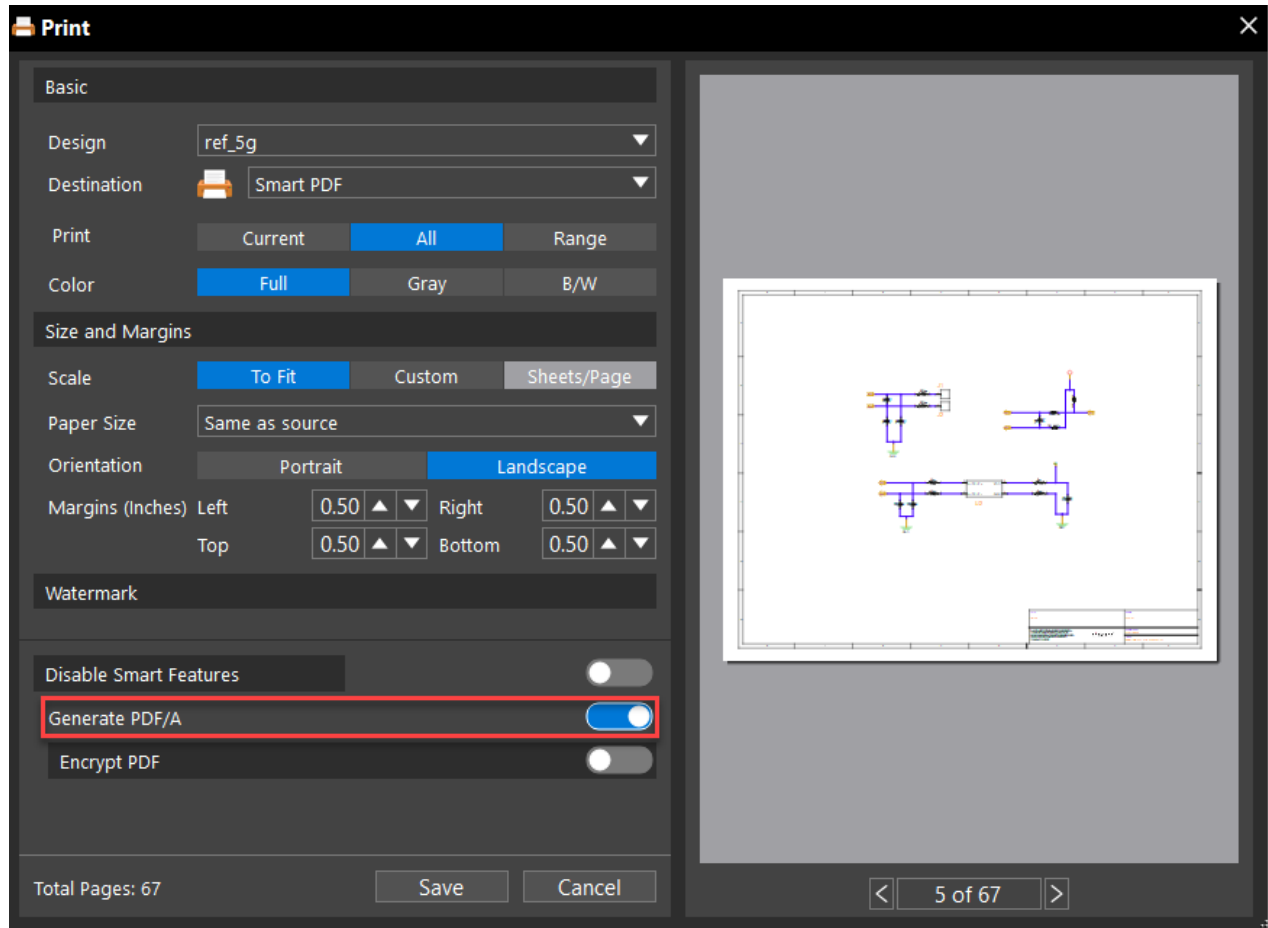
PDF/A Support Added

System Capture now includes support for the PDF/A format when creating a SmartPDF for a design. PDF/A is an industry-standard format that embeds all the required font information within the document, making it more suitable for long-term archiving. A document that

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complies with the PDF/A format can be opened on any machine, not just those with the fonts used to create the PDF file.



Related Documentation

[Create a PDF](#)

Updated Bill of Materials Dialog Box

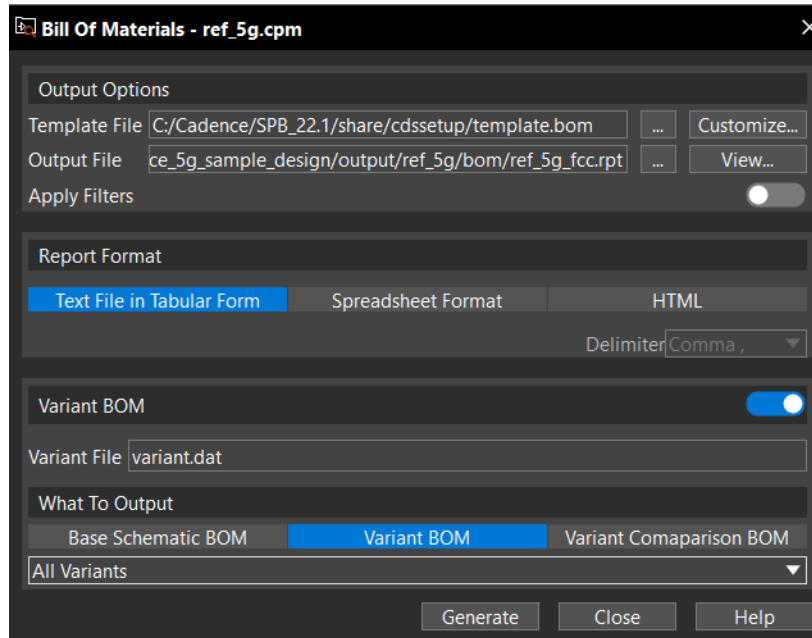
The *Bill of Materials* dialog box UI has been updated. Here are some of the changes:

- Radio buttons replaced by tiles

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- Check boxes replaced by toggle buttons



Related Documentation

- [Using Bill of Materials](#)
- [Bill of Materials Dialog Box](#)

Documentation Updates

Based on user feedback and to support new enhancements and features, the following guides have substantial changes:

- *Design Integrity and Analysis in Allegro® X System Capture*
- *Allegro® X System Capture User Guide*
- *Allegro System Capture Pulse User Guide*

This guide has been renamed to *Design Data Management in Allegro X System Capture*.

- *Allegro System Capture Tcl Commands*

The following commands have been added or updated:

- ☐ `checkIfSyscapUpdateAvailable`
- ☐ `crossprobeInTopology`
- ☐ `dbGetActivePageSpath`
- ☐ `dbGetSPathForActiveTab`
- ☐ `dbSelectObjectById`
- ☐ `downloadAttachment`
- ☐ `dumpMenus`
- ☐ `exportDashboardAsCsv`
- ☐ `exportDashboardAsPdf`
- ☐ `extractPTree`
- ☐ `importBlock`
- ☐ `MTBFPreferences`
- ☐ `newProject`
- ☐ `print`
- ☐ `PTreePreferences`
- ☐ `runMTBFAnalysis`
- ☐ `runThermalAnalysis`

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- ❑ variantEditor
- ❑ viewThermalMap