Jai Luthra

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EDUCATION

NOVEMBER 2018 Graduating MAY 2019	**IIIT-Delhi** 7th Semester, B.Tech - **Computer Science and Engineering** GPA: 8.09/10.0
APRIL 2015	**Bal Bharati Public School, Pitampura**, New Delhi All-India Senior Secondary Certificate Examination (CBSE), Class XII SCORE: 94.8%

INTERNSHIPS

MAY - JULY 2018	**Amazon Development Center, Bengaluru** *Software Development Engineer - Intern* • Worked with the **Automated Advertising** team on a Spring-MVC web project. • Successfully converted the internship into a full-time offer.

MAY - AUGUST 2016	**Google Summer of Code - FFmpeg**
	Student Contributor (Project Page)
	• Wrote an encoder for Dolby TrueHD, a lossless audio compression codec.
	• Updated and merged the MLP (Meridian Lossless Packing) encoder,
	  originally written by Ramiro Polla in 2009.
	  Mentor: Rostislav Pehlivanov (atomnuker)

ACHIEVEMENTS

AUGUST 2014	**Procon Junior** *Rank 4* • Procon Jr. is a competitive programming contest for school students, organized by IIIT-D. • Top 10 students get bonus marks during IIIT-D admission counselling through JEE.
DECEMBER 2012, 2013	**Google Code-In - KDE, NetBSD** *Completed 6 challenges* • GCI is an yearly contest organized for school students to promote open source culture. • KDE - Bug fixes for Trojita, KStars and Amarok. • NetBSD - Documentation on how-to dual boot with NetBSD.

SKILLS

SOFTWARE:	C, Python, C++, Java, Git, Linux, Kaldi, Django, Spring
PLATFORMS:	Arduino/AVR, x86 asm, ARM asm
HARDWARE:	Embedded C, Verilog

PROJECTS

JULY 2017	**kaldi-asr**	Making speech-to-text easily accessible using Kaldi.
NOVEMBER 2016	**armsimc** & **armcpu**	An ARM simulator in C, and a baby ARM CPU in Verilog. (Course project)
APRIL 2016	**Mapbots**	Mapped rooms using a ring of ultrasonic sensors mounted on a small robot. Map-generation implemented using Hough Transform and Kalman Filters. (Course project)
2016	**Kompress**	Text compression tool written in C, based on Huffman Coding.

AREAS OF INTEREST

Linear Algebra	Signals and Systems	Communication Systems	Embedded Systems
Digital Signal Processing	Operating Systems	Algorithm Design	Computer Architecture