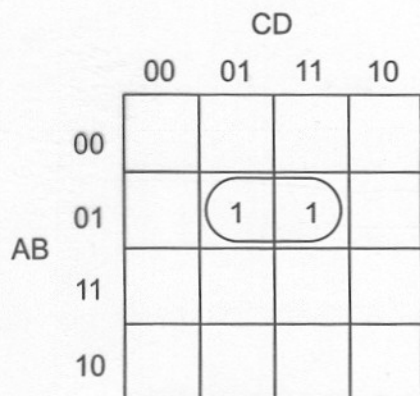
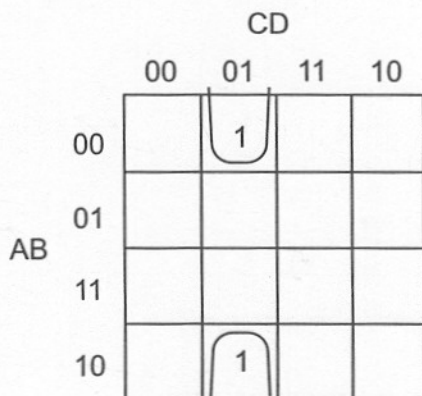


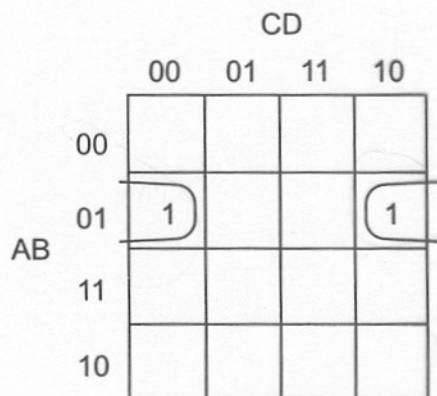
**Figura A.34.** Ejemplo de un visualizador LED de siete segmentos.



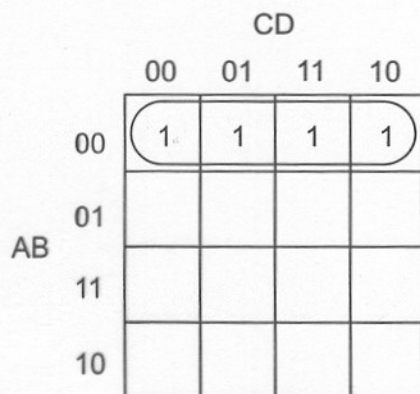
(a)  $\bar{A}BD$



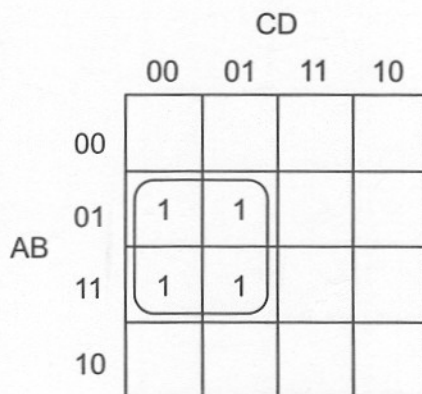
(b)  $\bar{B}CD$



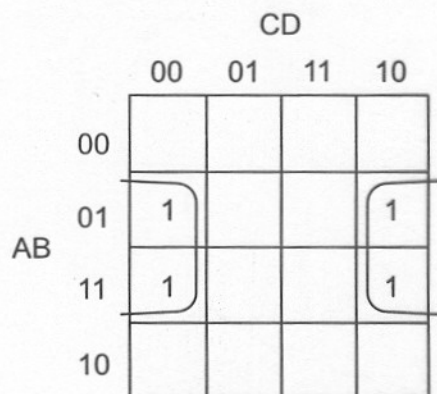
(c)  $\bar{A}BD$



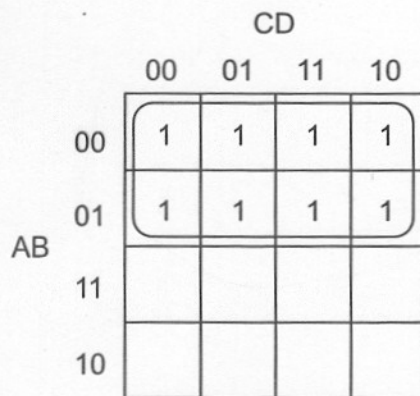
(d)  $\bar{A}\bar{B}$



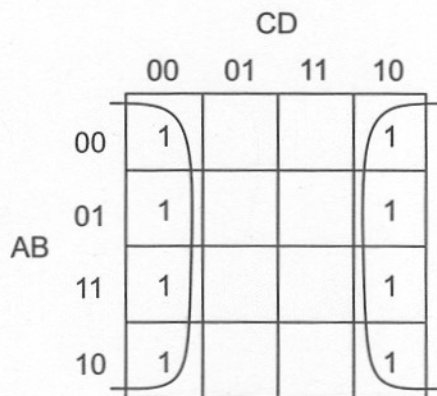
(e)  $B\bar{C}$



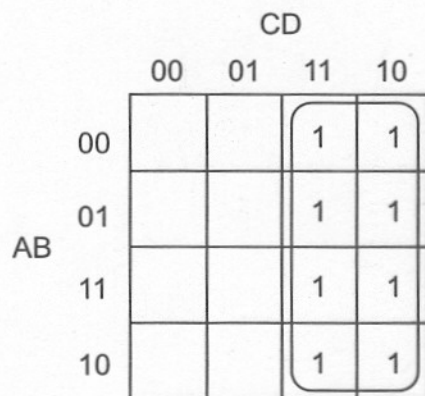
(f)  $B\bar{D}$



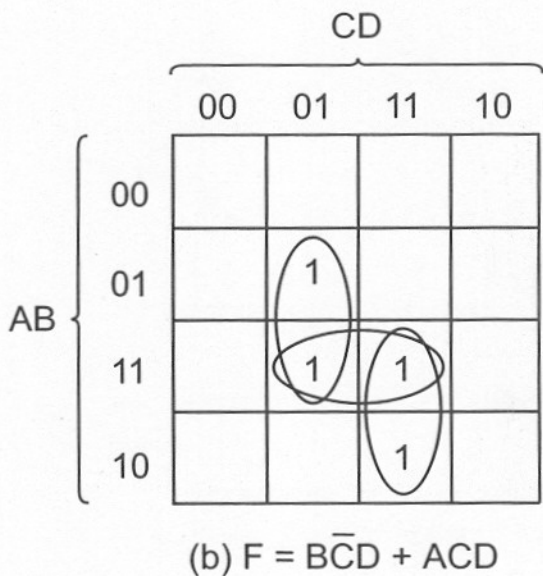
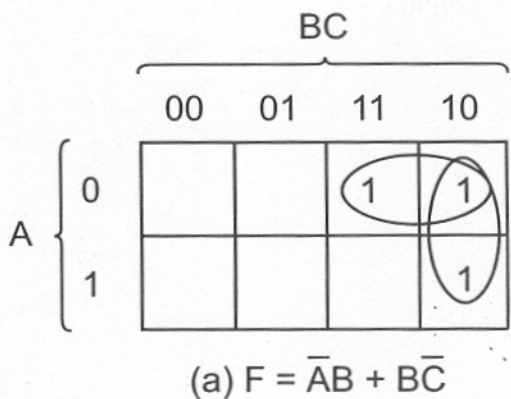
(g)  $\bar{A}$



(h)  $\bar{D}$



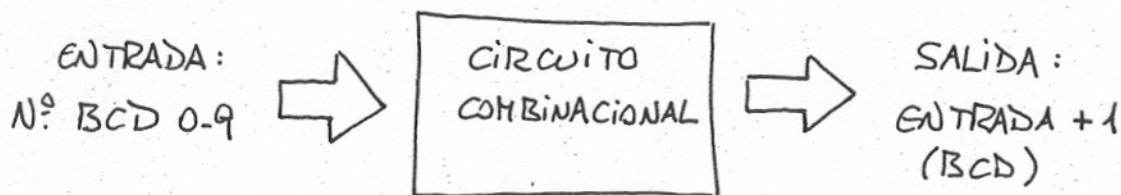
(i)  $C$



**Figura A.9.** Grupos solapados.

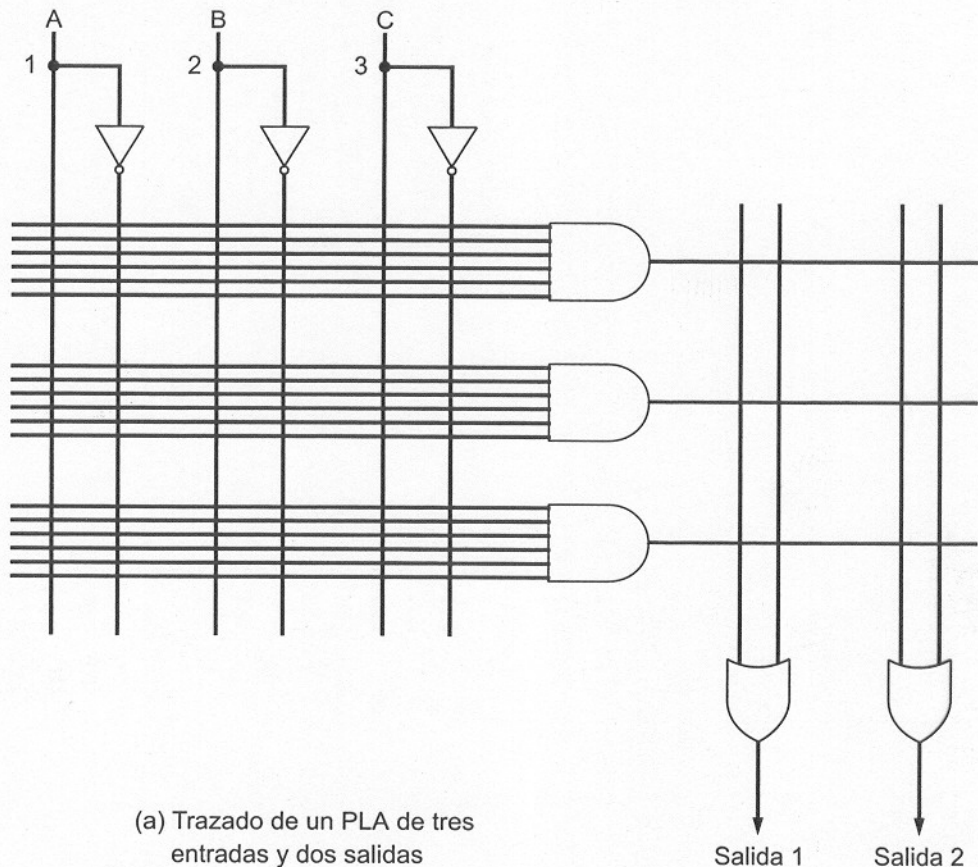
Tabla A.4. Tabla verdad de un contador digital de un dígito

Número	Entrada				Número	Salida			
	A	B	C	D		W	X	Y	Z
0	0	0	0	0	1	0	0	0	1
1	0	0	0	1	2	0	0	1	0
2	0	0	1	0	3	0	0	1	1
3	0	0	1	1	4	0	1	0	0
4	0	1	0	0	5	0	1	0	1
5	0	1	0	1	6	0	1	1	0
6	0	1	1	0	7	0	1	1	1
7	0	1	1	1	8	1	0	0	0
8	1	0	0	0	9	1	0	0	1
9	1	0	0	1	0	0	0	0	0
Indiferencias	1	0	1	0		d	d	d	d
	1	0	1	1		d	d	d	d
	1	1	0	0		d	d	d	d
	1	1	0	1		d	d	d	d
	1	1	1	0		d	d	d	d
	1	1	1	1		d	d	d	d

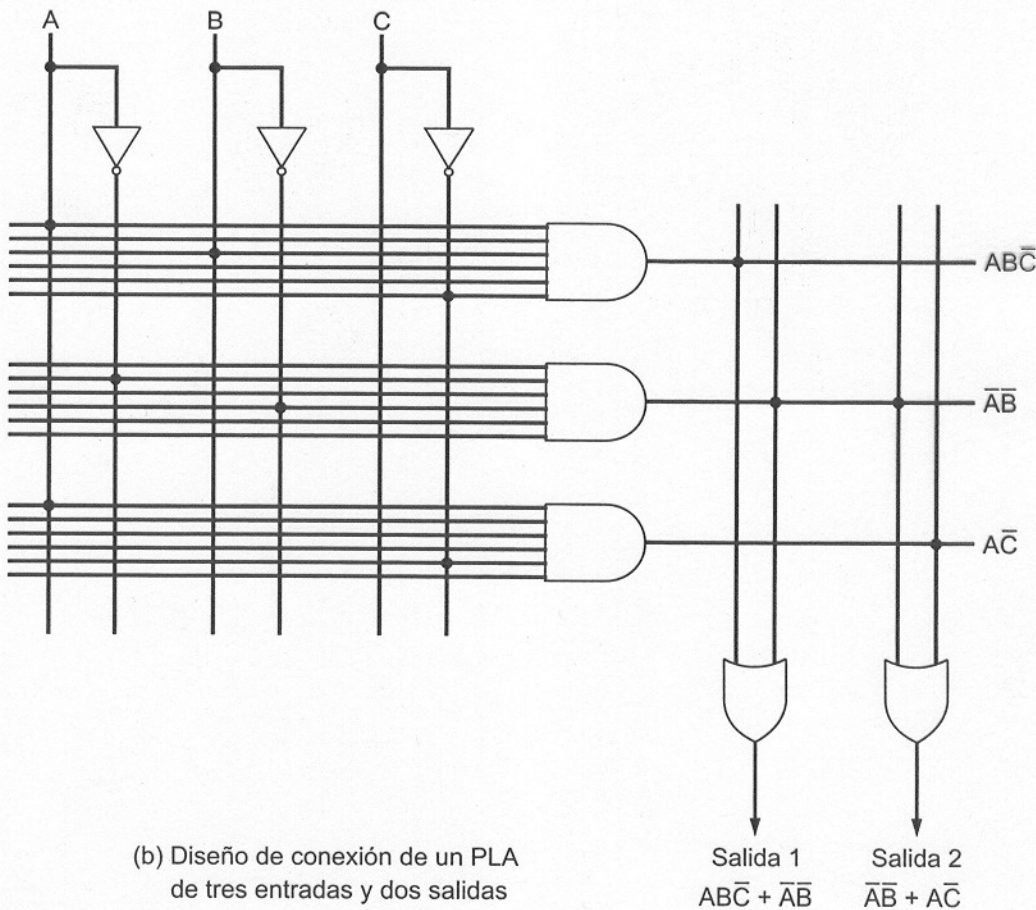


- CÓDIGO BCD (Decimal Codificado en Binario):

N° decimal	BCD				← PESOS
	8	4	2	1	
0	0	0	0	0	
1	0	0	0	1	
2	0	0	1	0	
3	0	0	1	1	
4	0	1	0	0	
5	0	1	0	1	
6	0	1	1	0	
7	0	1	1	1	
8	1	0	0	0	
9	1	0	0	1	



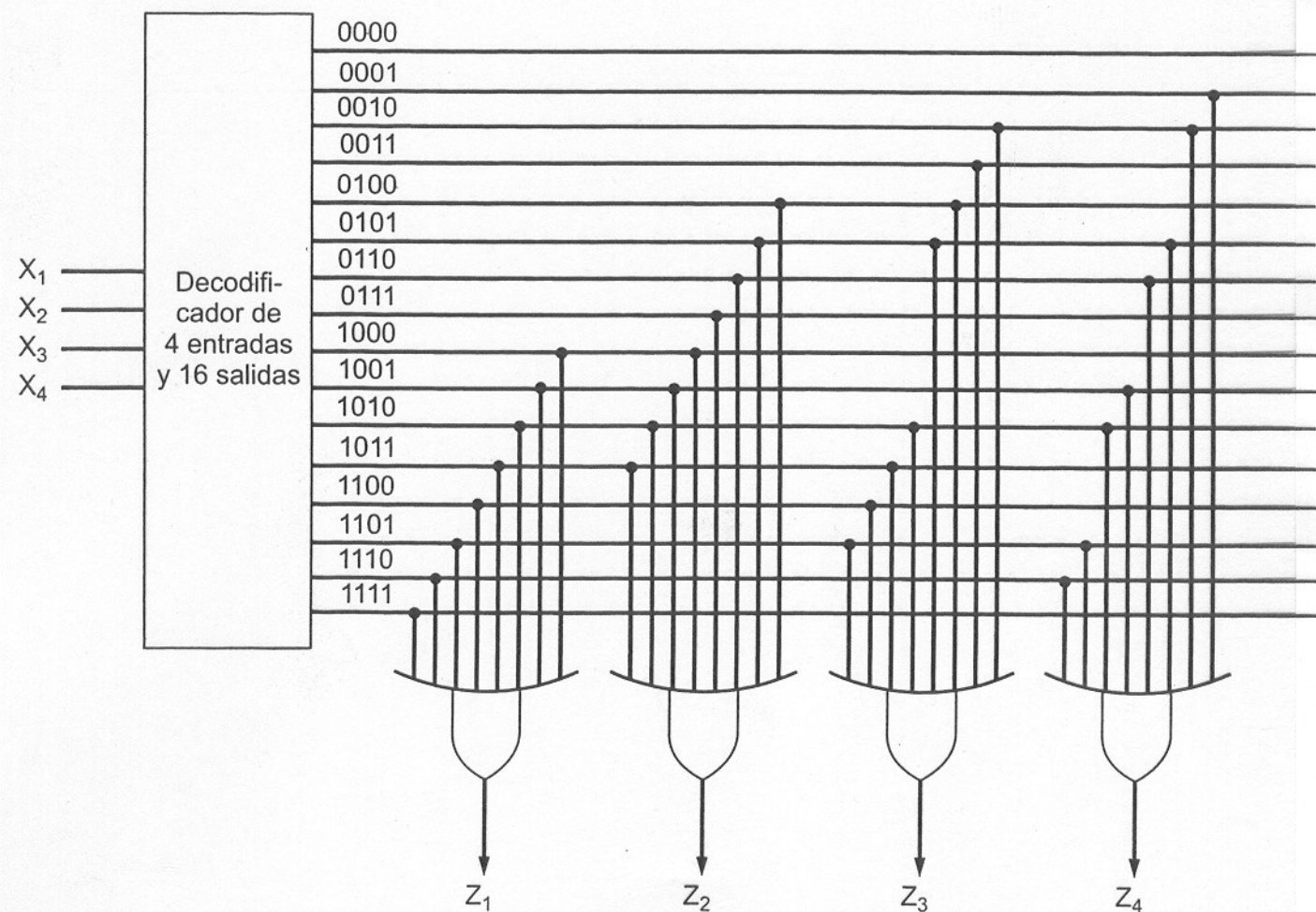
(a) Trazado de un PLA de tres entradas y dos salidas



**Figura A.19.** Ejemplo de un conjunto lógico programable.

**Table A.8 Truth Table for a ROM**

Input				Output			
0	0	0	0	0	0	0	0
0	0	0	1	0	0	0	1
0	0	1	0	0	0	1	1
0	0	1	1	0	0	1	0
0	1	0	0	0	1	1	0
0	1	0	1	0	1	1	1
0	1	1	0	0	1	0	1
0	1	1	1	0	1	0	0
1	0	0	0	1	1	0	0
1	0	0	1	1	1	0	1
1	0	1	0	1	1	1	1
1	0	1	1	1	1	1	0
1	1	0	0	1	0	1	0
1	1	0	1	1	0	1	1
1	1	1	0	1	0	0	1
1	1	1	1	1	0	0	0



**Figura A.20.** ROM de 64 bit.



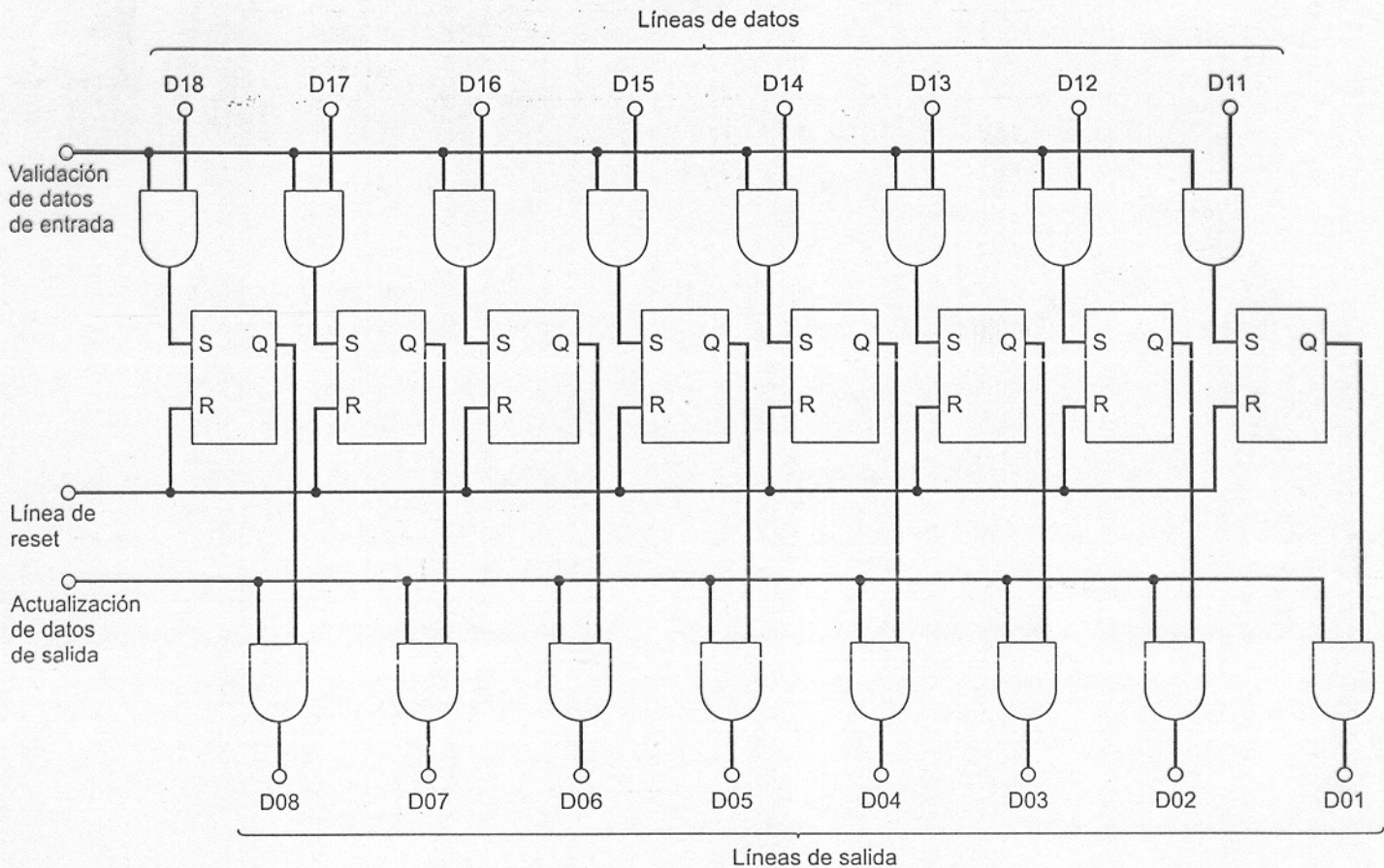
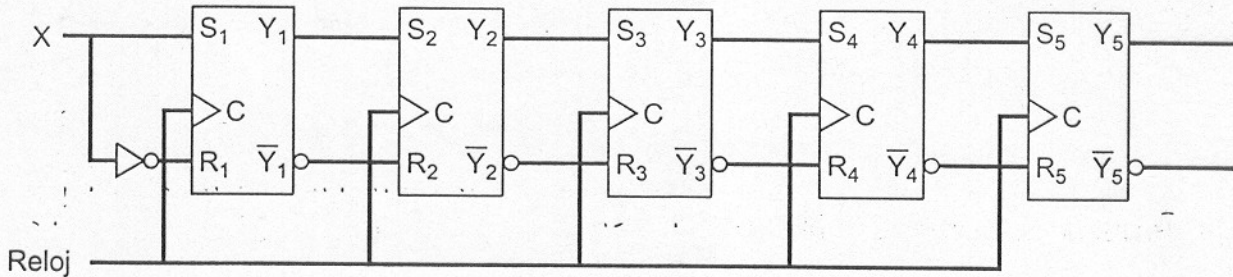
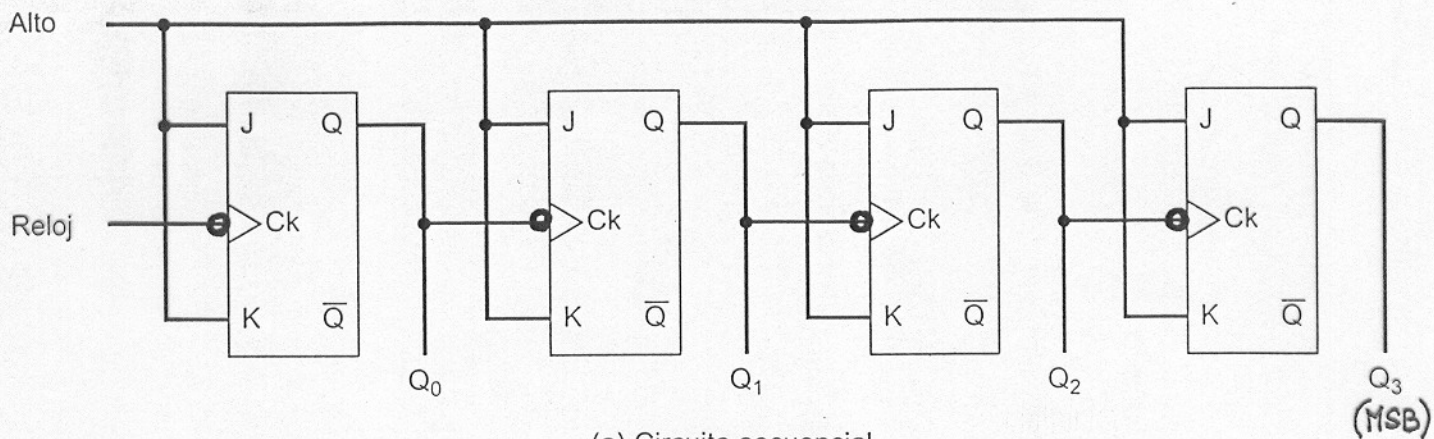


Figura A.30. Registro paralelo de 8 bits.

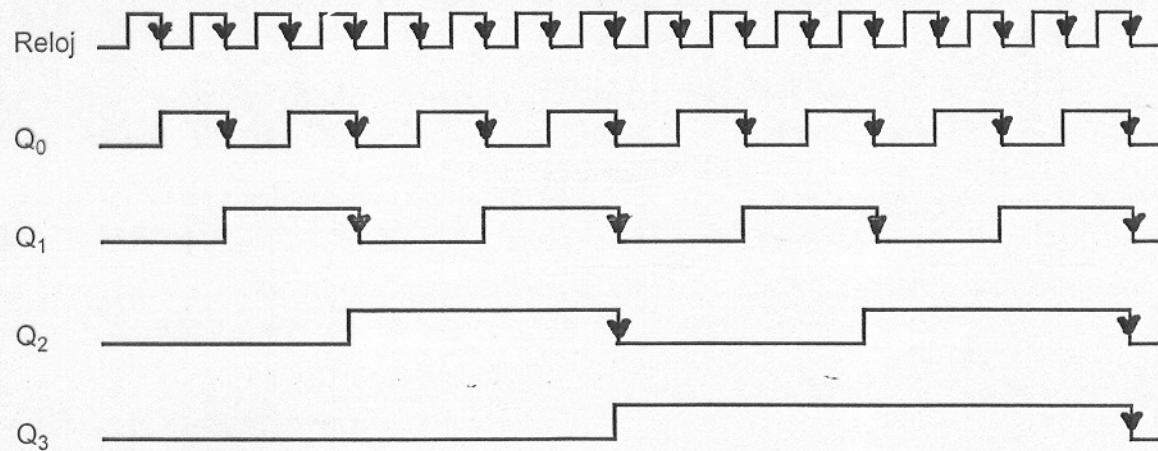




**Figura A.31.** Registro de desplazamiento de 5 bits.



(a) Circuito secuencial



(b) Diagrama de tiempo

**Figura A.32.** Contador ondulado.