COM	COURSE NO.	COURSE TITLE		REC P		S E C	INSTRUCTOR-IN-CHARGE/ Instructor	ROOM	DAYS	HOURS	COMMON HOUR D H R	COMPRE DATE & SESSION
							Shrimathi H P					
						3	STP Srinivas	J114	W	4 5		
							Amar Kumar Verma					
							Manoj Samal					
						4	STP Srinivas	J114	F	4 5		
							Amar Kumar Verma					
							Manoj Samal					
		Tutorial				1	Sudha Radhika	I114	F	1		
						2	Sudha Radhika	l114	W	1		
						3	Sudha Radhika	l114	Т	7		
1902	EEE F348	FPGA BASED SYSTEM DESIGN LAB	-	2	2	1	SAYAN KANUNGO	l124	MF	4 5		
							G Sahith					
						2	Sayan Kanungo	l124	T Th	9 10		
							Simhadri Hariprasad					
1731	EEE F366	LABORATORY PROJECT	-	-	3	1	PARIKSHIT PARSHURAM S.					
1742	EEE F367	LABORATORY PROJECT	-	-	3	1	PARIKSHIT PARSHURAM S.					
1691	EEE F376	DESIGN PROJECT	-	-	3	1	KARUMBAIAH CHAPPANDA					
1758	EEE F377	DESIGN PROJECT	-	-	3	1	KARUMBAIAH CHAPPANDA					
2288	EEE F411	INTERNET OF THINGS	3	1	4	1	SANDEEP KUMAR	F101	MWF	9		11/05 FN
		Practical				1	Sandeep Kumar	J105	М	4 5		
							Madhusudan B Kulkarni					
						2	Sandeep Kumar	J105	F	6 7		
							Pavankumar Reddy B					
						3	Sandeep Kumar	J105	W	4 5		
							Madhusudan B Kulkarni					
							Pavankumar Reddy B					
1257	EEE F435	DIGITAL IMAGE PROCESSING	3	-	3	1	VENKATESWARAN R	F107	T Th S	4		12/05 FN