| COM COD | COUR | SE NO | COURSE TITLE                 | TEXT BOOK(S)   |
|---------|------|-------|------------------------------|--|
|         |      |       | ANALOG & DIGITAL VLSI DES    | (ii) Razavi, Behzad Design of Analog CMOS Integrated Circuits TMH, 2001                |
| 1902    | EEE  | F348  | FPGA BASED SYSTEM DESIGN LAB | (i) Pong P. Chu. FPGA prototyping by Verilog examples Wiley, 2008                      |
|         |      |       |                              | (ii) Xilinx<br>FPGA tutorial   |
|         |      |       |                              | (iii) Wolf Wayne FPGA Based System Design Pearson Edu, 2004                            |
|         |      |       |                              | (iv) Samir Palnitkar<br>Verilog HDL: A Guide to Digital Design and Synthesis Book      |
| 1731    | EEE  | F366  | LABORATORY PROJECT           | No text book required  |
| 1742    | EEE  | F367  | LABORATORY PROJECT           | No text book required  |
| 1691    | EEE  | F376  | DESIGN PROJECT               | No text book required  |
| 1758    | EEE  | F377  | DESIGN PROJECT               | No text book required  |
| 1256    | EEE  | F434  | DIGITAL SIGNAL PROCESS       | Mitra & Sanjit K Digital Signal Processing TMH, 3rd ed, 2006                           |
| 1257    | EEE  | F435  | DIGITAL IMAGE PROCESSING     | Gonzalez & Woods Digital Image Processing 3rd Edition , Pearson                        |
| 324     | EEE  | G512  | EMBEDDED SYSTEM DESIGN       | Wolf, Wayne Comp. as Components: Prin. of Emb. Comp. Elsevier , 2nd ed, 2008           |
| 698     | EEE  | G581  | RF & MICROWAVE ENGG          | (i) Liao, S Y Microwave Devices & Circuits PHI/ Pearson Edu., 3rd ed, 2003             |
|         |      |       |                              | (ii) Pozar, David M<br>Microwave Engg.<br>WSE , 4th ed , 2012                          |
|         |      |       |                              | (iii) Balanis, Constantine A. Antenna Theory: Analysis and Design, WSE, 3 rd ed, 2005. |