

II. COURSEWISE TIMETABLE

32

FIRST SEMESTER 2019-2020

COM COD	COURSE NO.	COURSE TITLE	CREDIT L P U	S E C	INSTRUCTOR-IN-CHARGE/ Instructor	ROOM	DAYS	HOURS	COMMON HOUR D H R	COMPRE DATE & SESSION
					V Sarath Sankar					
				4	Balasubramanian M	H106	F	4 5		
					Pranjali Gajbhiye					
				5	Prasant Kumar P	H106	Th	8 9		
					R Venkata Sravya					
		Tutorial		1	Runa Kumari	I221	M	1		
				2	Balasubramanian M	I211	F	1		
				3	Balasubramanian M	I222	M	1		
				4	Balasubramanian M	I114	Th	7		
1243	EEE F313	ANALOG & DIGITAL VLSI DES	3 - 3	1	SURYA SHANKAR DAN	F104	T Th S	2		10/12 FN
		Tutorial		1	Surya Shankar Dan	I212	F	1		
				2	Surya Shankar Dan	I212	W	1		
				3	Surya Shankar Dan	I212	Th	7		
1902	EEE F348	FPGA BASED SYSTEM DESIGN LAB	- 2 2	1	S K SAHOO	I124	M F	2 3		
					G Sahith					
					Simhadri Hariprasad					
				2	S K Sahoo	I124	W F	6 7		
					Anil Kumar U					
					Samala Jagadheesh					
1731	EEE F366	LABORATORY PROJECT	- - 3	1	SUDHA RADHIKA					
1742	EEE F367	LABORATORY PROJECT	- - 3	1	SUDHA RADHIKA					
1691	EEE F376	DESIGN PROJECT	- - 3	1	SAROJ MONDAL					
1758	EEE F377	DESIGN PROJECT	- - 3	1	SAROJ MONDAL					
1256	EEE F434	DIGITAL SIGNAL PROCESS	3 1 4	1	BVVSN PRABHAKAR RAO	F104	T Th S	3		05/12 FN
		Practical		1	Ramakant	I011	T	8 9		
					Priyanka B G					
				2	Venkateswaran R	I011	W	4 5		