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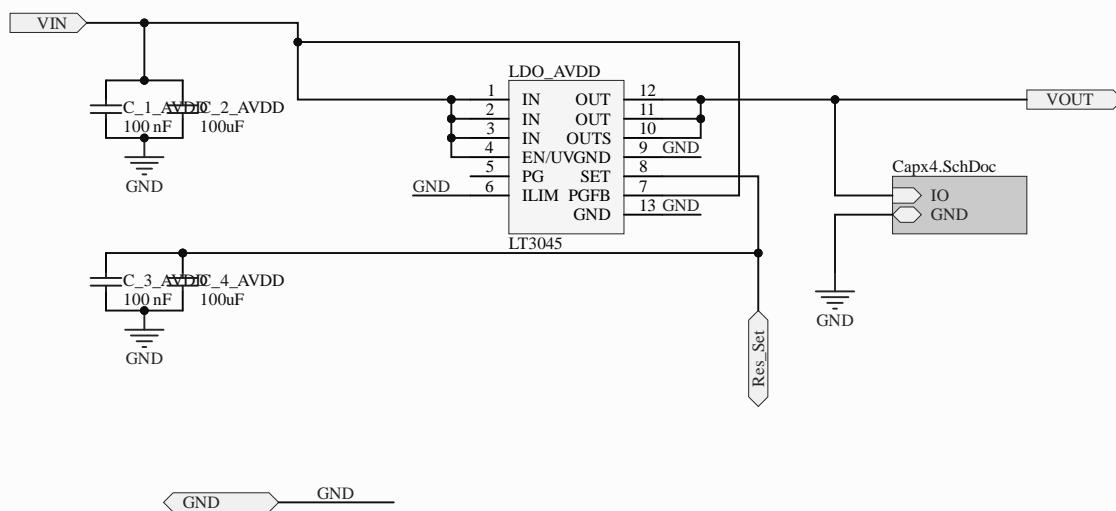
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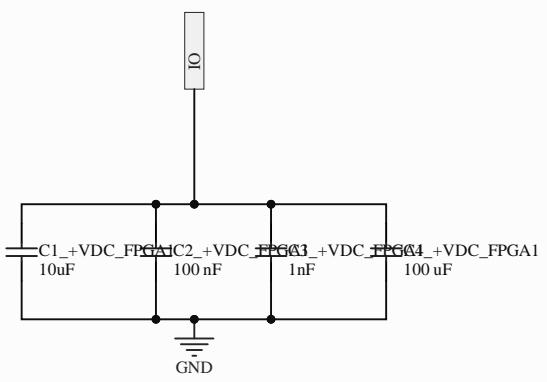
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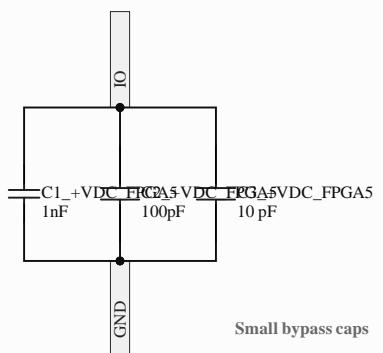
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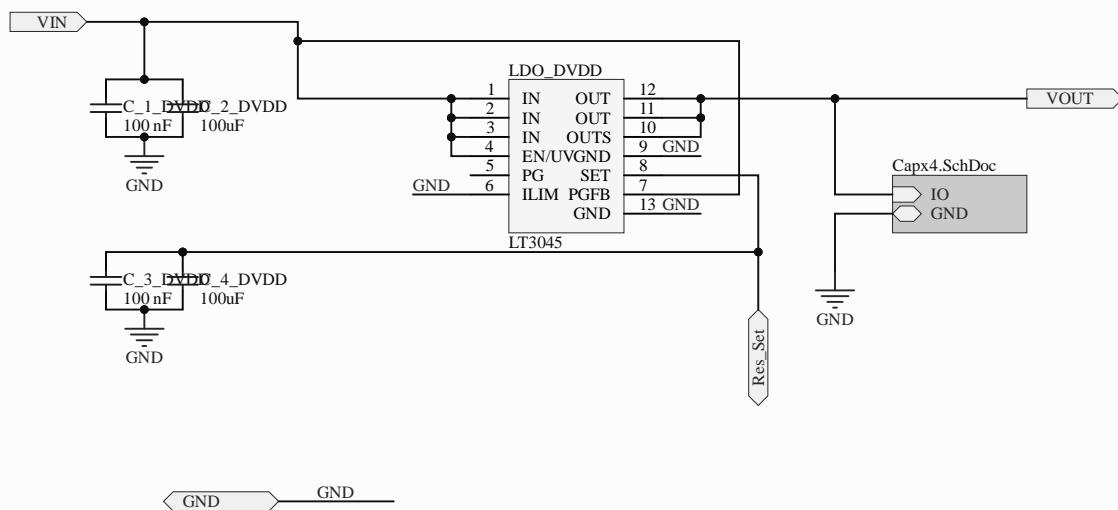
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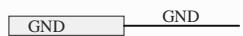
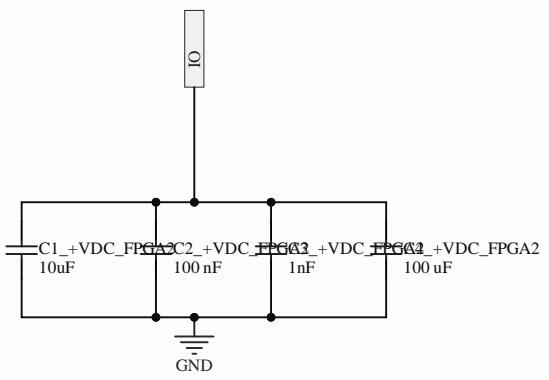
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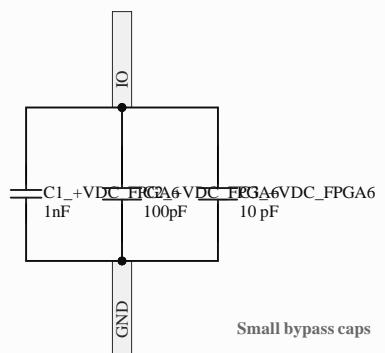
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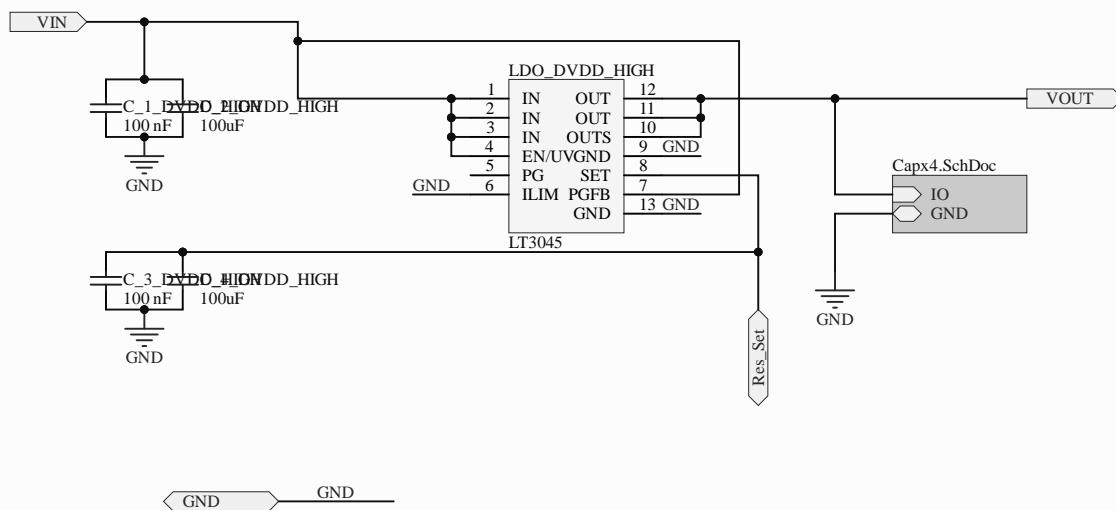
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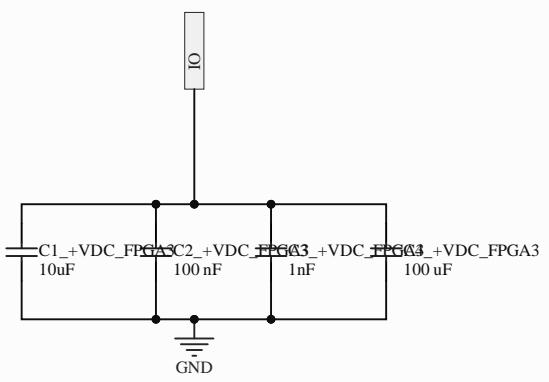
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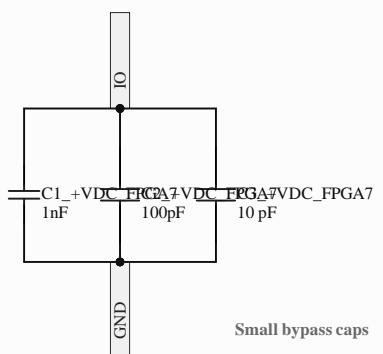
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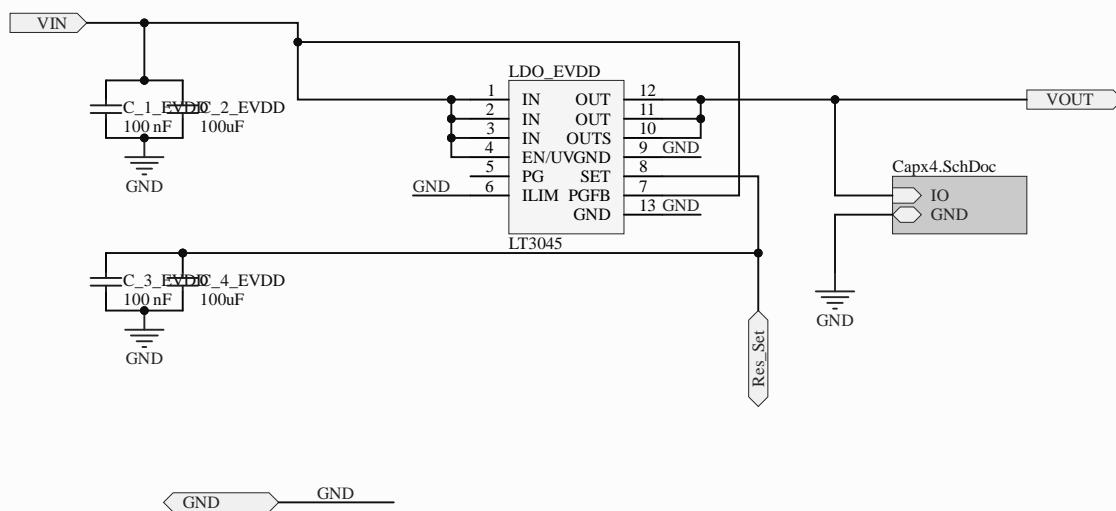
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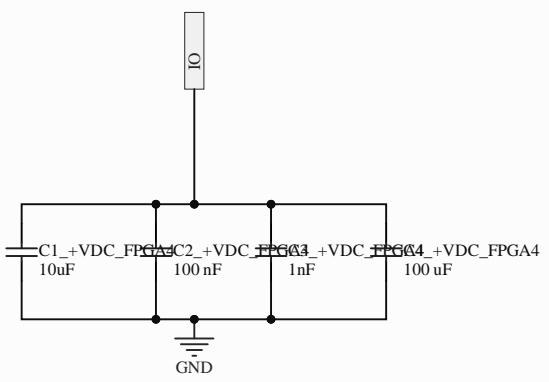
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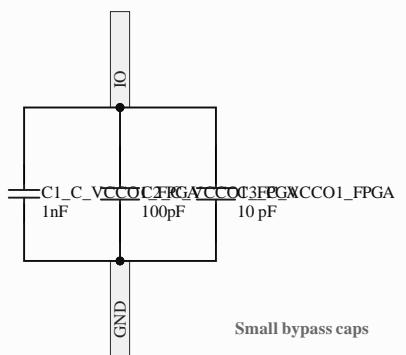
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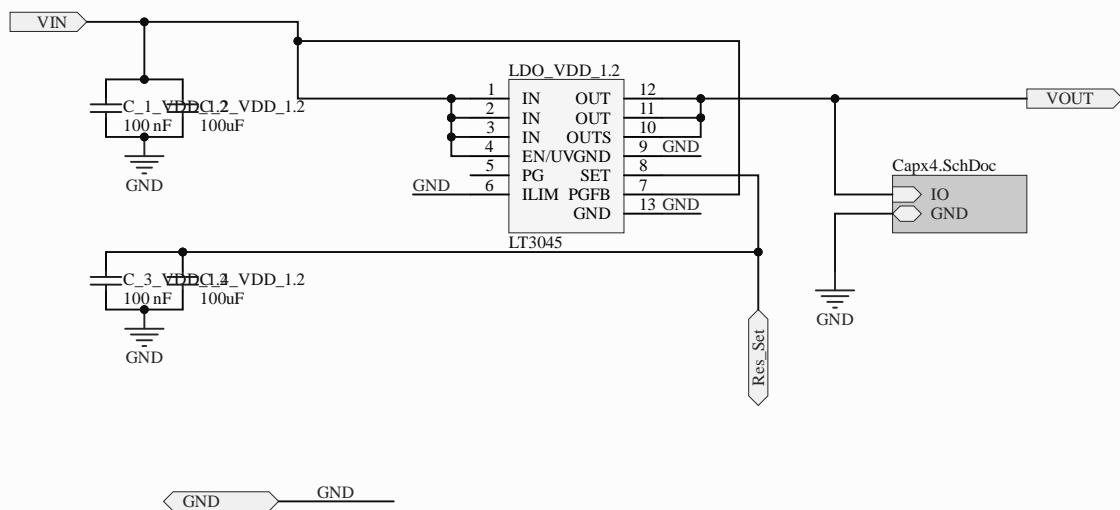
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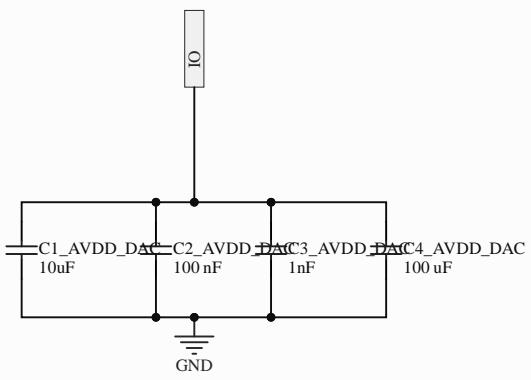
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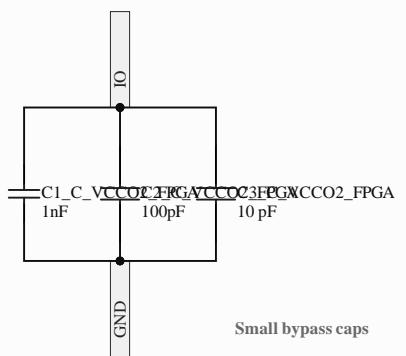
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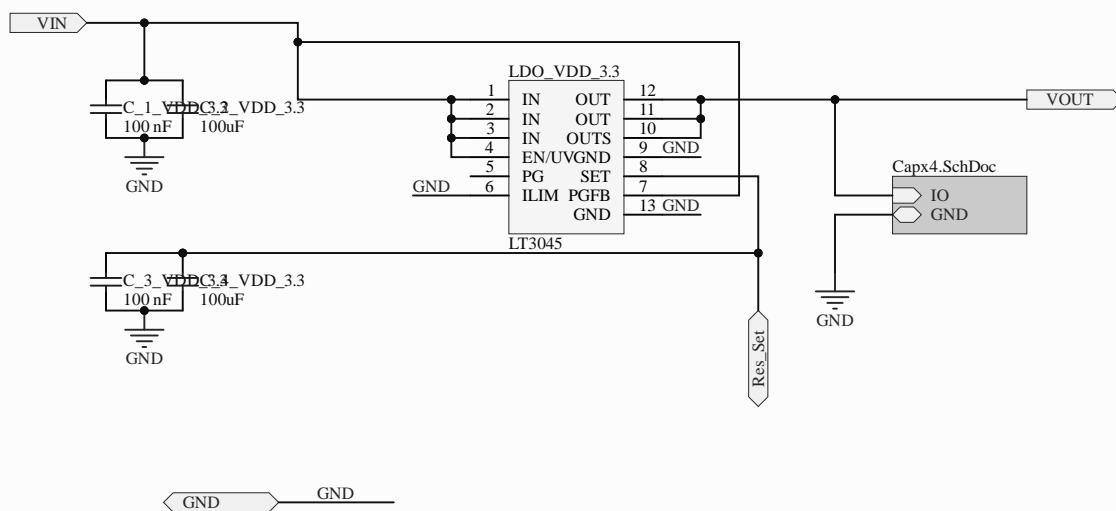
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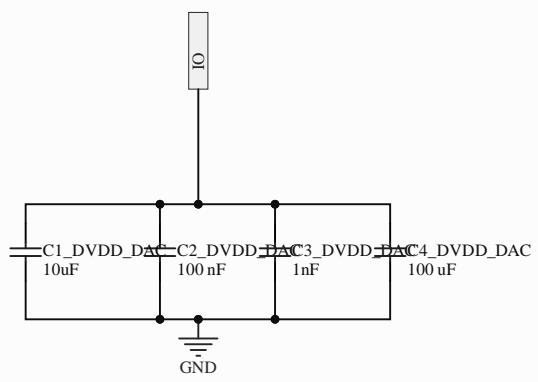
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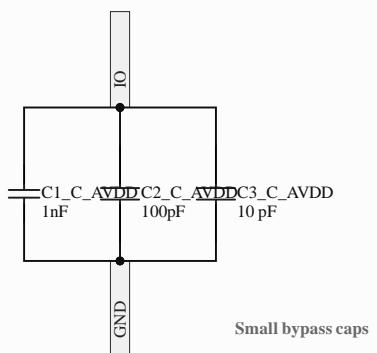
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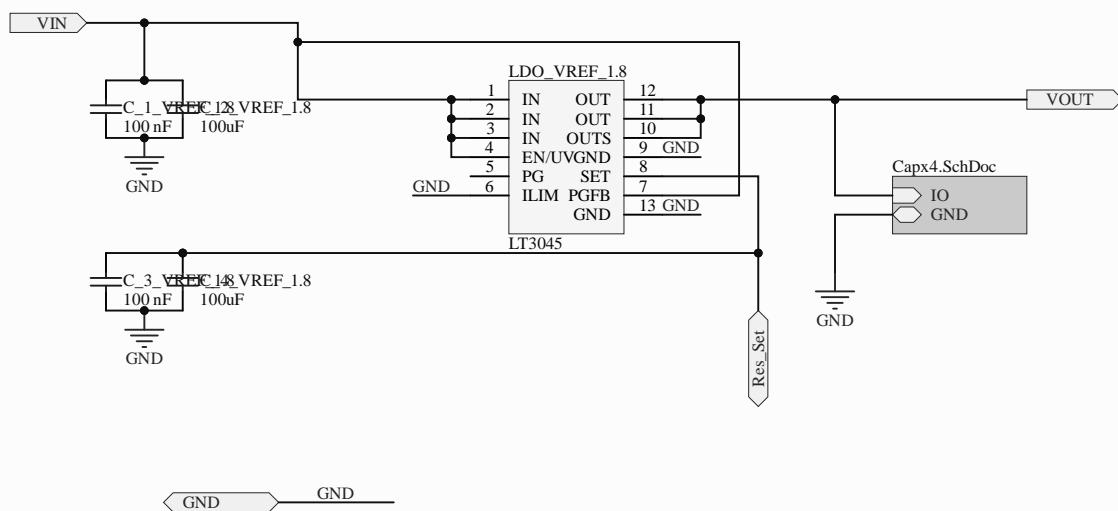
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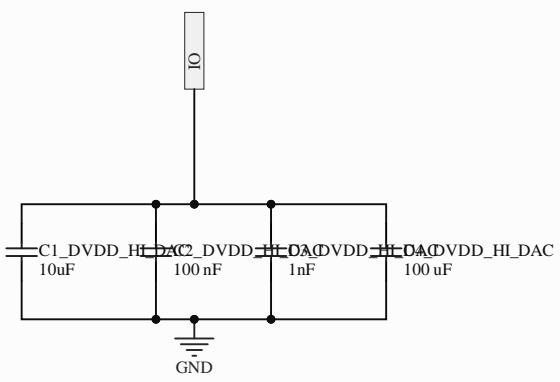
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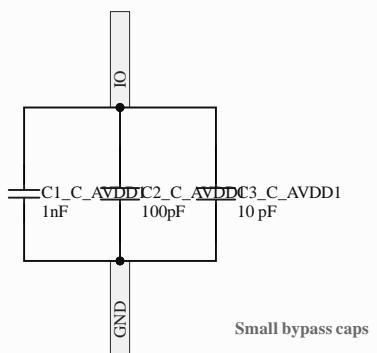
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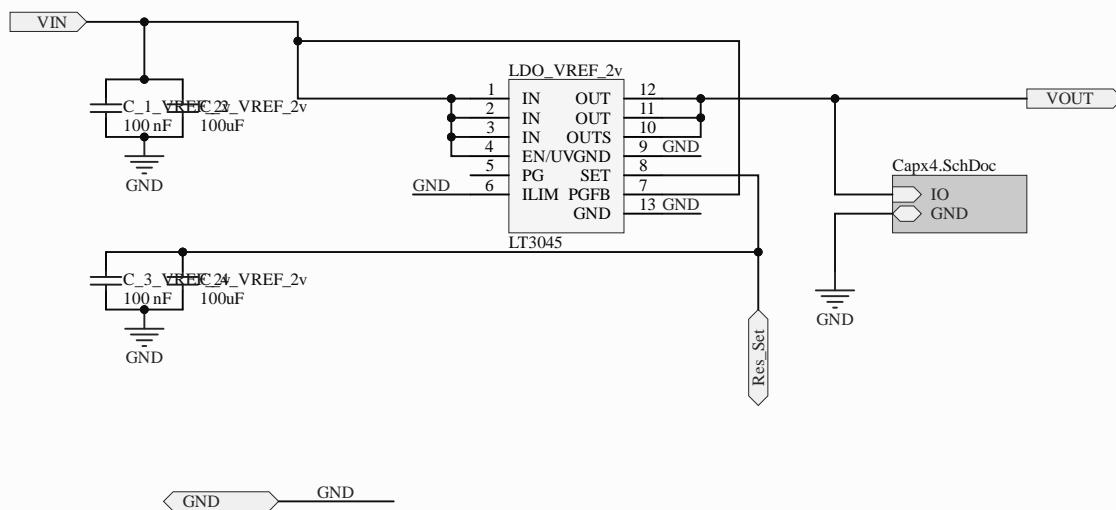
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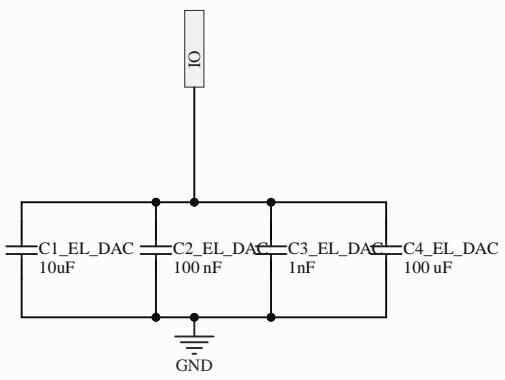
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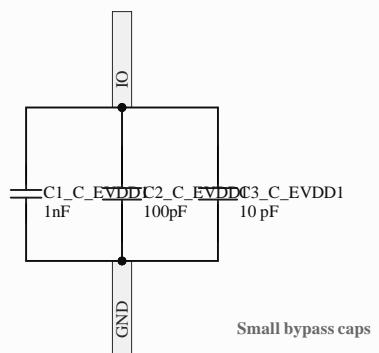
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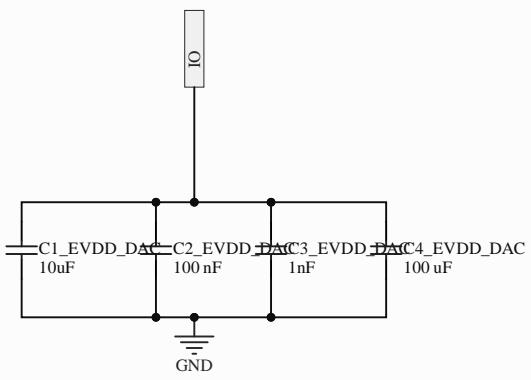
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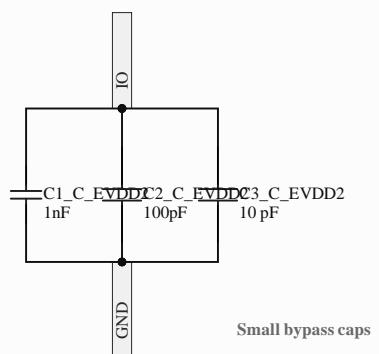
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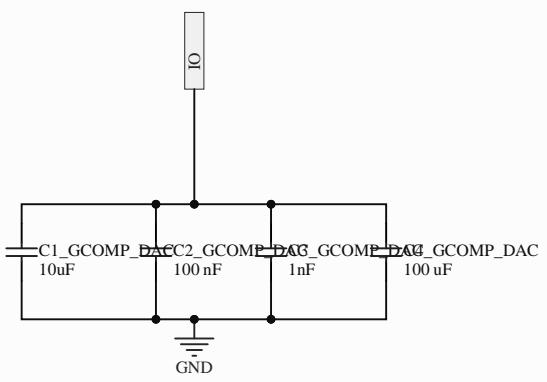
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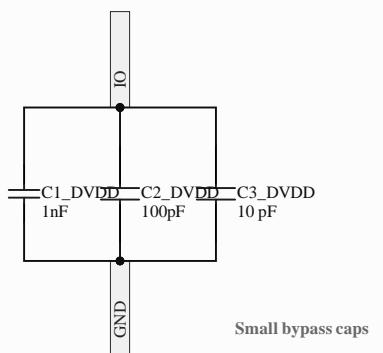
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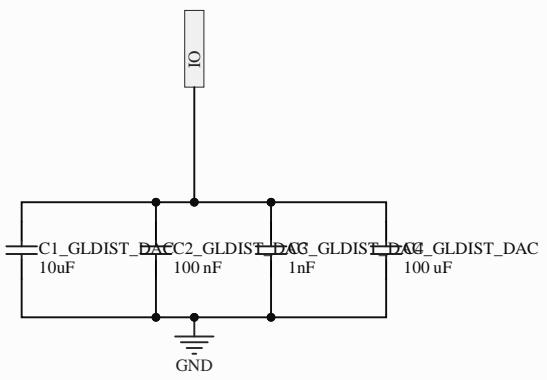
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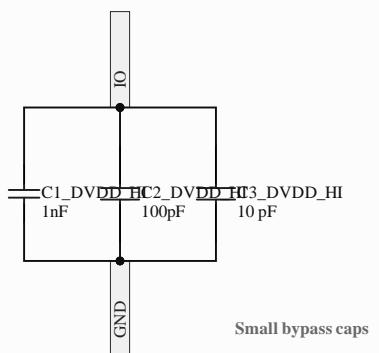
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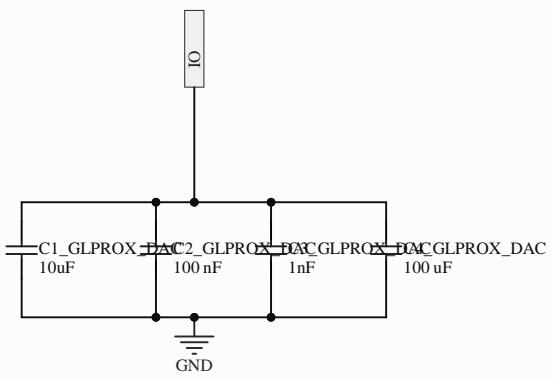
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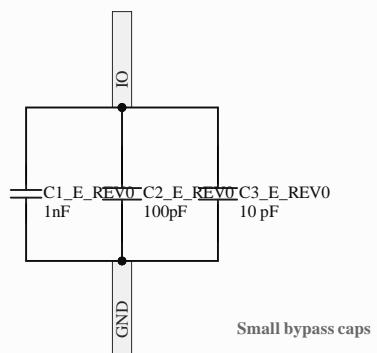
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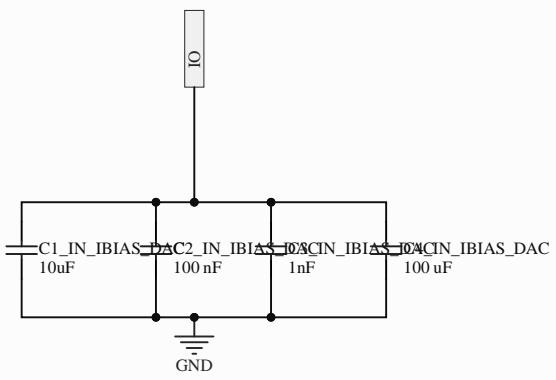
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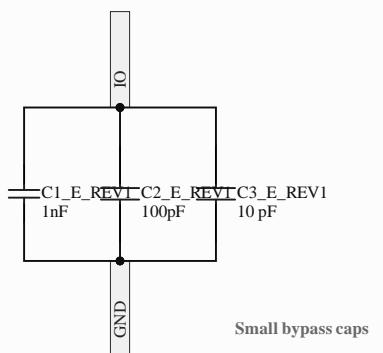
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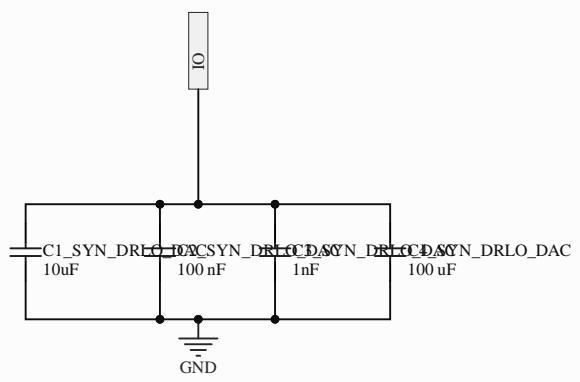
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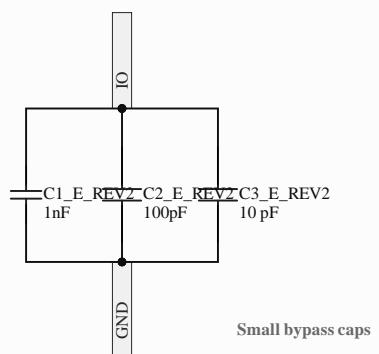
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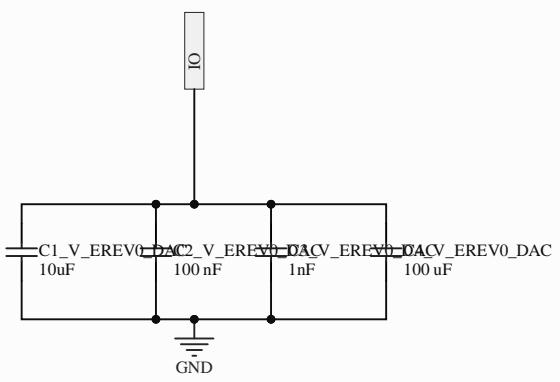
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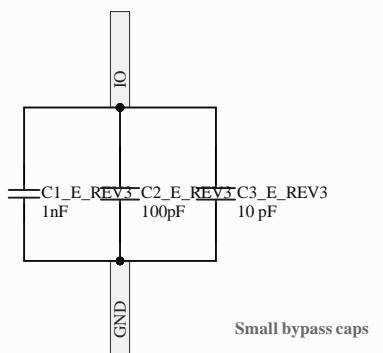
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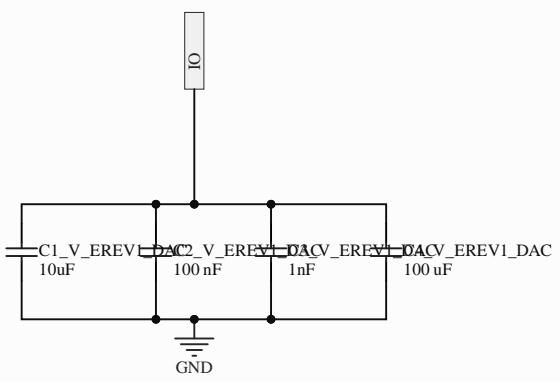
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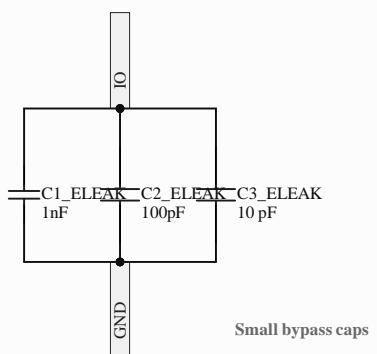
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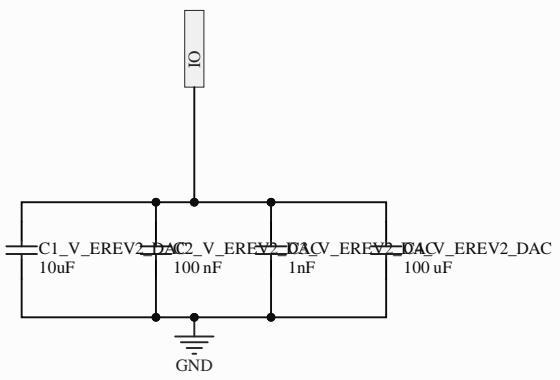
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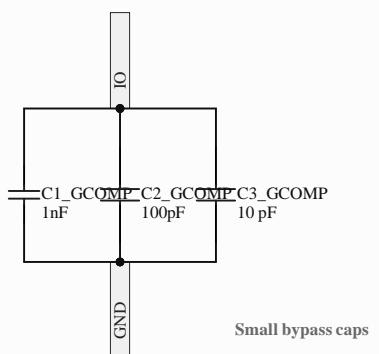
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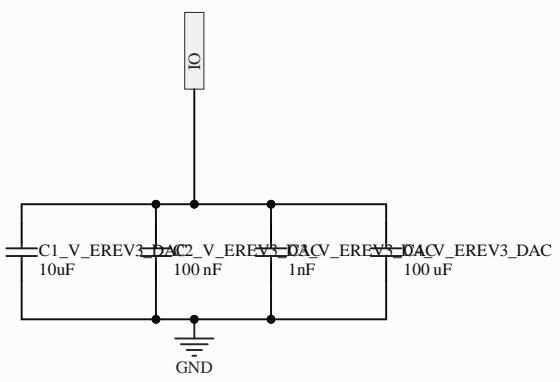
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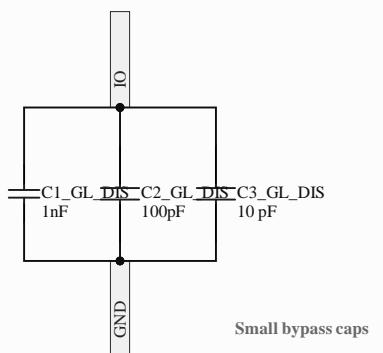
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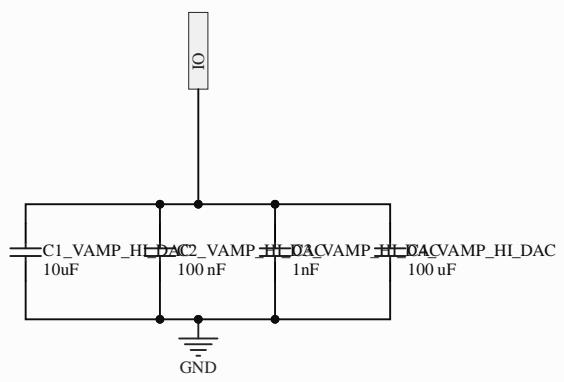
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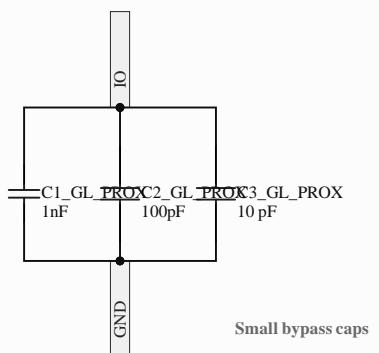
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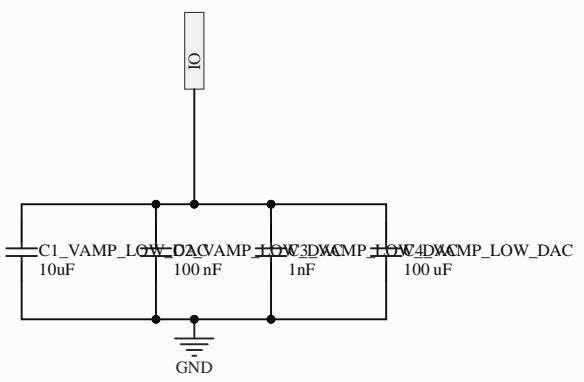
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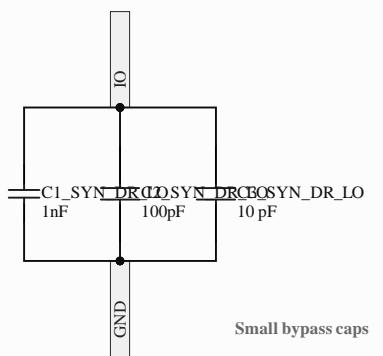
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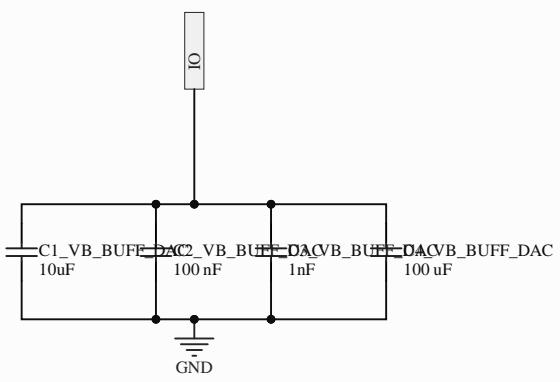
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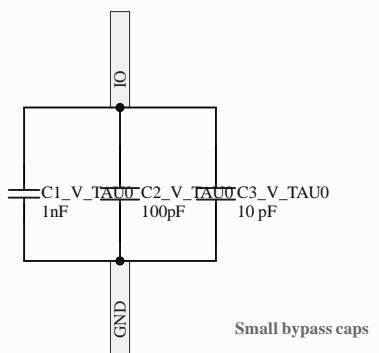
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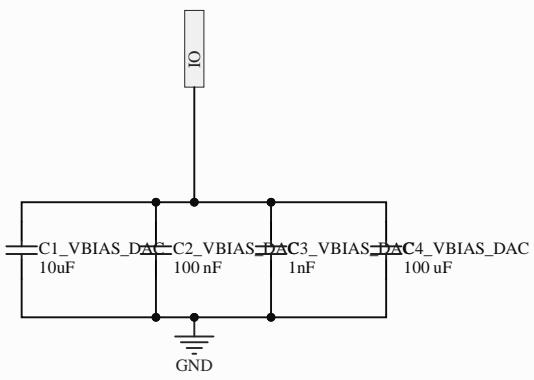
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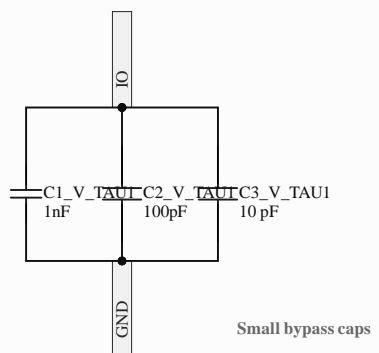
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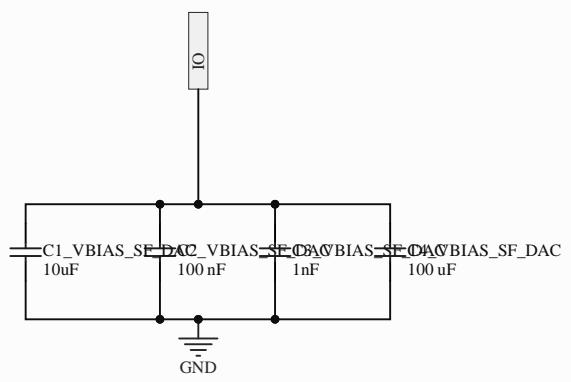
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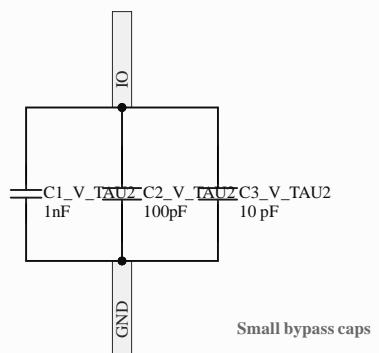
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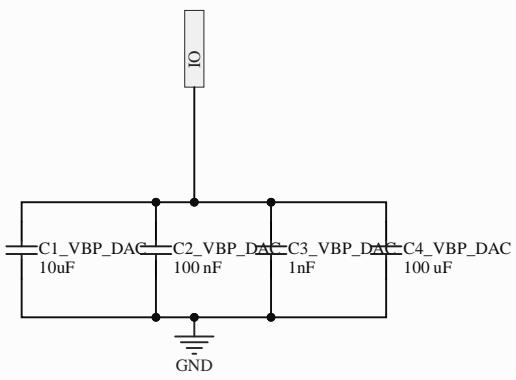
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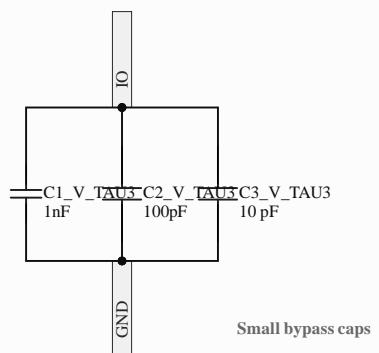
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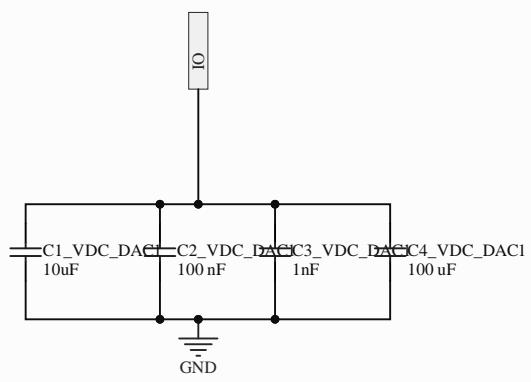
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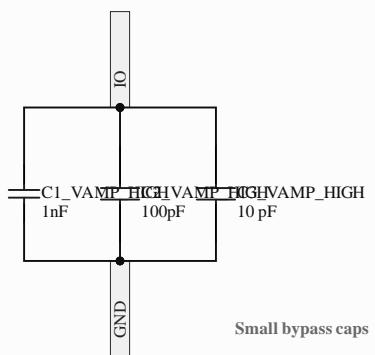
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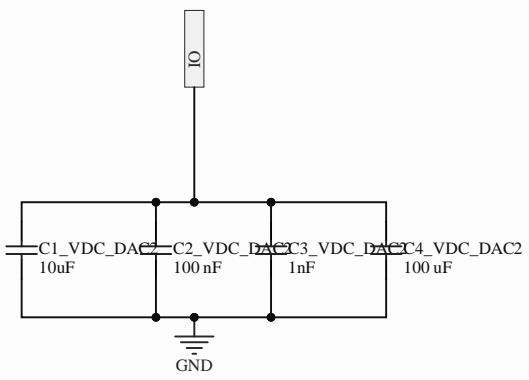
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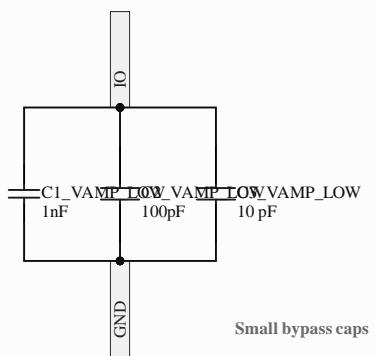
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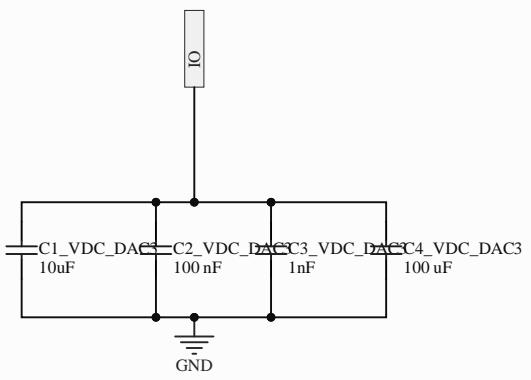
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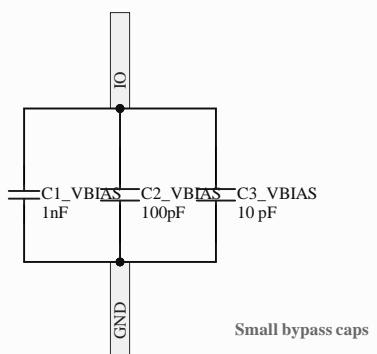
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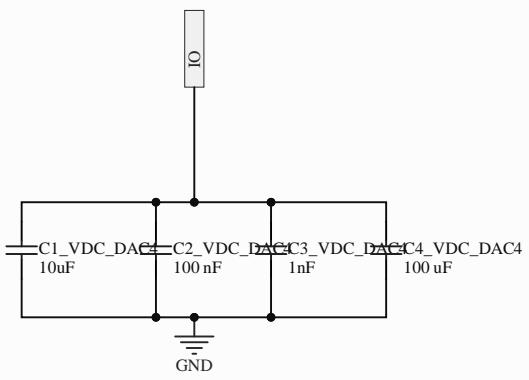
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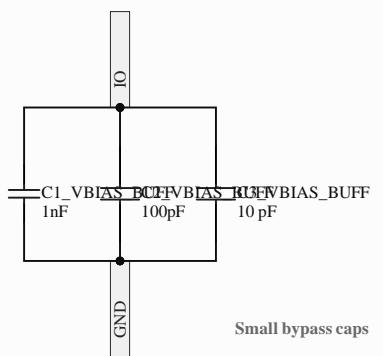
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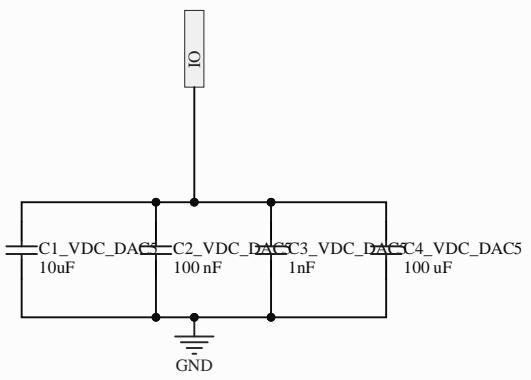
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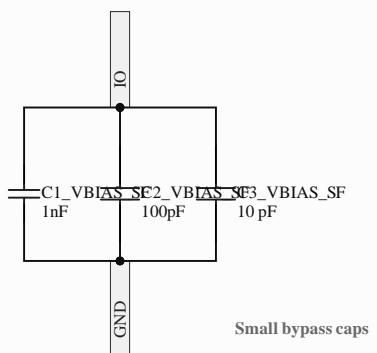
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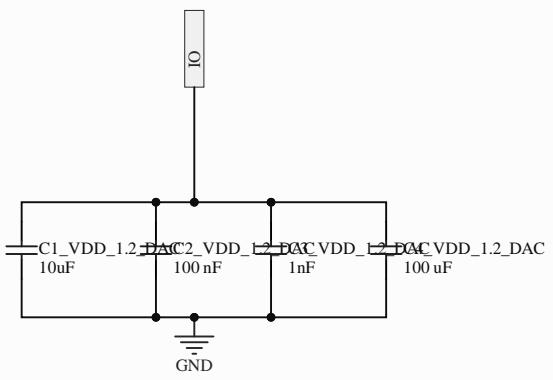
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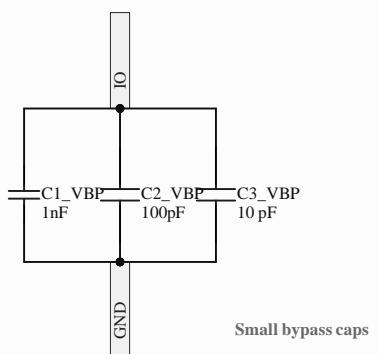
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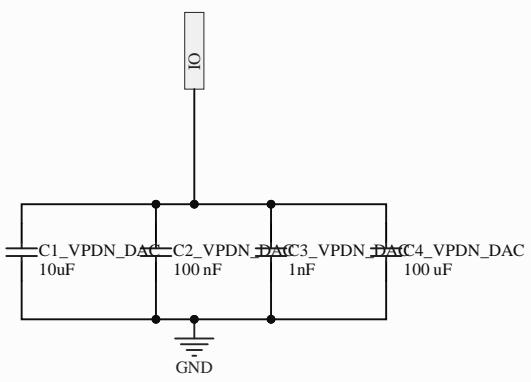
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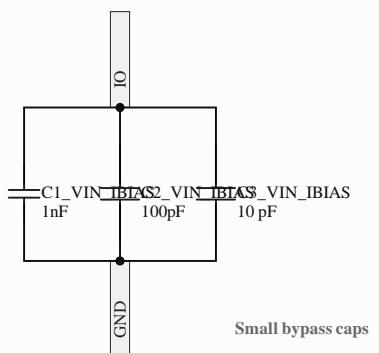
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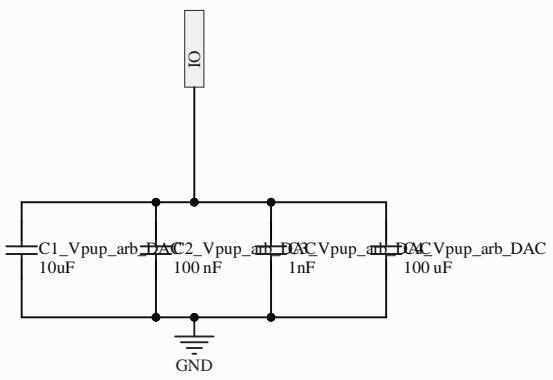
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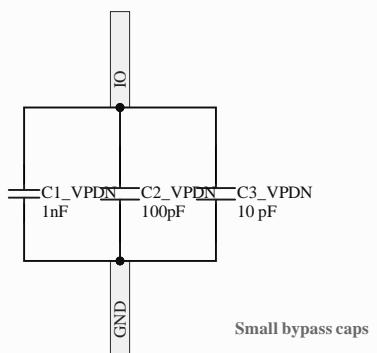
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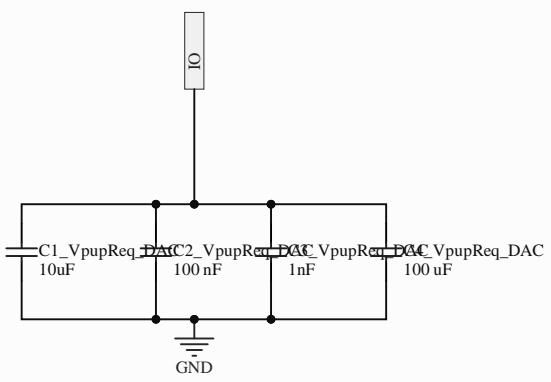
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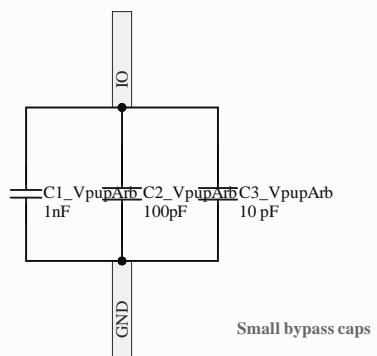
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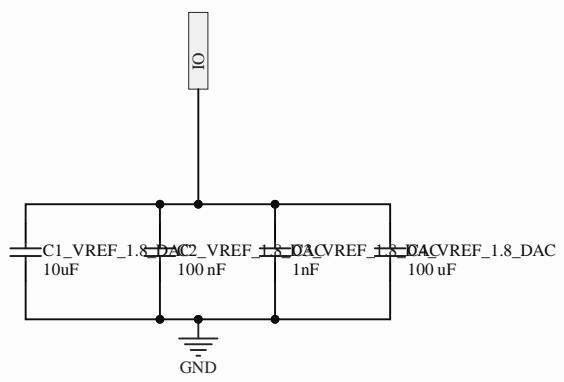
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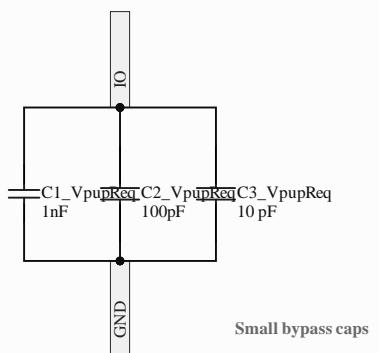
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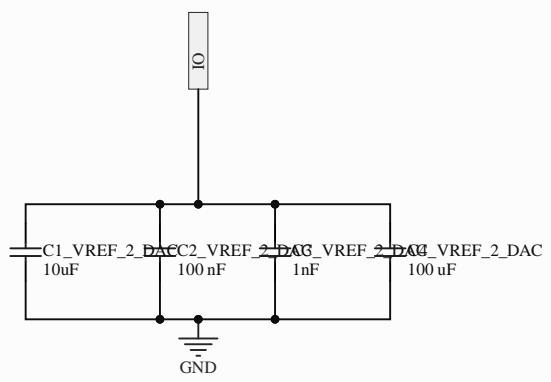
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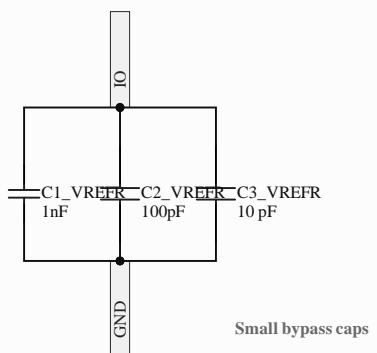
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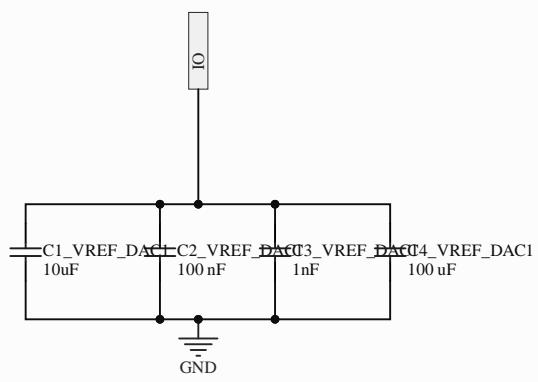
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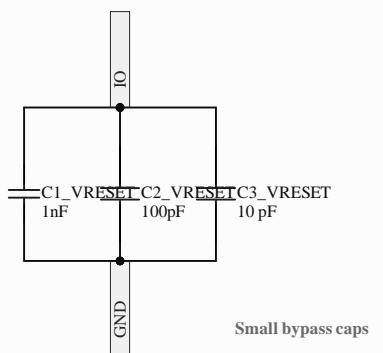
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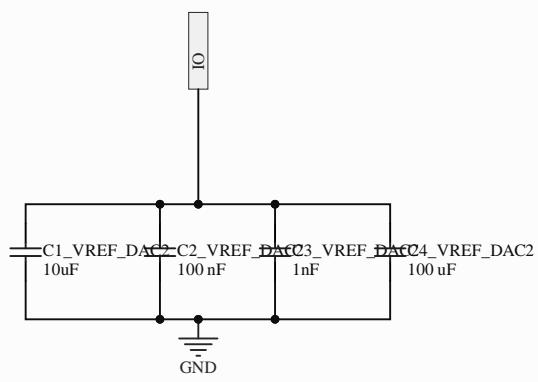
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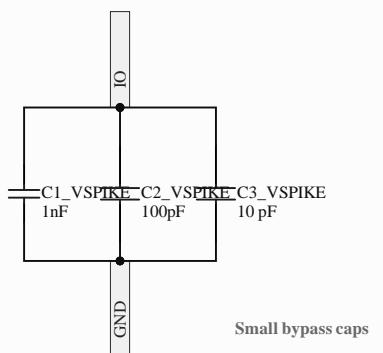
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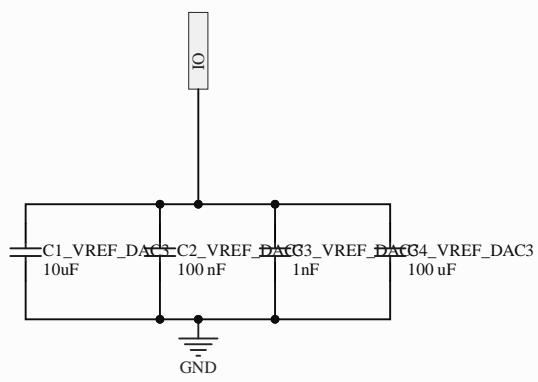
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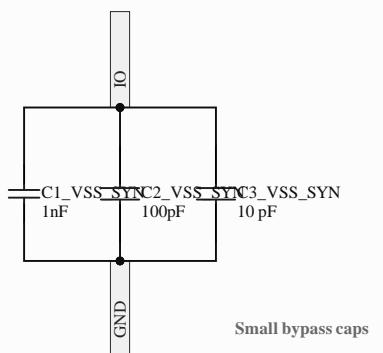
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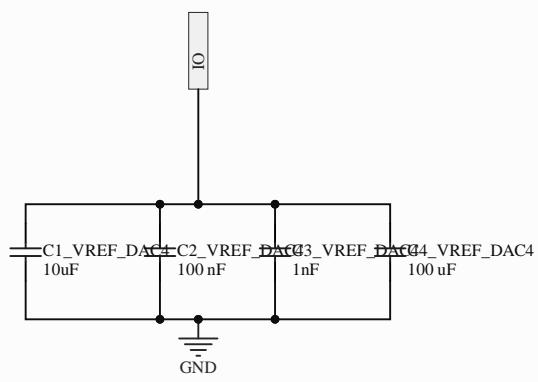
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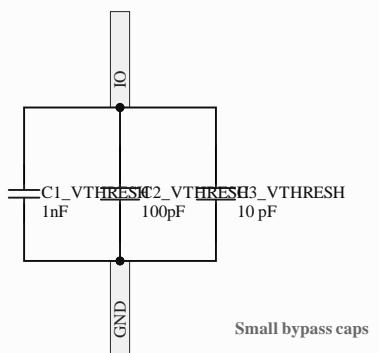
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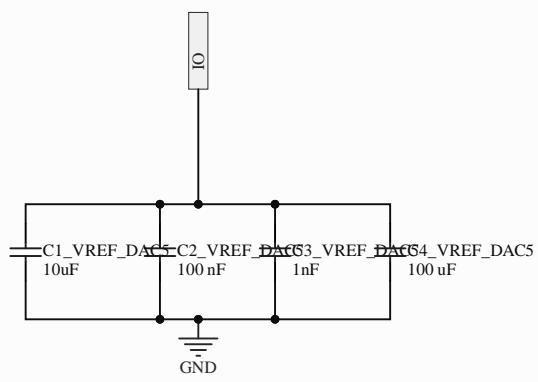
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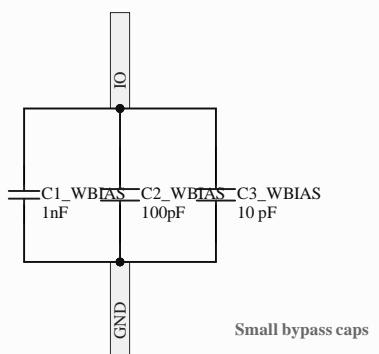
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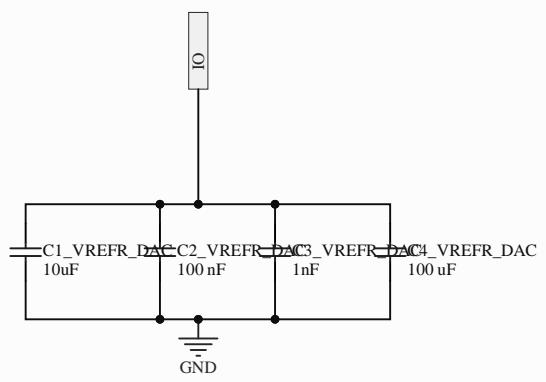
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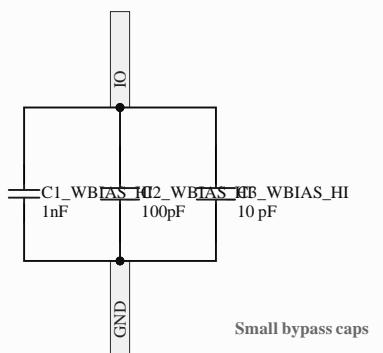
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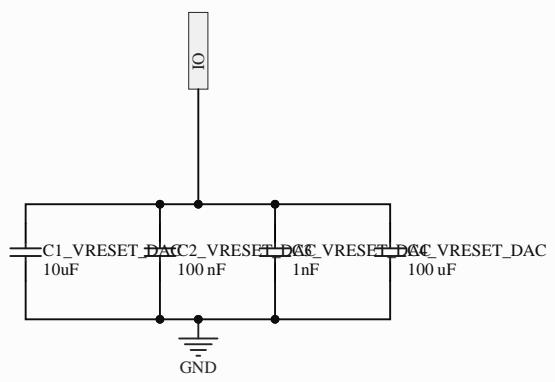
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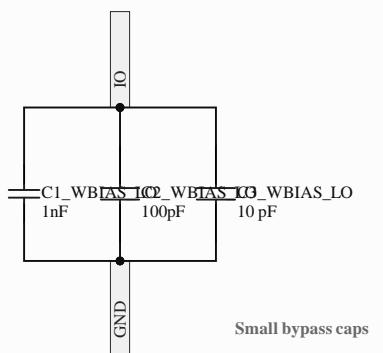
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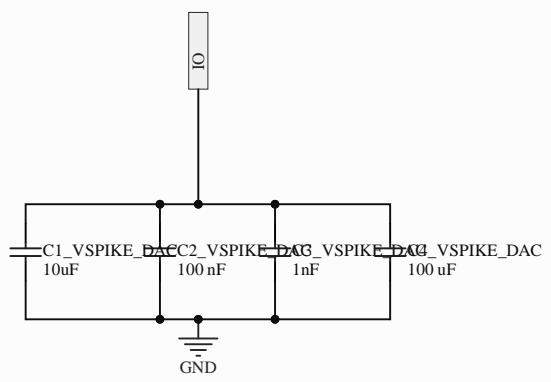
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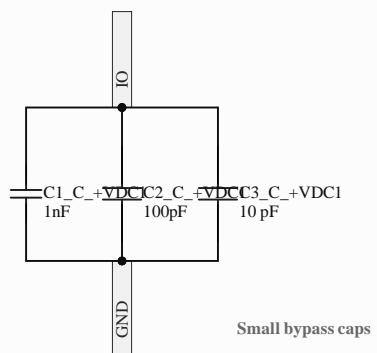
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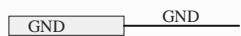
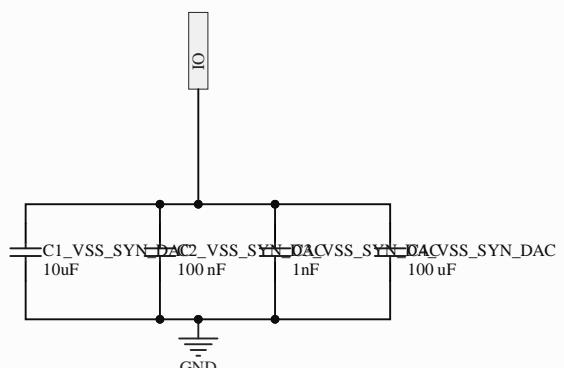
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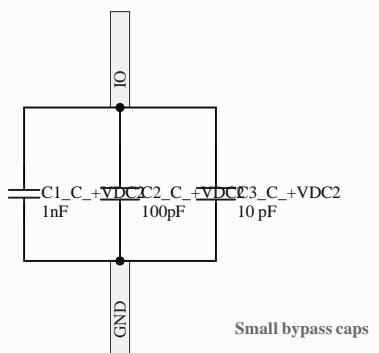
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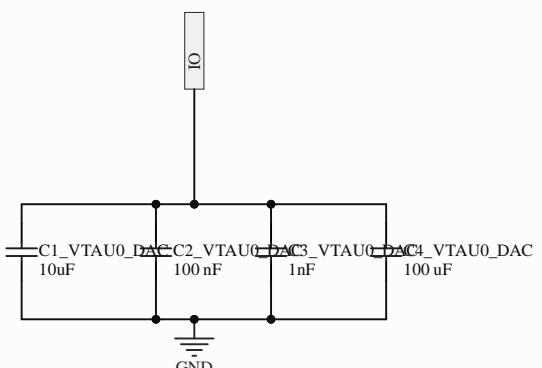
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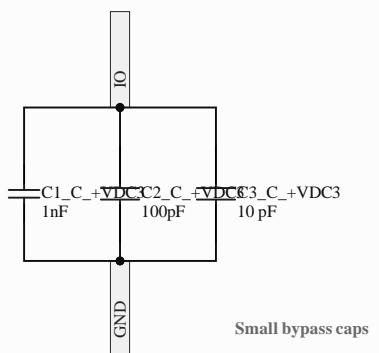
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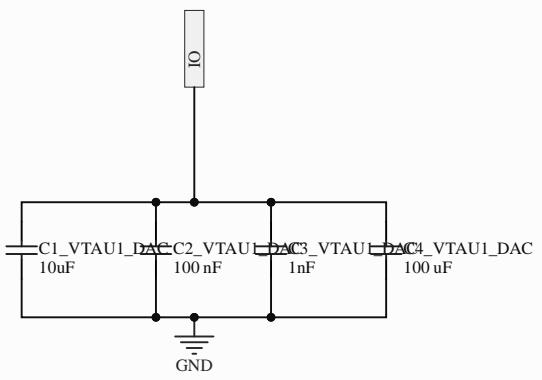
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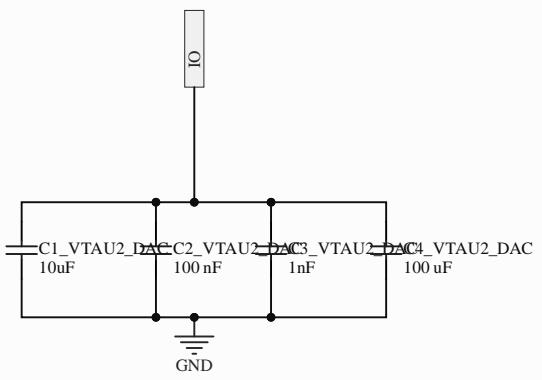
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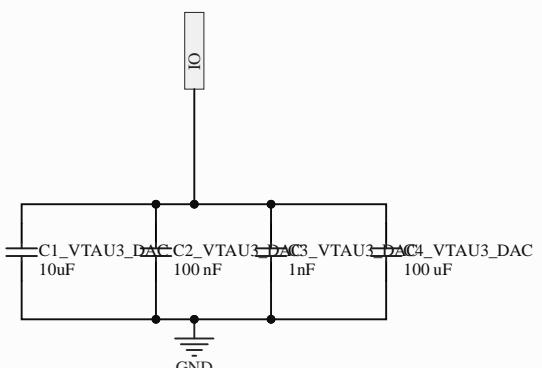
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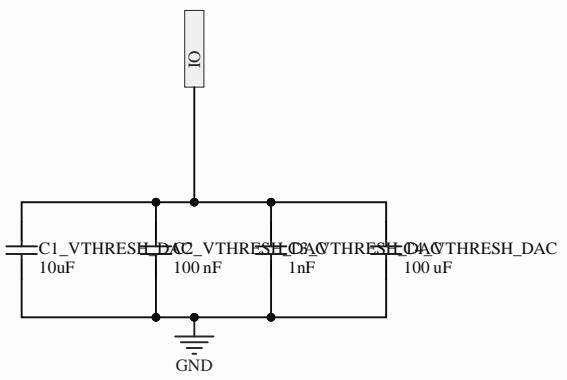
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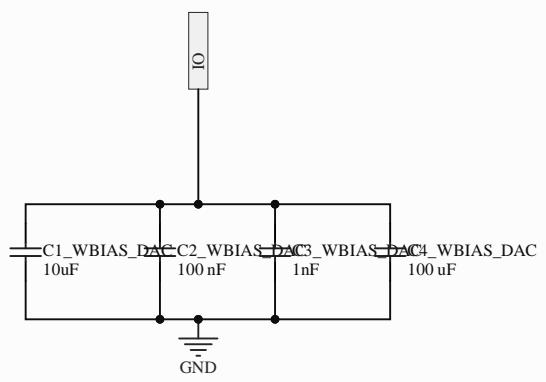
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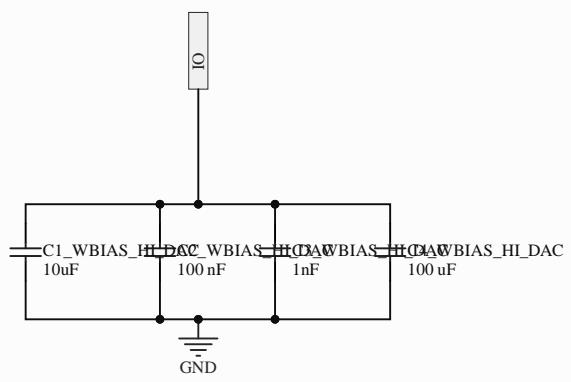
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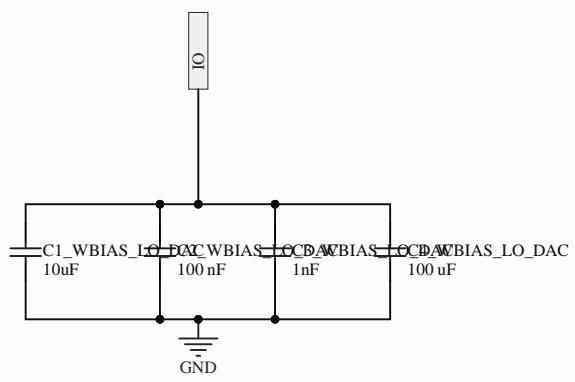
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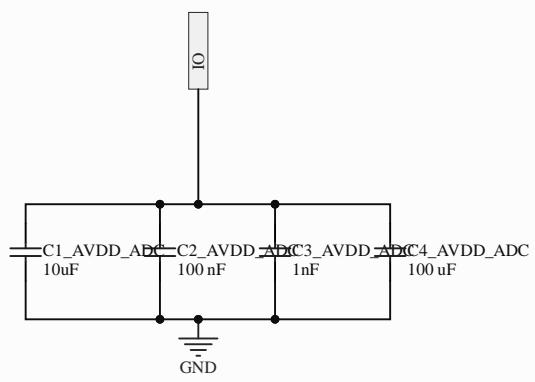
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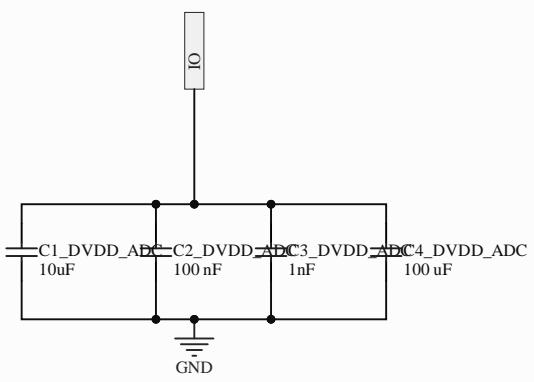
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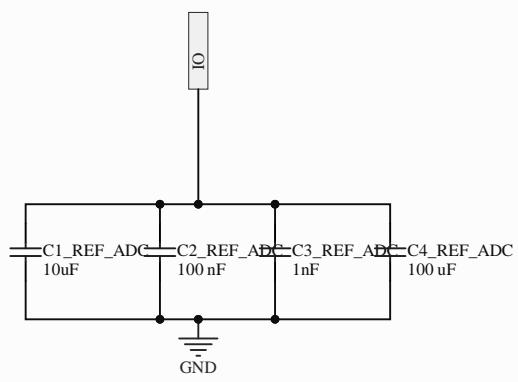
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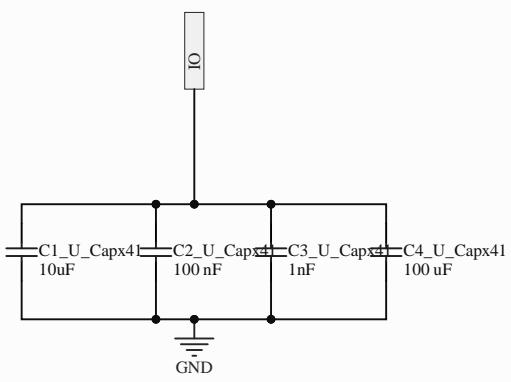
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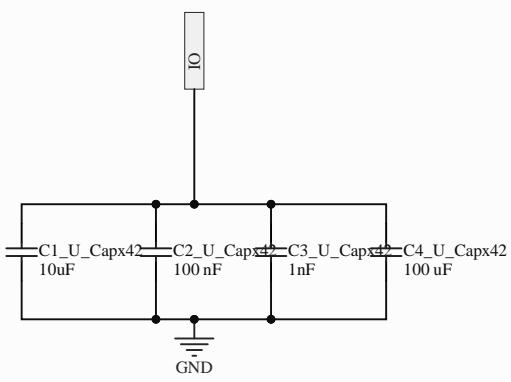
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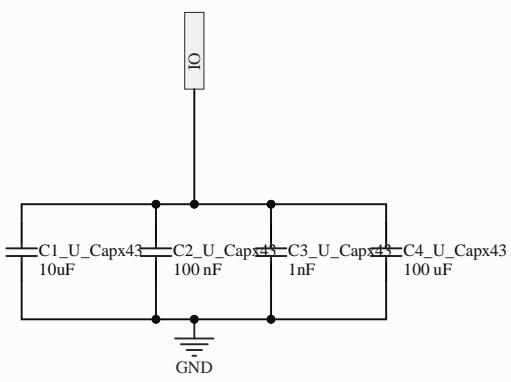
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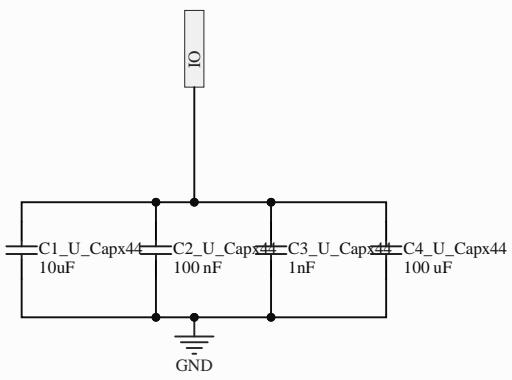
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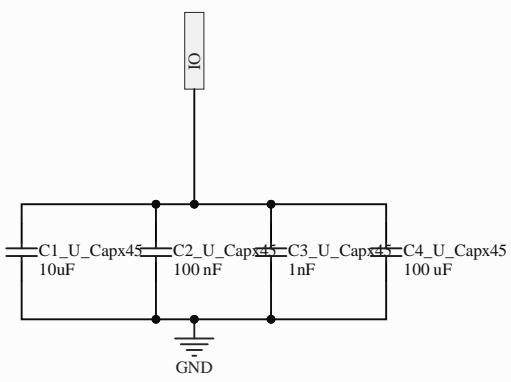
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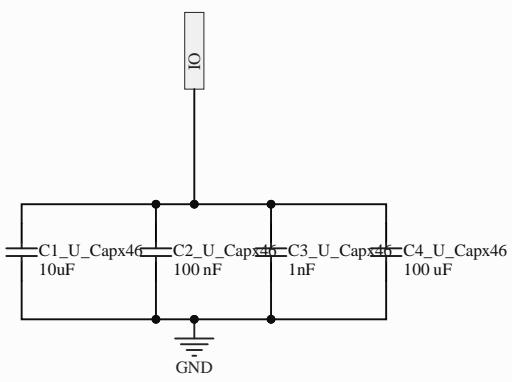
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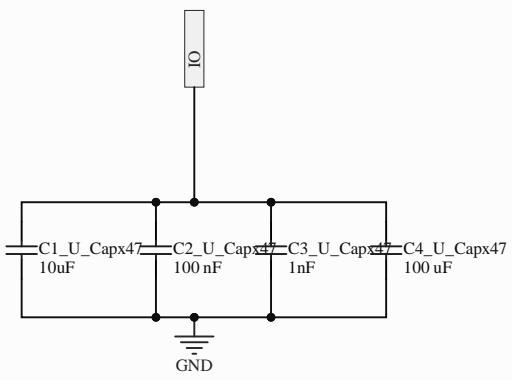
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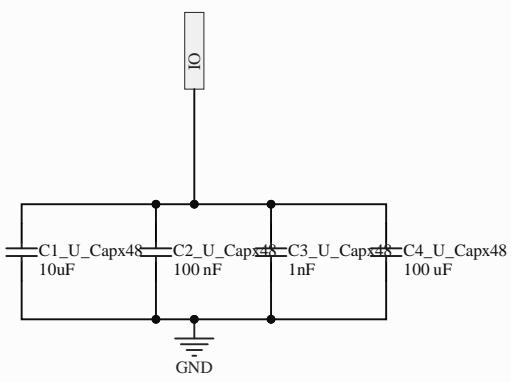
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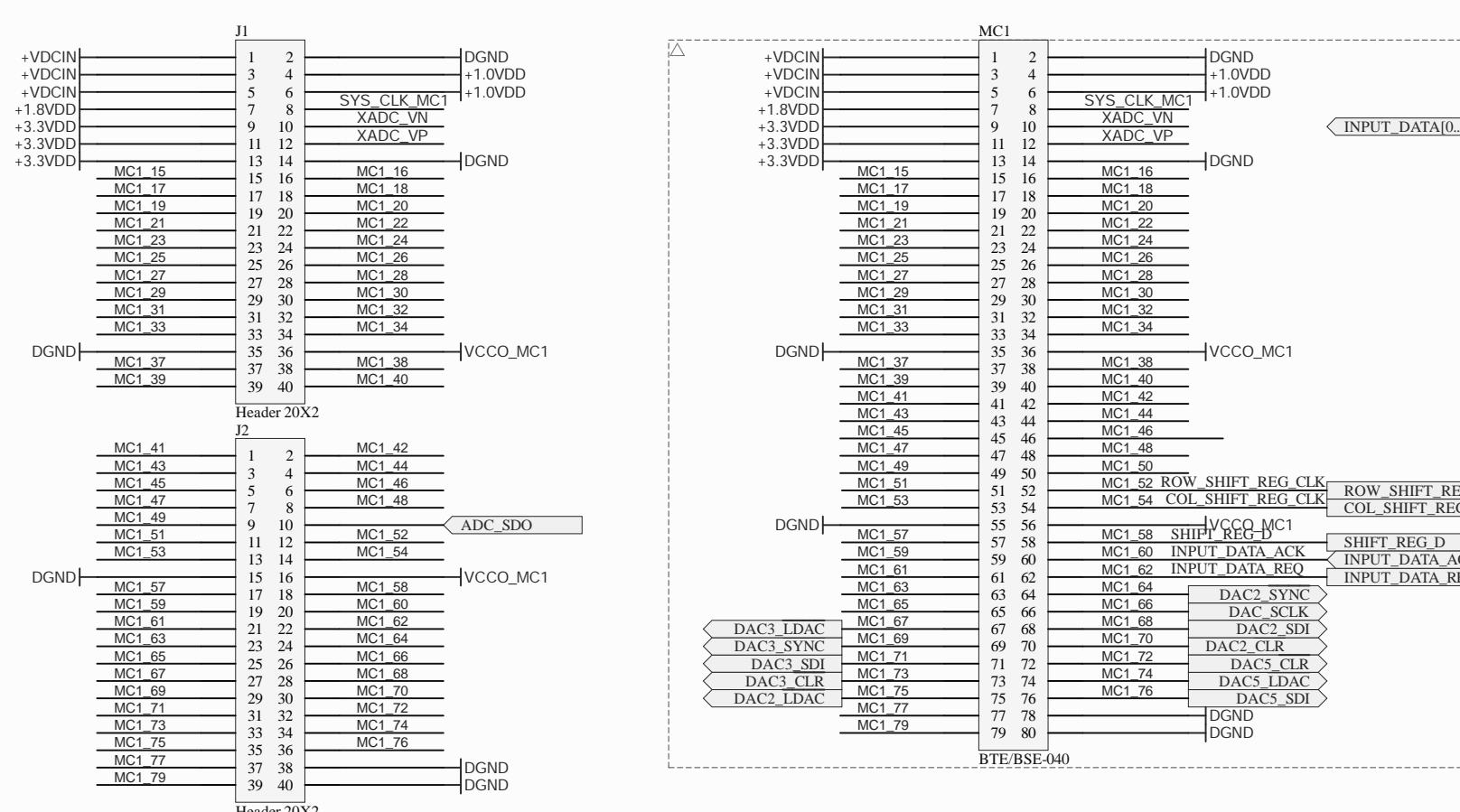
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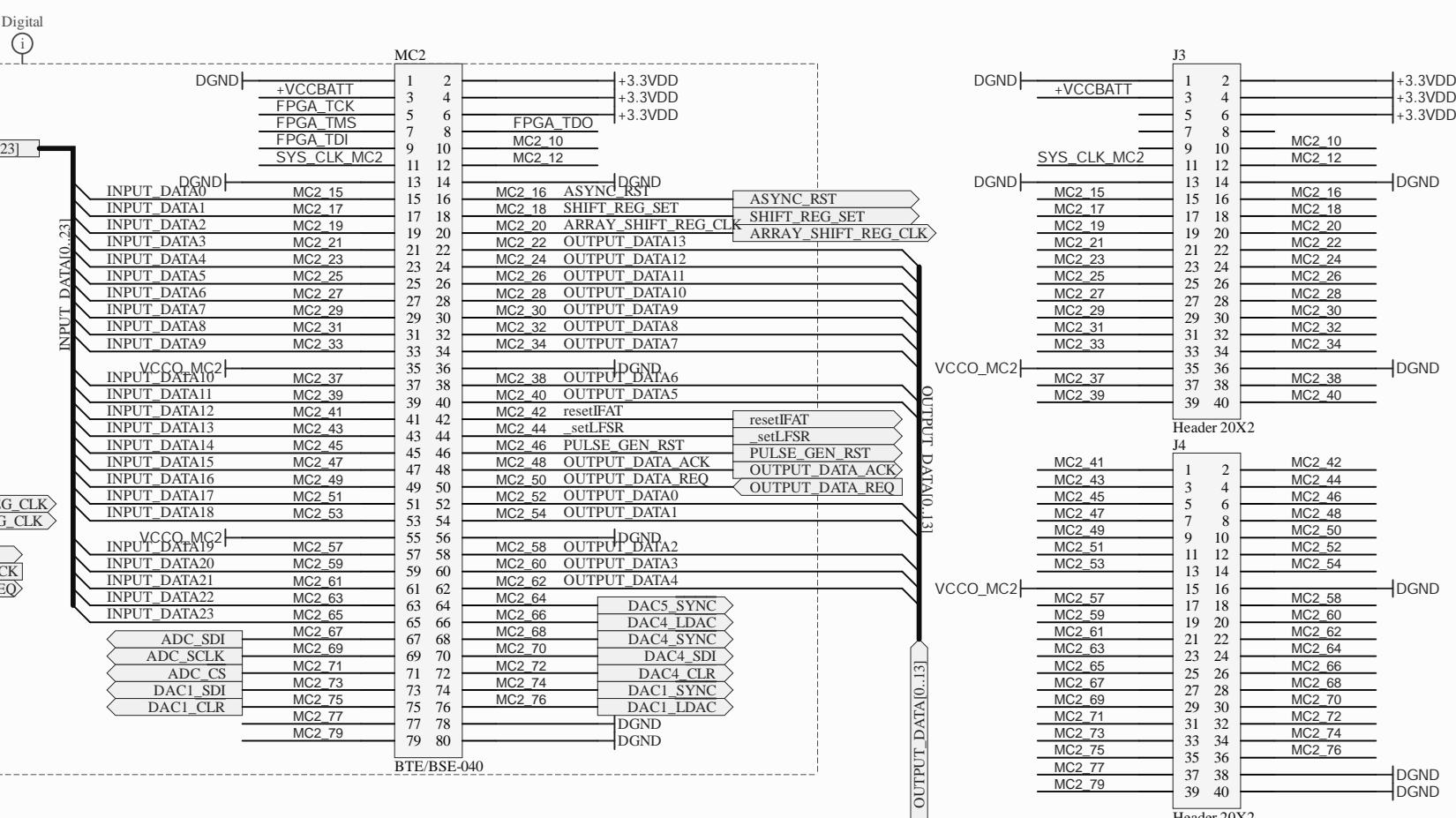
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DACs, ADCs I/Os - Bank 13 (3.3v)

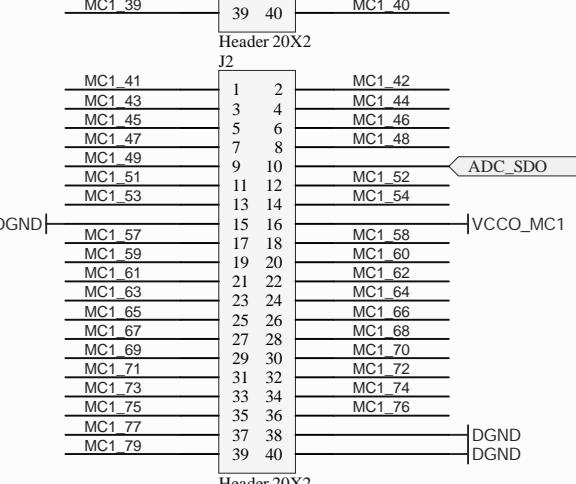


IFAT I/Os - Bank 34, 35



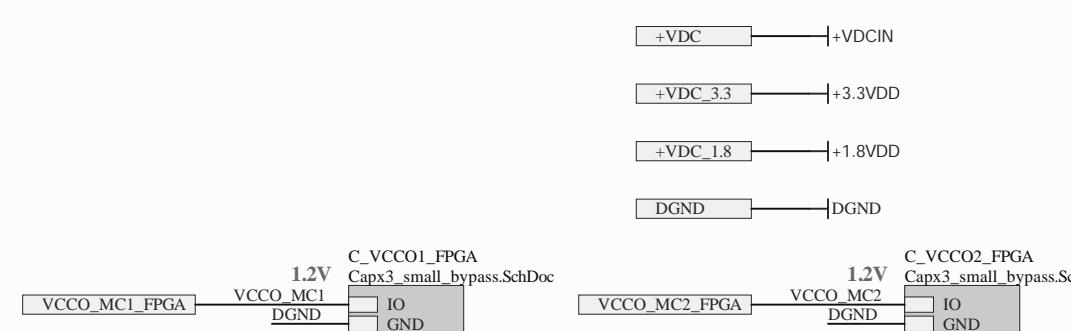
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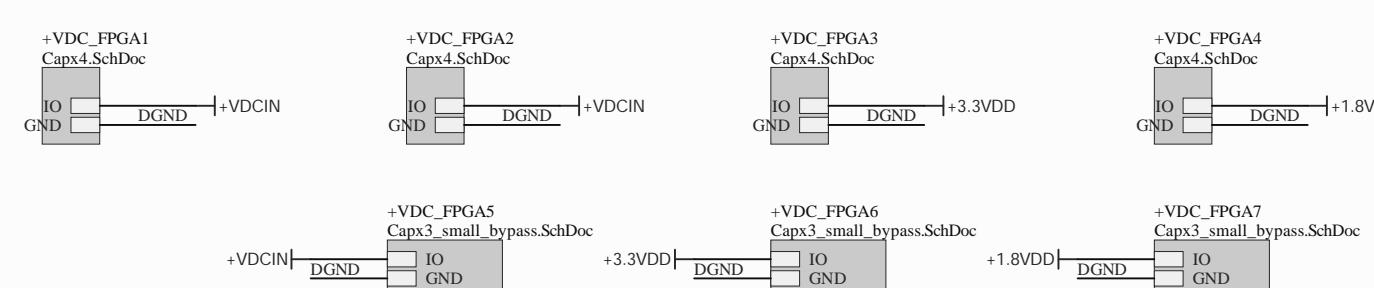


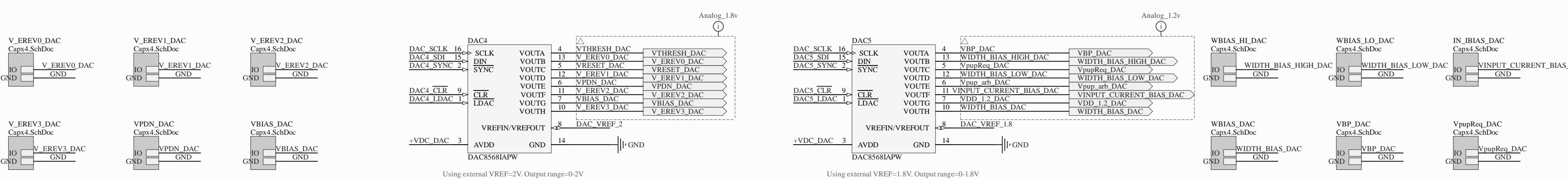
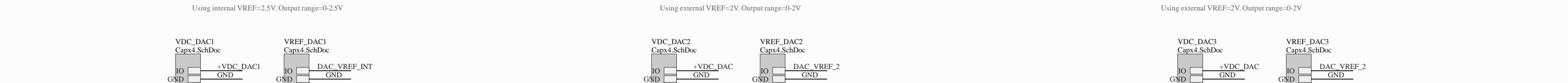
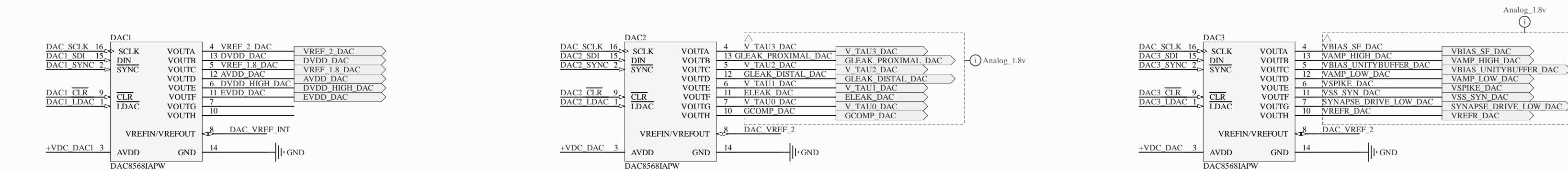
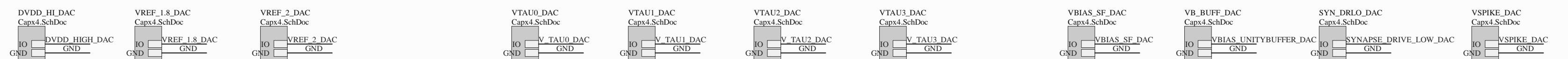
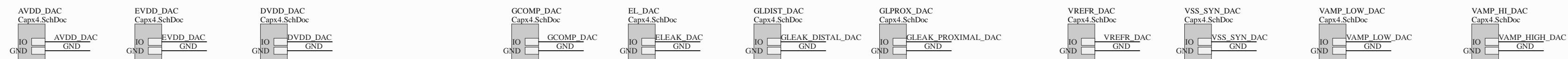
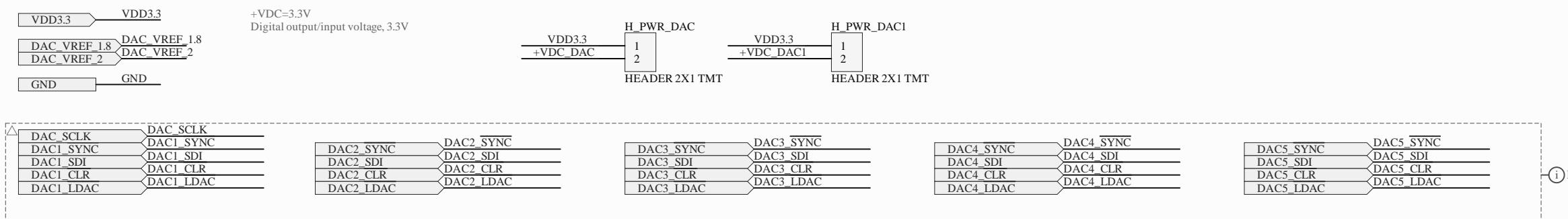
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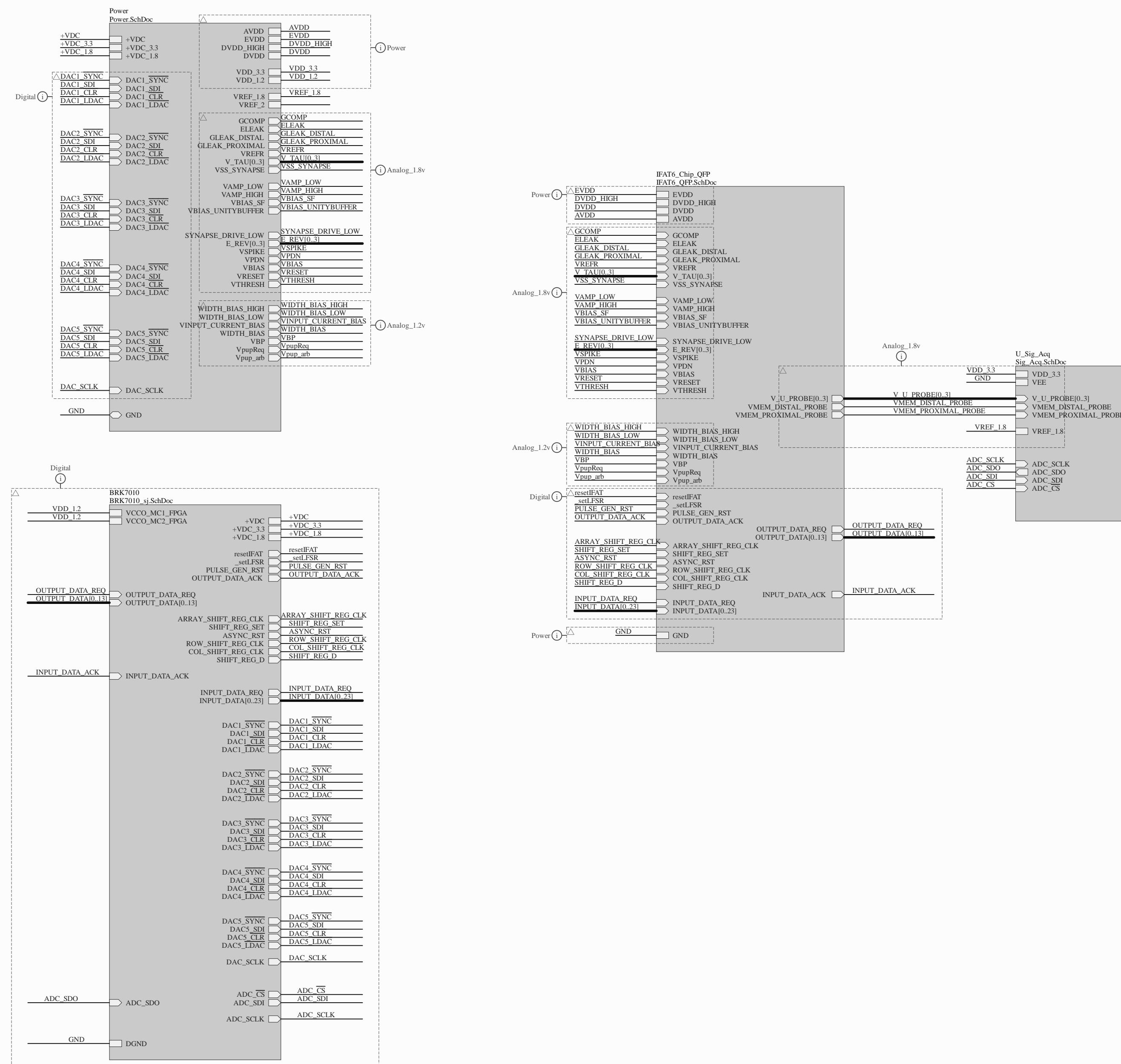
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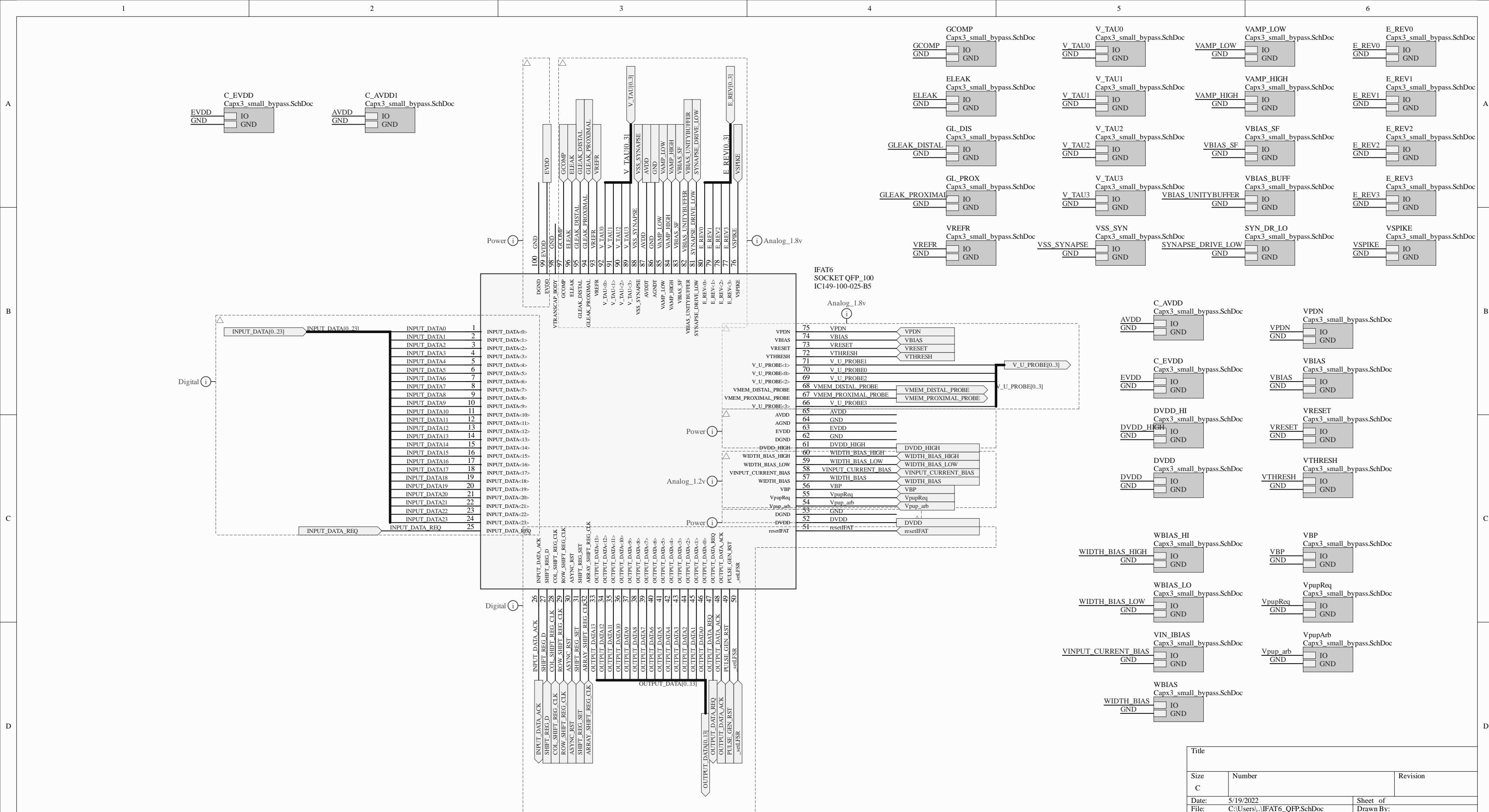
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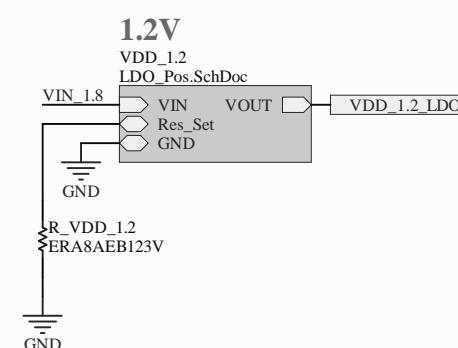
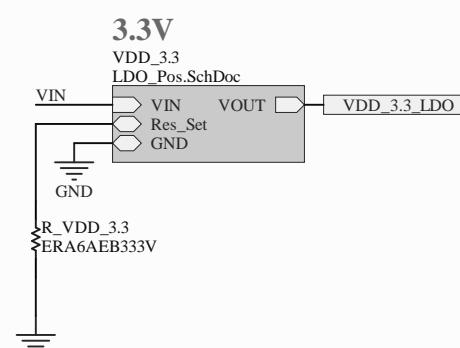
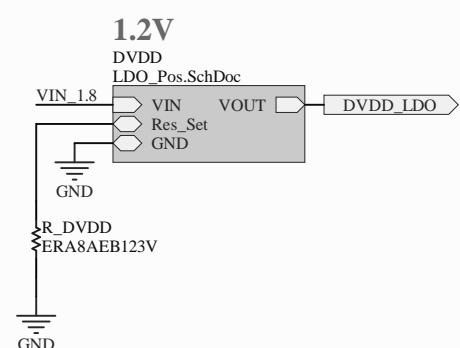
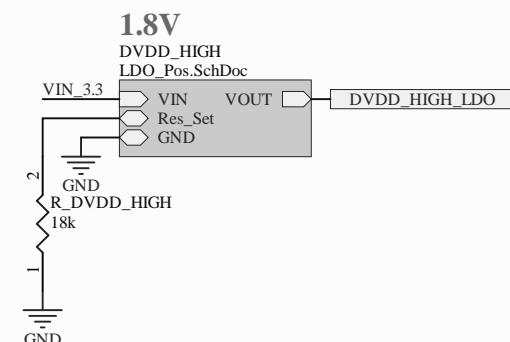
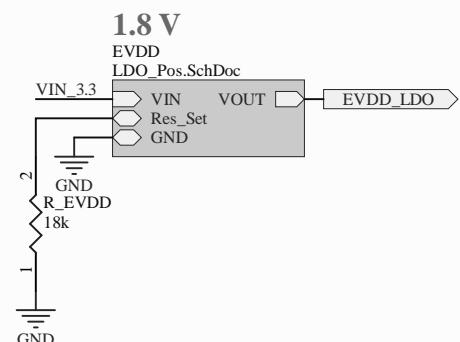
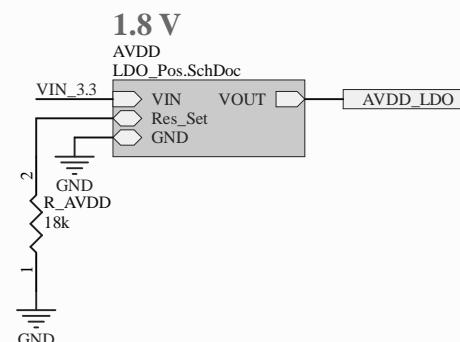




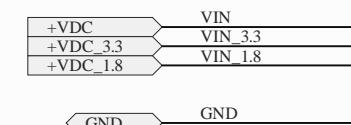
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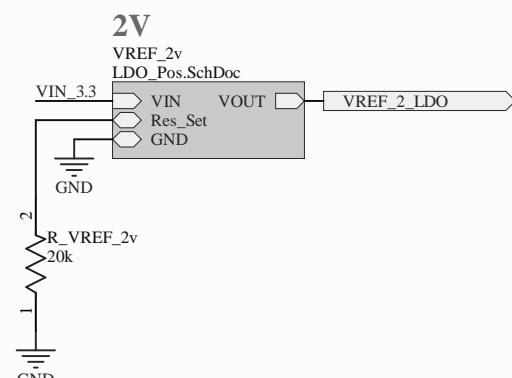
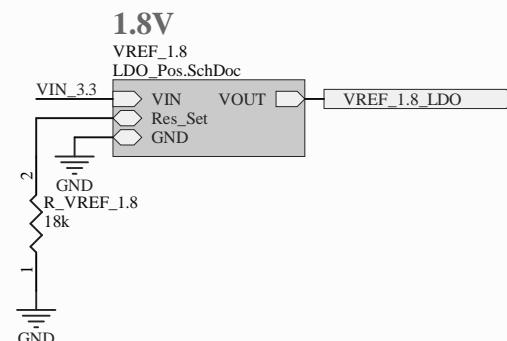
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Supply

LT3045
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 $+VDC = 5.0 \text{ V}$

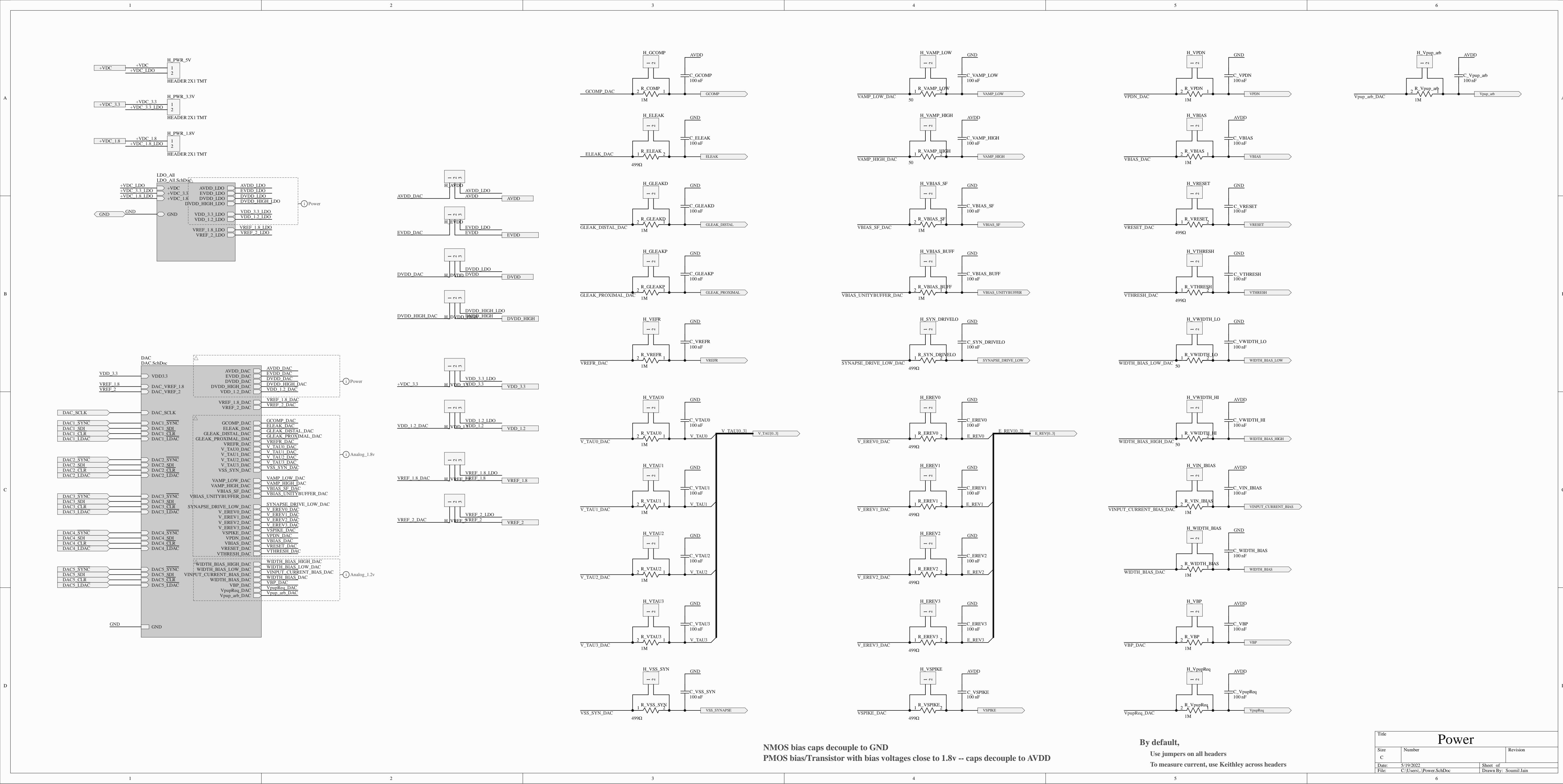


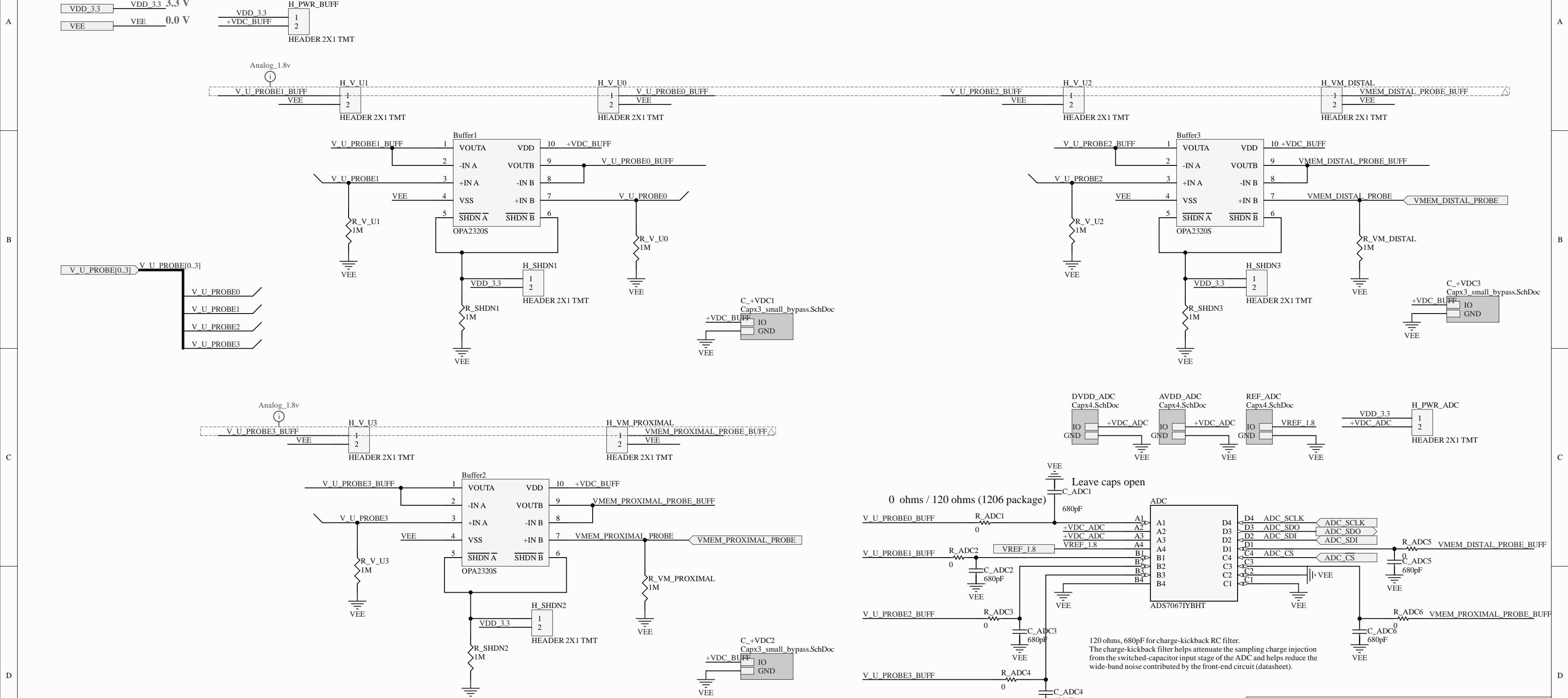
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