root
: Root
system
: System
system_port
sys_port_proxy
: RubyPortProxy
in_ports
ruby
: RubySystem
tcc_cntrl0
: TCCCntrl
sqc_cntrl0
: SQCCntrl
scalar_cntrl0
: SQCCntrl
tcp_cntrl2
: TCPCntrl
tcp_cntrl3
: TCPCntrl
tcp_cntrl0
: TCPCntrl
tcp_cntrl1
: TCPCntrl
coalescer
: VIPERCoalescer
sequencer
: VIPERSequencer
network
: SimpleNetwork
cp_cntrl1
: CPCntrl
cp_cntrl0
: CPCntrl
sequencer
: RubySequencer
sequencer1
: RubySequencer
dir_cntrl0
: DirCntrl
memory_out_port
dma_cntrl0
: GPU_VIPER_DMA_Controller
dma_cntrl1
: GPU_VIPER_DMA_Controller
dma_sequencer
: DMASequencer
: MessageBuffer
in_port
out_port
in_ports
mem_request_port
piobus
: IOXBar
cpu_side_ports
mem_side_ports
mem_ctrls
: MemCtrl
port
cpu1
: X86TimingSimpleCPU
cpu2
: X86TimingSimpleCPU
cpu0
: X86TimingSimpleCPU
interrupts
: X86LocalApic
icache_port
dcache_port
int_requestor
pio
int_responder
workload
: Process
drivers0
: GPUComputeDriver
device
: GPUCommandProcessor
hsapp
: HSAPacketProcessor
dma
l3_tlb
: X86GPUTLB
l3_coalescer
: TLBCoalescer
scalar_tlb
: X86GPUTLB
l2_tlb
: X86GPUTLB
sqc_tlb
: X86GPUTLB
l2_coalescer
: TLBCoalescer
l1_tlb2
: X86GPUTLB
l1_tlb3
: X86GPUTLB
l1_tlb0
: X86GPUTLB
l1_tlb1
: X86GPUTLB
scalar_coalescer
: TLBCoalescer
l1_coalescer2
: TLBCoalescer
l1_coalescer3
: TLBCoalescer
l1_coalescer0
: TLBCoalescer
l1_coalescer1
: TLBCoalescer
sqc_coalescer
: TLBCoalescer
cpu3
: Shader
CUs2
: ComputeUnit
CUs3
: ComputeUnit
CUs0
: ComputeUnit
CUs1
: ComputeUnit
ldsPort
sqc_port
scalar_port
gmTokenPort
memory_port
scalar_tlb_port
translation_port
sqc_tlb_port
localDataStore
: LdsState
cuPort
ldsBus
: Bridge
cpu_side_port
mem_side_port