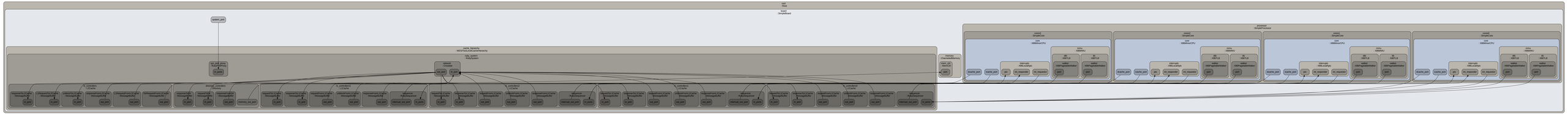

root
: Root

board
: SimpleBoard

processor
: SimpleProcessor

cores0

: SimpleCore

core

: X86MinorCPU

mmu

: X86MMU

itb
: X86TLB

walker
: X86PagetableWalker

dtb
: X86TLB

walker
: X86PagetableWalker

interrupts
: X86LocalApic

cores1

: SimpleCore

core

: X86MinorCPU

mmu

: X86MMU

itb
: X86TLB

walker
: X86PagetableWalker

dtb
: X86TLB

walker
: X86PagetableWalker

interrupts
: X86LocalApic

cores2

: SimpleCore

core

: X86MinorCPU

mmu

: X86MMU

itb
: X86TLB

walker
: X86PagetableWalker

dtb
: X86TLB

walker
: X86PagetableWalker

interrupts
: X86LocalApic

cores3

: SimpleCore

core

: X86MinorCPU

mmu

: X86MMU

itb
: X86TLB

walker
: X86PagetableWalker

dtb
: X86TLB

walker
: X86PagetableWalker

interrupts
: X86LocalApic

memory
: ChanneledMemory

mem_ctrl
: MemCtrl

cache_hierarchy
: MESITwoLevelCacheHierarchy

ruby_system
: RubySystem

network
: Crossbar

l1_controllers0
: L1Cache

requestFromL1Cache
: MessageBuffer

responseFromL1Cache
: MessageBuffer

unblockFromL1Cache
: MessageBuffer

requestToL1Cache
: MessageBuffer

responseToL1Cache
: MessageBuffer

sequencer
: RubySequencer

l1_controllers1
: L1Cache

requestFromL1Cache
: MessageBuffer

responseFromL1Cache
: MessageBuffer

unblockFromL1Cache
: MessageBuffer

requestToL1Cache
: MessageBuffer

responseToL1Cache
: MessageBuffer

sequencer
: RubySequencer

l1_controllers2
: L1Cache

requestFromL1Cache
: MessageBuffer

responseFromL1Cache
: MessageBuffer

unblockFromL1Cache
: MessageBuffer

requestToL1Cache
: MessageBuffer

responseToL1Cache
: MessageBuffer

sequencer
: RubySequencer

l1_controllers3
: L1Cache

requestFromL1Cache
: MessageBuffer

responseFromL1Cache
: MessageBuffer

unblockFromL1Cache
: MessageBuffer

requestToL1Cache
: MessageBuffer

responseToL1Cache
: MessageBuffer

sequencer
: RubySequencer

l2_controllers
: L2Cache

DirRequestFromL2Cache
: MessageBuffer

L1RequestFromL2Cache
: MessageBuffer

responseFromL2Cache
: MessageBuffer

unblockToL2Cache
: MessageBuffer

L1RequestToL2Cache
: MessageBuffer

responseToL2Cache
: MessageBuffer

directory_controllers
: Directory

requestToDir
: MessageBuffer

responseToDir
: MessageBuffer

responseFromDir
: MessageBuffer

sys_port_proxy
: RubyPortProxy

system_port

in_ports icache_port

in_ports

dcache_port int_responder int_requestor port port

interrupt_out_port

icache_port pio

in_ports

dcache_port int_responder int_requestor port port

interrupt_out_port

icache_port pio

in_ports

dcache_port int_responder int_requestor port port

interrupt_out_port

icache_port pio

in_ports

dcache_port int_responder int_requestor port port

interrupt_out_port

port pio

memory_out_port

in_port

out_port out_port out_port out_port out_port out_port out_port out_port out_port out_port out_port out_port out_port out_port out_port out_port

out_port

in_port in_port in_port in_port in_port in_port in_port in_port in_port in_port in_port in_port in_port