

# Automated Design of Synthetic Biology Feedback Circuits

*Jacob Beal, Aaron Adler*

IBE Conference  
March, 2012

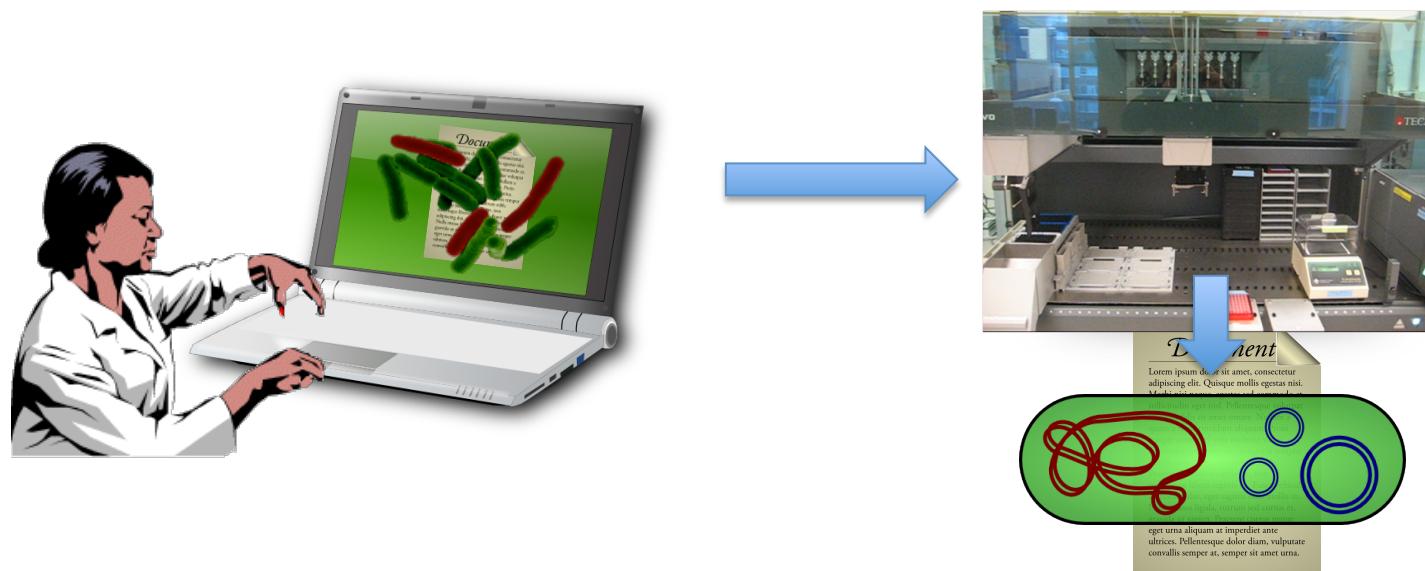


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**Raytheon**  
**BBN Technologies**

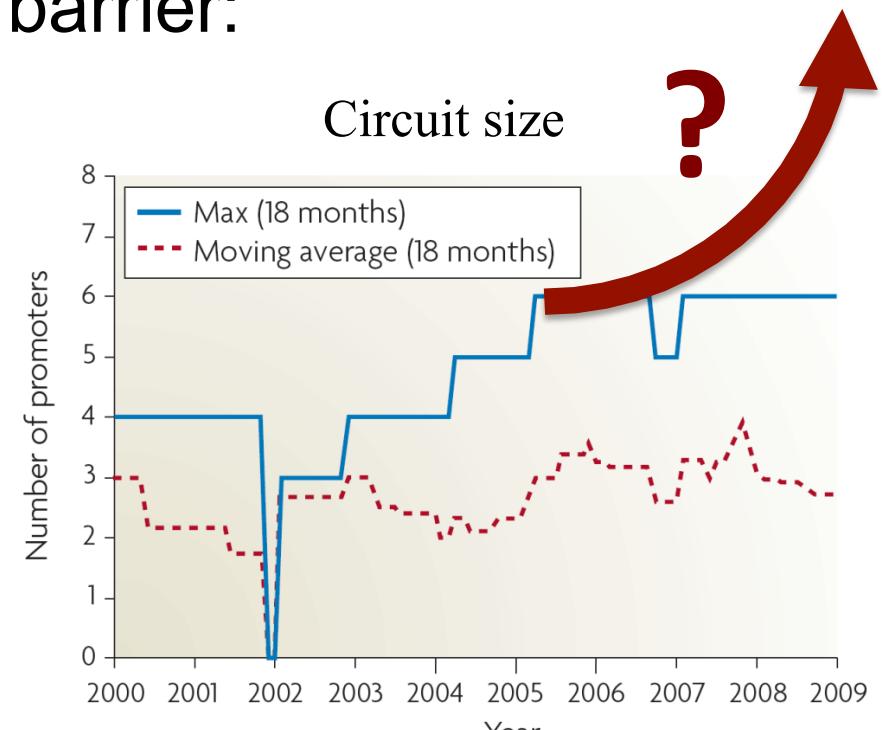
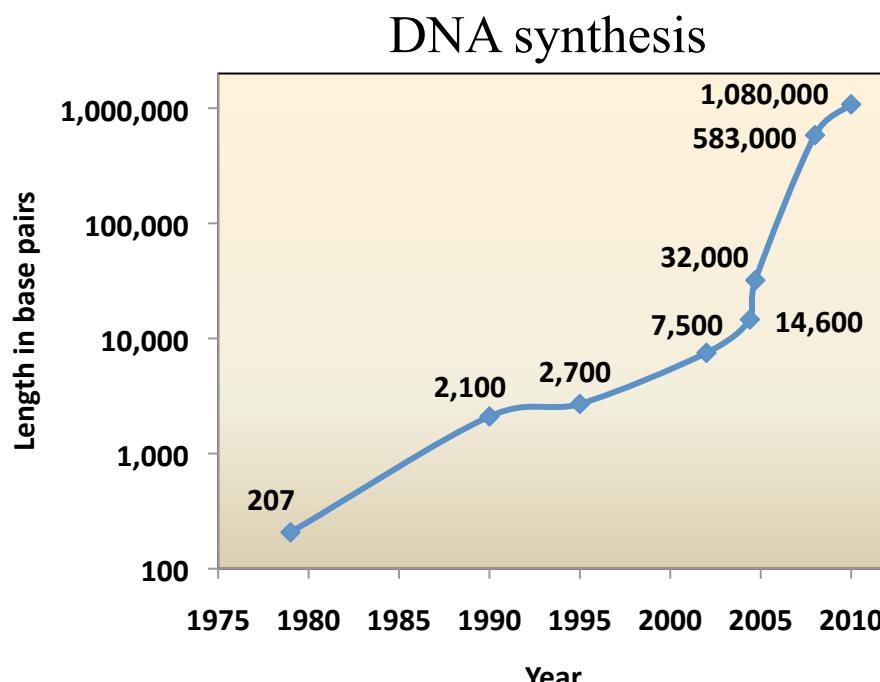
# Vision: WYSIWYG Synthetic Biology

Bioengineering should be like document preparation:



# Why is this important?

- Breaking the complexity barrier:



[Purnick & Weiss, '09]

- Multiplication of research impact
- Reduction of barriers to entry

# Why a tool-chain?

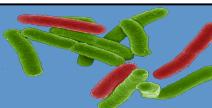
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Organism Level Description

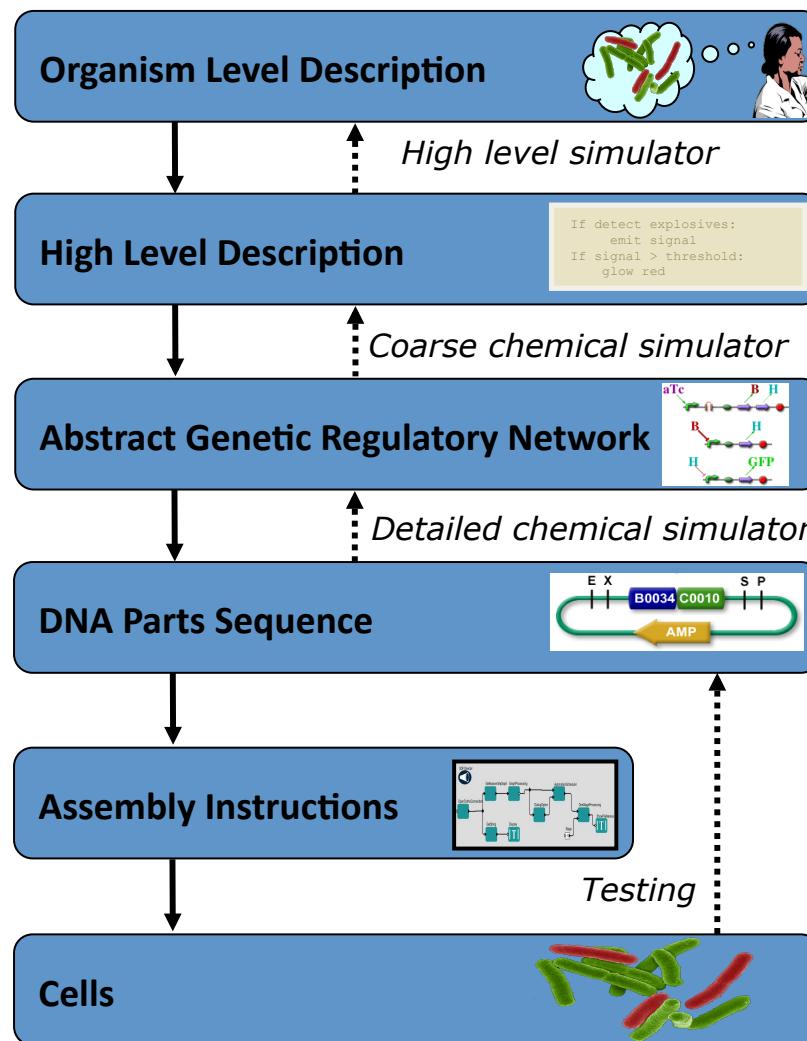


*This gap is too big  
to cross with a  
single method!*

Cells



# The TASBE architecture:



Collaborators:



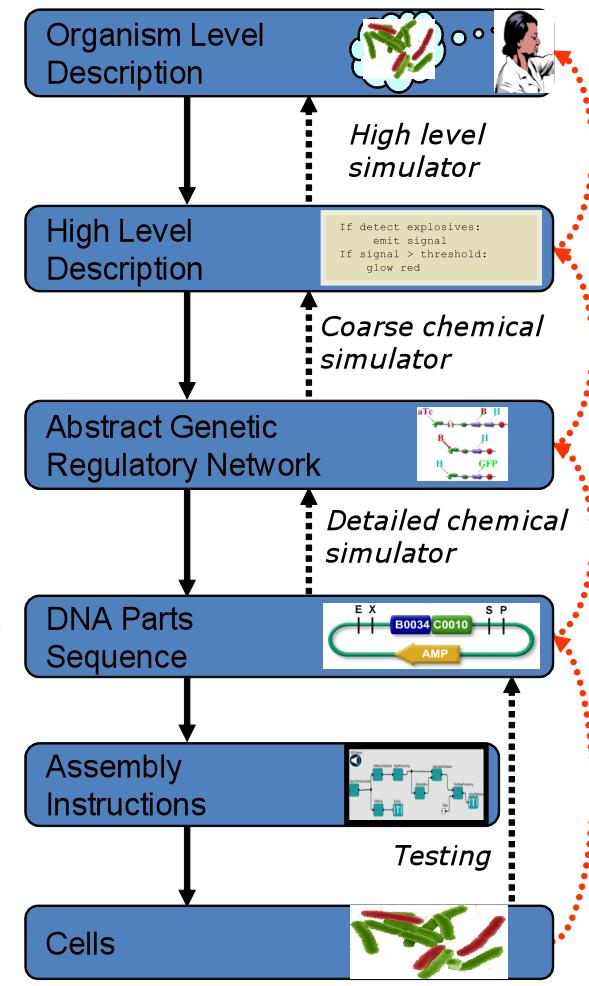
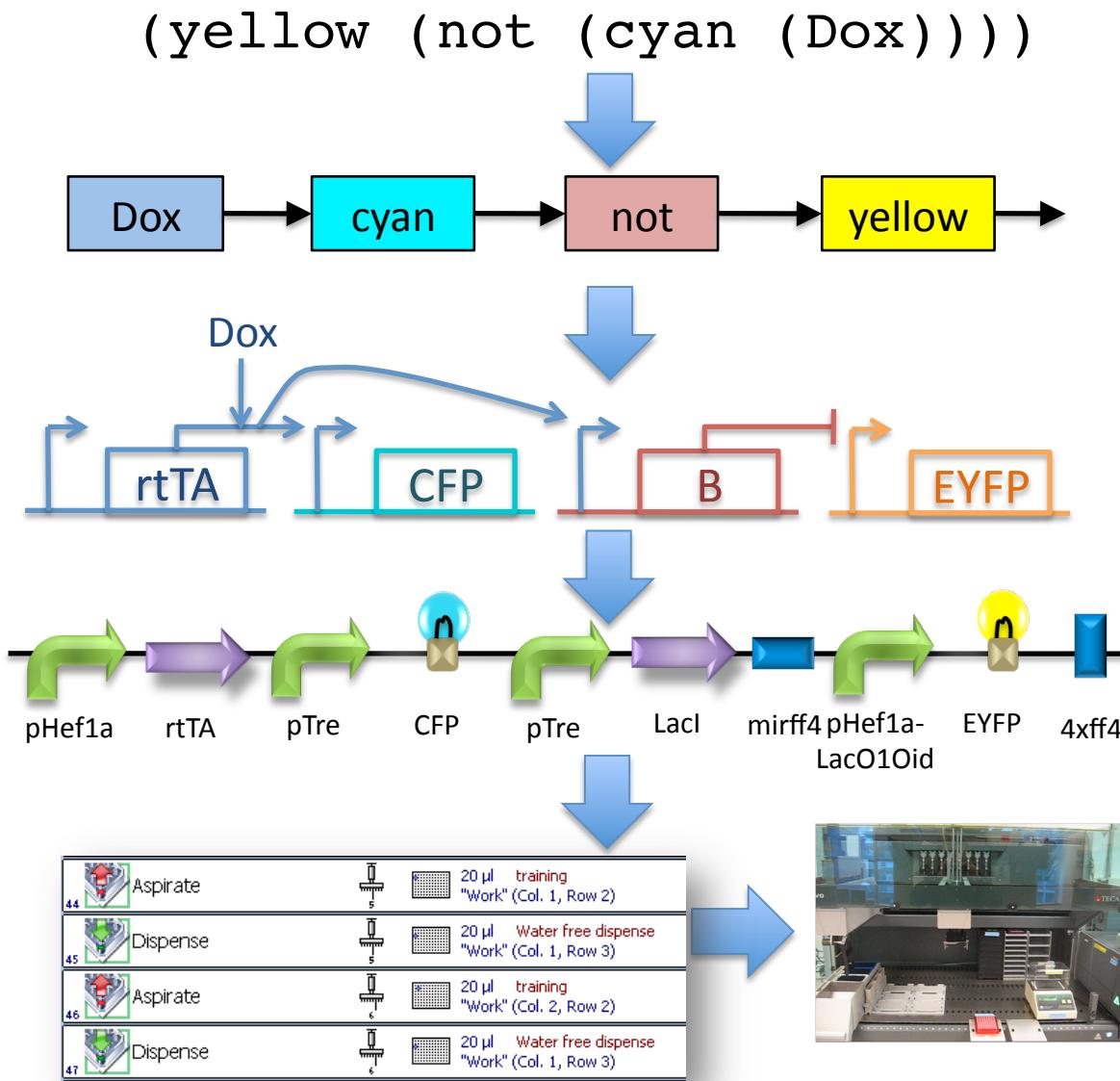
Ron  
Weiss



Douglas  
Densmore

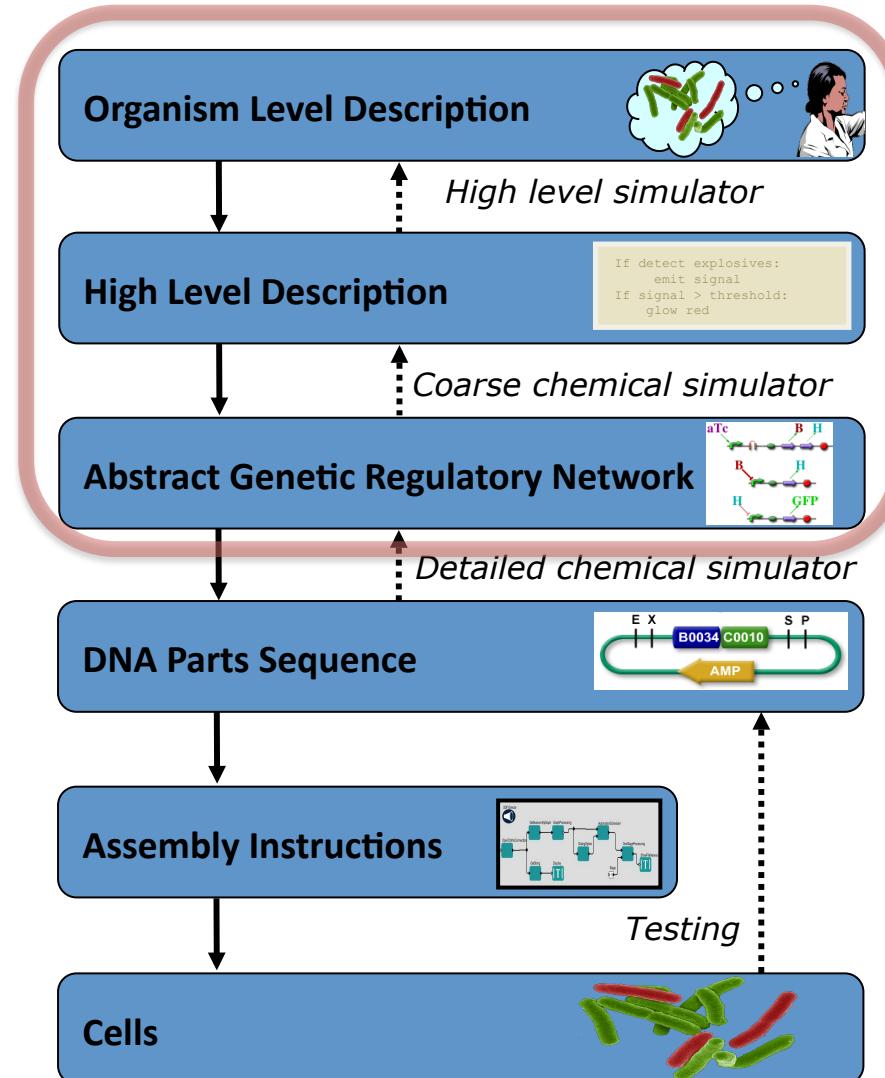
*Modular architecture  
also open for flexible  
choice of organisms,  
protocols, methods, ...*

# A Tool-Chain Example

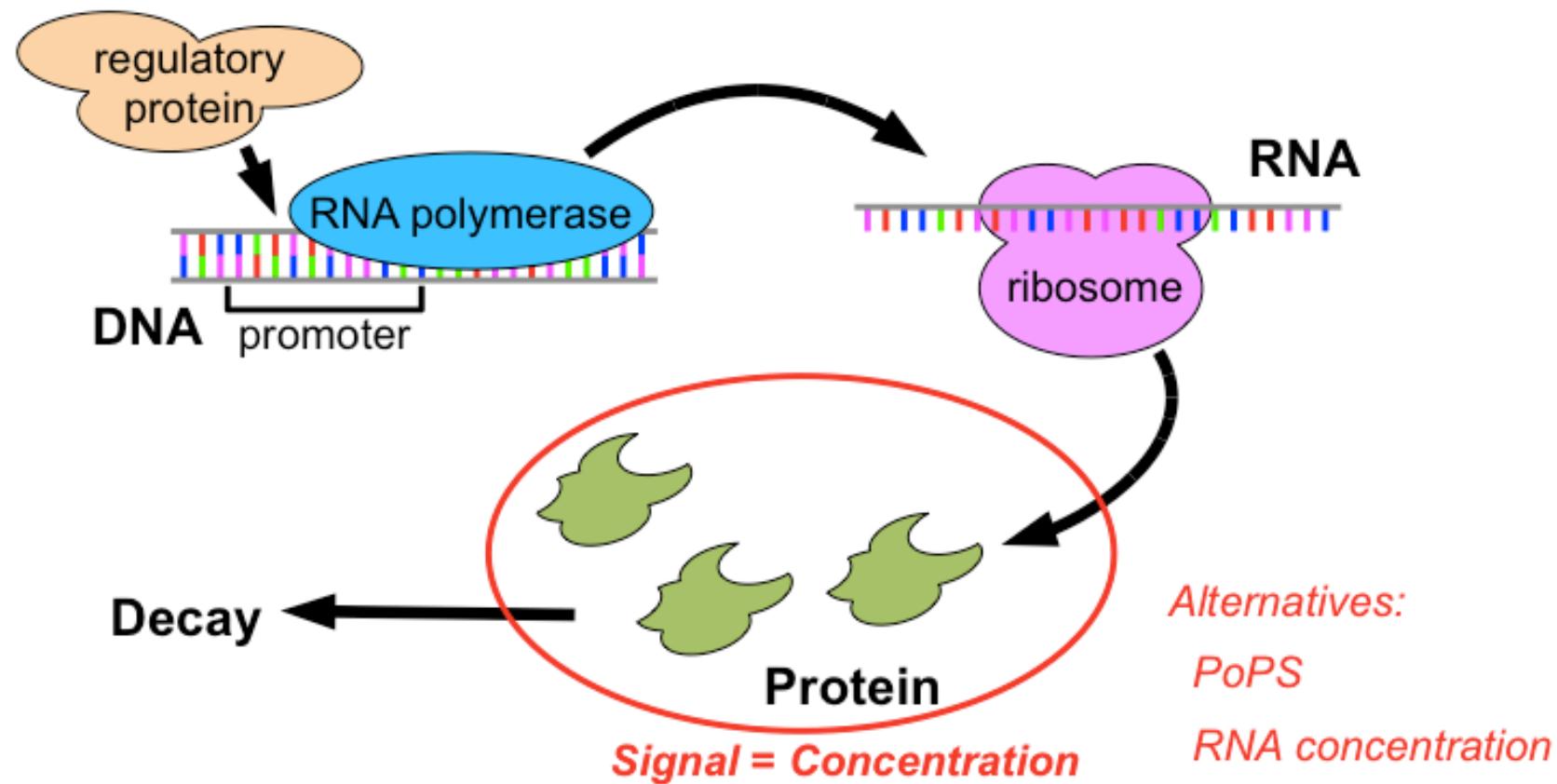


# Today's focus: BioCompiler

## Compilation & Optimization



# Transcriptional Logic



Stabilizes at  $decay = production$

# Motif-Based Compilation

- High-level primitives map to GRN design motifs
  - e.g. logical operators:

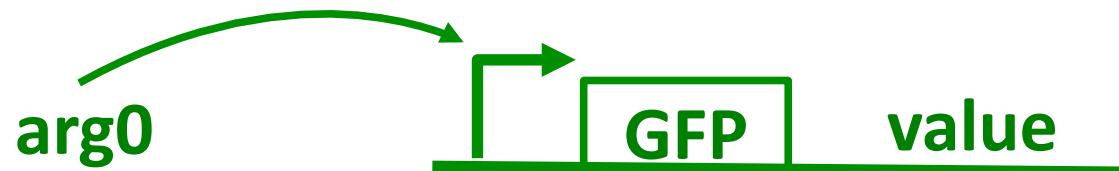
```
(primitive not (boolean) boolean  
:grn-motif ((P high R- arg0 value T)))
```



# Motif-Based Compilation

- High-level primitives map to GRN design motifs
  - e.g. logical operators, **actuators**:

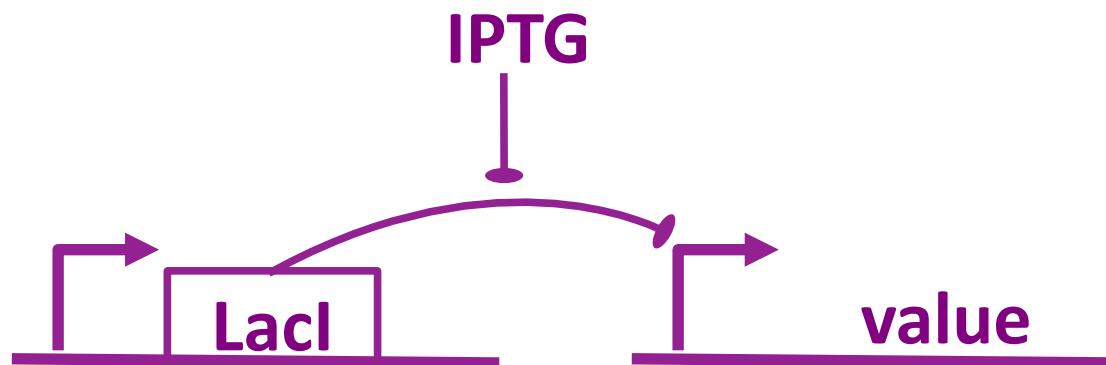
```
(primitive green (boolean) boolean :side-effect  
:type-constraints ((= value arg0))  
:grn-motif ((P R+ arg0 GFP|arg0 value T)))
```



# Motif-Based Compilation

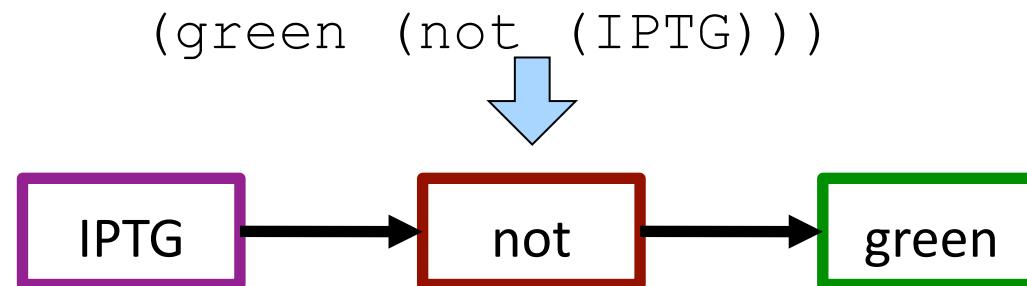
- High-level primitives map to GRN design motifs
  - e.g. logical operators, actuators, **sensors**:

```
(primitive IPTG () boolean  
  :grn-motif ((P high LacI|boolean T)  
    (RXN (IPTG|boolean) represses LacI)  
    (P high R- LacI value T)))
```



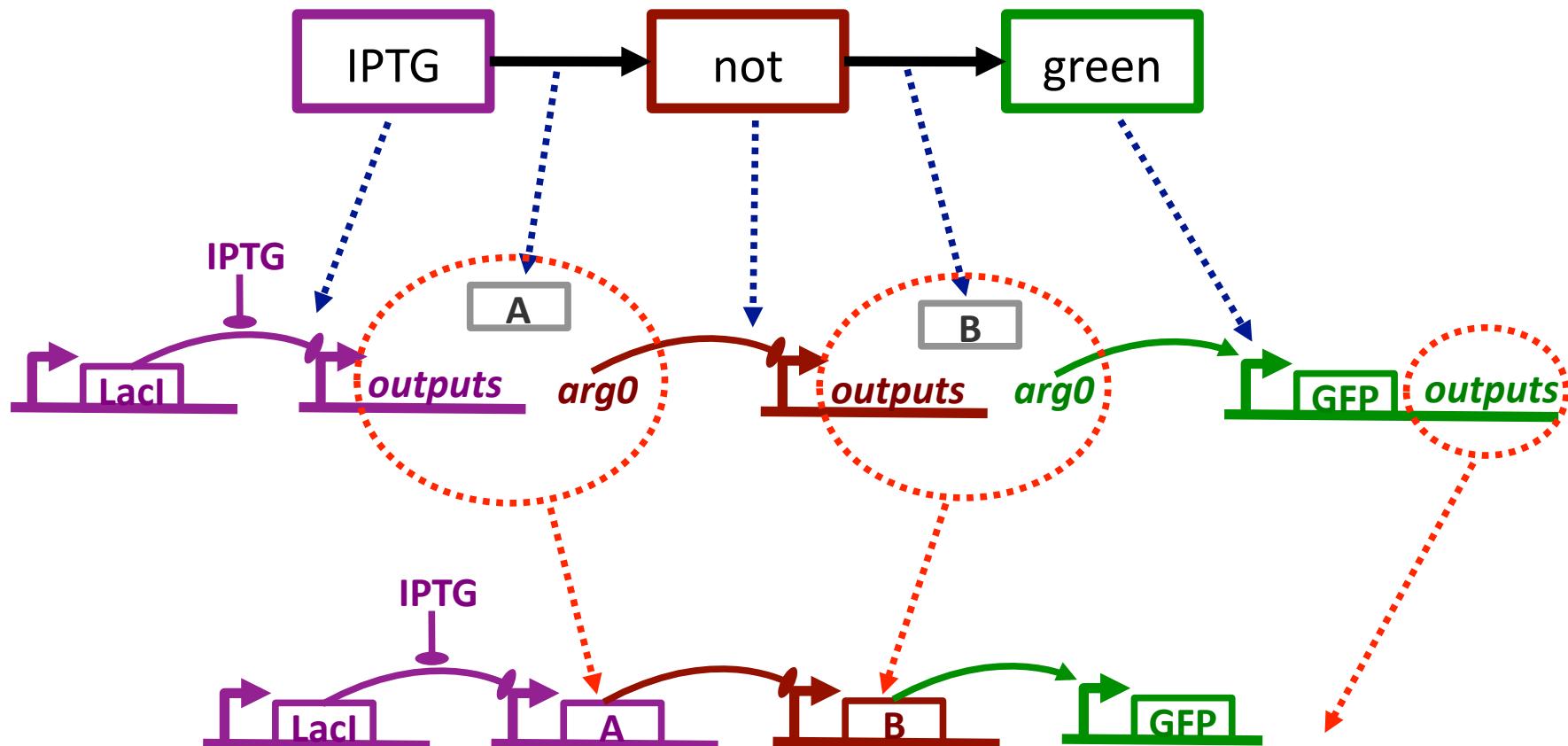
# Motif-Based Compilation

- Functional program gives dataflow computation:

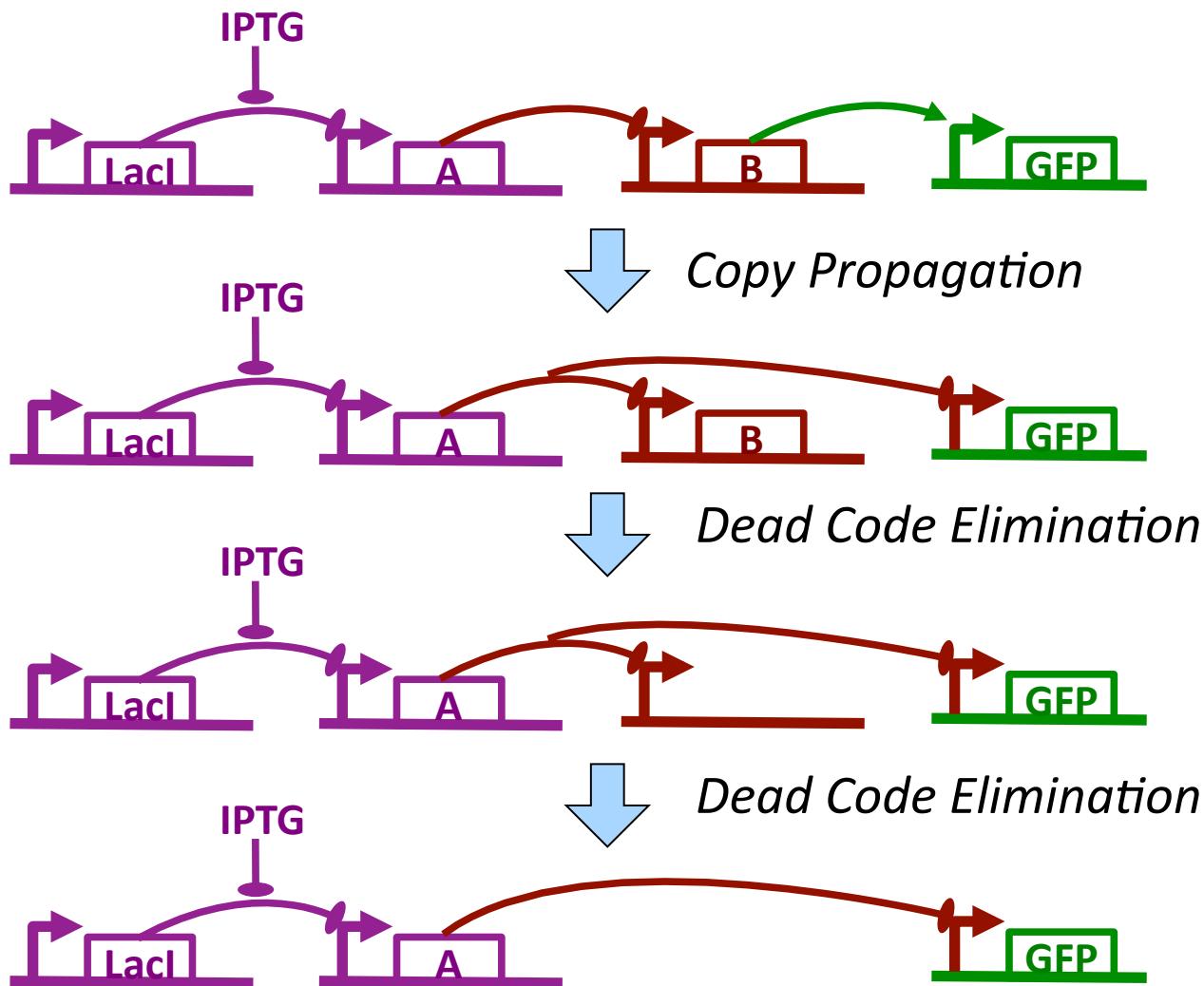


# Motif-Based Compilation

- Operators translated to motifs:



# Optimization



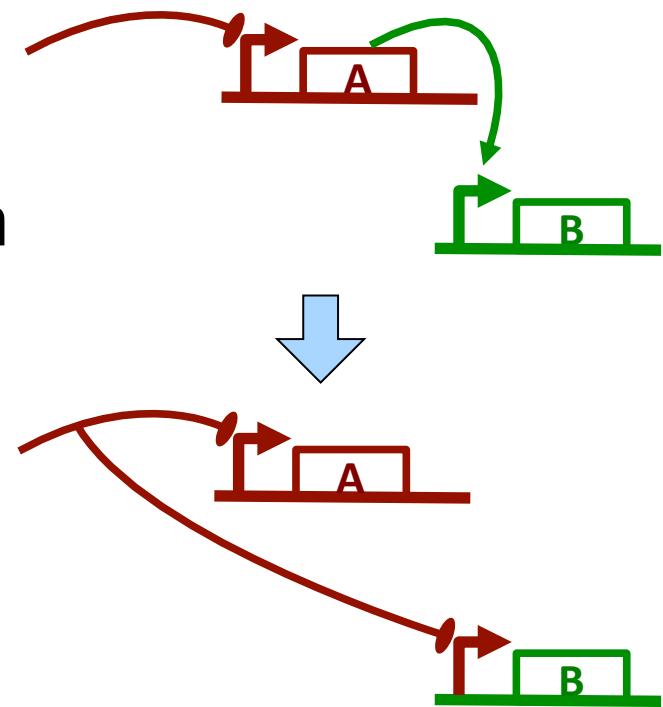
# GRN Optimization Library

---

- Copy Propagation
- Dead Code Elimination
- Double-Negative Elimination
- Constant Eliminator
- CSE: Output Consolidation
- CSE: Merge Duplicate Inputs
- NOR Compression

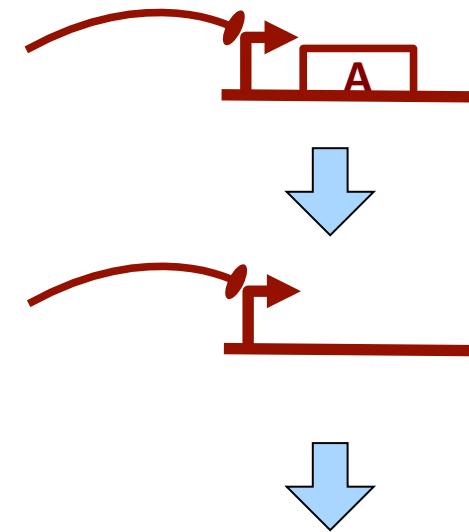
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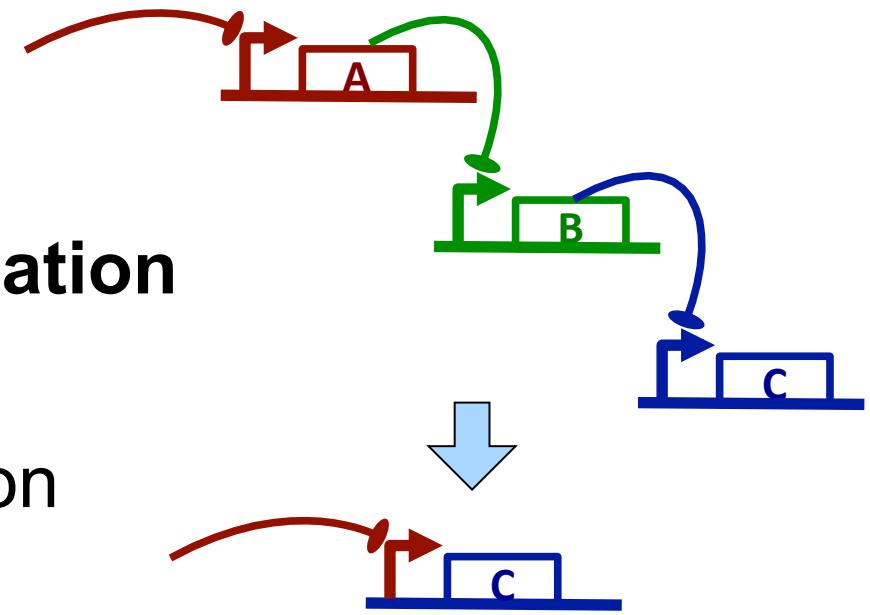
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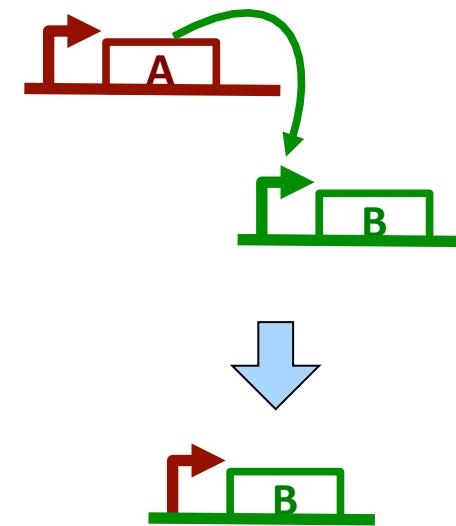
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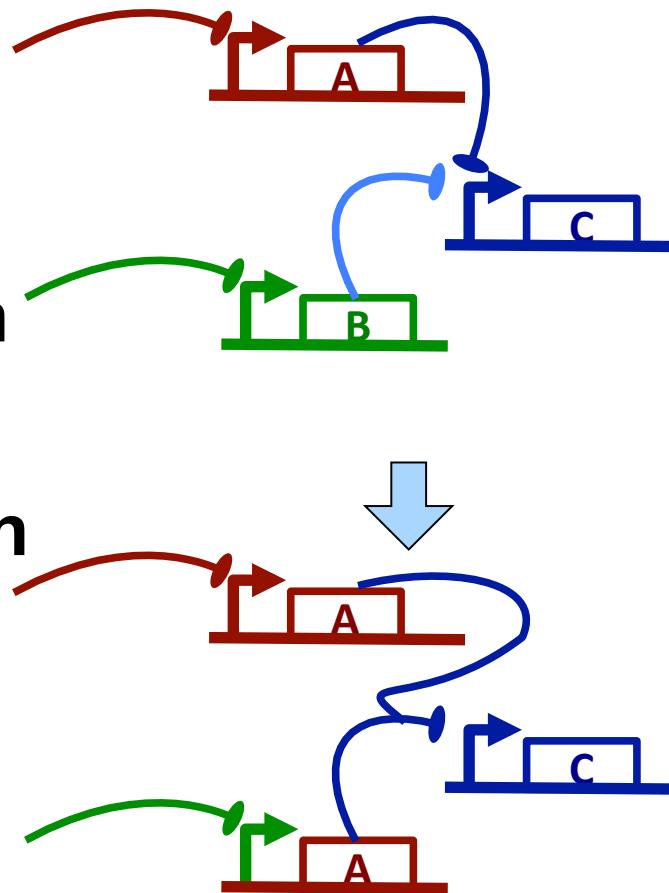
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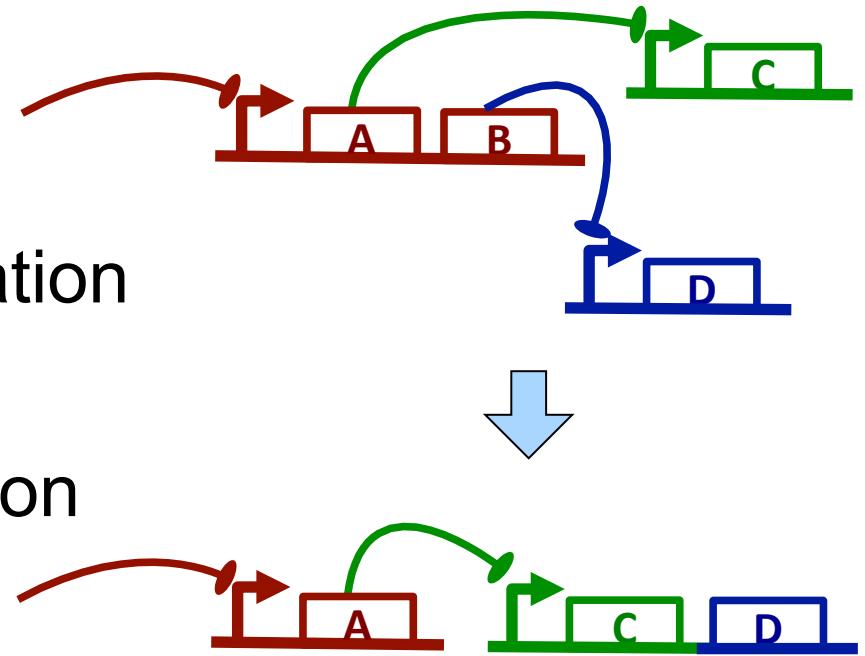
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*GRN-specific*

# GRN Optimization Library

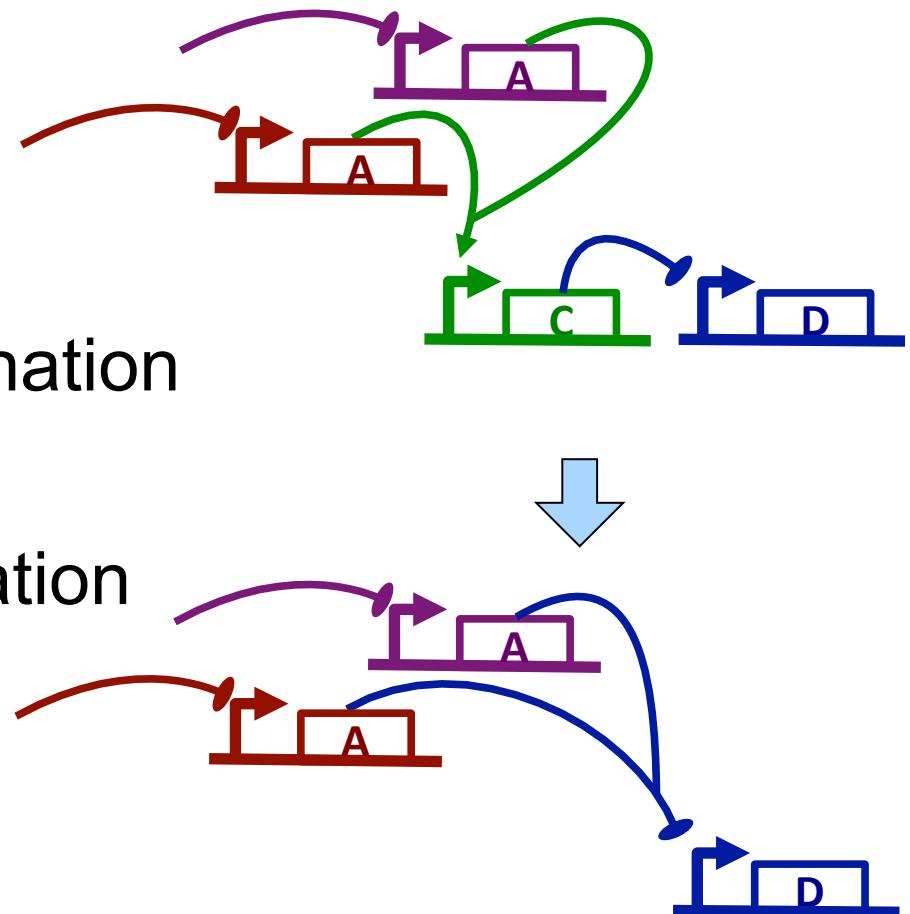
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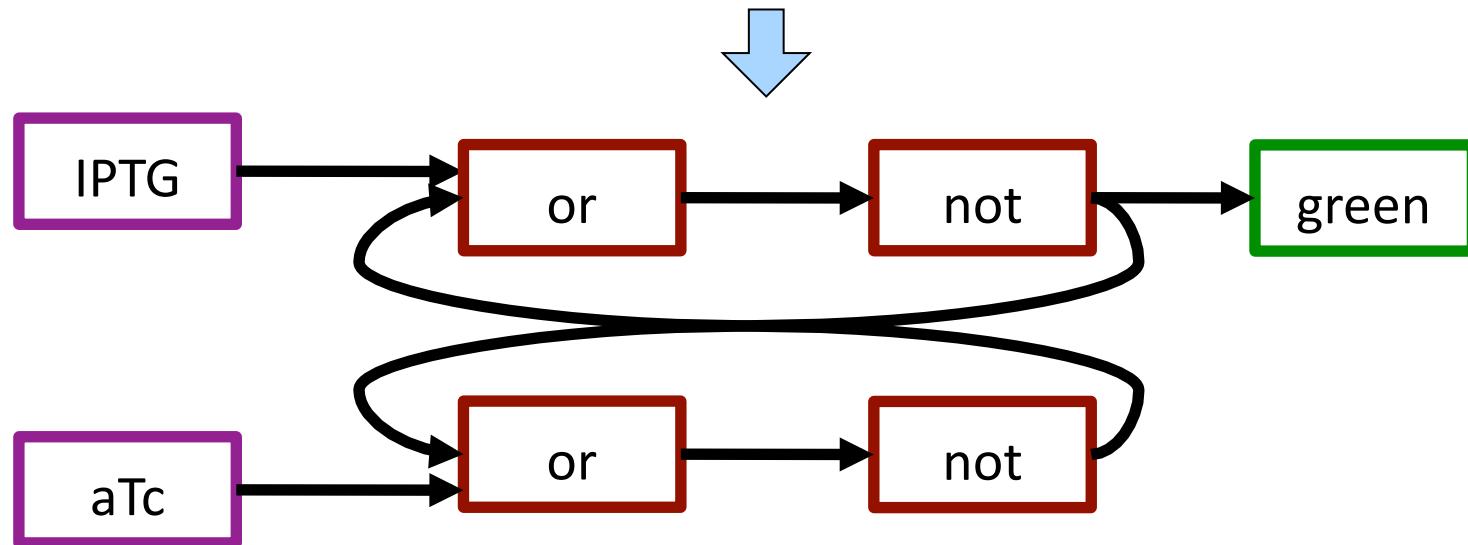


*GRN-specific*

# Feedback System: SR-Latch

```
(def sr-latch (s r)
  (letfed+ ((o boolean (not (or r o-bar)))
            (o-bar boolean (not (or s o))))
    o))
```

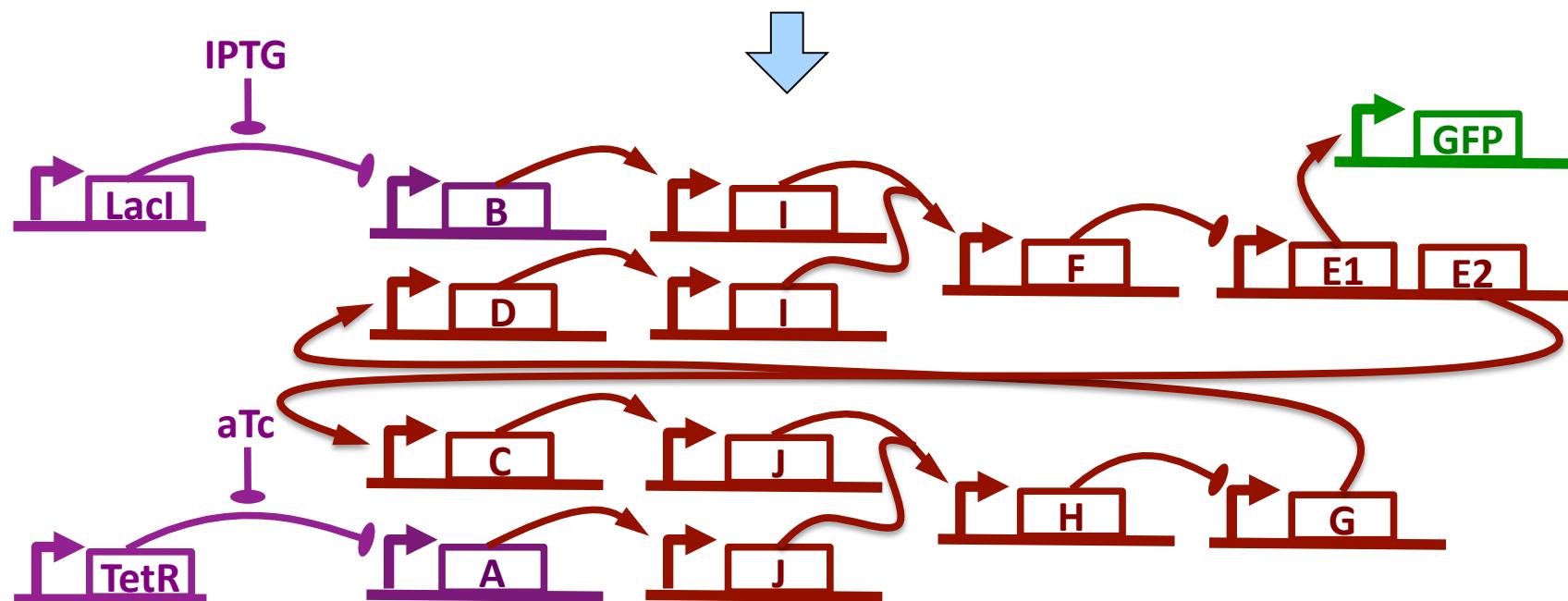
(**green** (**sr-latch** (**aTc**) (**IPTG**)))



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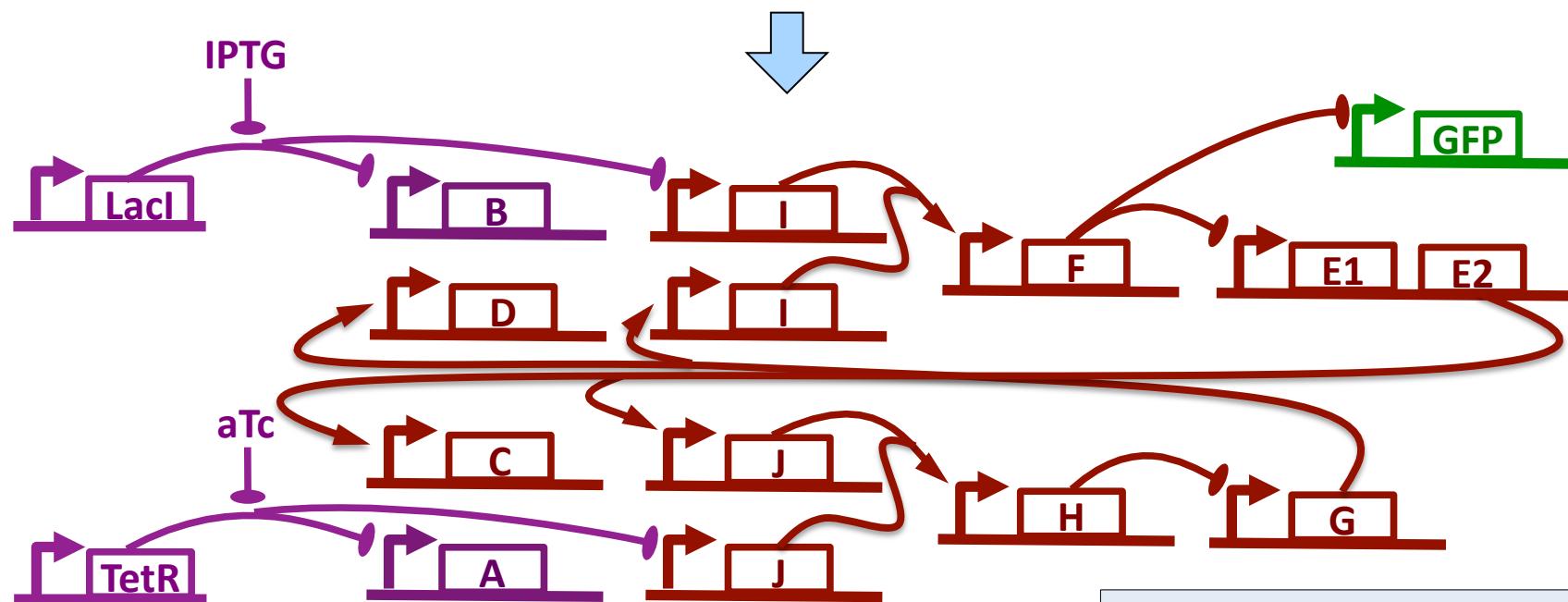


*Unoptimized: 15 functional units, 13 transcription factors*

# Optimization of Complex Designs

```
(def sr-latch (s r)
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    o))  

(green (sr-latch (aTc) (IPTG)))
```



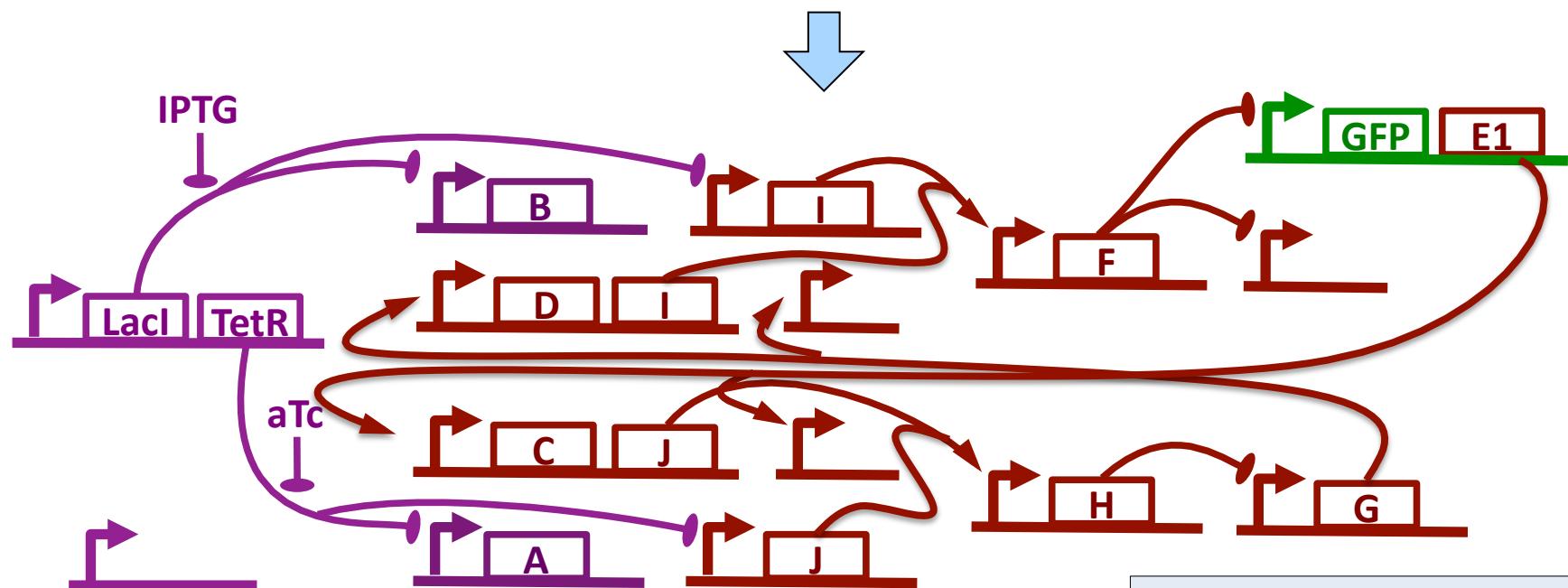
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*Copy Propagation*<sub>25</sub>

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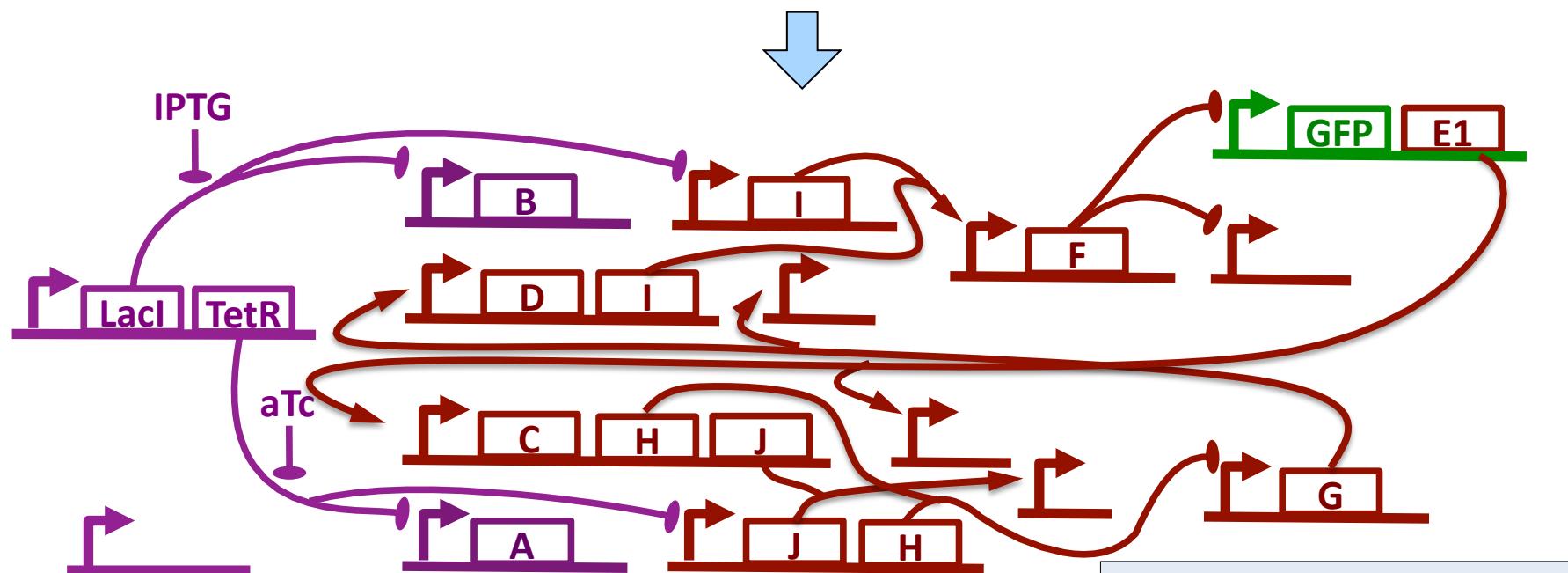
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**Common Subexp. Elim.**

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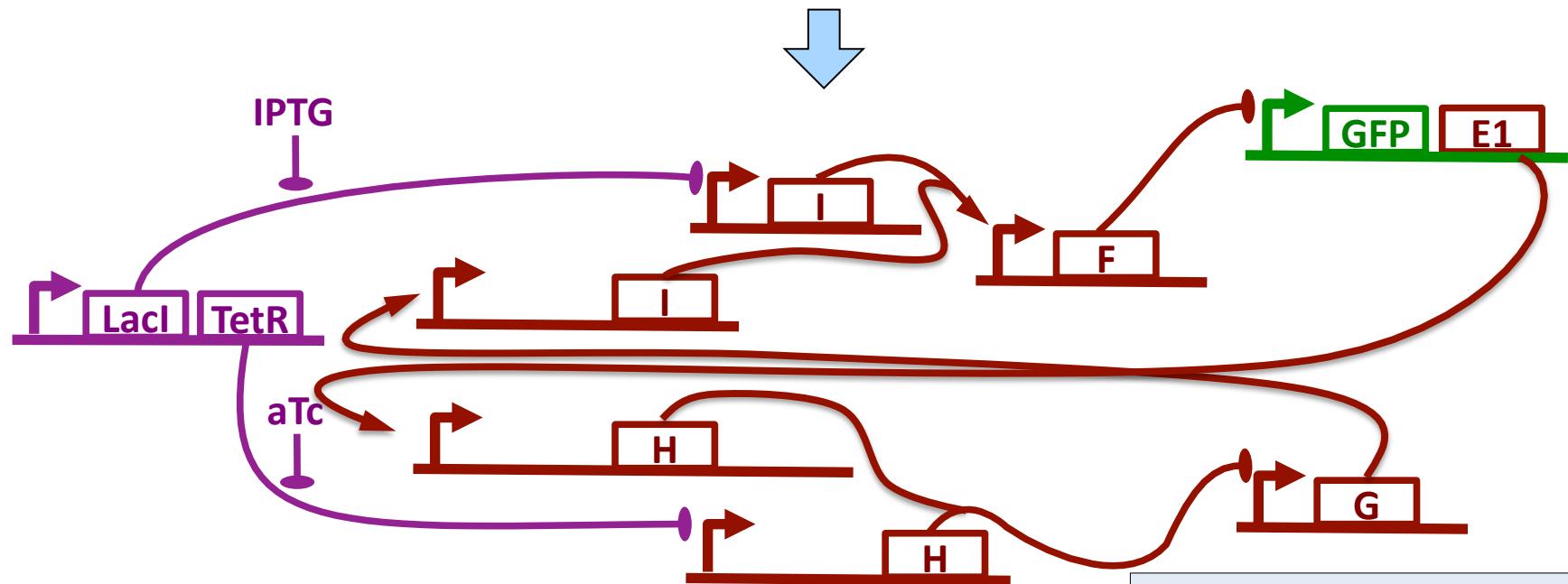
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**NOR Compression**<sub>27</sub>

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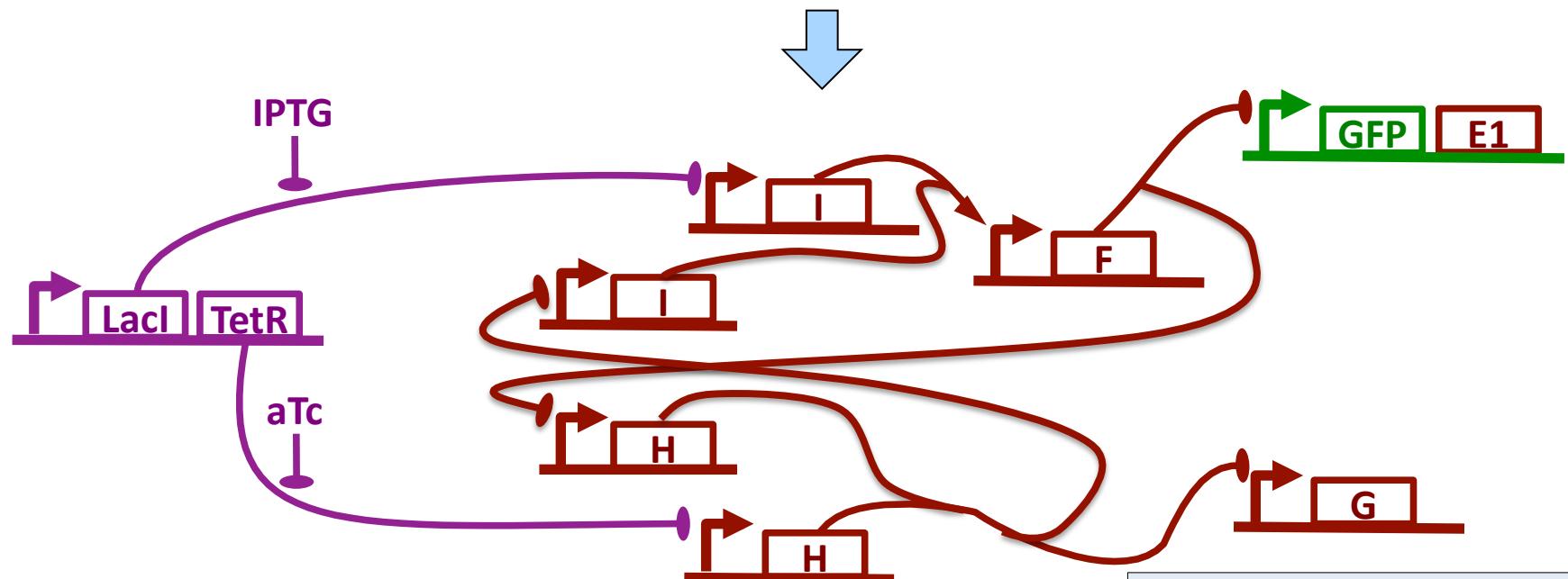
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**Dead Code Elimination** 28

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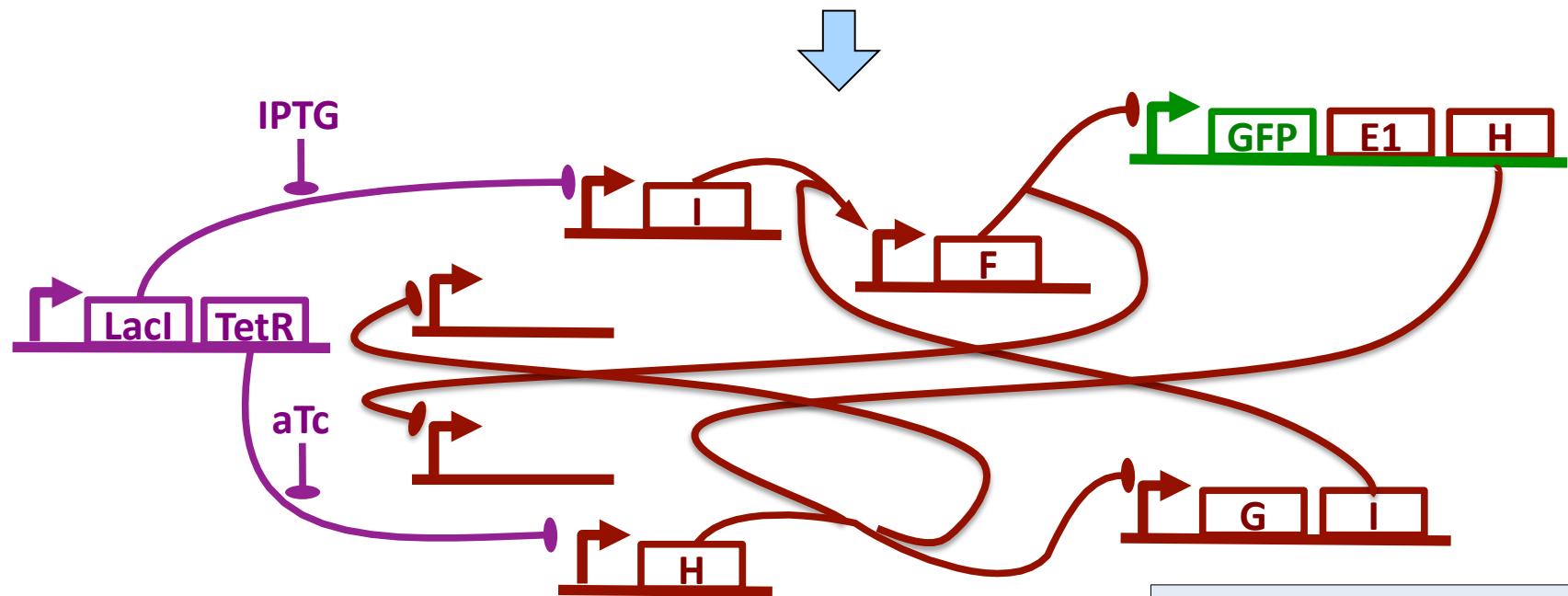
*Unoptimized: 15 functional units, 13 transcription factors*

*Copy Propagation*<sub>29</sub>

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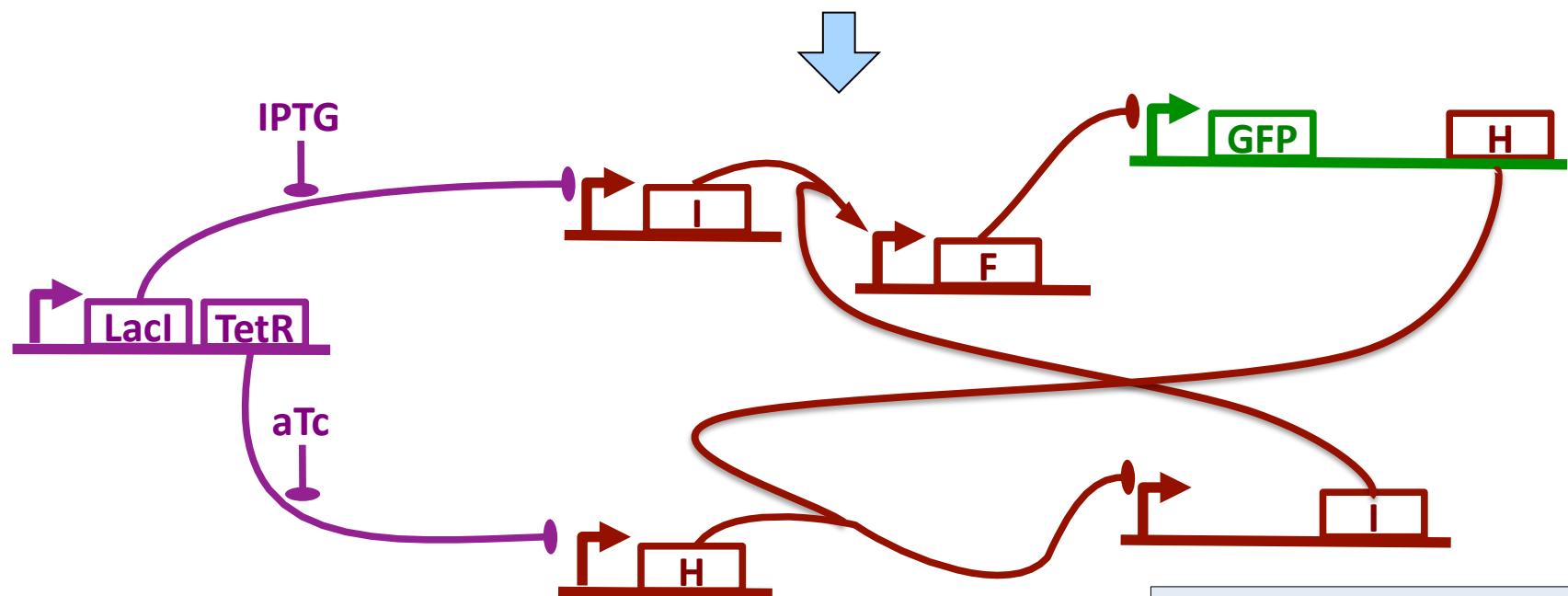
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*Common Subexp. Elim.*

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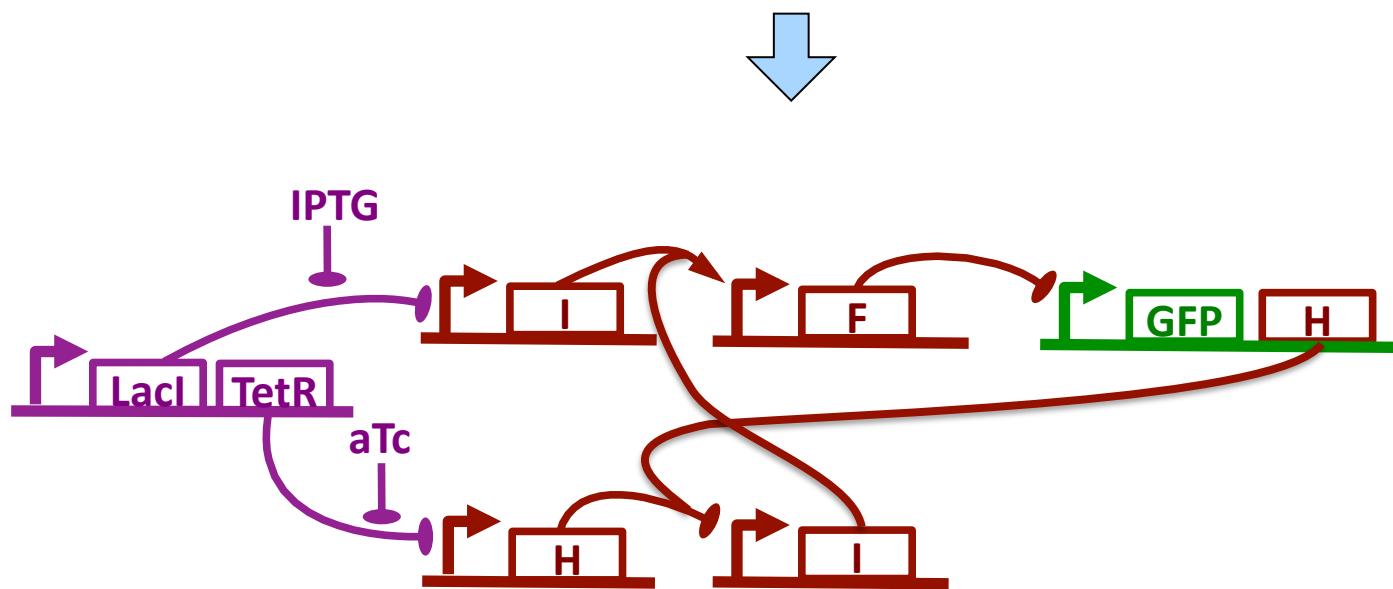
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**Dead Code Elimination** 91

# Optimization of Complex Designs

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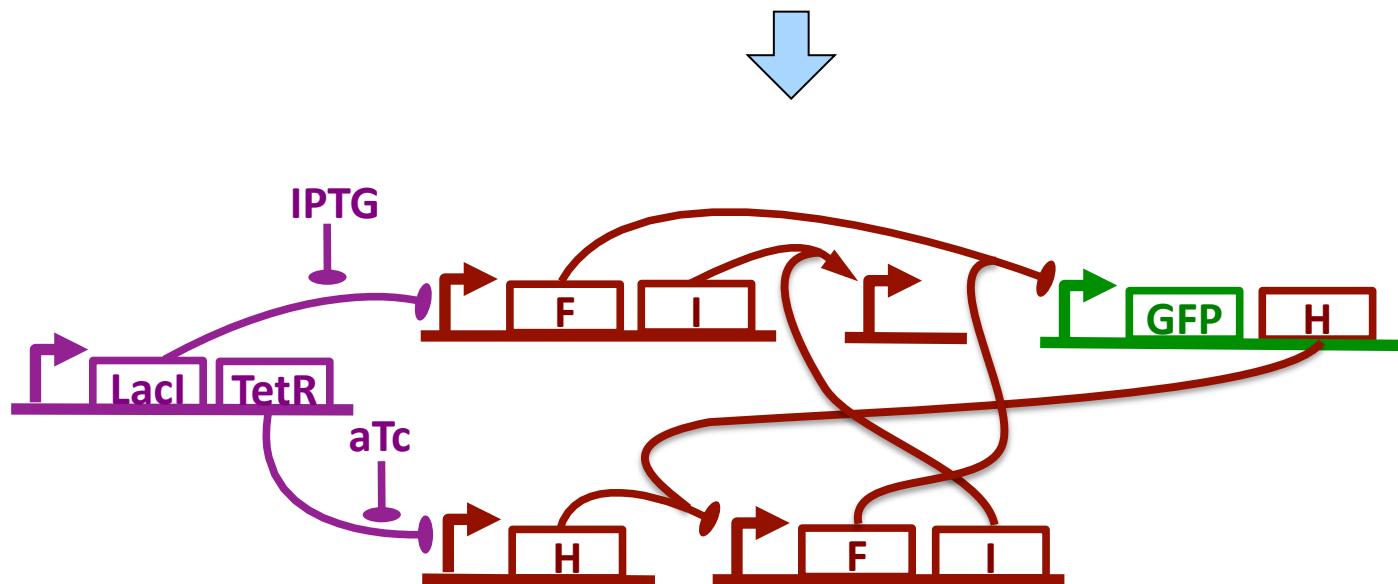


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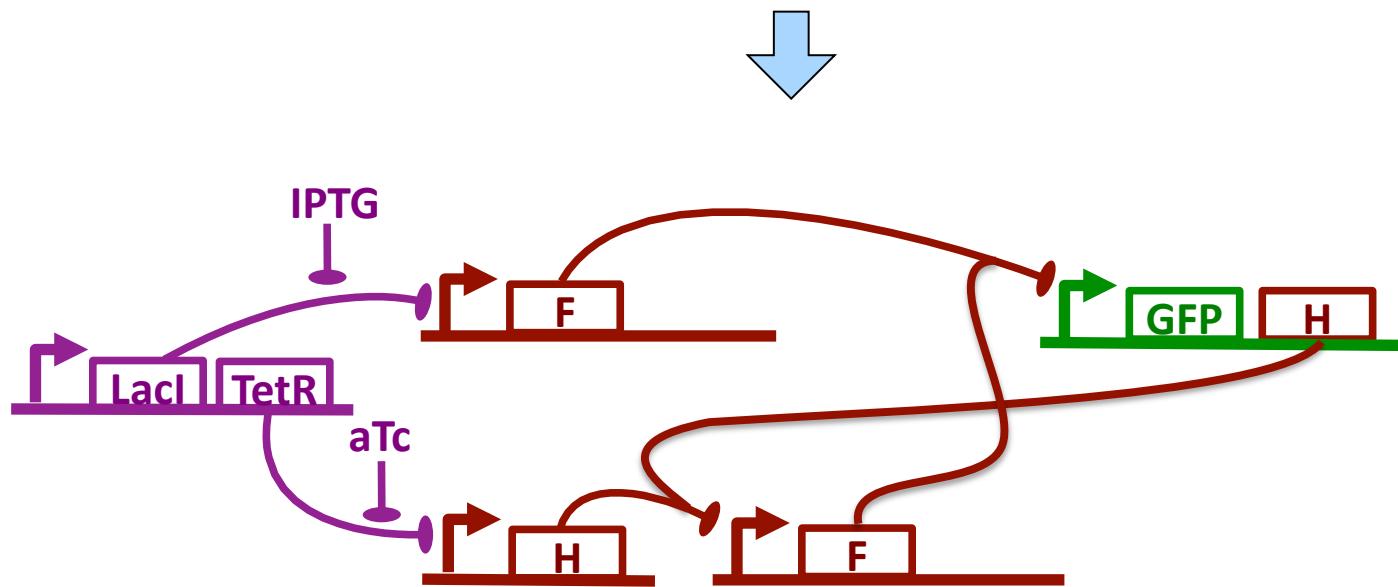
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**NOR Compression**<sub>33</sub>

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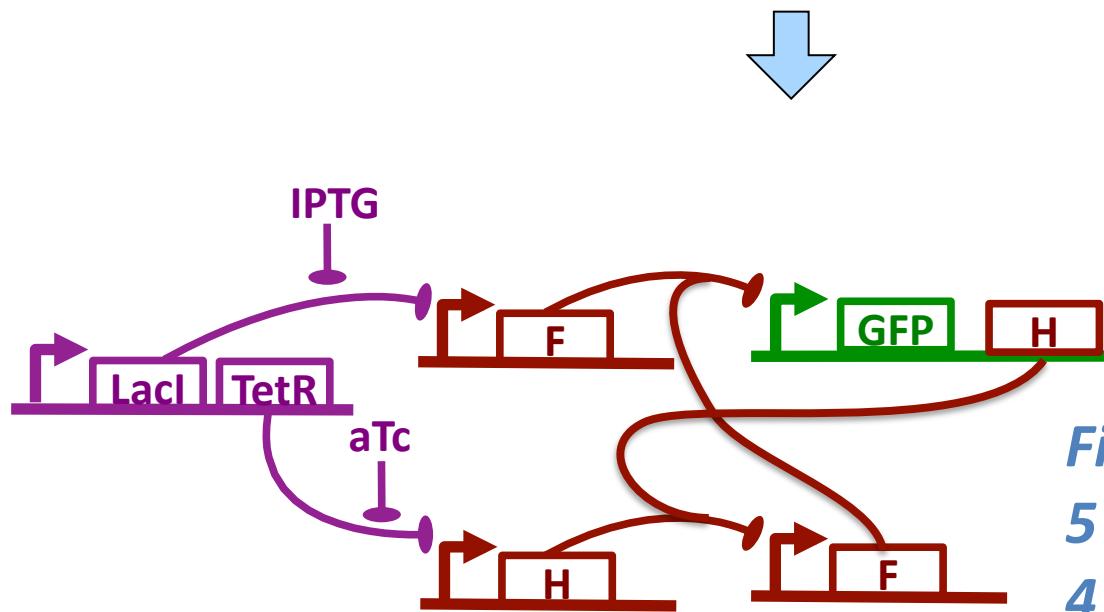
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**Dead Code Elimination**

# Optimization of Complex Designs

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    o))  

(green (sr-latch (aTc) (IPTG)))
```



*Final Optimized:  
5 functional units  
4 transcription factors*

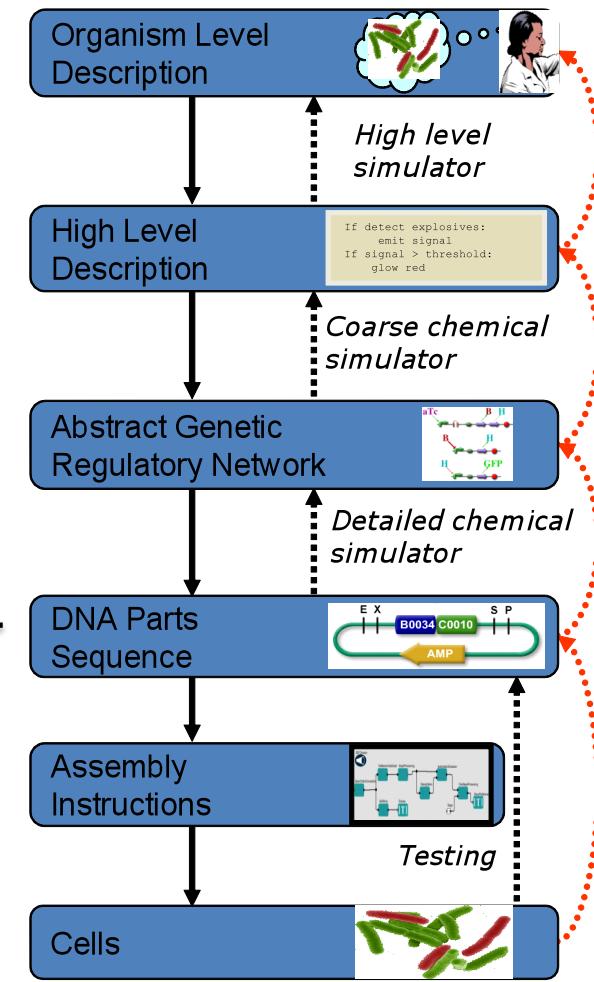
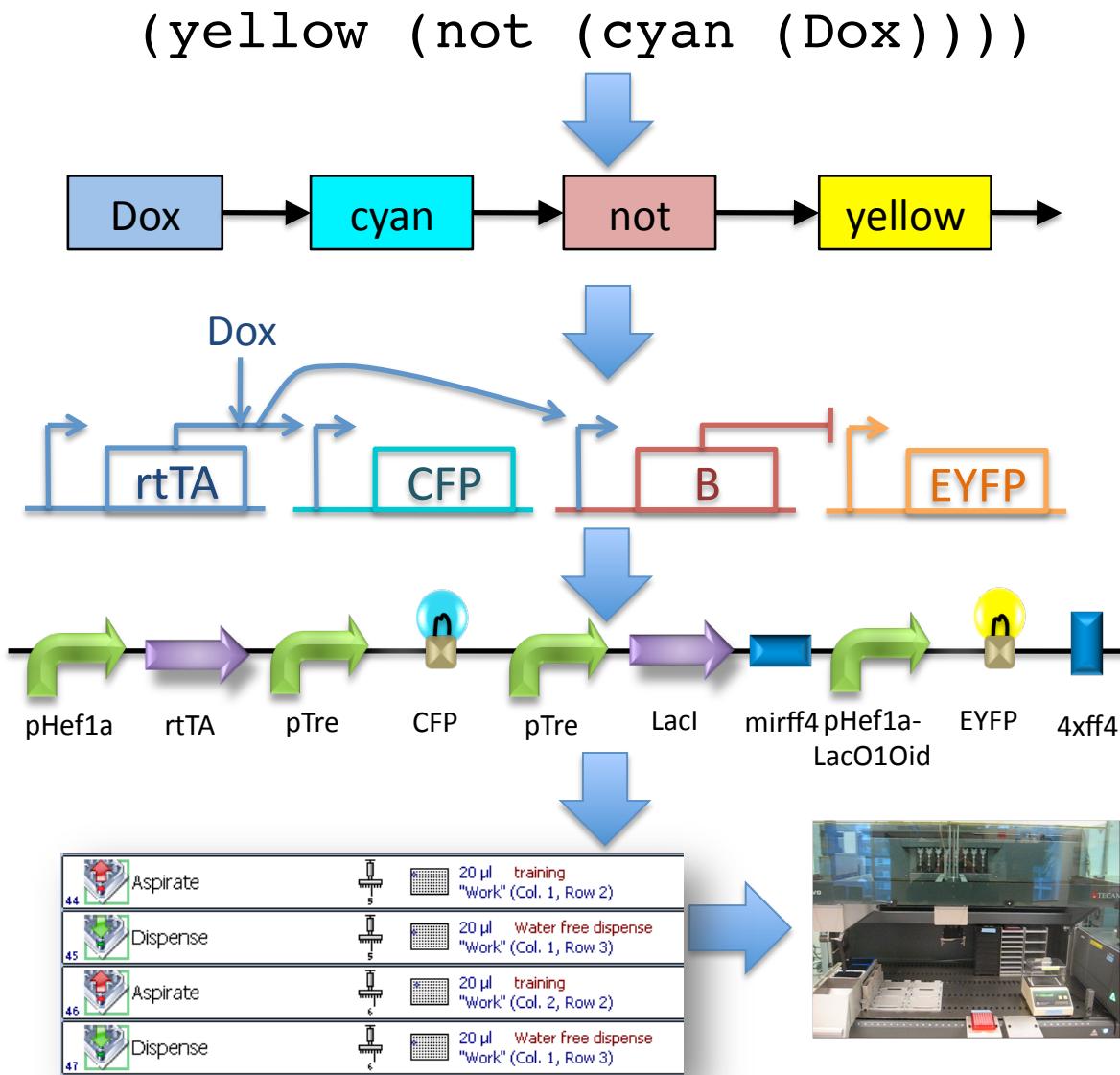
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# Compilation & Optimization Results:

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- Automated GRN design for arbitrary boolean logic and feedback systems
  - Verification in ODE simulation
- Optimization competitive with human experts:
  - Test systems have 25% to 71% complexity reduction
  - Optimized systems are often homologous with hand designed networks
- Routine use within team

# Onward Through the Tool-Chain...



# Contributions

---

- Boolean & feedback logic circuits can be expressed in high-level language.
- GRN-specific optimizations allow dramatic optimization of circuit size.
- Automatically generated circuits are competitive with hand-designs created by experts.

Next steps: automatic verification, numerical computation, intercellular communication