60V N-CHANNEL MOSFET H-BRIDGE

SUMMARY

 $V_{(BR)DSS}$ = 60V : $R_{DS(on)}$ = 0.3 Ω ; I_{D} = 1.6A

DESCRIPTION

This new generation of trench MOSFETs from Zetex utilizes a unique structure that combines the benefits of low on-resistance with fast switching speed. This makes them ideal for high efficiency, low voltage, power management applications.



SM8

FEATURES

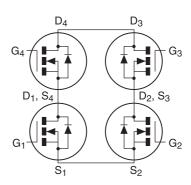
- Compact package
- Low on state losses
- Low drive requirements
- Operates up to 60V
- 1 Amp continuous rating

APPLICATIONS

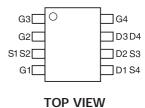
Motor control

ORDERING INFORMATION

DEVICE	REEL SIZE	TAPE WIDTH	QUANTITY PER REEL
ZXMHN6A07T8TA	7"	12mm	1,000 units
ZXMHN6A07T8TC	13"	12mm	4,000 units



PINOUT



DEVICE MARKING

ZXMH N6A07



ABSOLUTE MAXIMUM RATINGS

PARAMETER	SYMBOL	LIMIT	UNIT
Drain-source voltage	V _{DSS}	60	V
Gate-source voltage	V _{GS}	± 20	V
Continuous drain current (V _{GS} = 10V; T _A = 25°C) ^{(b) (d)}	I _D	1.6	А
$(V_{GS} = 10V; T_A = 70^{\circ} C)^{(b)}$		1.3	А
$(V_{GS} = 10V; T_A = 25^{\circ}C)^{(a)}$		1.4	А
Pulsed drain current (c)	I _{DM}	9	A
Continuous source current (body diode) (b) (d)	I _S	1	А
Pulsed source current (body diode) (c)	I _{SM}	9	А
Total power dissipation at T _A = 25° C	P _{TOT}		
Any Single transistor "on" (a) (d)		1.1	W
Single transistor 'on' (b) (d)		1.4	W
Two transistors 'on' equally ^{(a) (e)}		1.6	W
Linear derating factor above 25° C (a)			
Single transistor "on" ^{(a) (d)}		8.8	mW/° C
Single transistor 'on' (b) (d)		11.2	mW/° C
Two transistors 'on' equally ^{(a) (e)}		13.2	mW/° C
Thermal resistance - junction to ambient	R _{th(j-amb)}		
Single transistor "on" ^{(a) (d)}		114	° C/W
Single transistor "on" ^{(b) (d)}		89	° C/W
Two transistors 'on' equally ^{(a) (e)}		76	° C/W
Operating and storage temperature range	T _j , T _{stg}	-55 to + 150	° C

⁽a) For a device mounted on 50mm x 50mm x 1.6mm FR4 PCB with a high coverage of single sided 2oz weight copper in still air conditions with the heat sink split into three equal areas, one for each drain connection.

(b) For a device surface mounted on a FR4 PCB at t <= 10 sec.

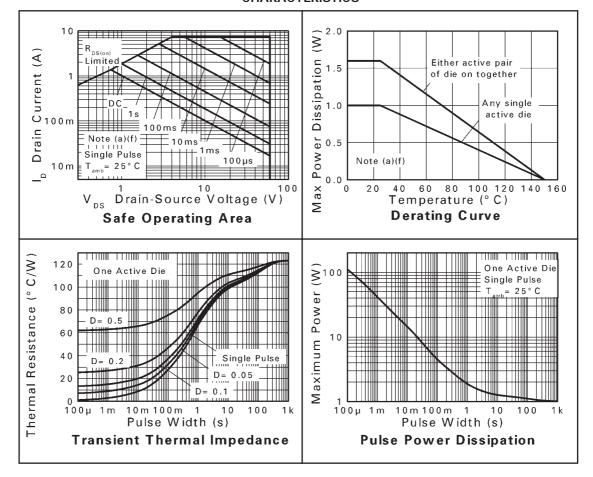


⁽c) Repetitive rating on 50mm x 50mm x 1.6mm FR4 PCB, duty cycle 2%, pulse width 300 µS in still air conditions with the heat sink split into three equal areas, one for each drain connection.

⁽d) For device with one active die.

⁽e) For any two die not sharing the same drain connection.

CHARACTERISTICS





ELECTRICAL CHARACTERISTICS (at $T_{amb} = 25^{\circ} C$ unless otherwise stated)

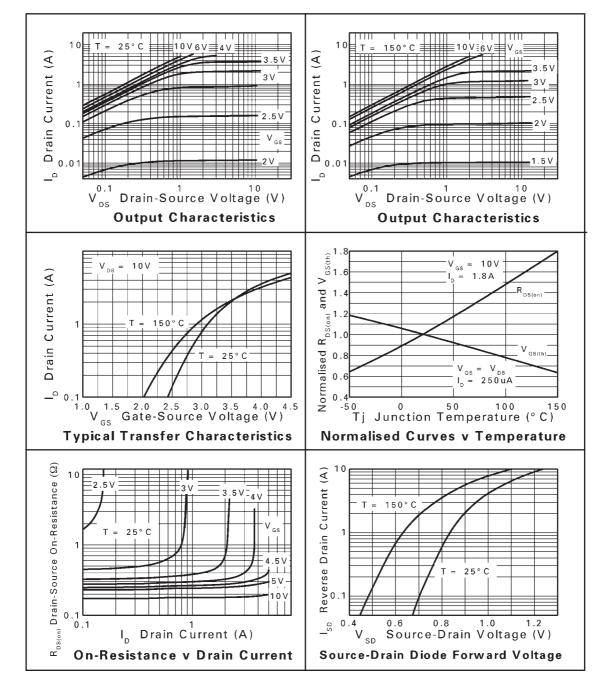
PARAMETER	SYMBOL	MIN.	TYP.	MAX.	UNIT	CONDITIONS	
STATIC	<u> </u>	1					
Drain-source breakdown voltage	V _{(BR)DSS}	60			V	I _D = 250μA, V _{GS} = 0V	
Zero gate voltage drain current	I _{DSS}			1.0	μΑ	V _{DS} = 60V, V _{GS} = 0V	
Gate-body leakage	I _{GSS}			100	nA	V_{GS} = ± 20V, V_{DS} = 0V	
Gate-source threshold voltage	V _{GS(th)}	1.0		3.0	V	I_D = 250 μ A, V_{DS} = V_{GS}	
Static drain-source on-state	R _{DS(on)}			0.3	Ω	V _{GS} = 10V, I _D = 1.8A	
resistance ⁽¹⁾				0.45	Ω	$V_{GS} = 4.5 V, I_{D} = 1.3 A$	
Forward transconductance (1) (3)	9 _{fs}		2.3		S	V _{DS} = 15V, I _D = 1.8A	
DYNAMIC (3)		•	•	•	•		
Input capacitance	C _{iss}		166		pF	V - 40V V - 0V	
Output capacitance	Coss		20		pF	V _{DS} = 40V, V _{GS} = 0V f= 1MHz	
Reverse transfer capacitance	C _{rss}		9		pF	1 - 1101112	
SWITCHING ^{(2) (3)}	·	•			•		
Turn-on-delay time	t _{d(on)}		1.8		ns		
Rise time	t _r		1.4		ns	$V_{DD} = 30V, I_{D} = 1.8A$	
Turn-off delay time	t _{d(off)}		4.9		ns	$R_G \cong 6.0\Omega$, $V_{GS} = 10V$	
Fall time	t _f		2.0		ns		
Total gate charge	Qg		3.2		nC	V _{DS} = 30V, V _{GS} = 10V	
Gate-source charge	Q _{gs}		0.7		nC	$I_{D} = 1.8A$	
Gate drain charge	Q _{gd}		0.8		nC		
SOURCE-DRAIN DIODE		•	•	•	•		
Diode forward voltage (1)	V _{SD}			0.95	V	T _j = 25°C, I _S = 0.45A,	
						V _{GS} = 0V	
Reverse recovery time (3)	t _{rr}		21		ns	T _j = 25° C, I _F = 1.0A,	
Reverse recovery charge (3)	Q _{rr}		21		nC	di/dt= 100A/μs	

NOTES

- (1) Measured under pulsed conditions. Pulse width $\leq 300 \,\mu s$; duty cycle $\leq 2\%$.
- (2) Switching characteristics are independent of operating junction temperature.
- (3) For design aid only, not subject to production testing.



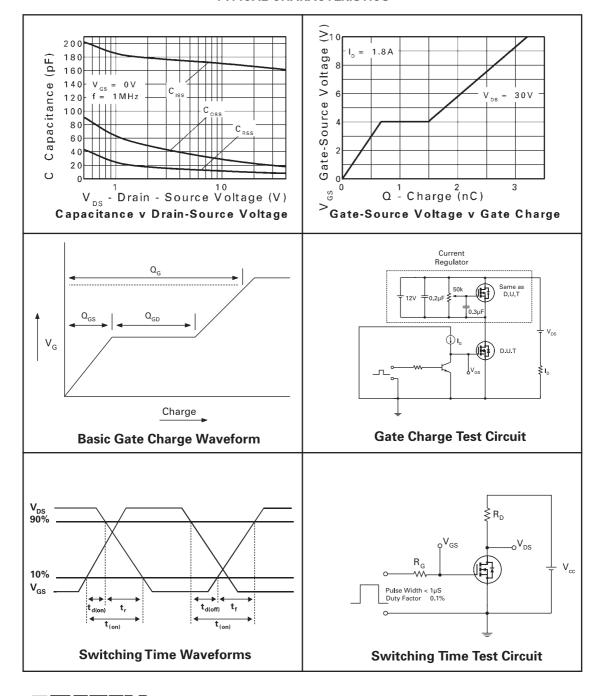
TYPICAL CHARACTERISTICS



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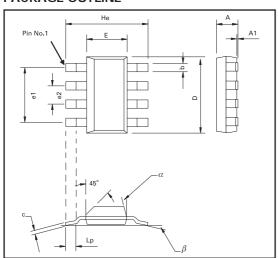


TYPICAL CHARACTERISTICS





PACKAGE OUTLINE



Controlling dimensions are in millimeters. Approximate conversions are given in inches

PACKAGE DIMENSIONS

DIM	IV.	Millimeters		Inches		DIM	Millimeters		Inches				
	Min	Max	Тур.	Min	Max	Тур.	Diivi	Min	Max	Тур.	Min	Max	Тур.
А	-	1.7	-	-	0.067	-	e1	-	-	4.59	-	-	0.18 07
A1	0.02	0.1	-	0.008	0.004	-	e2	-	-	1.53	-	-	0.06 02
b	-	-	0.7	-	-	0.0275	He	6.7	7.3	-	0.264	0.287	-
С	0.24	0.32	-	0.009	0.013	-	Lp	0.9	-	-	0.035	-	-
D	6.3	6.7	-	0.248	0.264	-	α	-	15°	-	-	15°	-
Е	3.3	3.7	-	0.130	0.145	-	β	-	-	10°	-	-	10°

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