

FEATURES

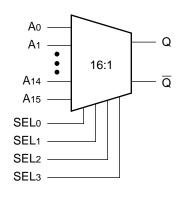
- 850ps Data Input to Output
- Extended 100E VEE range of -4.2V to -5.5V
- **■** Differential output
- Fully compatible with industry standard 10KH, 100K ECL levels
- Internal 75K Ω input pull-down resistors
- Fully compatible with Motorola MC10E/100E164
- Available in 28-pin PLCC package

DESCRIPTION

The SY10/100E164 are 16:1 multiplexers with a differential output. The select inputs (SEL0,1,2,3) control which one of the sixteen data inputs (A0-A15) is propagated to the output.

Special attention to the design layout results in a typical skew between the 16 inputs of only 50ps.

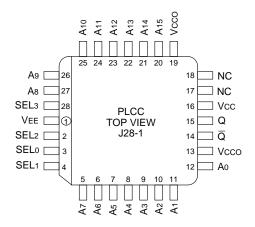
BLOCK DIAGRAM



PIN NAMES

Pin	Function
A0 – A15	Data Inputs
SEL[0:3]	Select Inputs
Q, Q	Outputs
Vcco	Vcc to Output

PACKAGE/ORDERING INFORMATION



28-Pin PLCC (J28-1)

Ordering Information⁽¹⁾

Part Number	Package Type	Operating Range	Lead Finish				
SY10E164JC	J28-1	Commercial	Sn-Pb				
SY10E164JCTR ⁽²⁾	J28-1	Commercial	Commercial SY10E164JC				
SY100E164JC	J28-1	Commercial	SY100E164JC	Sn-Pb			
SY100E164JCTR ⁽²⁾	J28-1	Commercial	ommercial SY100E164JC				
SY10E164JZ ⁽³⁾	J28-1	Commercial	SY10E164JZ with Pb-Free bar-line indicator	Matte-Sn			
SY10E164JZTR ^(2, 3)	J28-1	Commercial	SY10E164JZ with Pb-Free bar-line indicator	Matte-Sn			
SY100E164JZ ⁽³⁾	J28-1	Commercial	SY100E164JZ with Pb-Free bar-line indicator	Matte-Sn			
SY100E164JZTR ^(2, 3)	J28-1	Commercial	SY100E164JZ with Pb-Free bar-line indicator	Matte-Sn			

Notes

- 1. Contact factory for die availability. Dice are guaranteed at T_A = 25°C, DC Electricals only.
- 2. Tape and Reel.
- 3. Pb-Free package is recommended for new designs.

TRUTH TABLE

SEL3	SEL2	SEL1	SEL ₀	Data		
L	L	L	L	A ₀		
L	L	L	Н	A 1		
L	L	Н	L	A2		
L	L	Н	Н	Аз		
L	Н	L	L	A4		
L	Н	L	Н	A 5		
L	Н	Н	L	A ₆		
L	Н	Н	Н	A7		

SEL3	SEL ₂	SEL1	SEL ₀	Data
Н	L	L	L	A8
Н	L	L	Н	A 9
Н	L	Н	L	A 10
Н	L	Н	Н	A11
Н	Н	L	L	A12
Н	Н	L	Н	A13
Н	Н	Н	L	A14
Н	Н	Н	Н	A 15

DC ELECTRICAL CHARACTERISTICS

VEE = VEE (Min.) to VEE (Max.); VCC = VCCO = GND

		TA = 0°C			TA = +25°C			TA = +85°C				
Symbol	Parameter	Min.	Тур.	Max.	Min.	Тур.	Max.	Min.	Тур.	Max.	Unit	Condition
Iн	Input HIGH Current	_	_	150		_	150			150	μΑ	
IEE	Power Supply Current										mΑ	_
	10E	_	59	71	_	59	71	_	59	71		
	100E	_	59	71	_	59	71	_	68	81		

AC ELECTRICAL CHARACTERISTICS

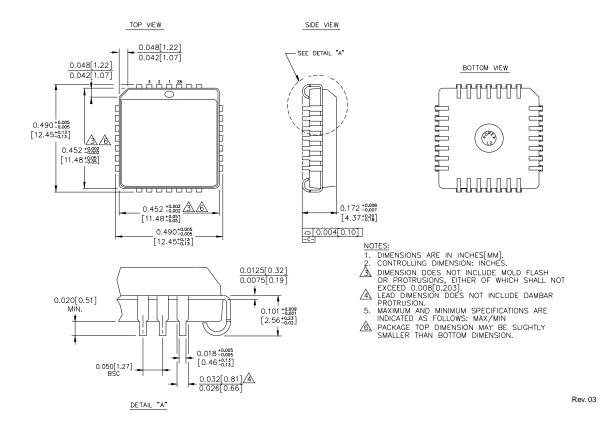
VEE = VEE (Min.) to VEE (Max.); VCC = VCCO = GND

		TA = 0°C			TA = +25°C			TA = +85°C				
Symbol	Parameter	Min.	Тур.	Max.	Min.	Тур.	Max.	Min.	Тур.	Max.	Unit	Condition
tPD	Propagation Delay to Output A Input SEL0 SEL1 SEL2 SEL3	350 500 400 400 400	600 700 675 675 550	850 900 900 900 700	350 500 400 400 400	600 700 675 675 550	850 900 900 900 700	350 500 400 400 400	600 700 675 675 550	850 900 900 900 700	ps	_
tskew	Within-Device Skew	_	50	_	_	50	_	_	50	_	ps	1
tr tf	Rise/Fall Times 20–80%	275	400	550	275	400	550	275	400	550	ps	_

Note:

1. Within-device skew is defined as the difference in the A to Q delay between the 16 different A inputs.

28-PIN PLCC (J28-1)



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